



Figure 351: U-NII-3_5745MHz_Low Ch_149_20MHz BW_n-mode_-27dBm_26-40GHz_Port 1.

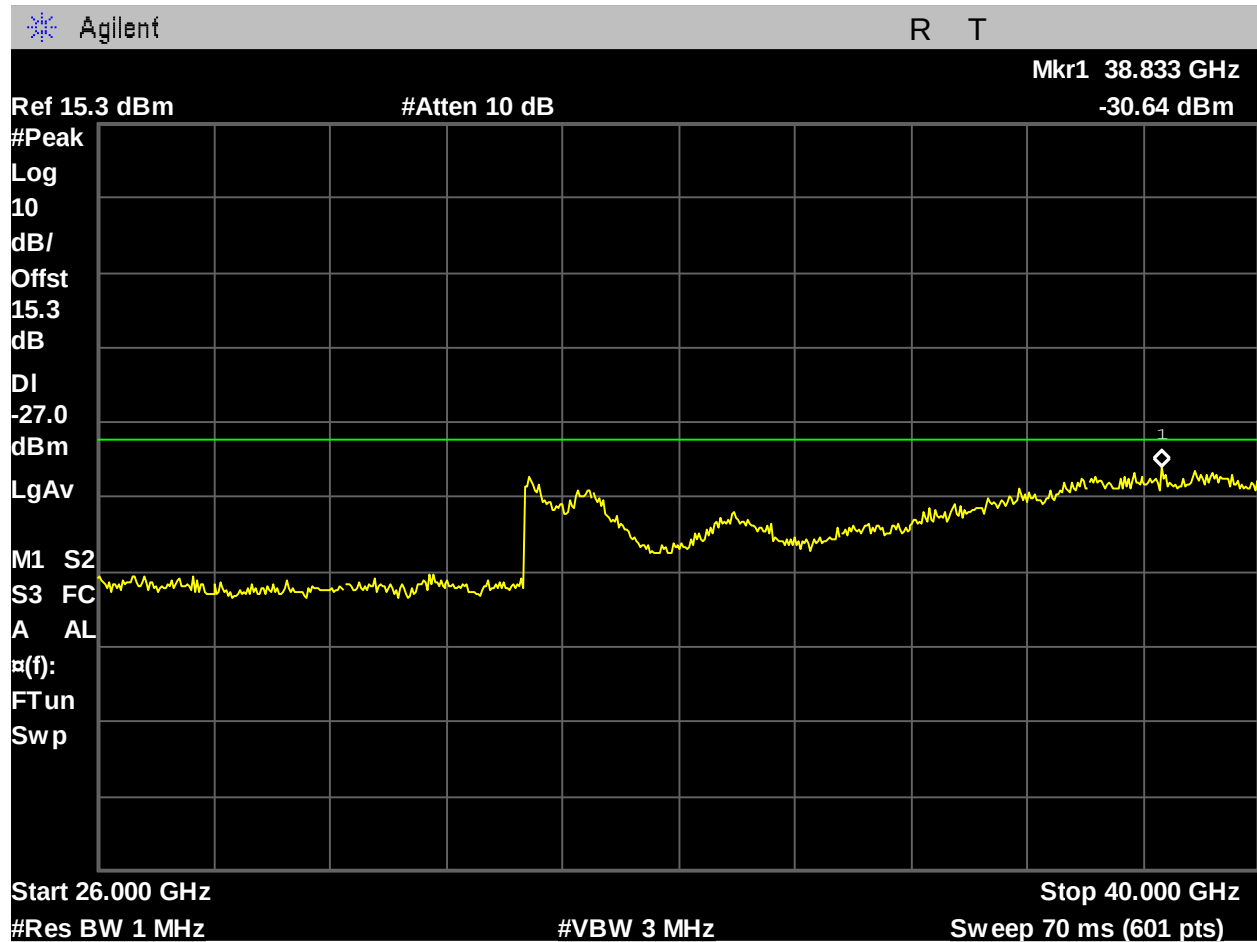


Figure 352: U-NII-3_5745MHz_Low Ch_149_20MHz BW_n-mode_-27dBm_26-40GHz_Port 2.

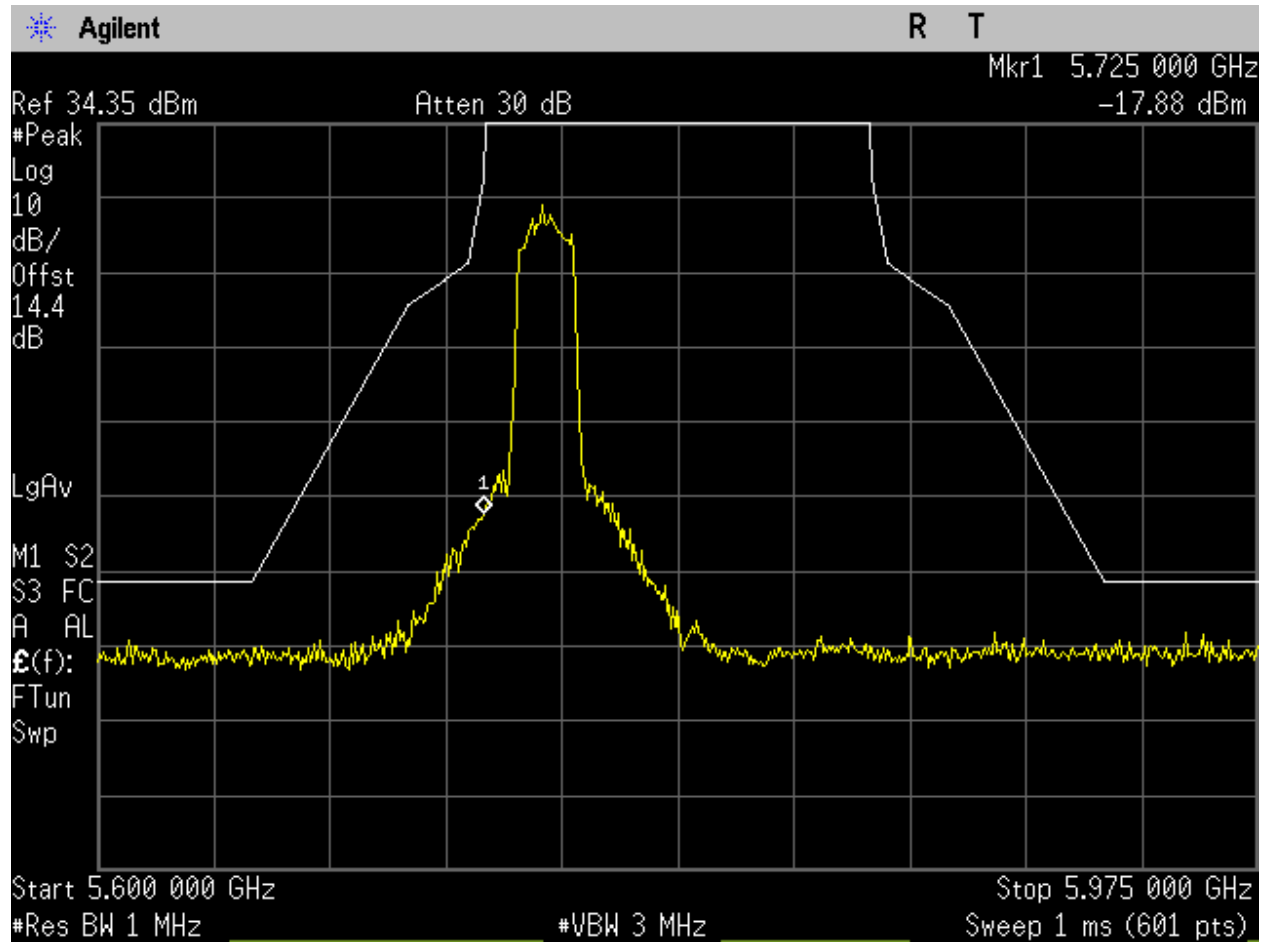


Figure 353: U-NII-3_5745MHz_Low Ch_149_20MHz BW_n-mode_Lower Emission Mask_Port 1.

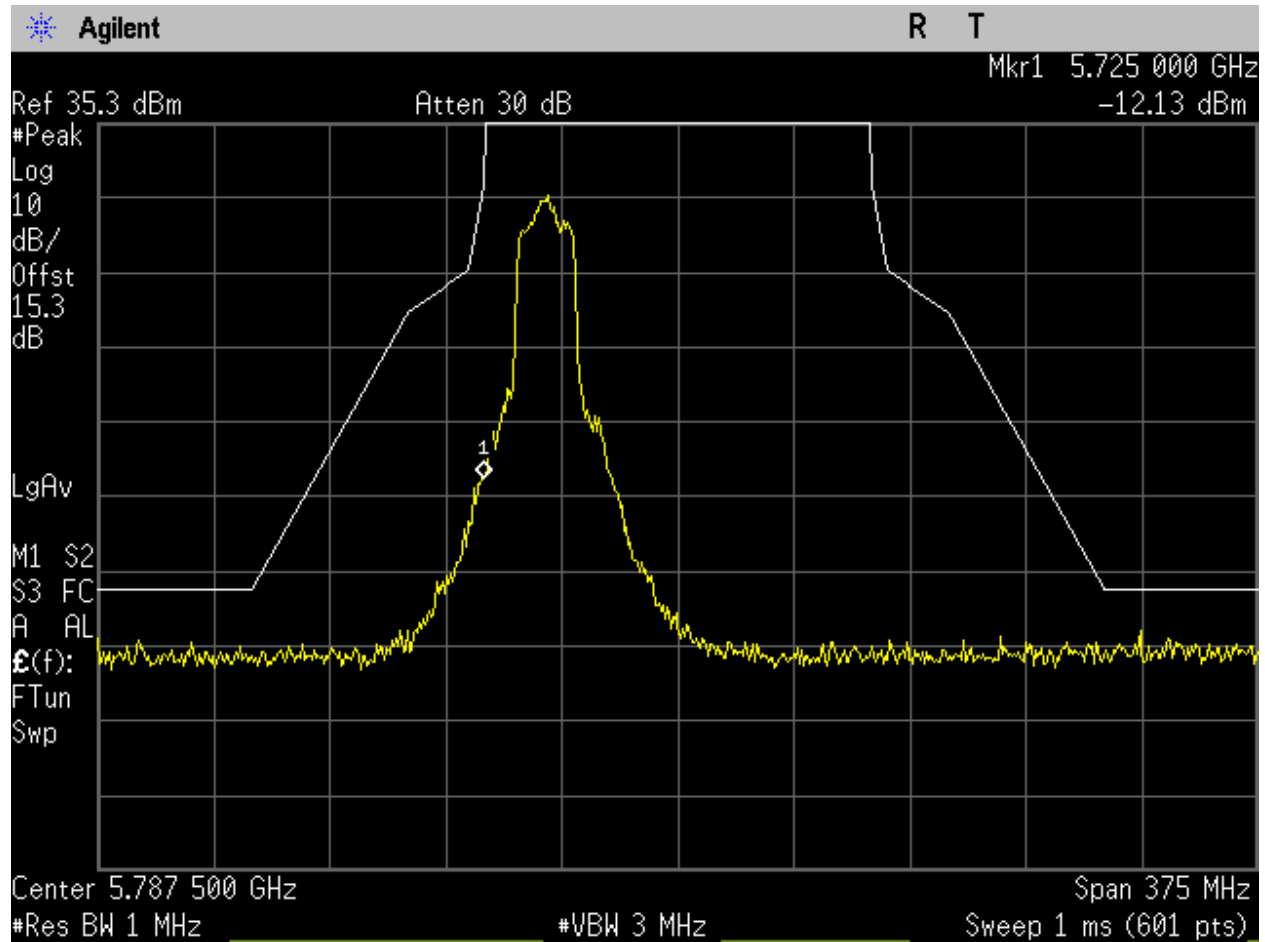


Figure 354: U-NII-3_5745MHz_Low Ch_149_20MHz BW_n-mode_Lower Emission Mask_Port 2.

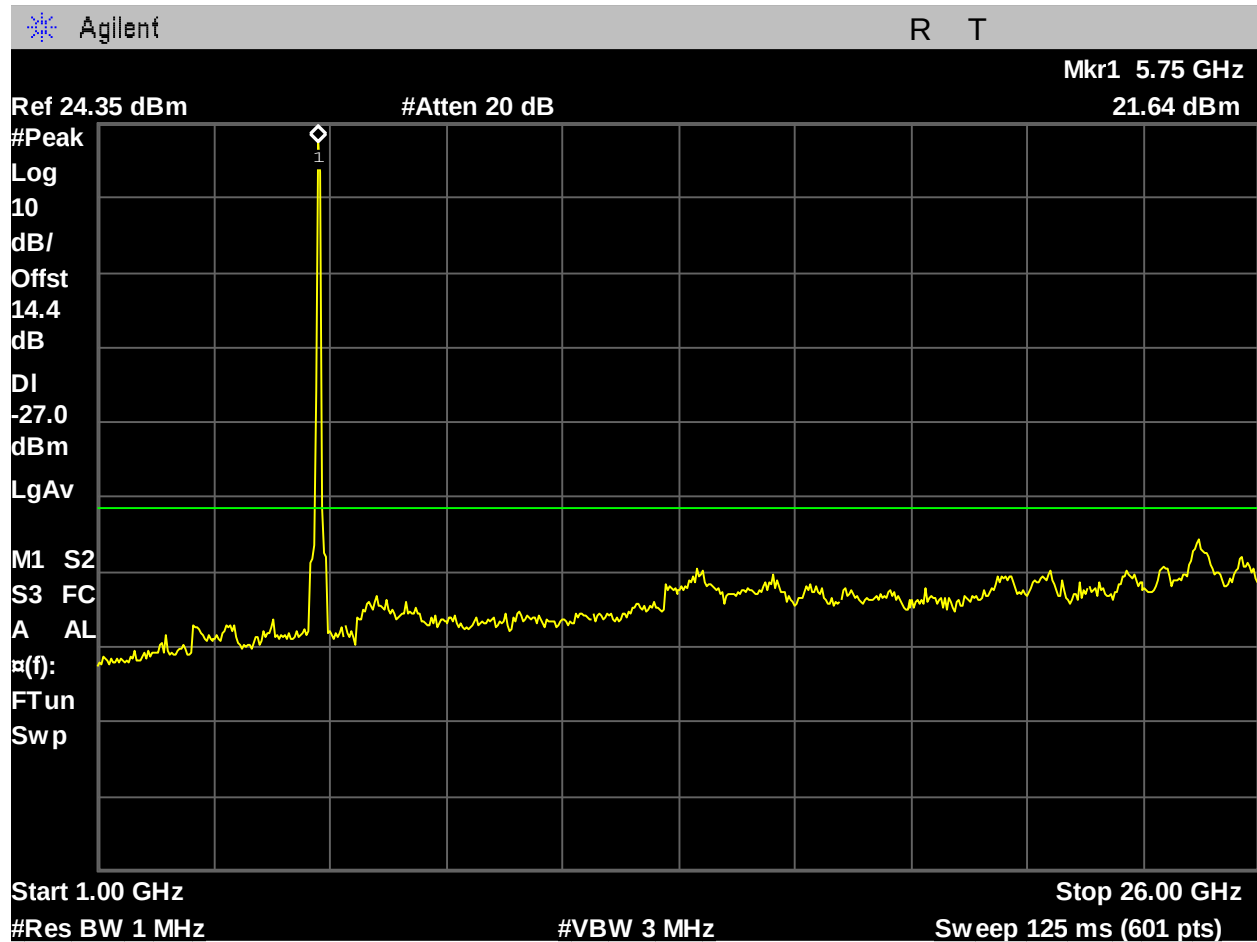


Figure 355: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ac-mode_-27dBm_1-26GHz_Port 1.

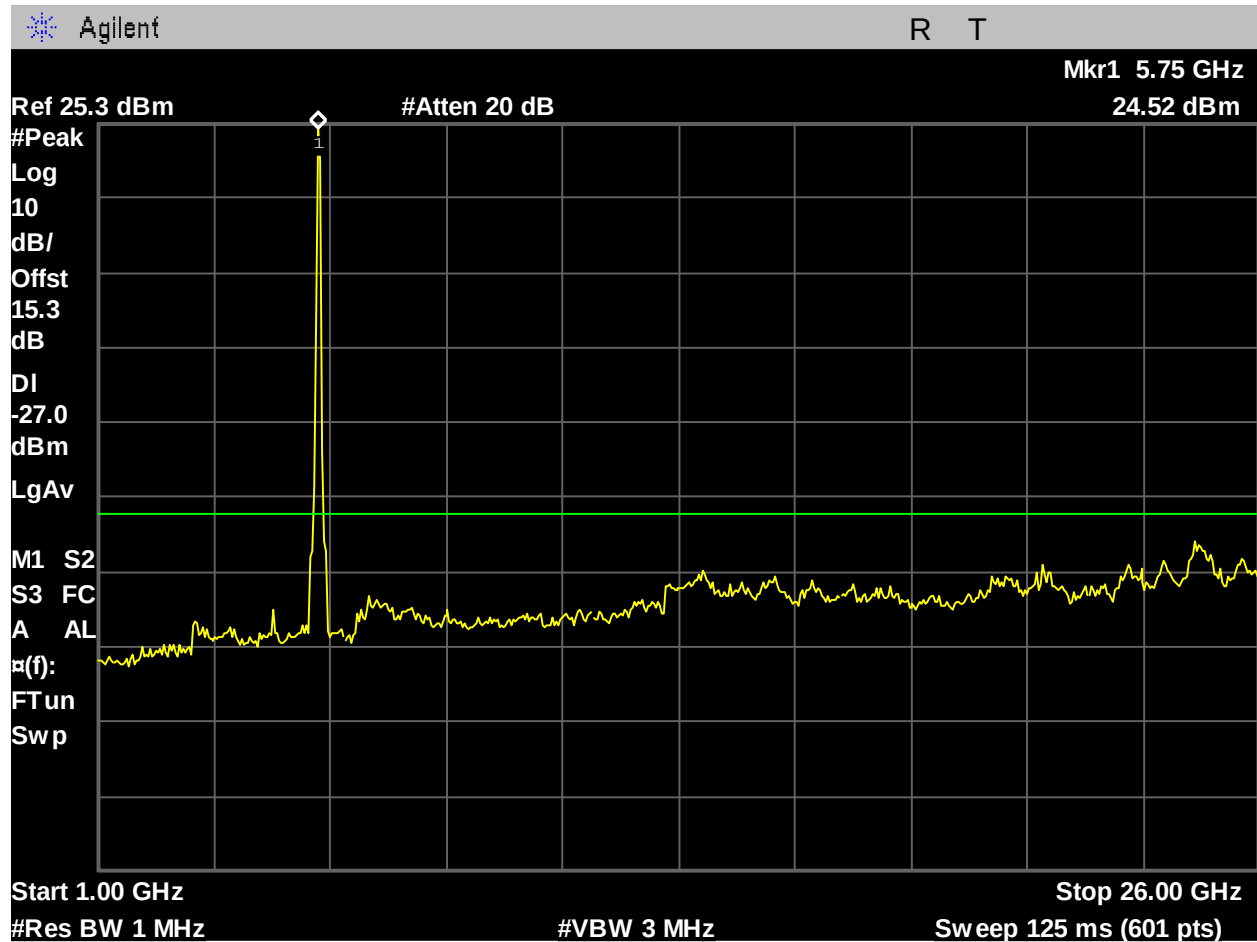


Figure 356: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ac-mode_-27dBm_1-26GHz_Port 2.



Figure 357: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ac-mode_-27dBm_26-40GHz_Port 1.

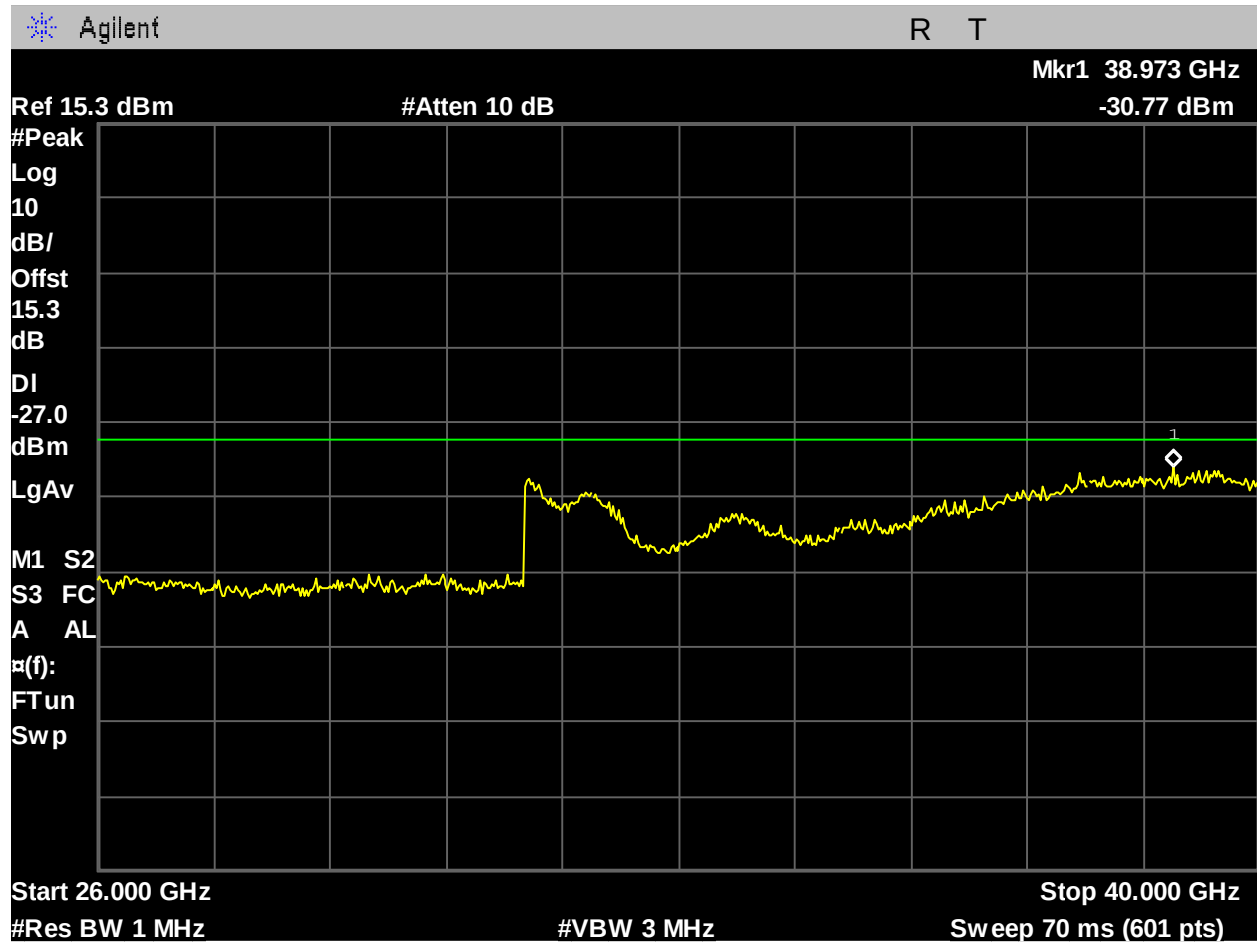


Figure 358: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ac-mode_-27dBm_26-40GHz_Port 2.

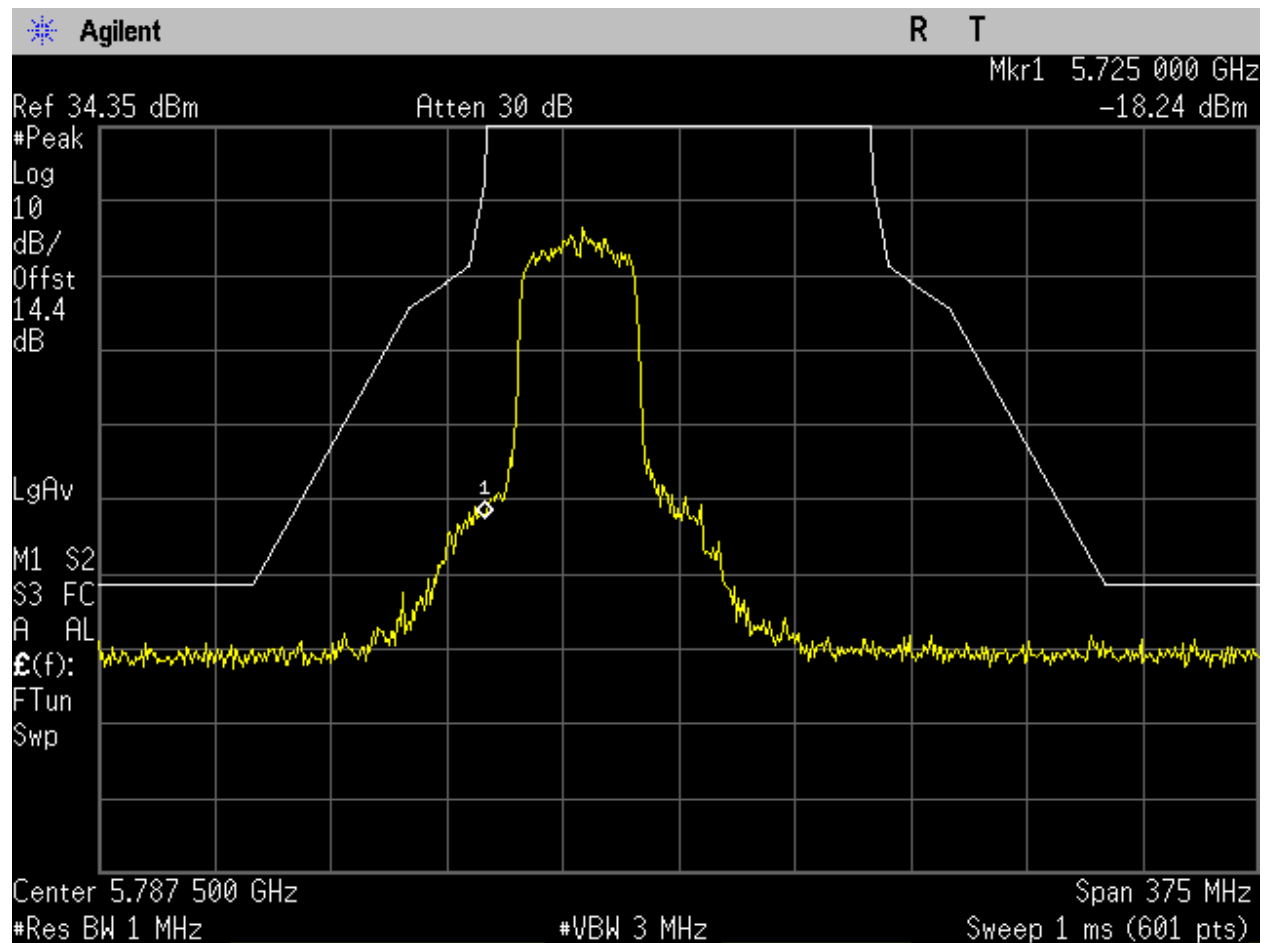


Figure 359: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ac-mode_Lower Emission Mask_Port 1.

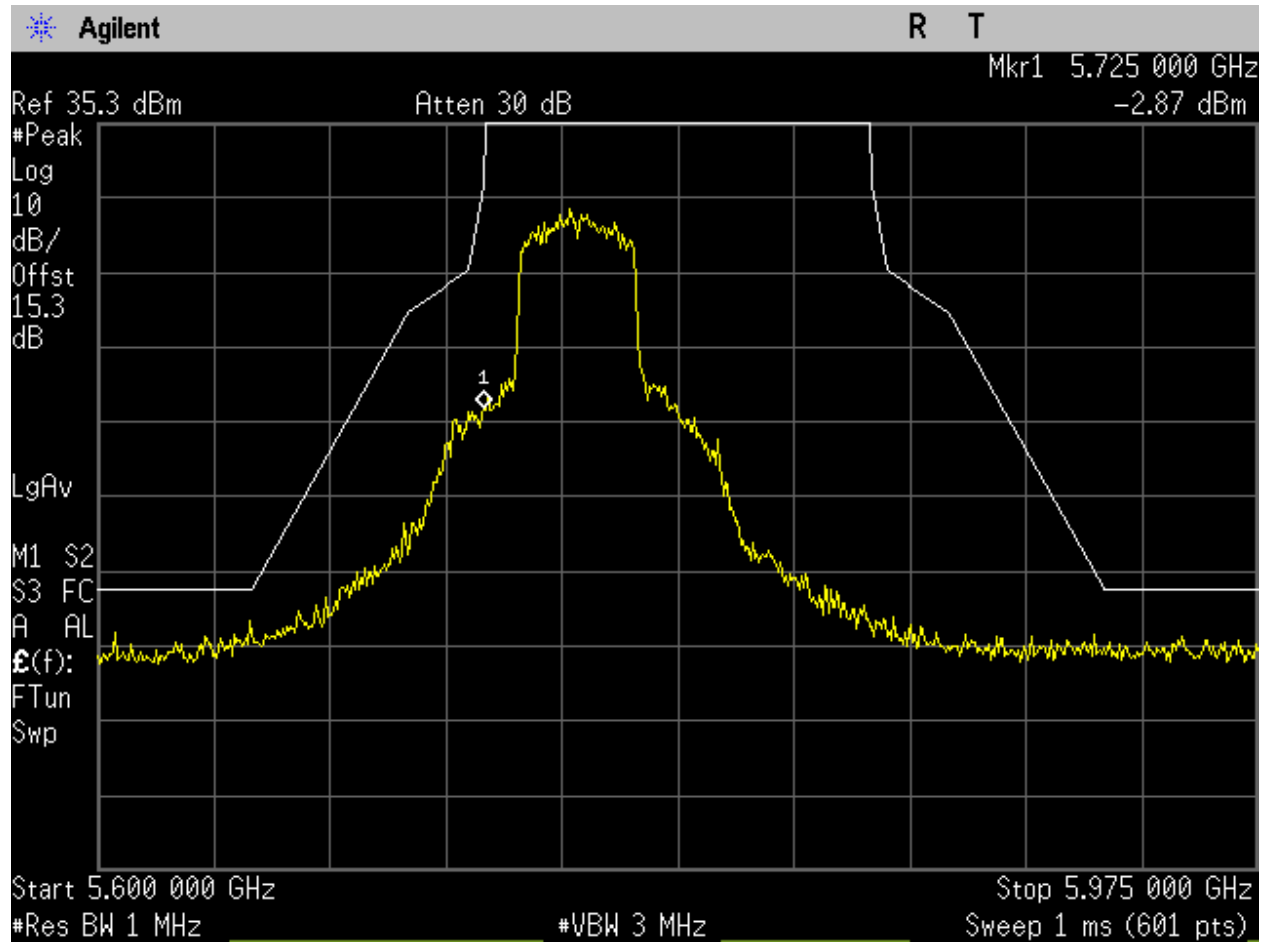


Figure 360: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ac-mode_Lower Emission Mask_Port 2.

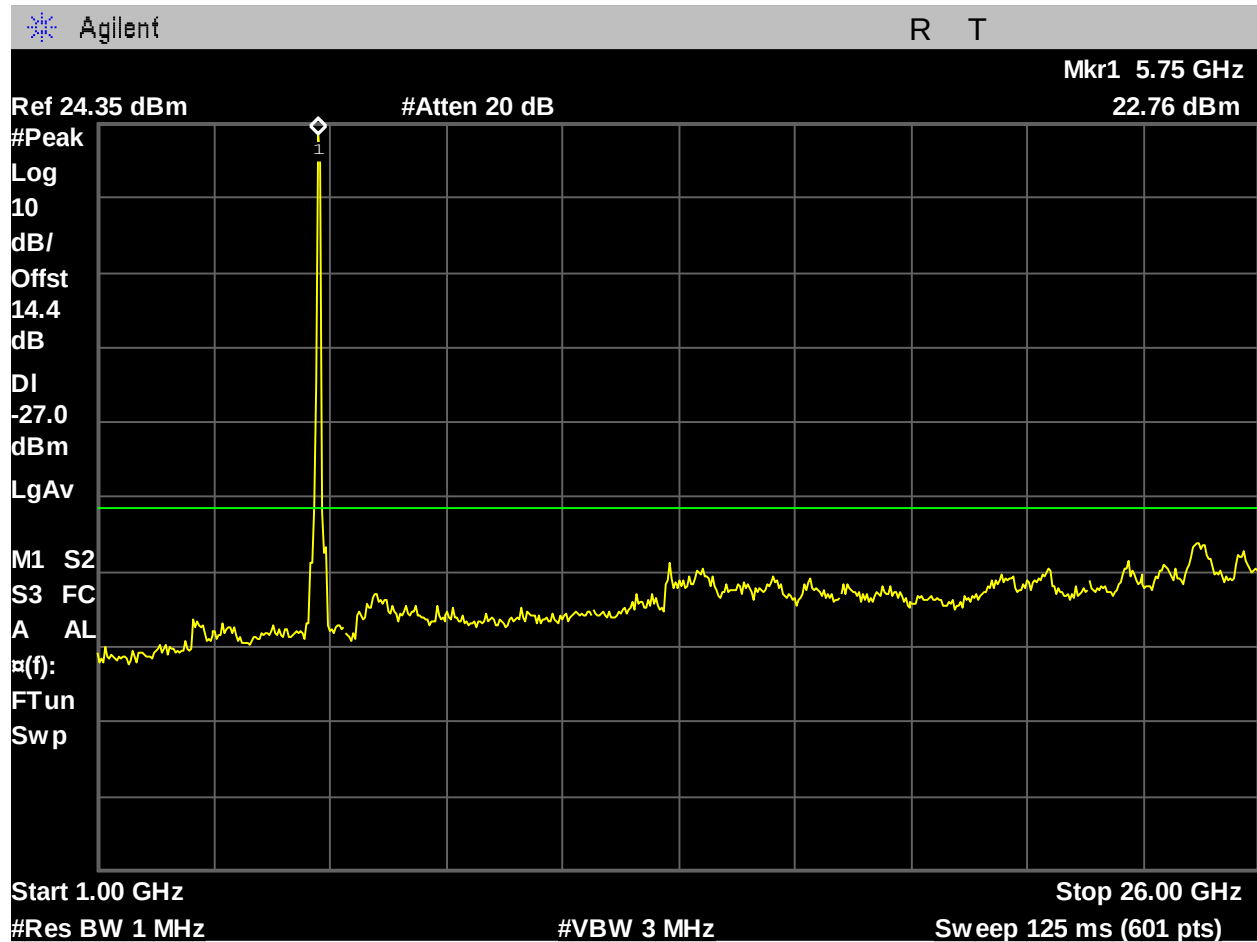


Figure 361: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ax-mode_-27dBm_1-26GHz_Port 1.

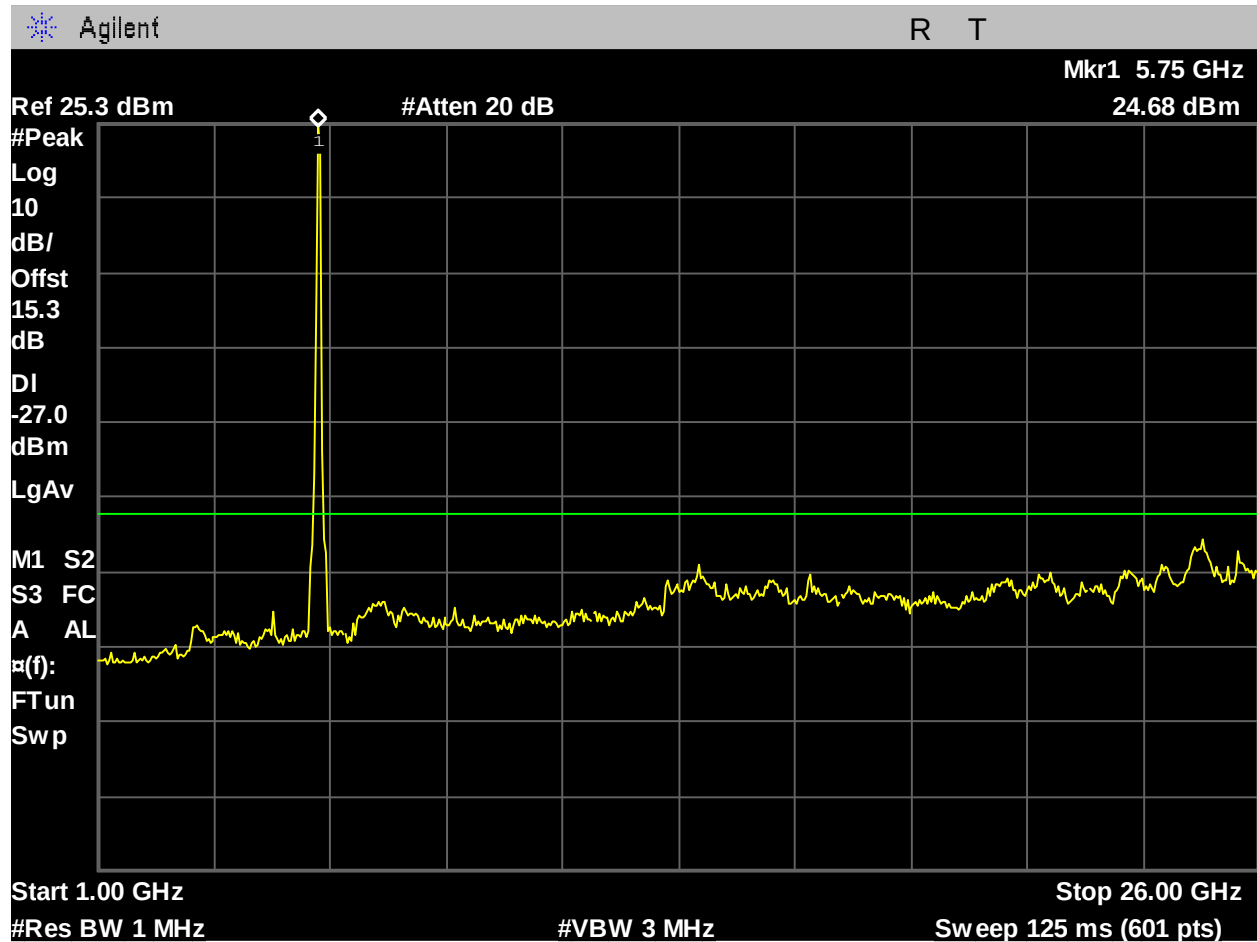


Figure 362: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ax-mode_-27dBm_1-26GHz_Port 2.

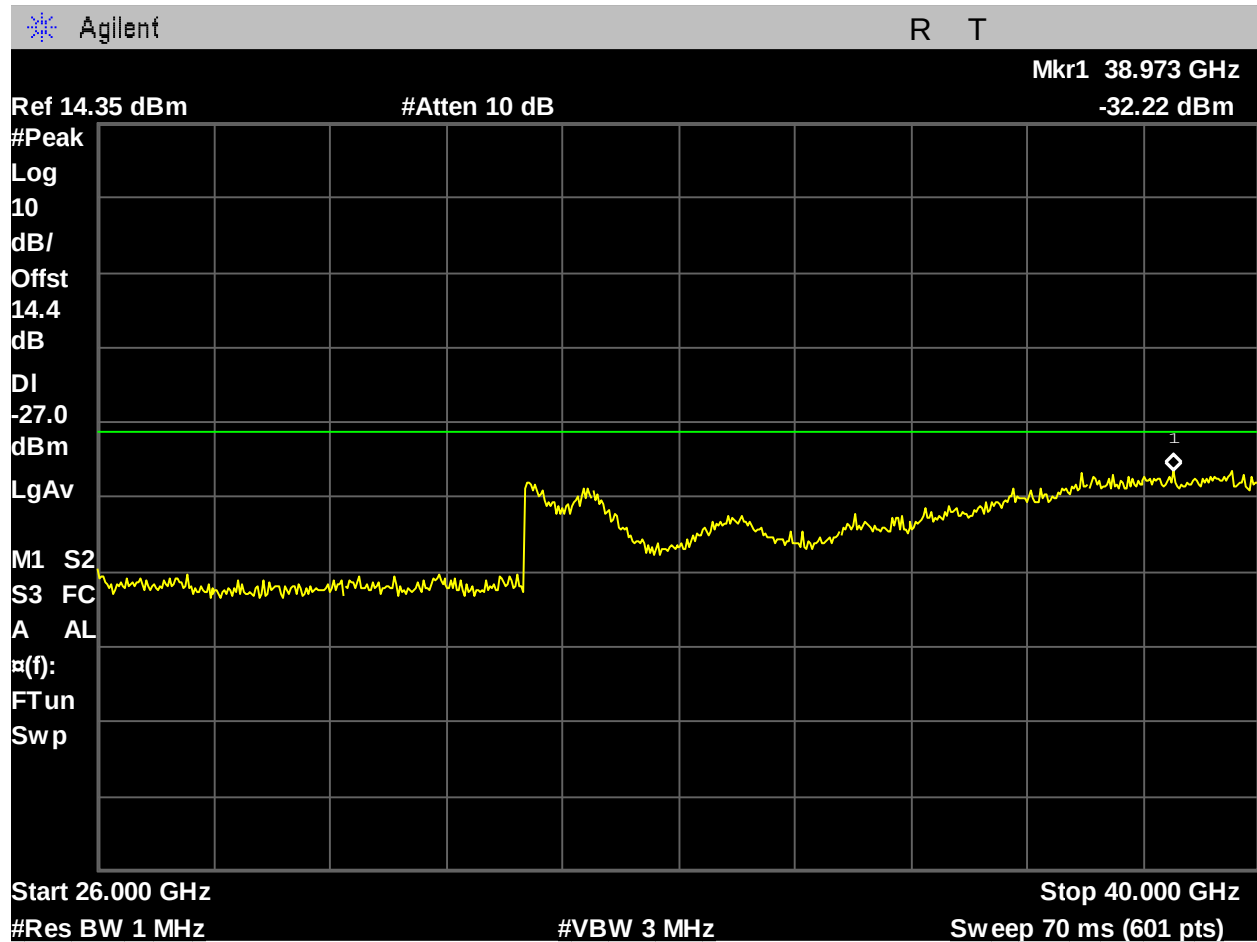


Figure 363: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ax-mode_-27dBm_26-40GHz_Port 1.

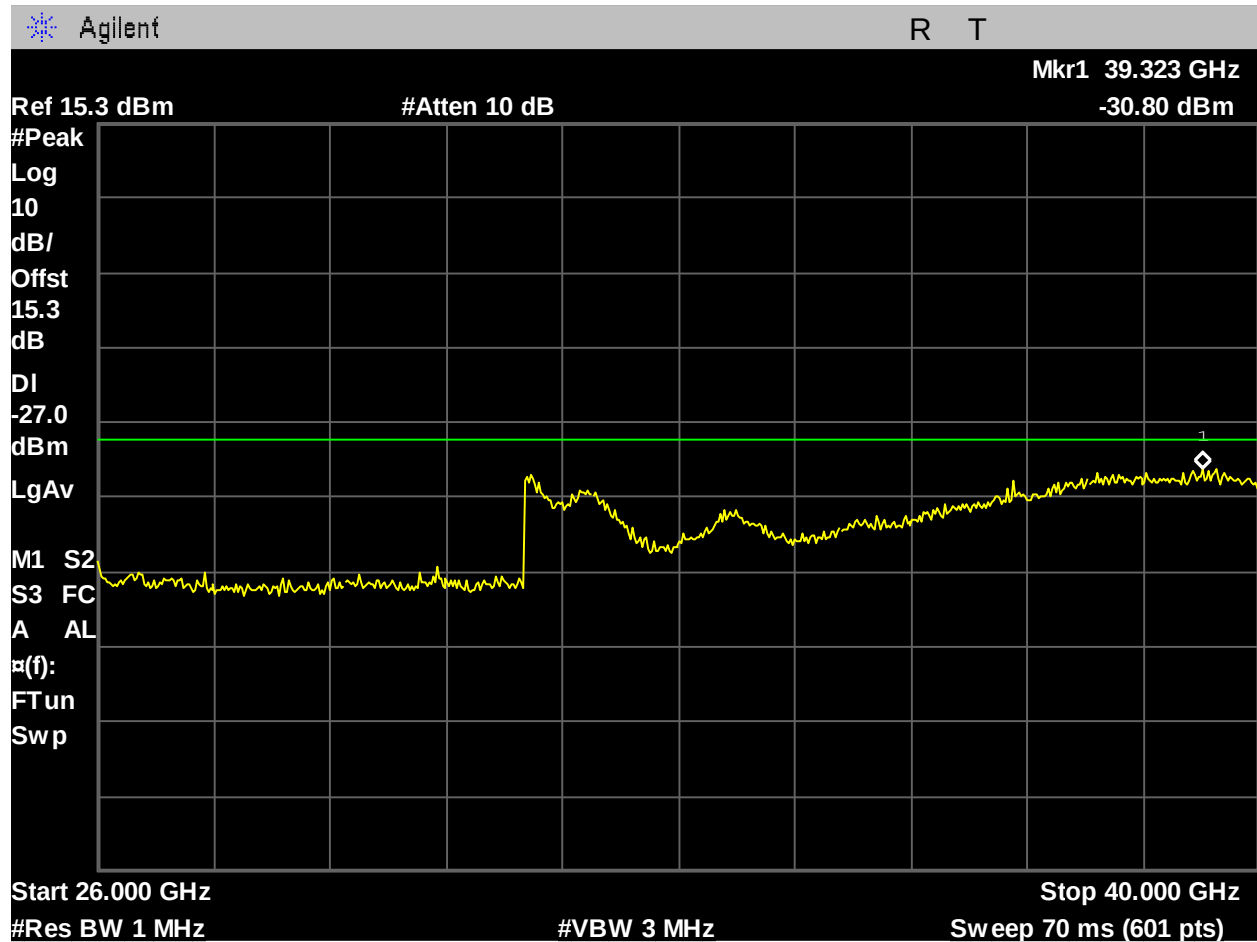


Figure 364: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ax-mode_-27dBm_26-40GHz_Port 2.

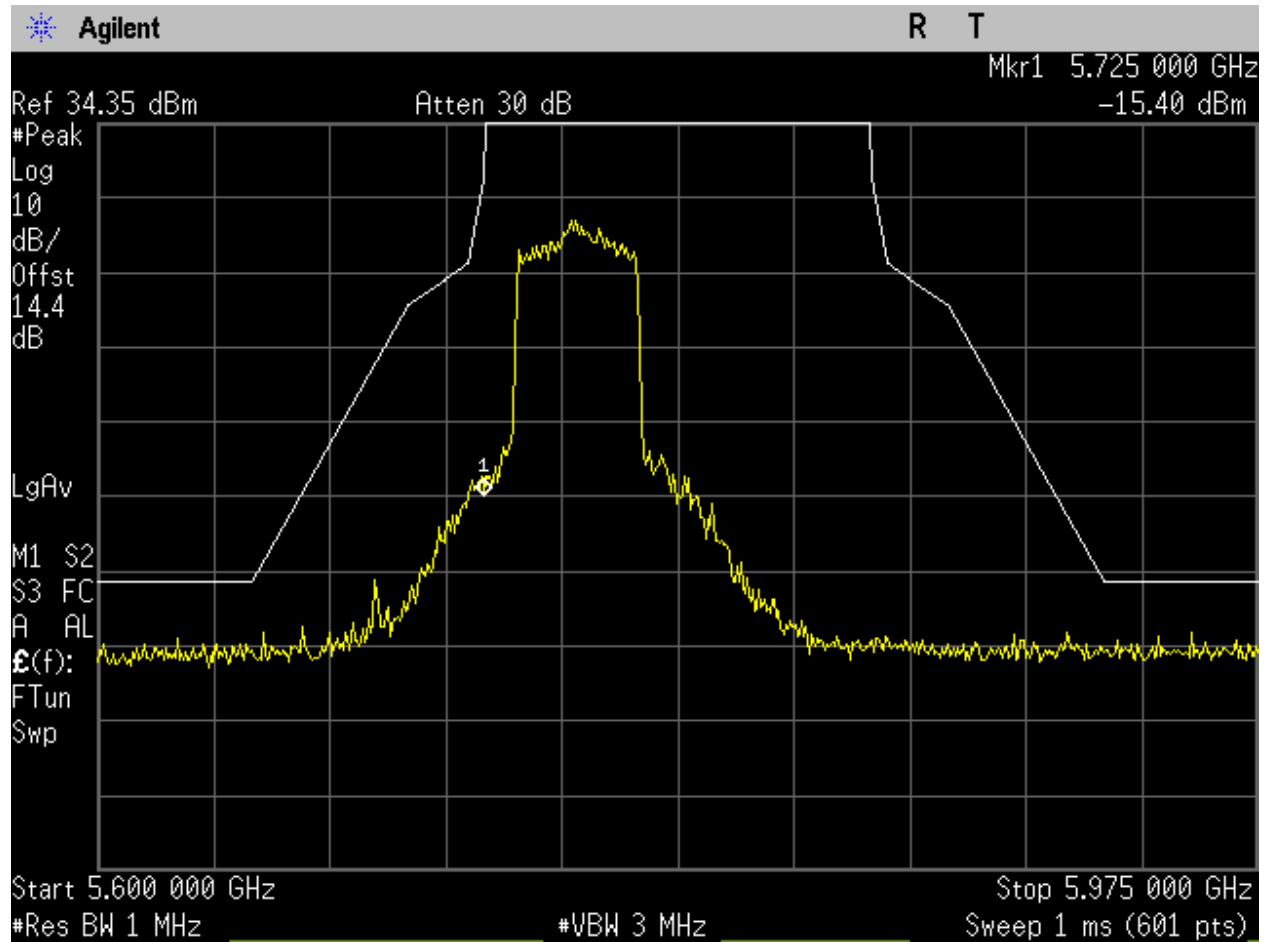


Figure 365: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ax-mode_Lower Emission Mask_Port 1.

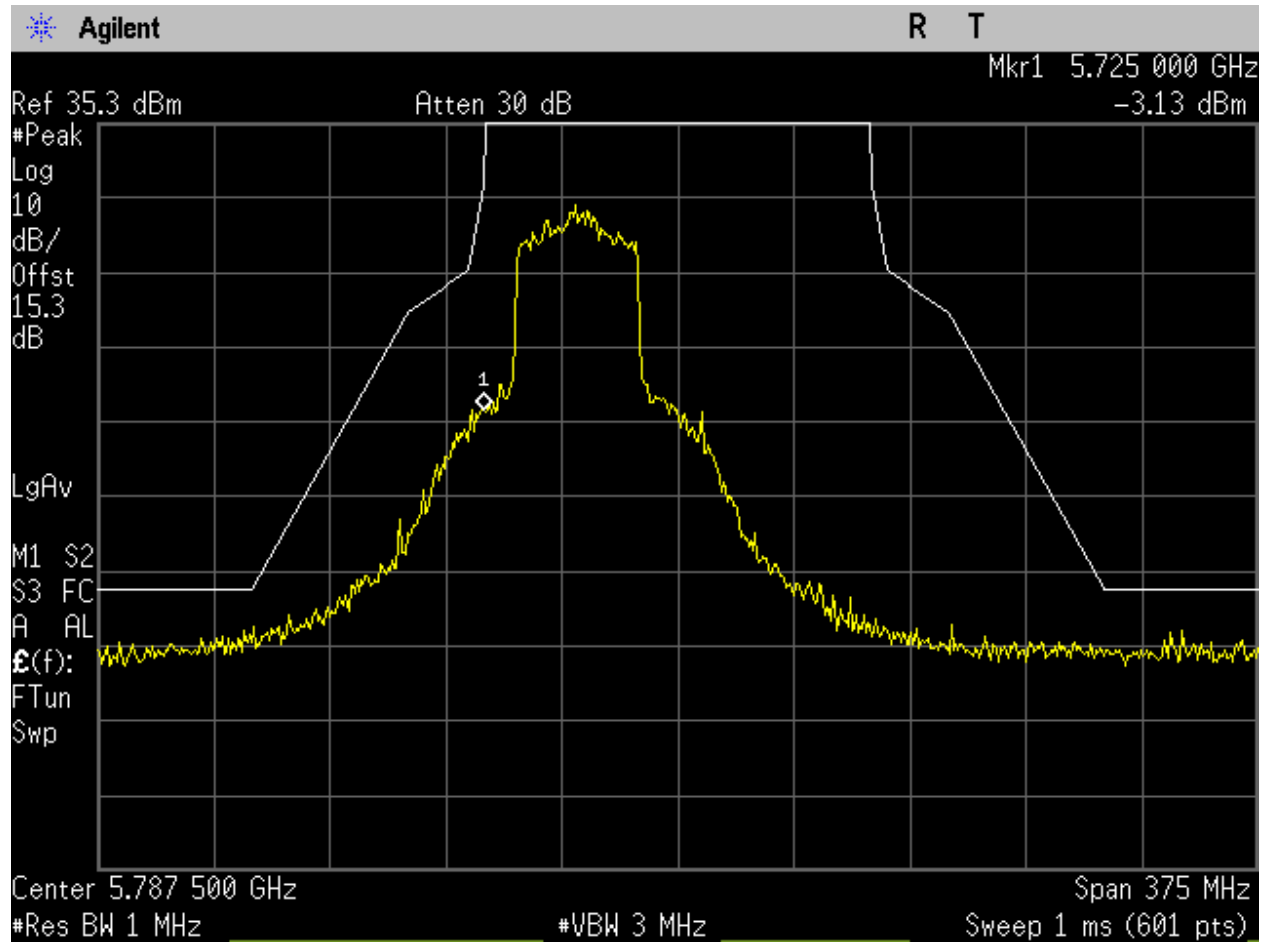


Figure 366: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ax-mode_Lower Emission Mask_Port 2.

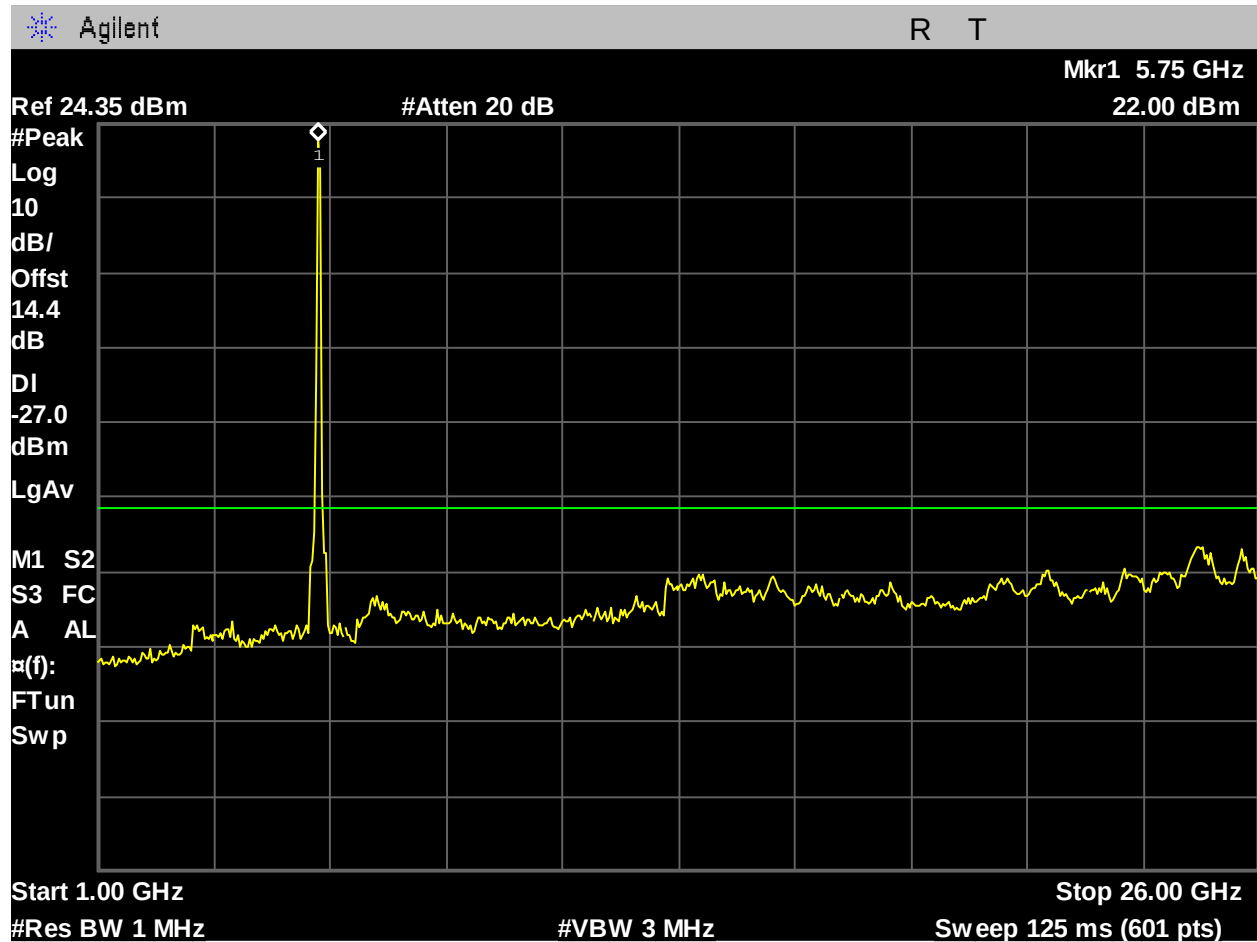


Figure 367: U-NII-3_5755MHz_Low Ch_151_40MHz BW_n-mode_-27dBm_1-26GHz_Port 1.

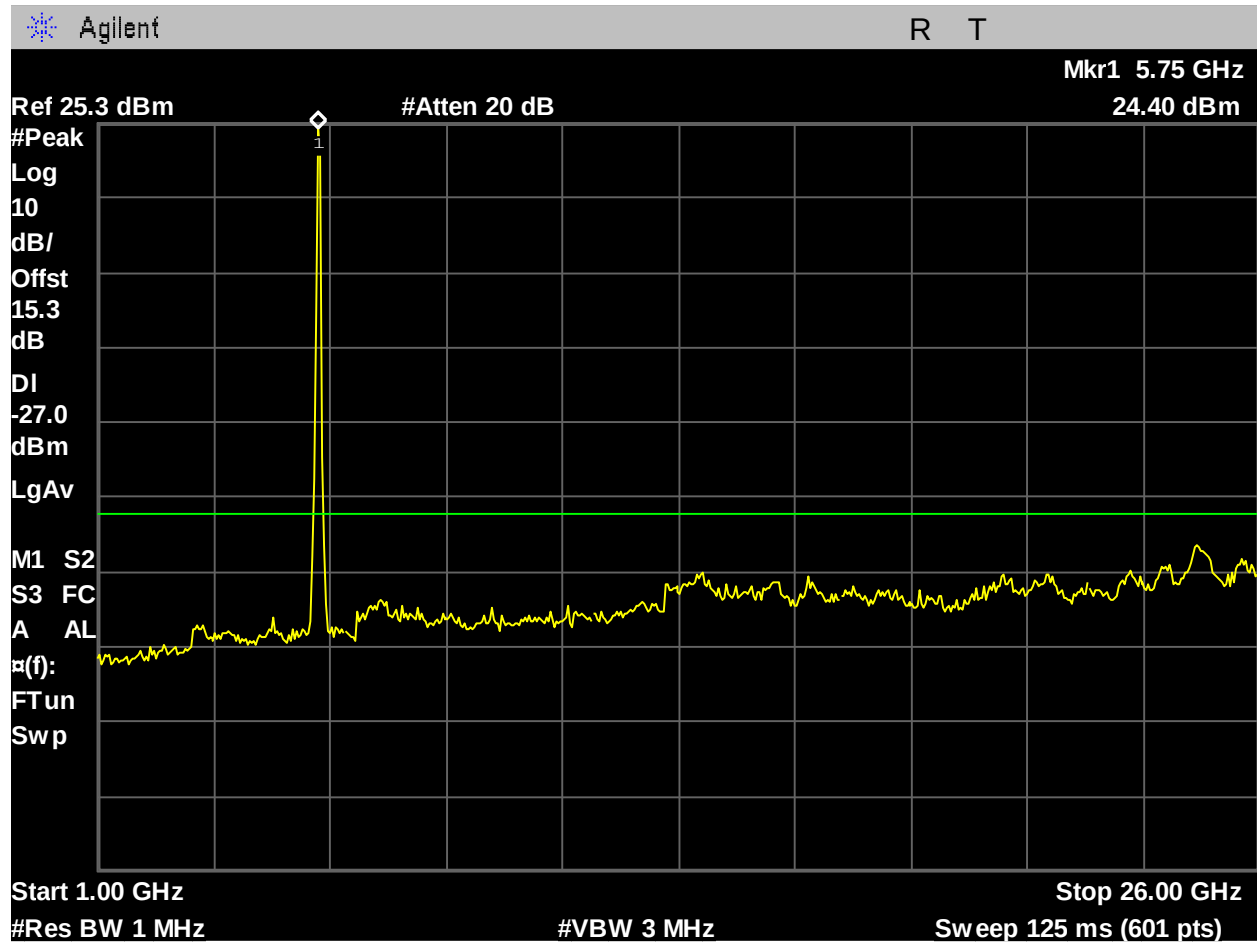


Figure 368: U-NII-3_5755MHz_Low Ch_151_40MHz BW_n-mode_-27dBm_1-26GHz_Port 2.

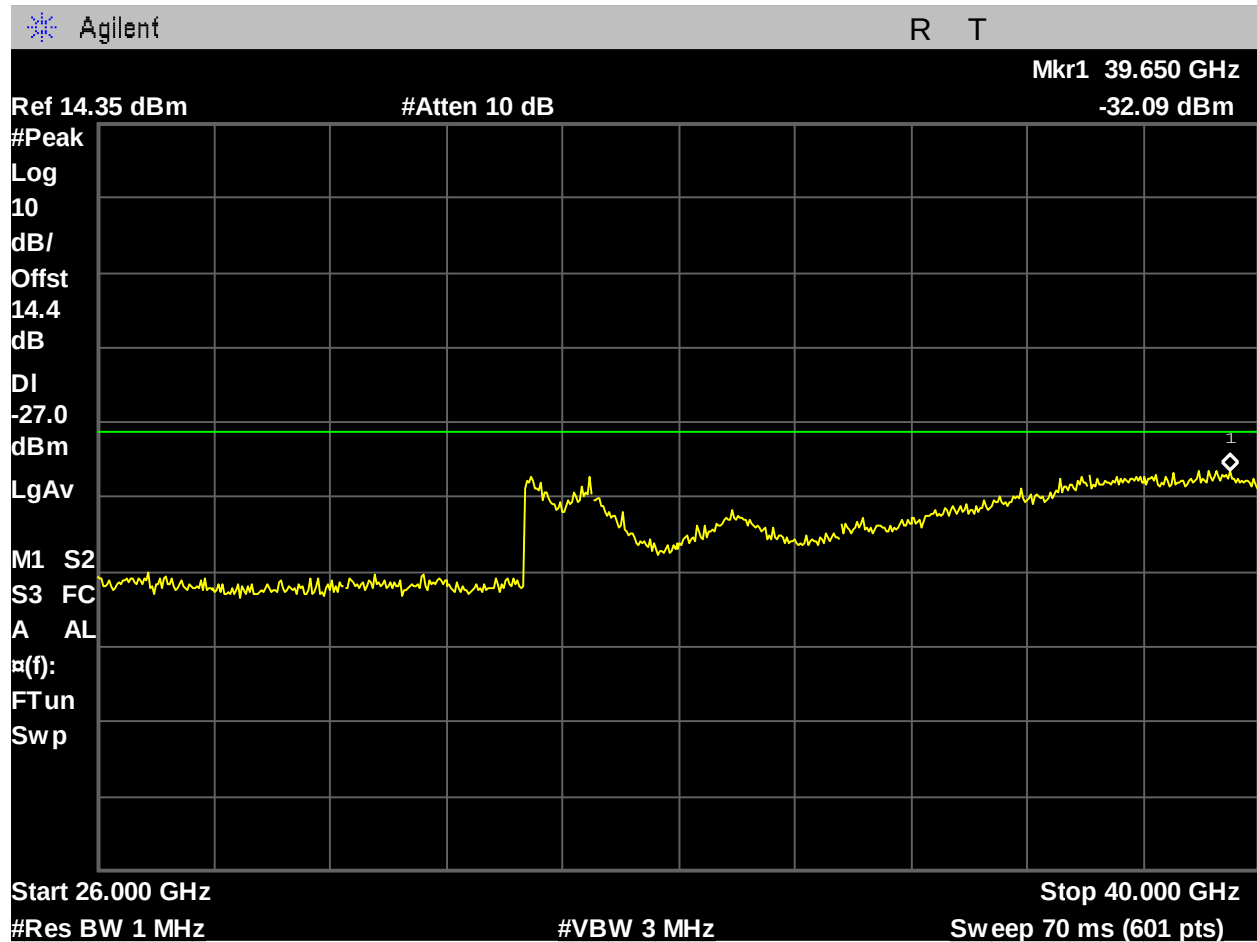


Figure 369: U-NII-3_5755MHz_Low Ch_151_40MHz BW_n-mode_-27dBm_26-40GHz_Port 1.

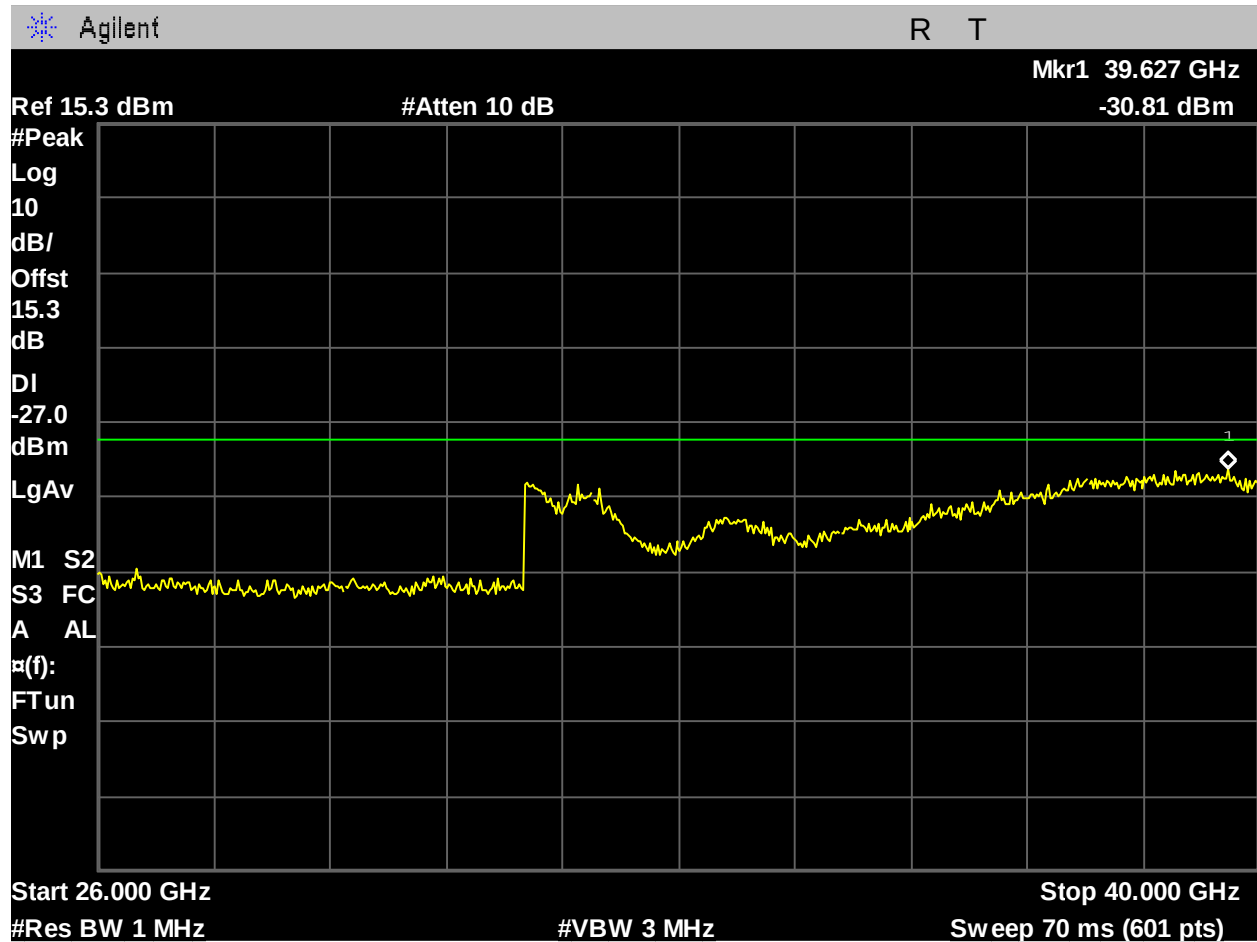


Figure 370: U-NII-3_5755MHz_Low Ch_151_40MHz BW_n-mode_-27dBm_26-40GHz_Port 2.

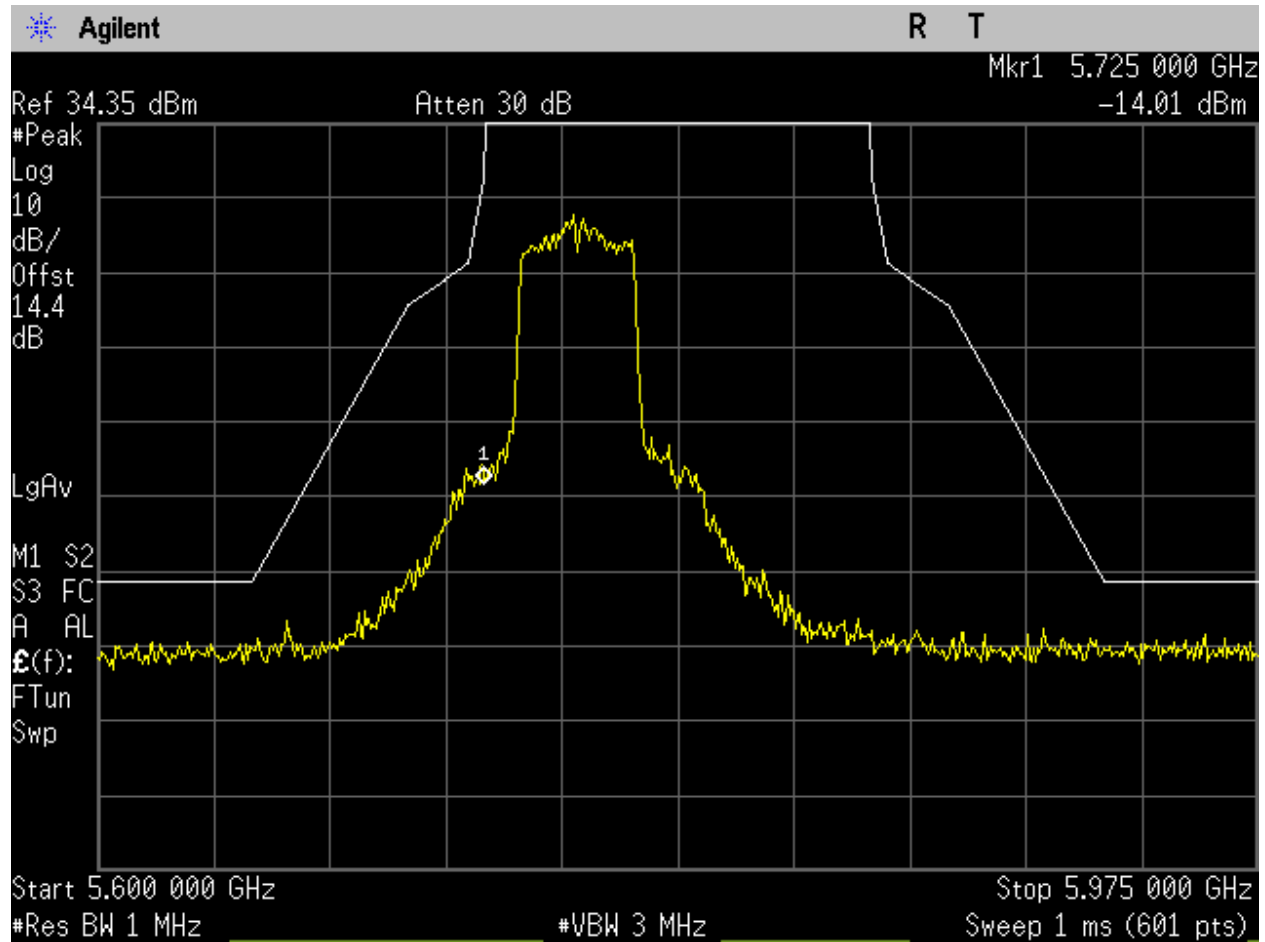


Figure 371: U-NII-3_5755MHz_Low Ch_151_40MHz BW_n-mode_Lower Emission Mask_Port 1.

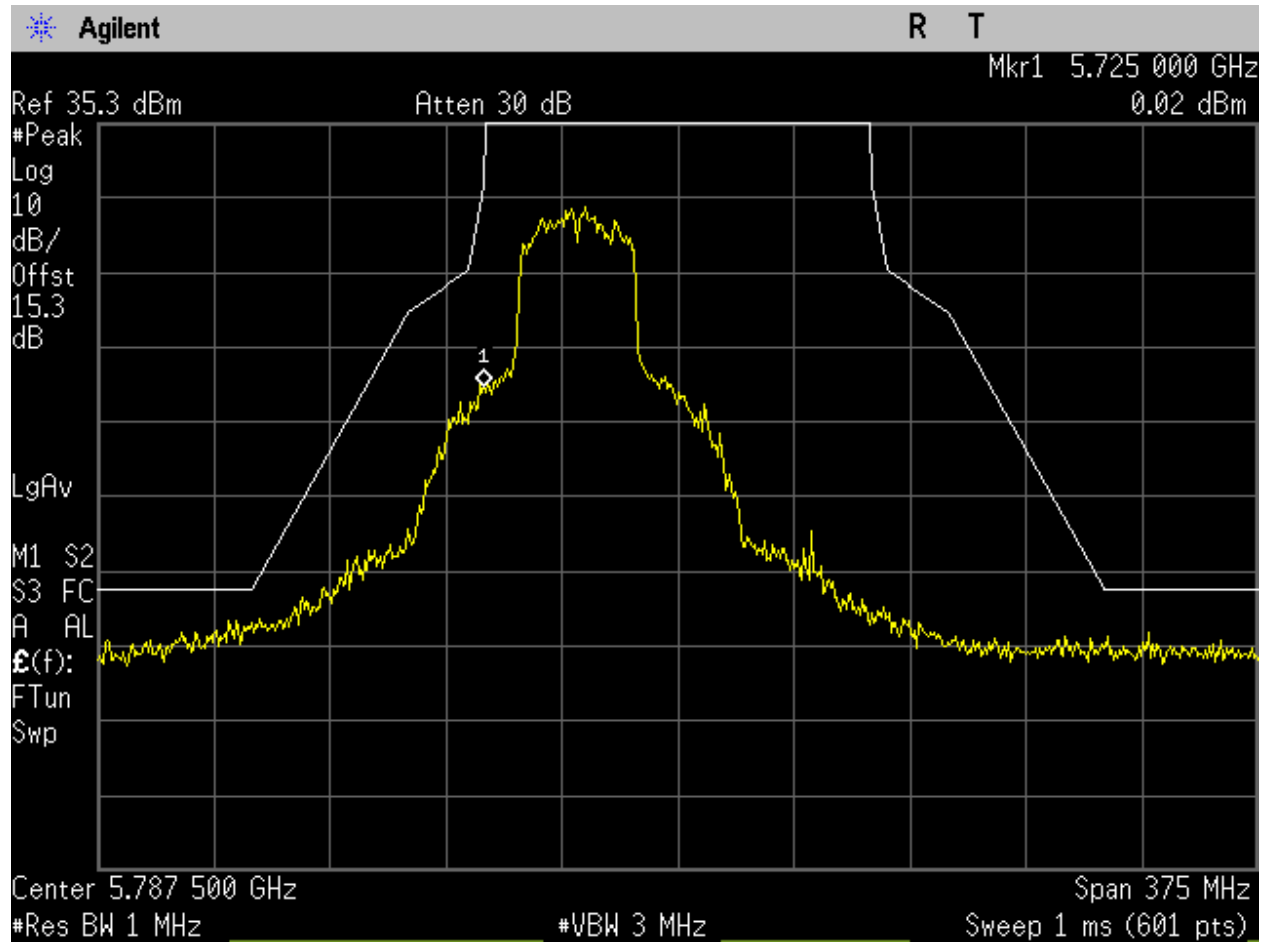


Figure 372: U-NII-3_5755MHz_Low Ch_151_40MHz BW_n-mode_Lower Emission Mask_Port 2.

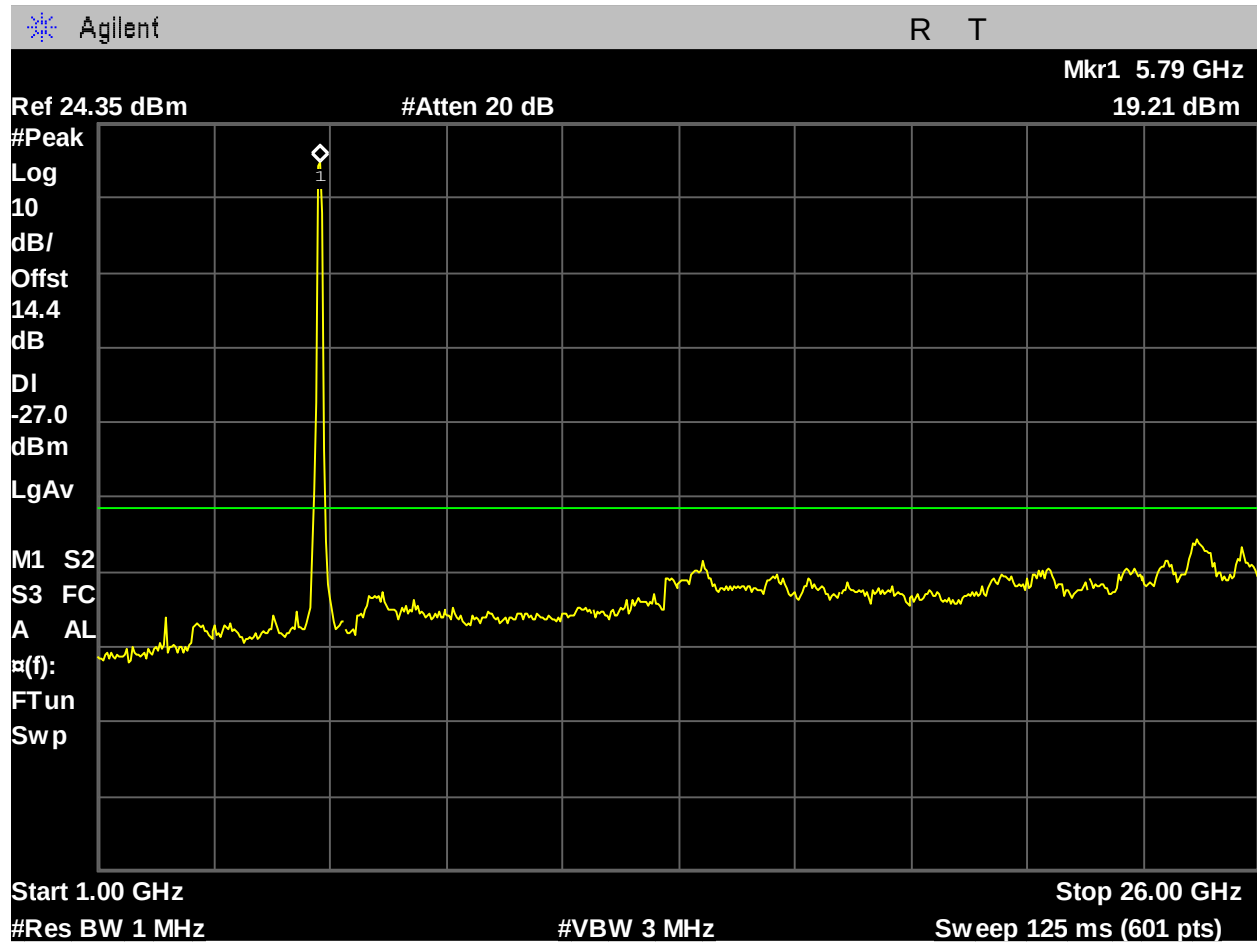


Figure 373: U-NII-3_5775MHz_High_Mid Ch_155_80MHz BW_ac-mode_-27dBm_1-26GHz_Port 1.

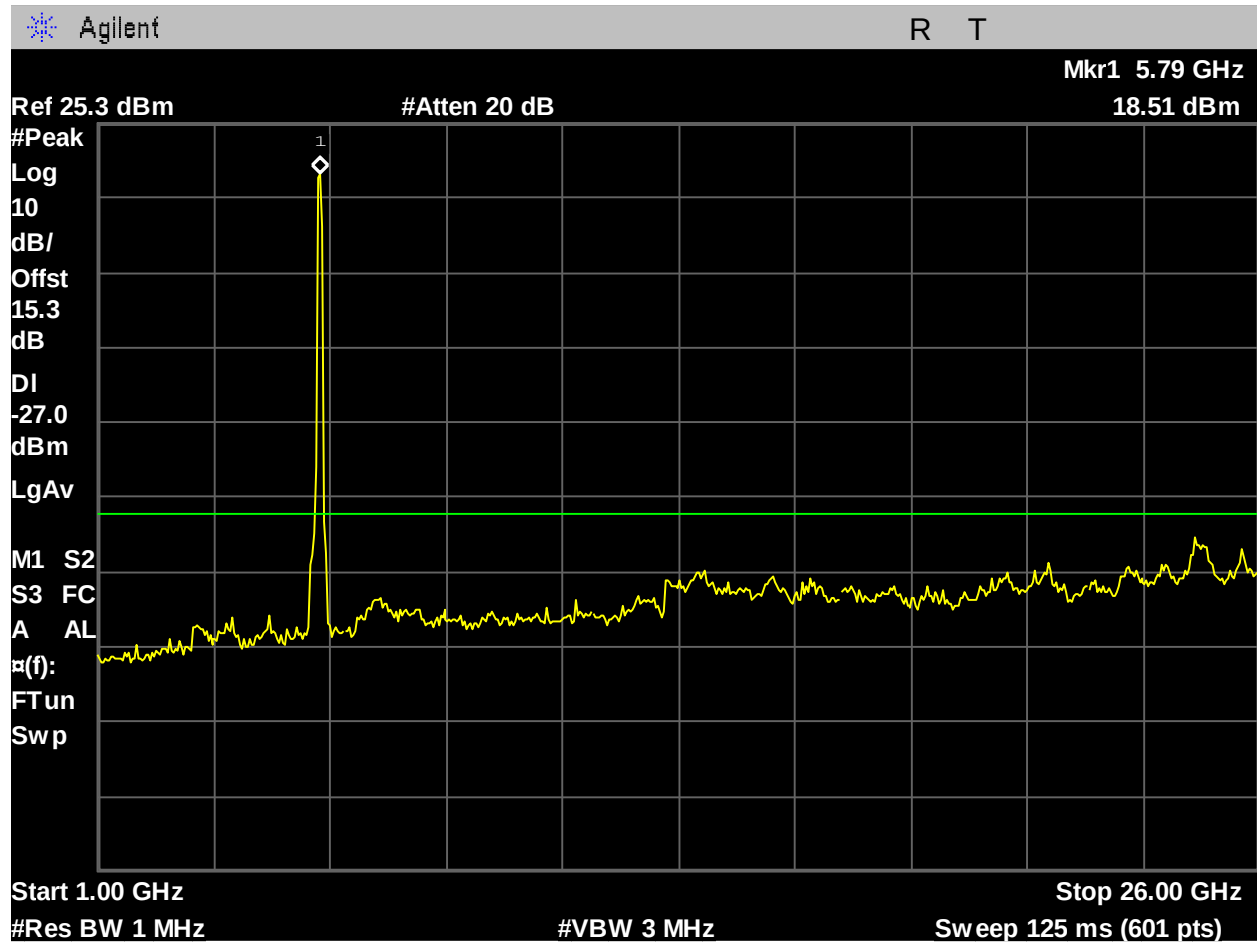


Figure 374: U-NII-3_5775MHz_High_Mid Ch_155_80MHz BW_ac-mode_-27dBm_1-26GHz_Port 2.



Figure 375: U-NII-3_5775MHz_High_Mid Ch_155_80MHz BW_ac-mode_-27dBm_26-40GHz_Port 1.

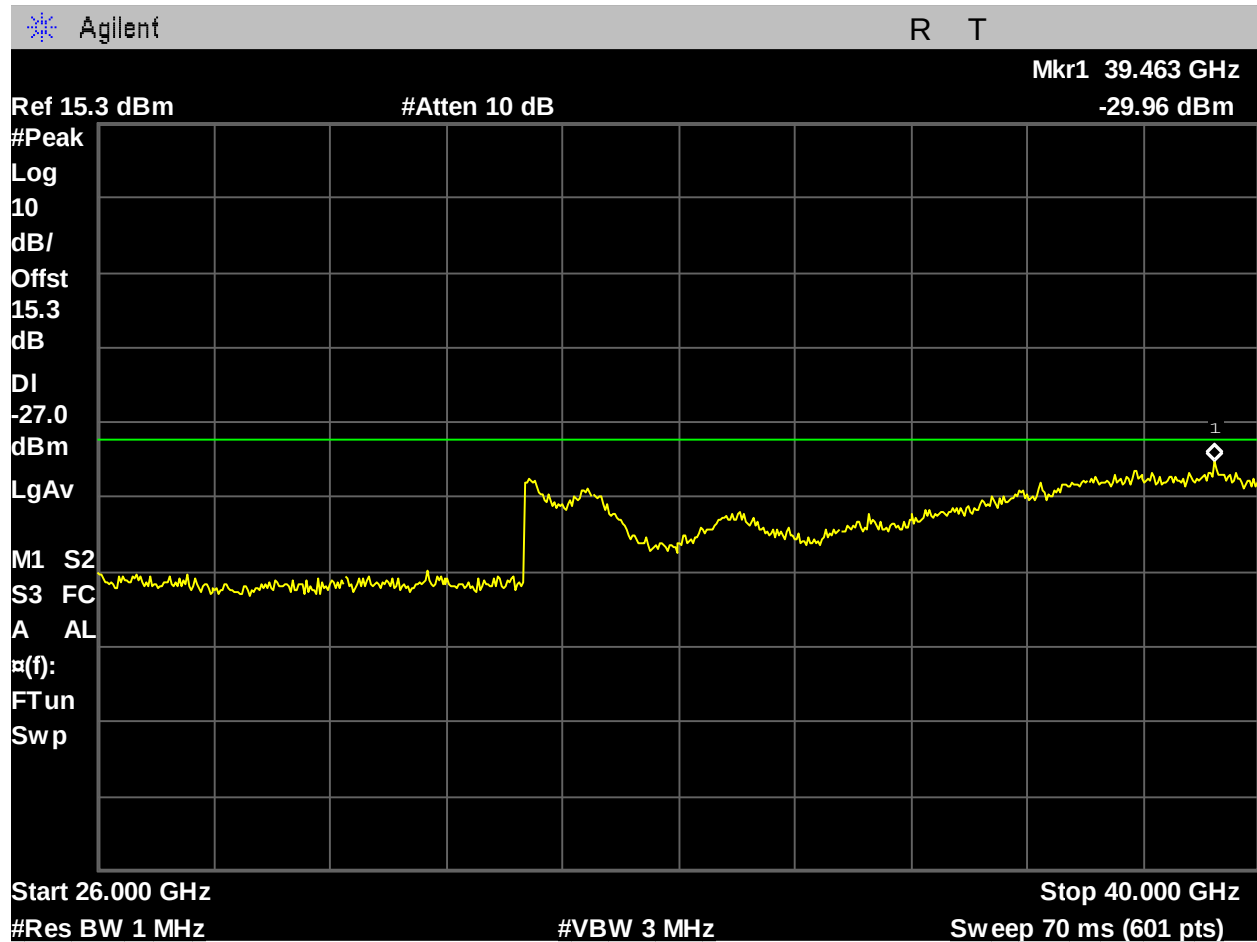


Figure 376: U-NII-3_5775MHz_High_Mid Ch_155_80MHz BW_ac-mode_-27dBm_26-40GHz_Port 2.

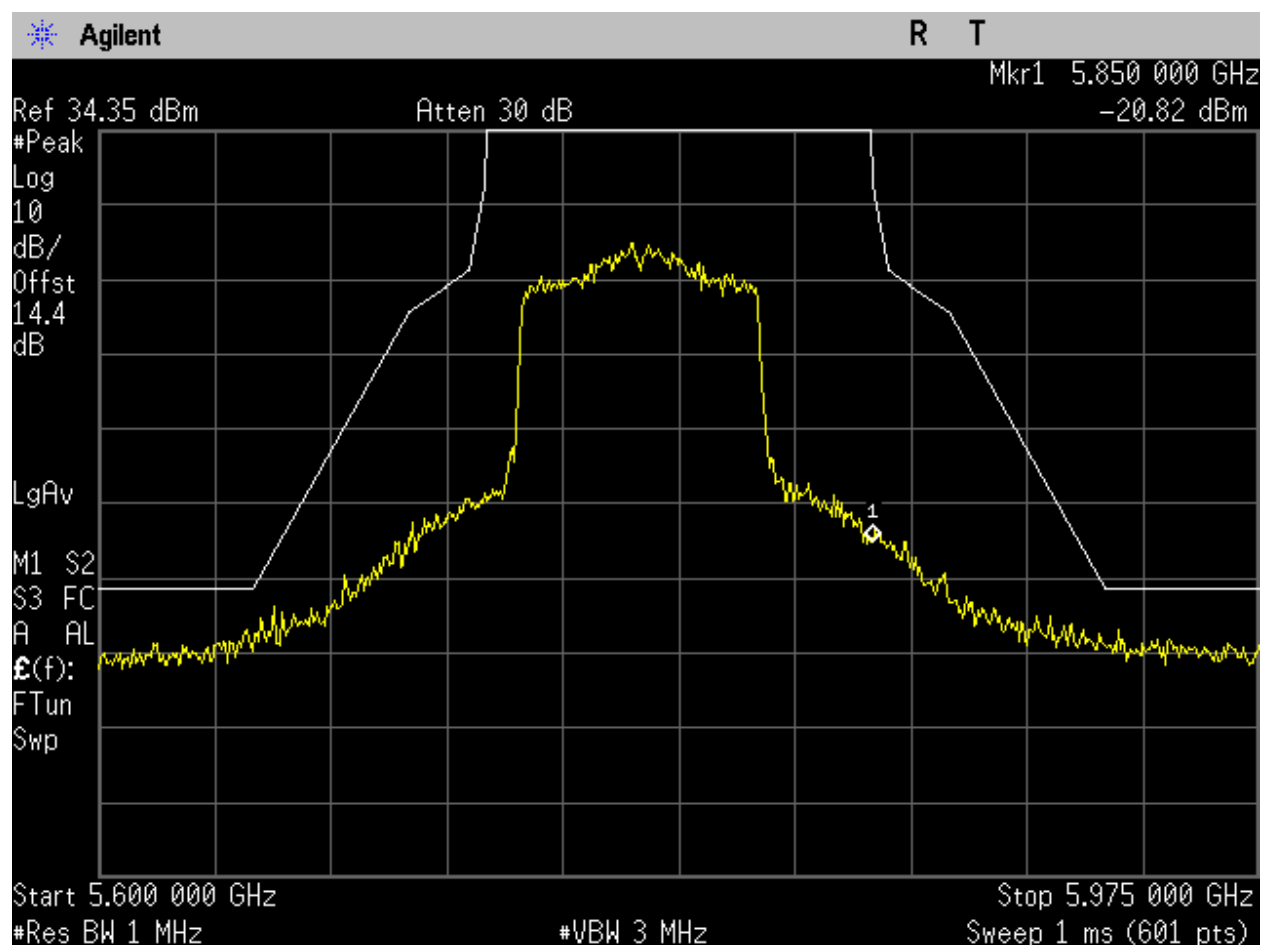


Figure 377: U-NII-3_5775MHz_High_Mid Ch_155_80MHz BW_ac-mode_Upper Emission Mask_Port 1.

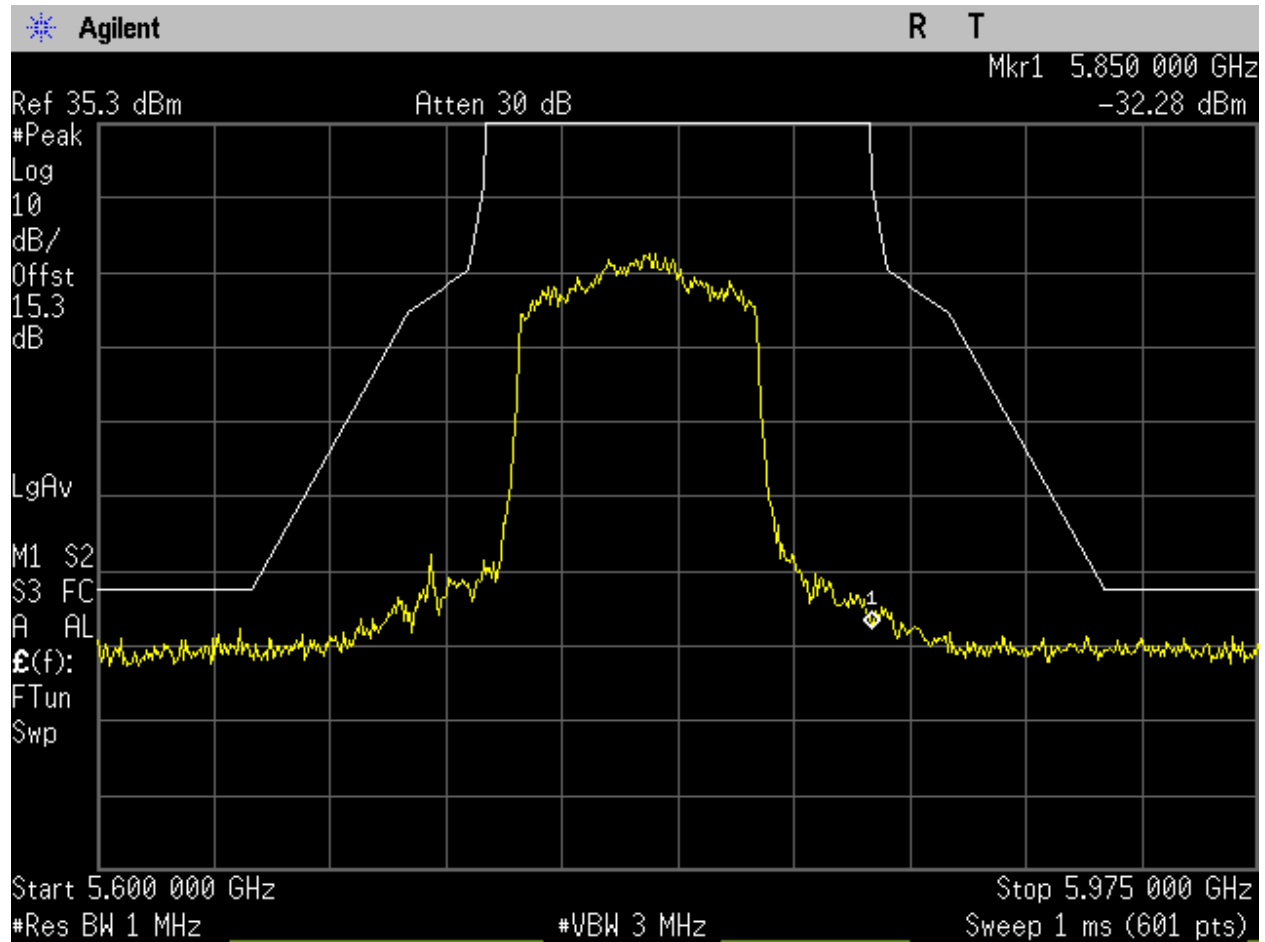


Figure 378: U-NII-3_5775MHz_High_Mid Ch_155_80MHz BW_ac-mode_Upper Emission Mask_Port 2.

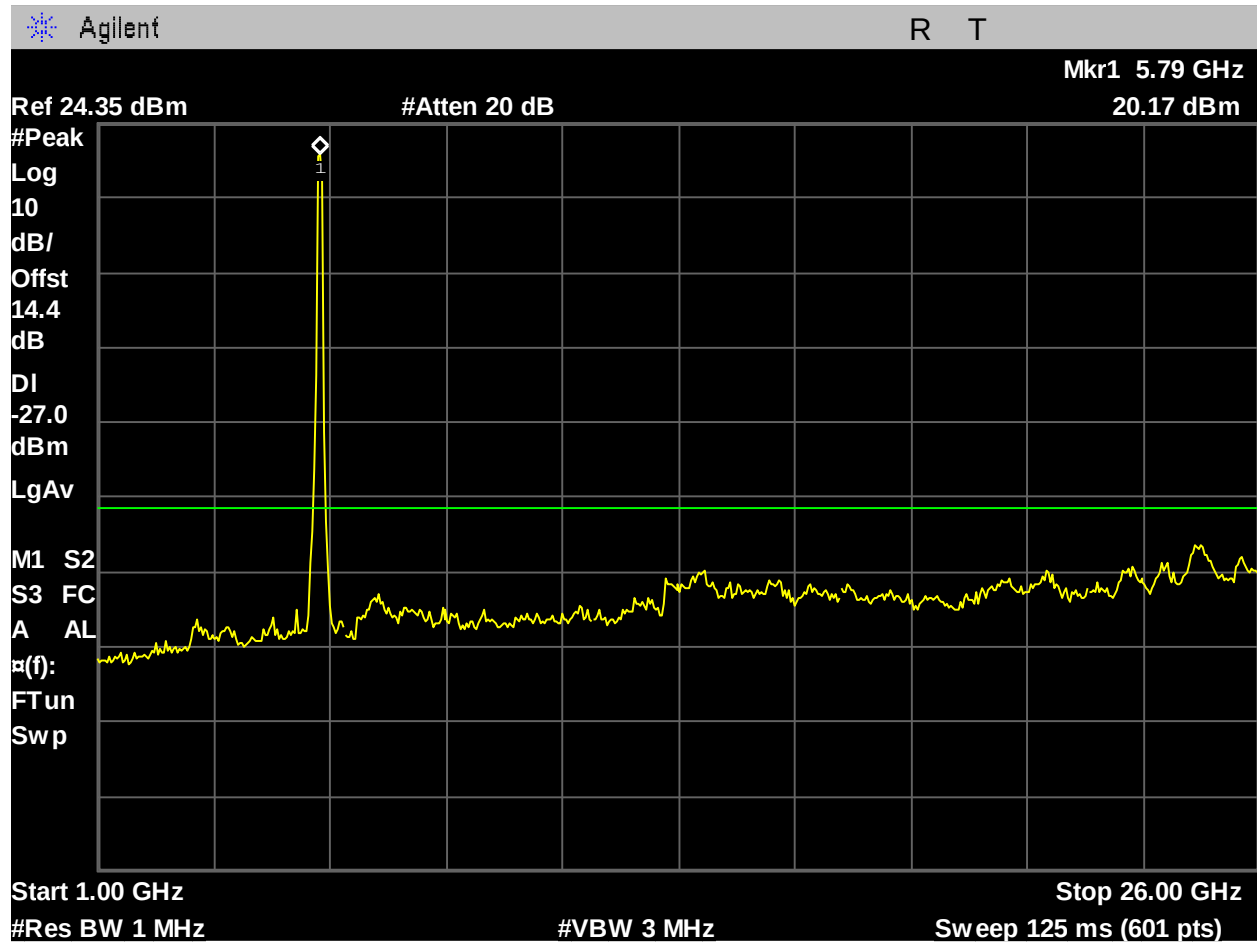


Figure 379: U-NII-3_5775MHz_High_Mid Ch_155_80MHz BW_ax-mode_-27dBm_1-26GHz_Port 1.

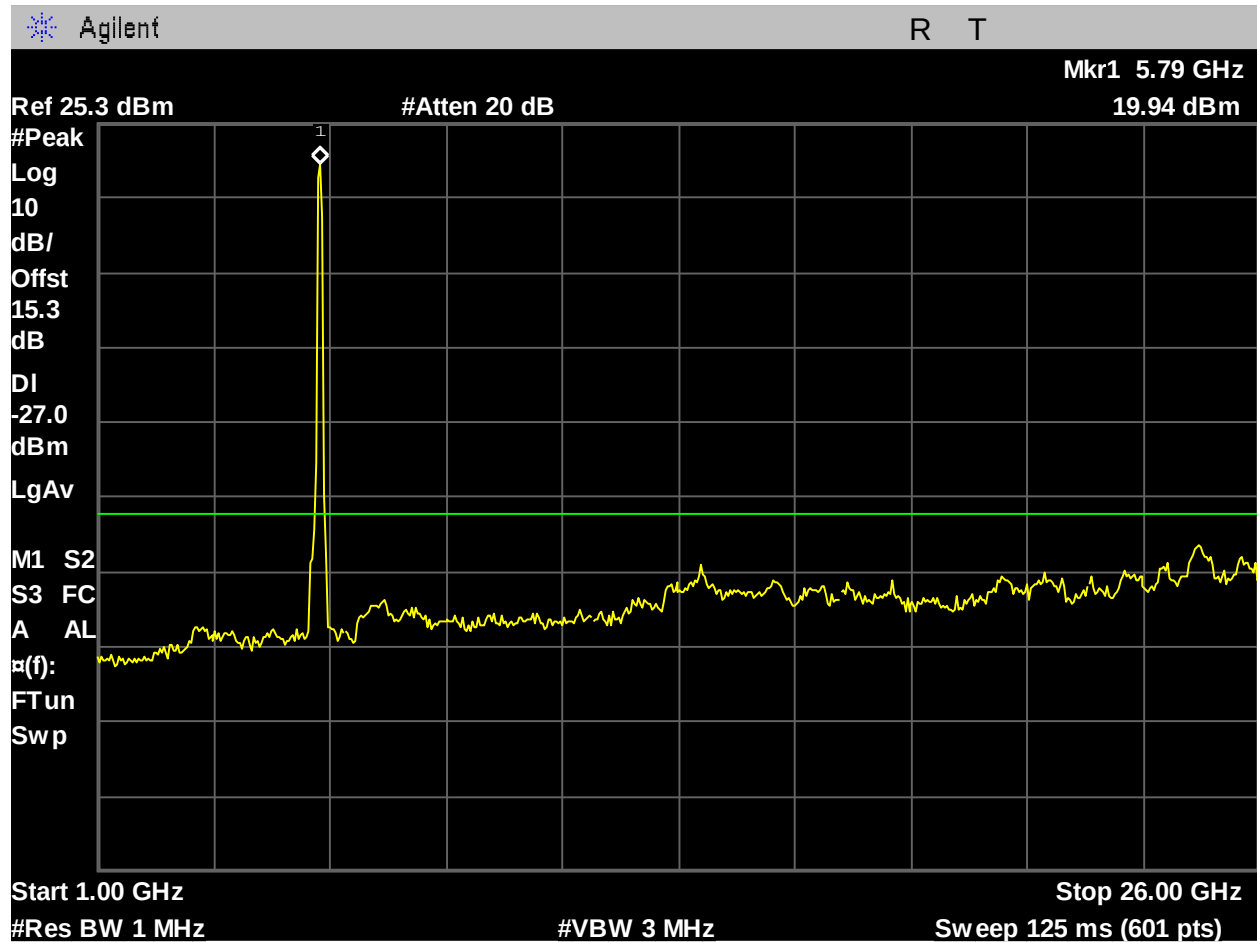


Figure 380: U-NII-3_5775MHz_High_Mid Ch_155_80MHz BW_ax-mode_-27dBm_1-26GHz_Port 2.

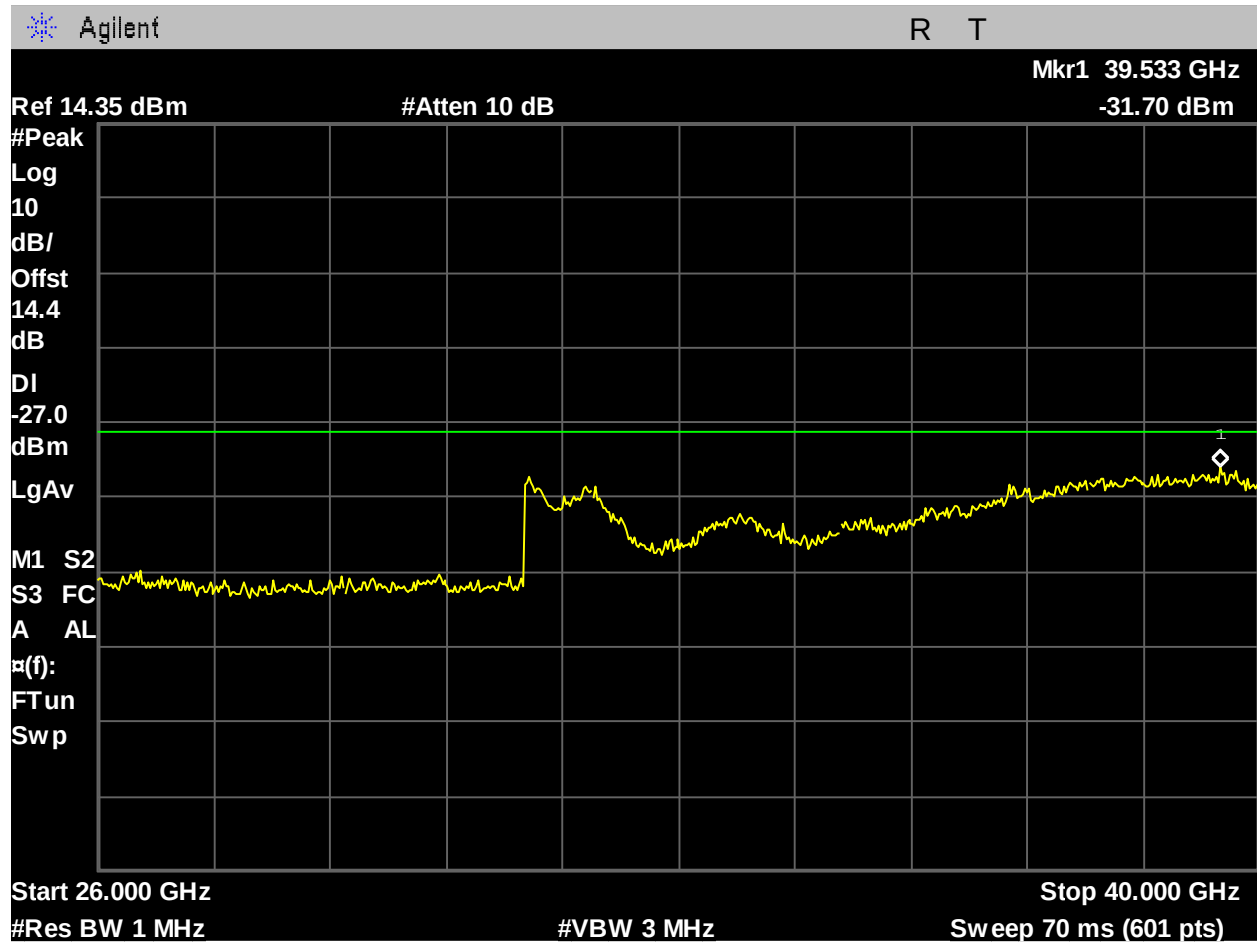


Figure 381: U-NII-3_5775MHz_High_Mid Ch_155_80MHz BW_ax-mode_-27dBm_26-40GHz_Port 1.

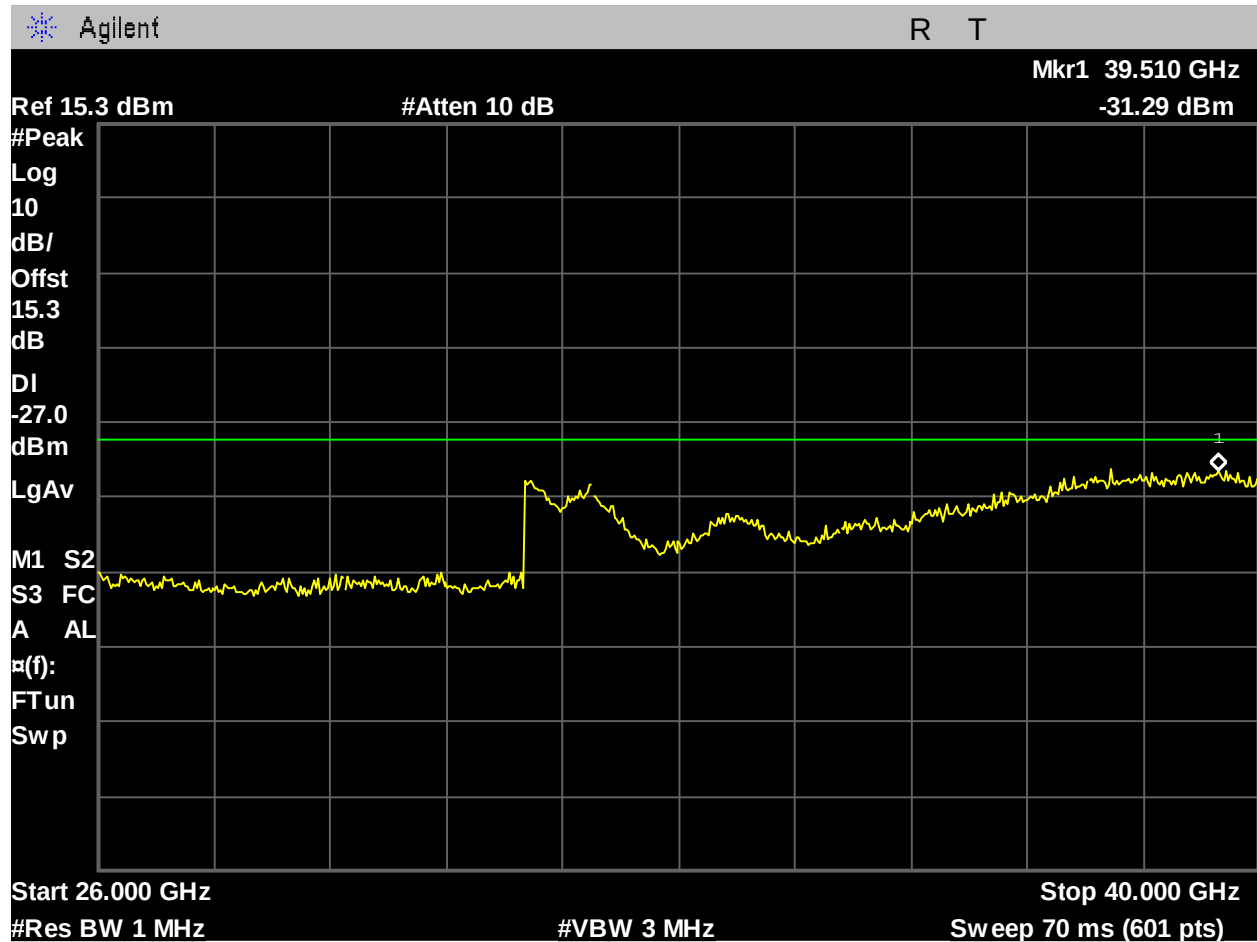


Figure 382: U-NII-3_5775MHz_High_Mid Ch_155_80MHz BW_ax-mode_-27dBm_26-40GHz_Port 2.

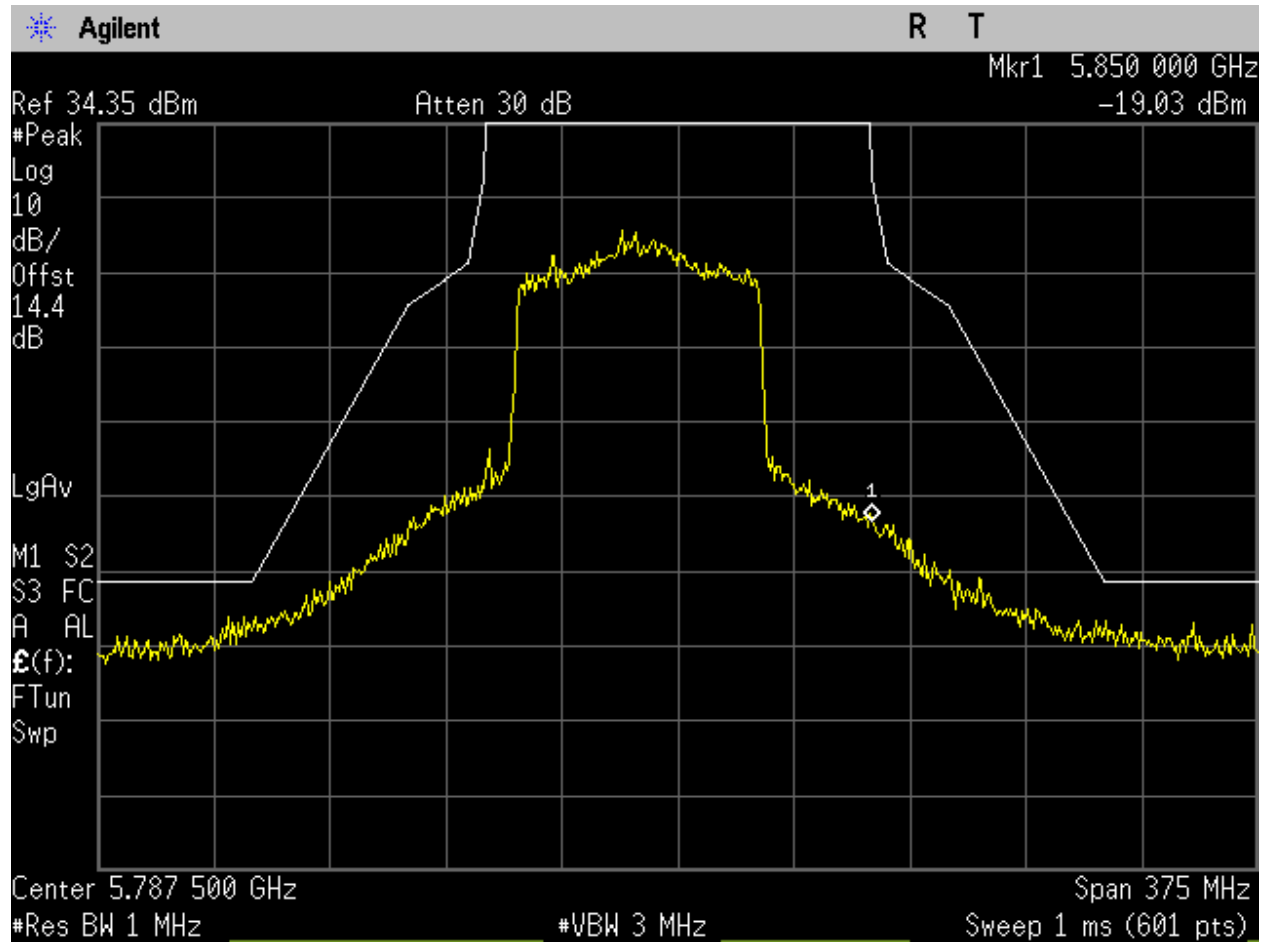


Figure 383: U-NII-3_5775MHz_High_Mid Ch_155_80MHz BW_ax-mode_Upper Emission Mask_Port 1.

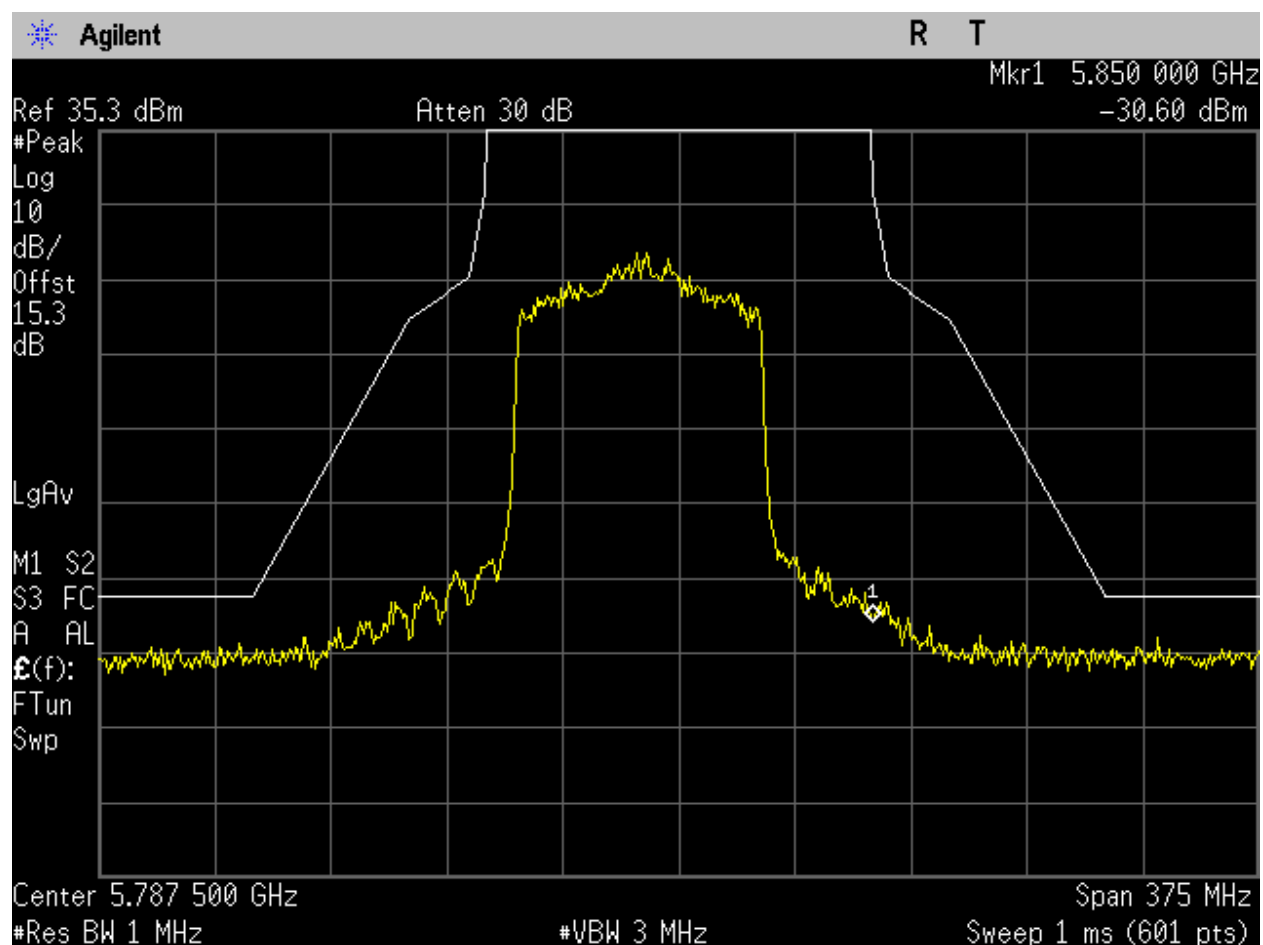


Figure 384: U-NII-3_5775MHz_High_Mid Ch_155_80MHz BW_ax-mode_Upper Emission Mask_Port 2.

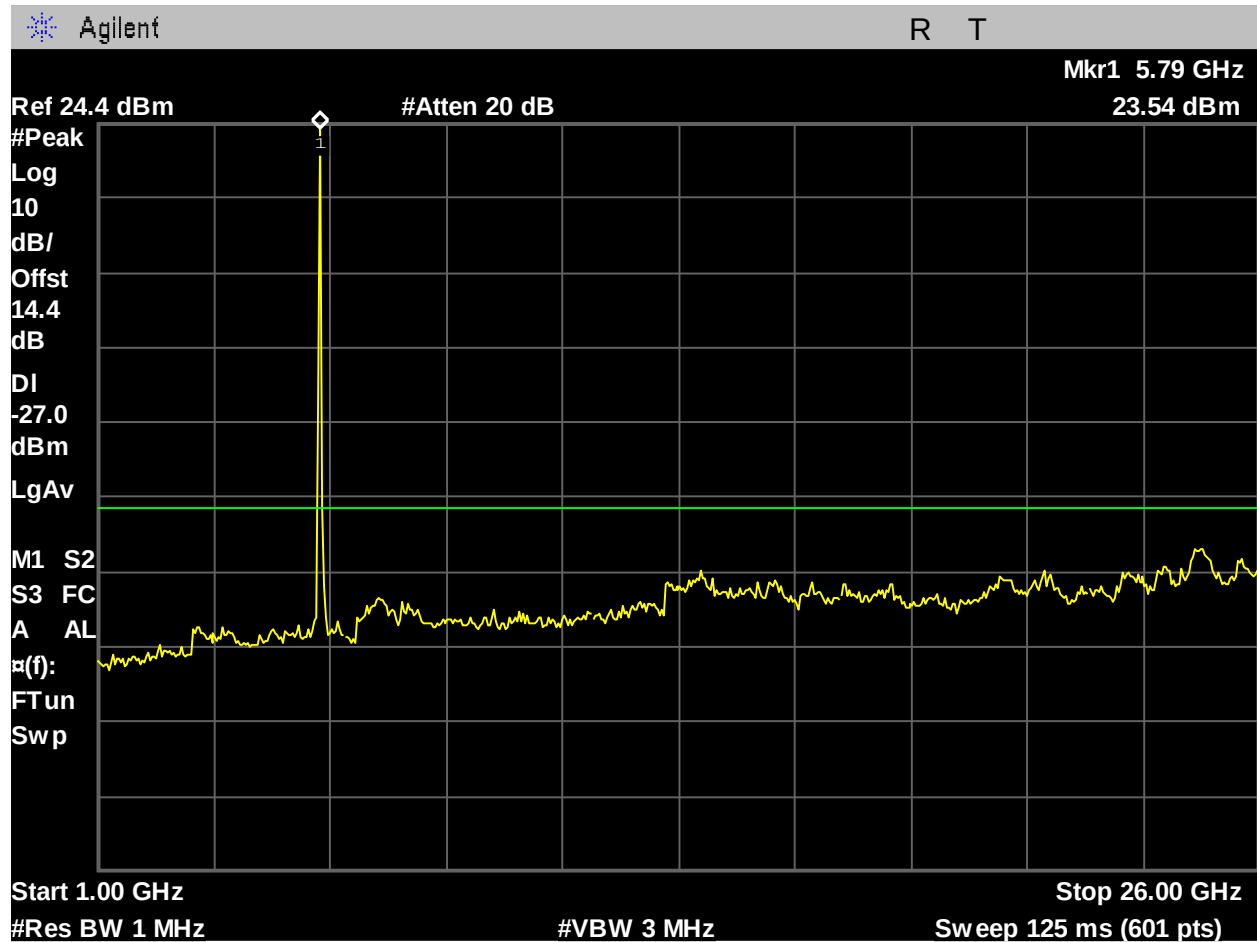


Figure 385: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_a-mode_-27dBm_1-26GHz_Port 1.

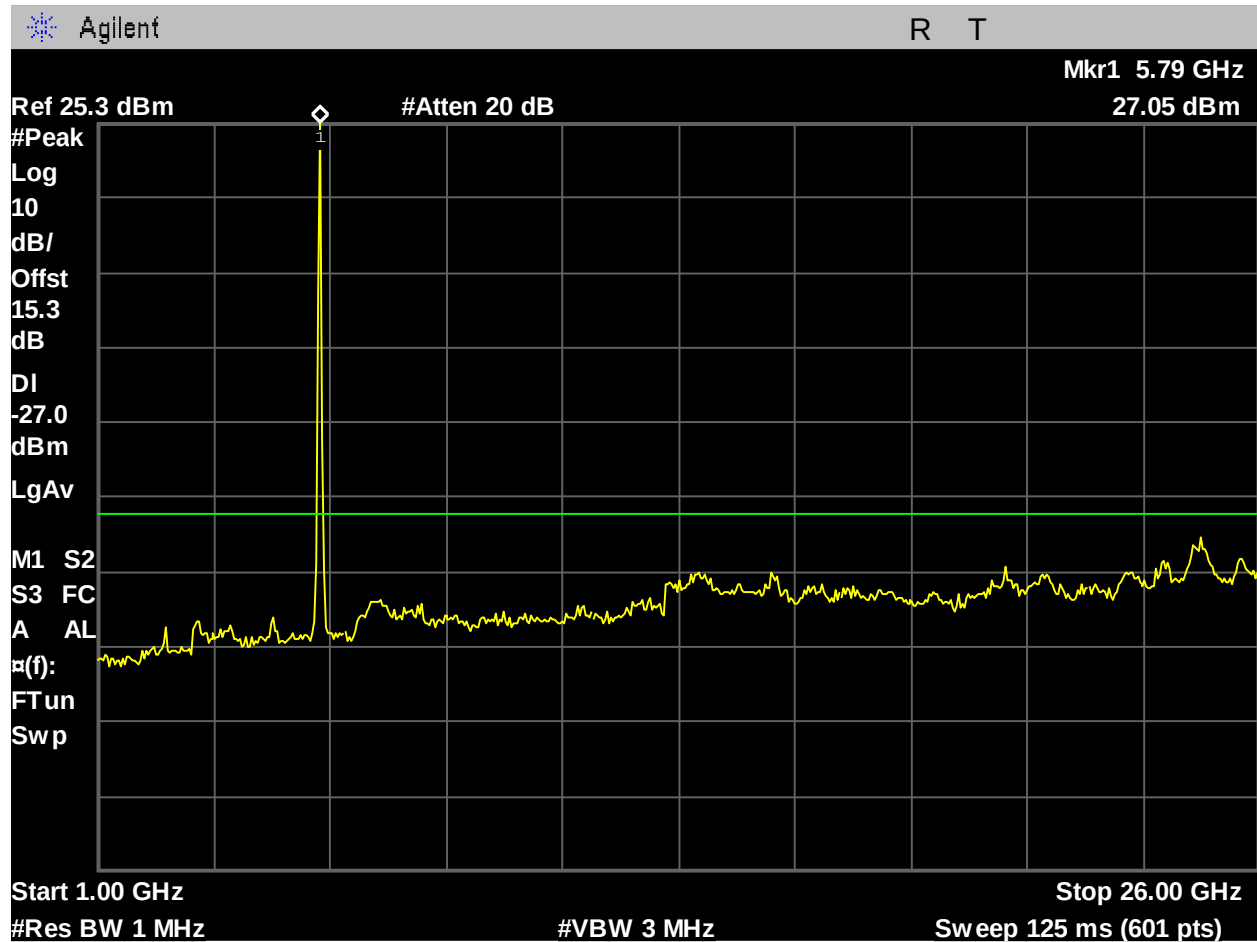


Figure 386: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_a-mode_-27dBm_1-26GHz_Port 2.

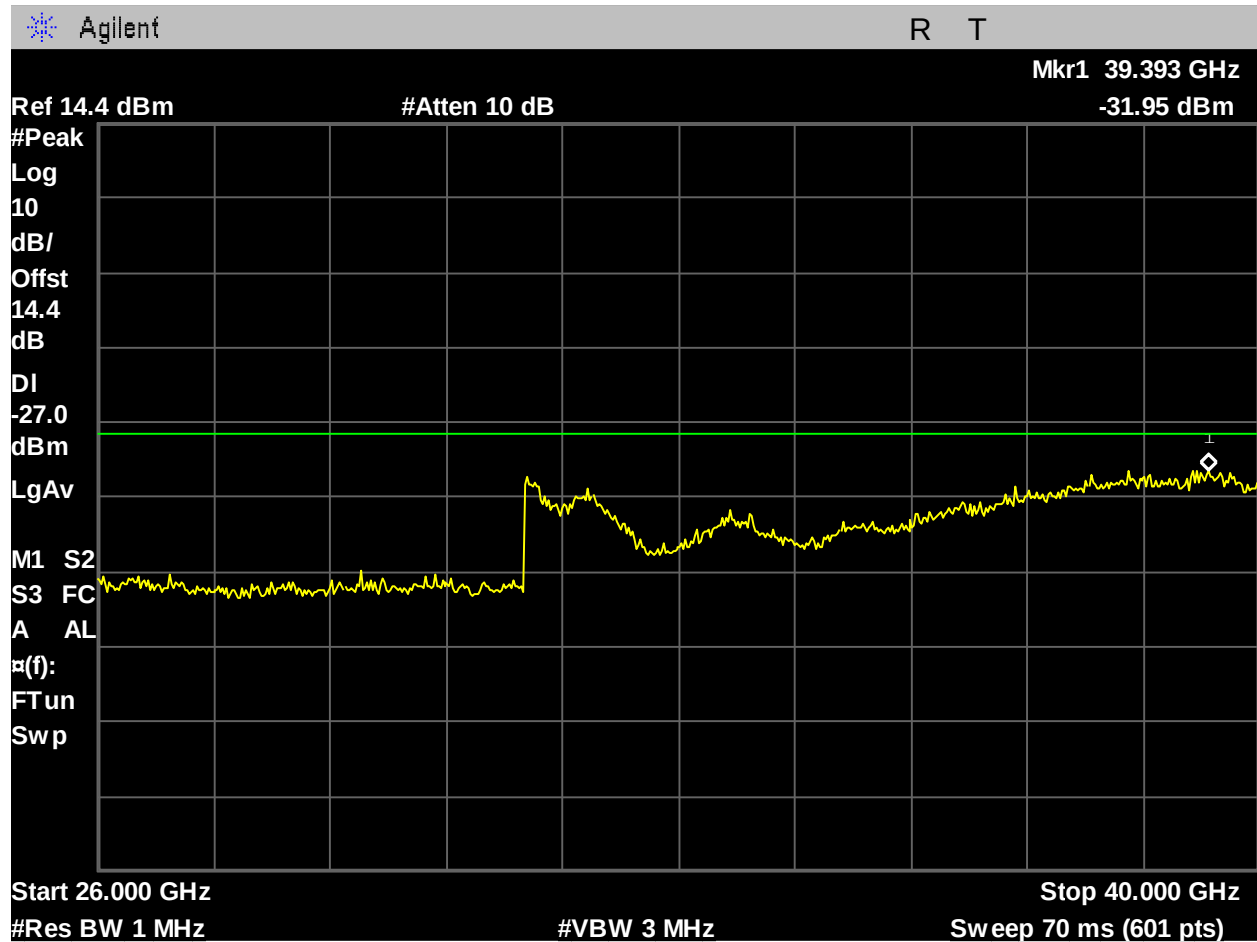


Figure 387: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_a-mode_-27dBm_26-40GHz_Port 1.

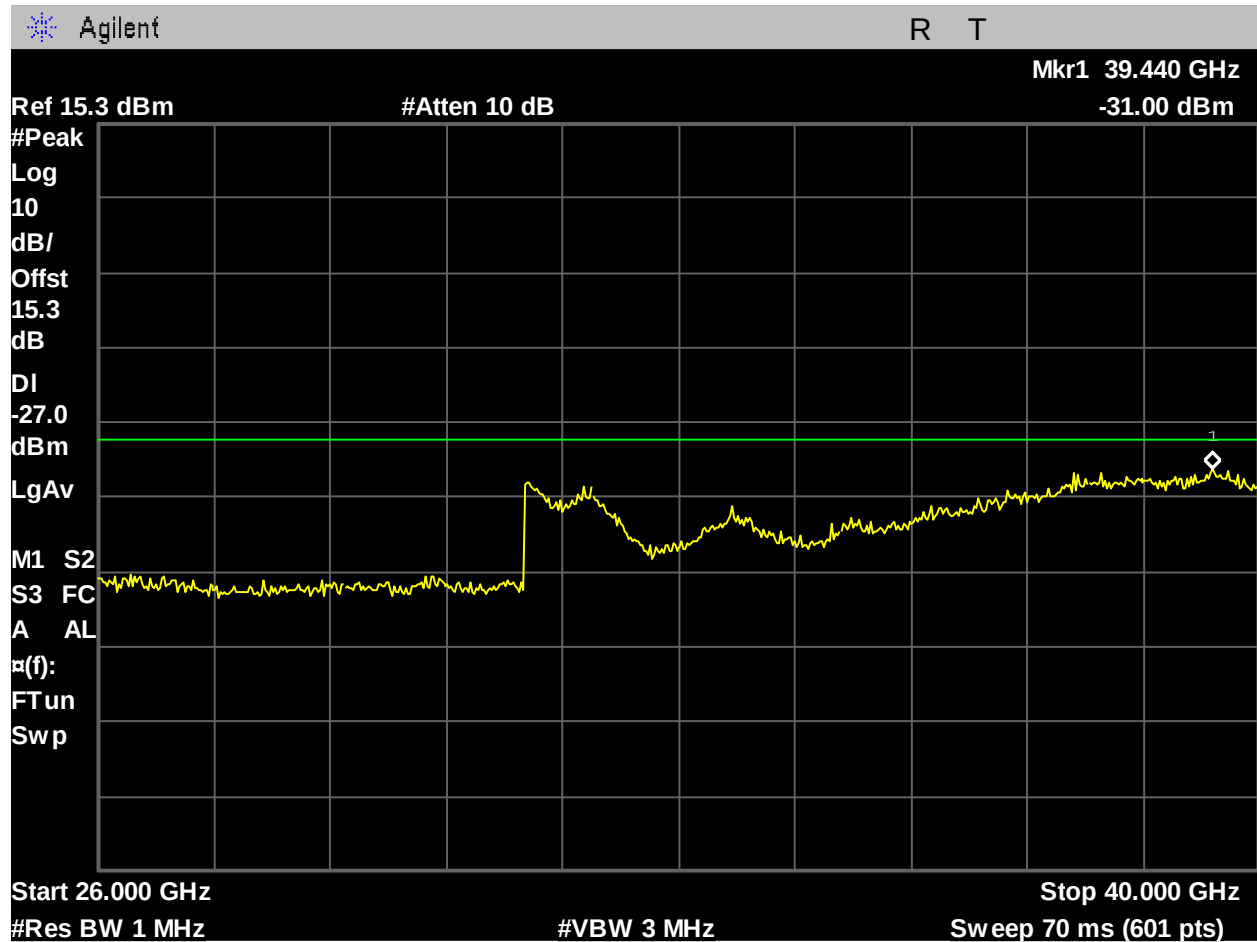


Figure 388: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_a-mode_-27dBm_26-40GHz_Port 2.

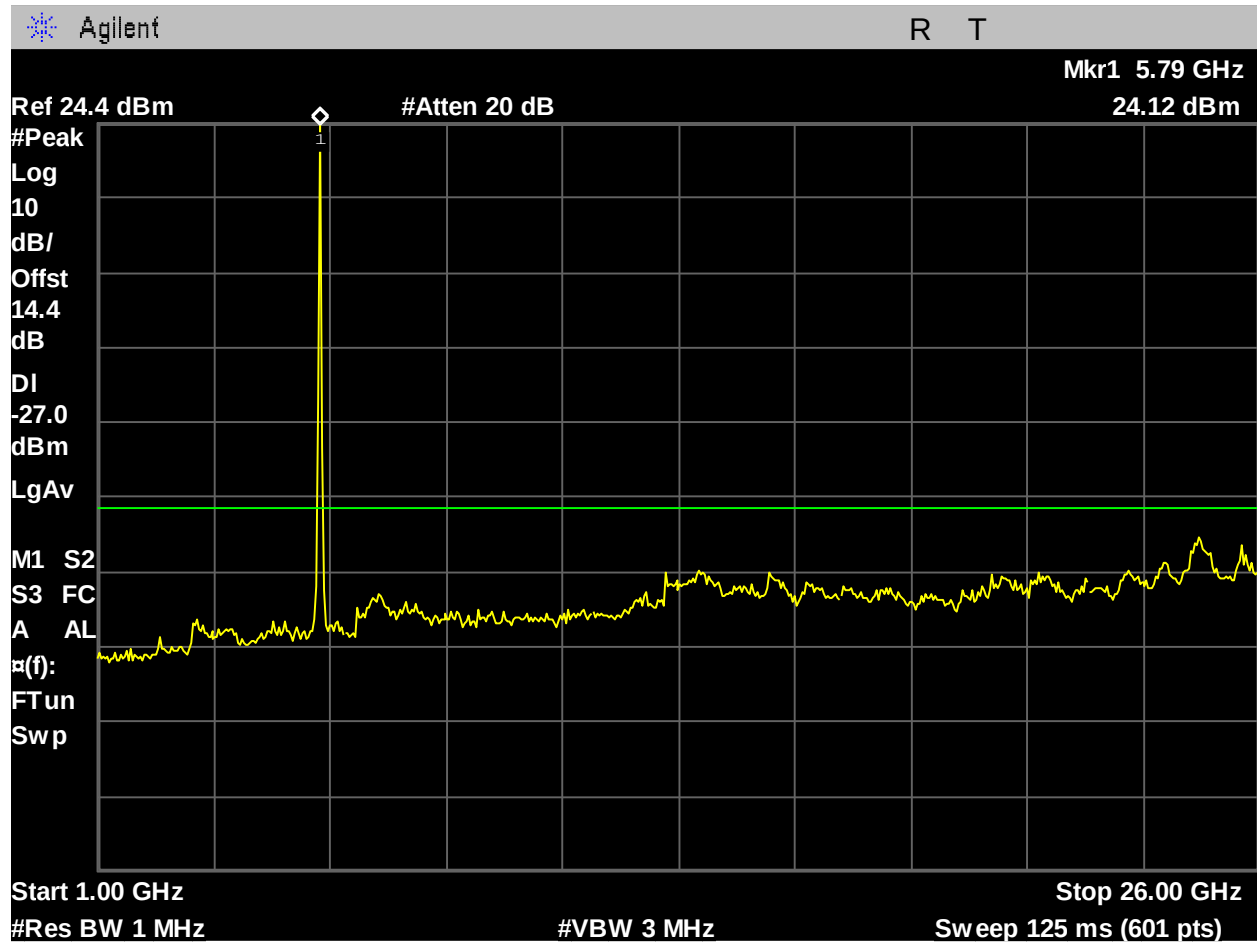


Figure 389: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_ac-mode_-27dBm_1-26GHz_Port 1.

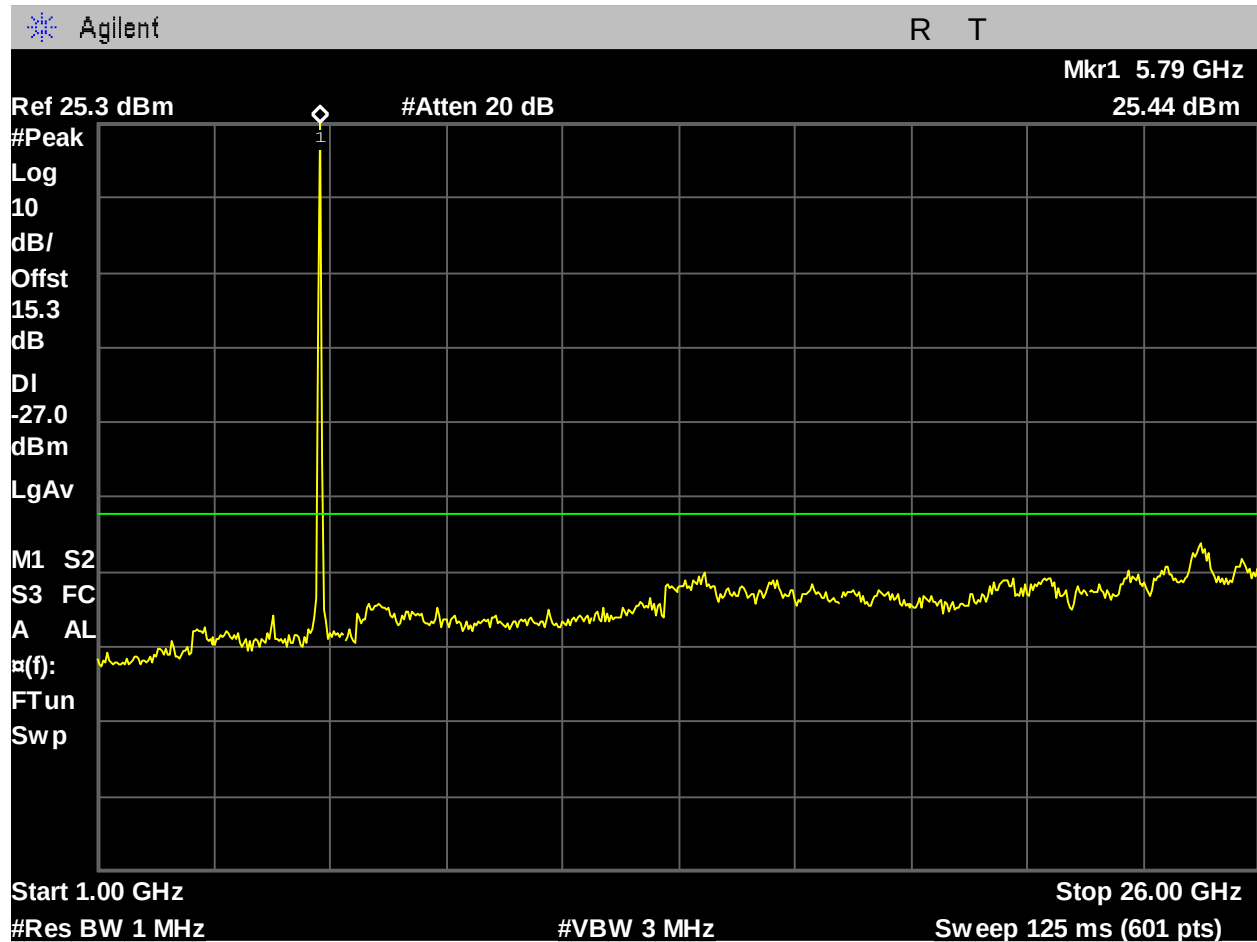


Figure 390: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_ac-mode_-27dBm_1-26GHz_Port 2.

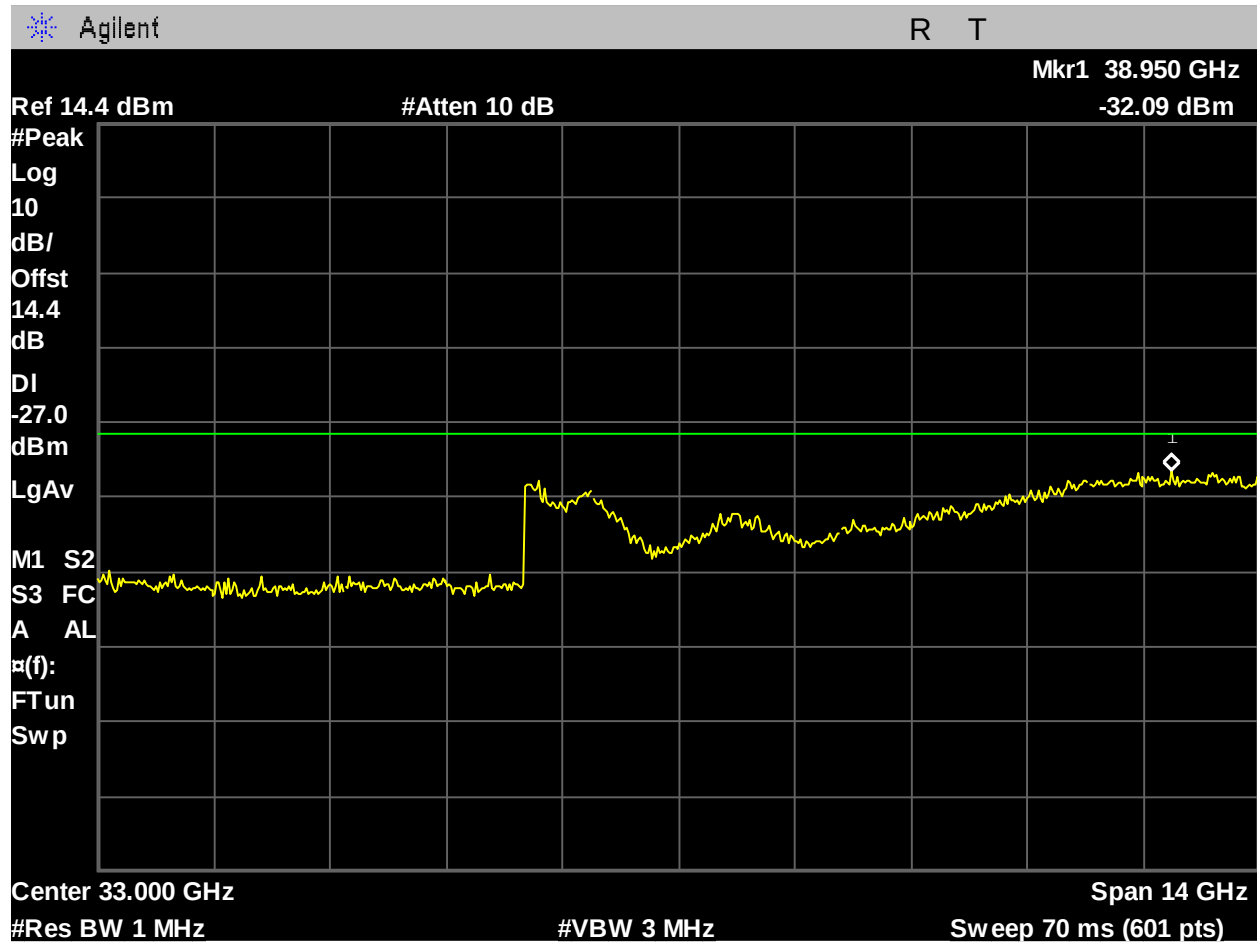


Figure 391: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_ac-mode_-27dBm_26-40GHz_Port 1.

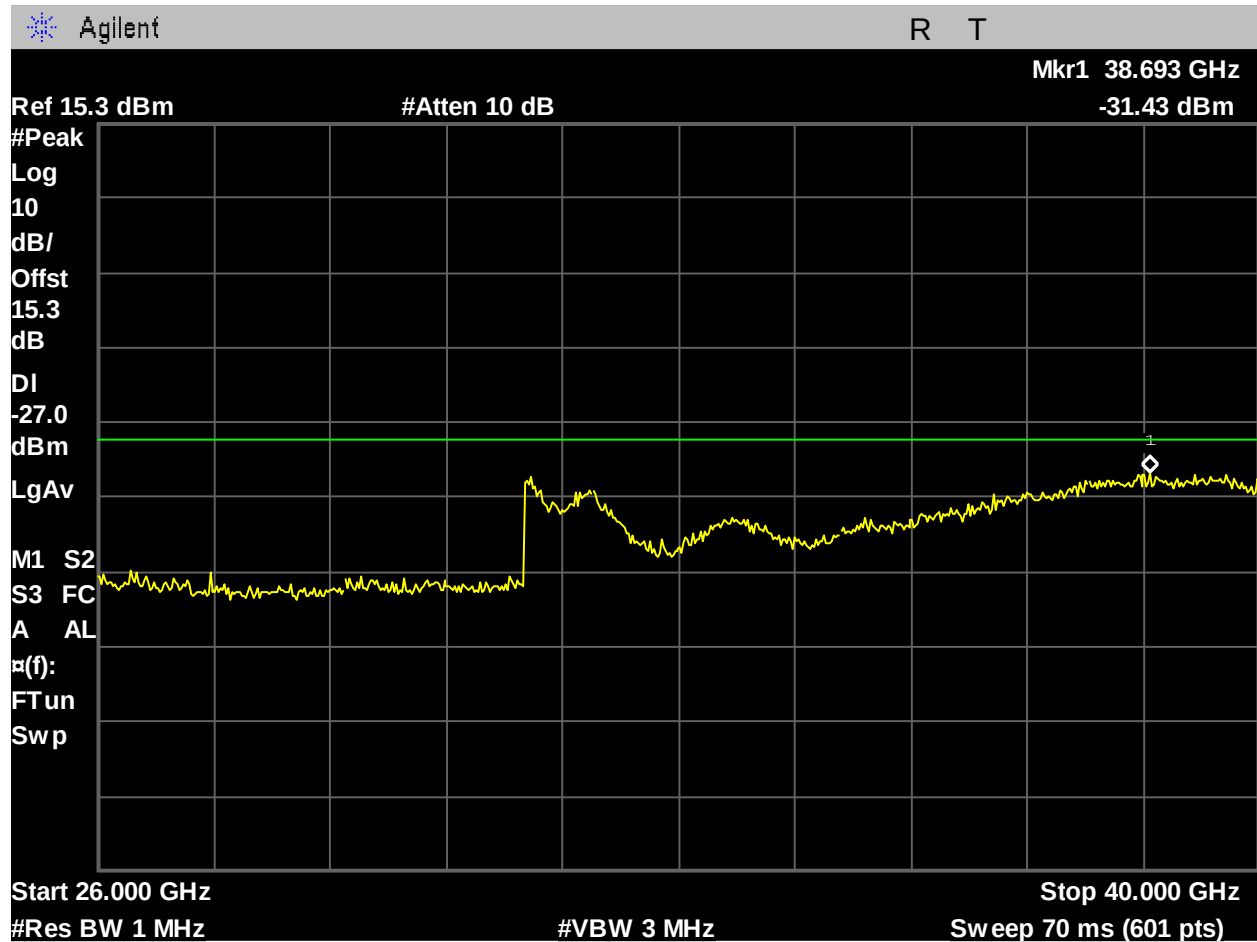


Figure 392: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_ac-mode_-27dBm_26-40GHz_Port 2.

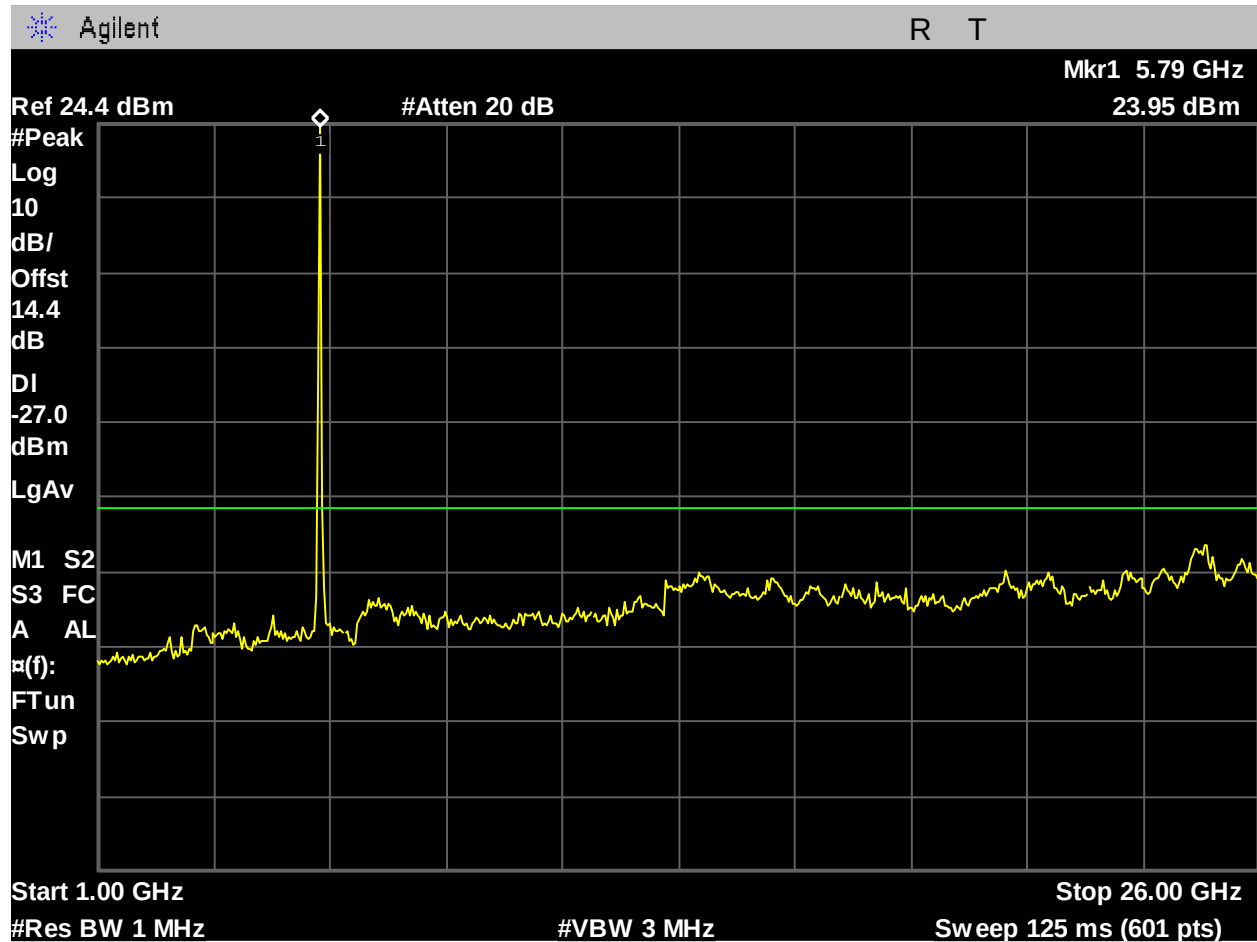


Figure 393: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_ax-mode_-27dBm_1-26GHz_Port 1.

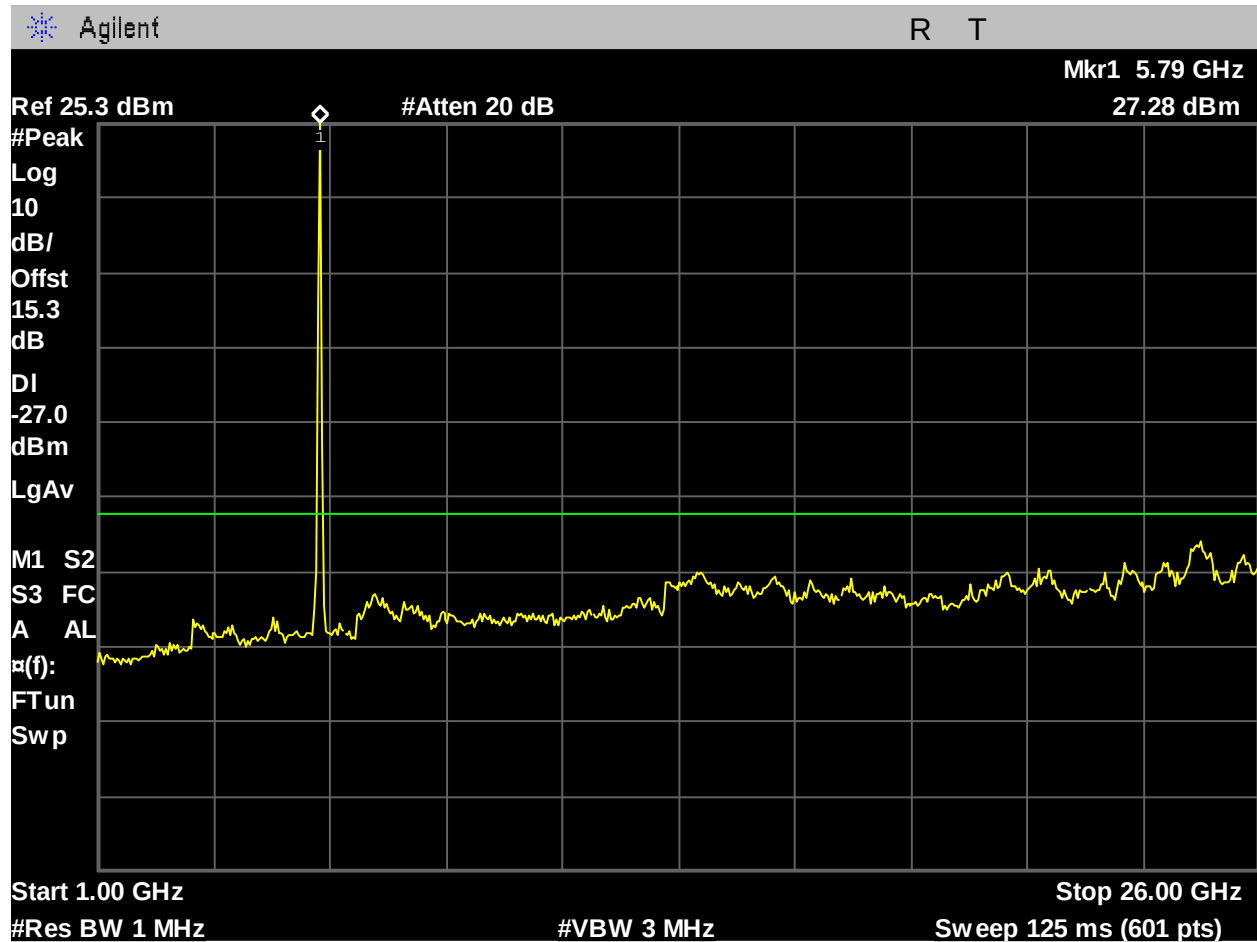


Figure 394: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_ax-mode_-27dBm_1-26GHz_Port 2.

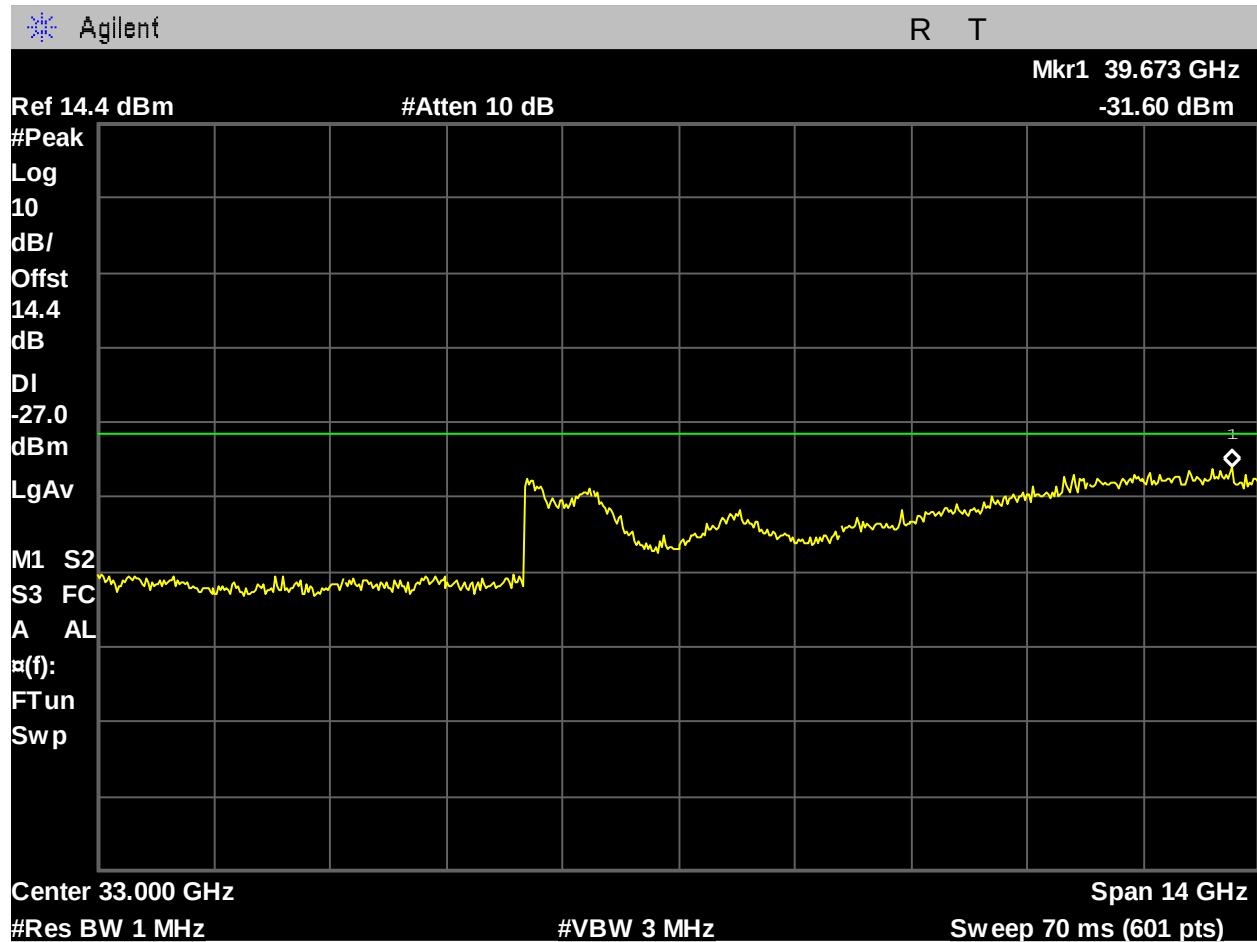


Figure 395: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_ax-mode_-27dBm_26-40GHz_Port 1.

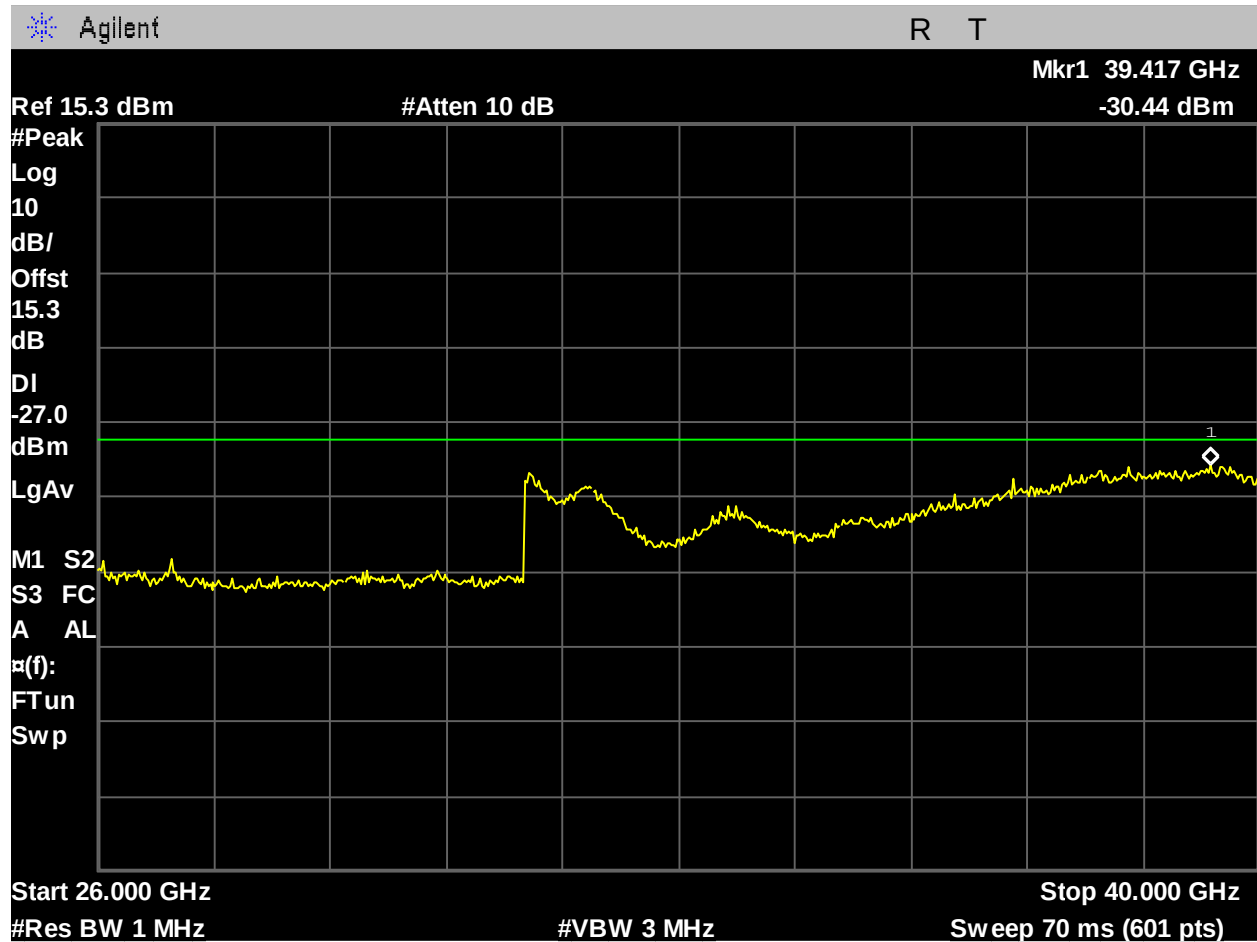


Figure 396: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_ax-mode_-27dBm_26-40GHz_Port 2.

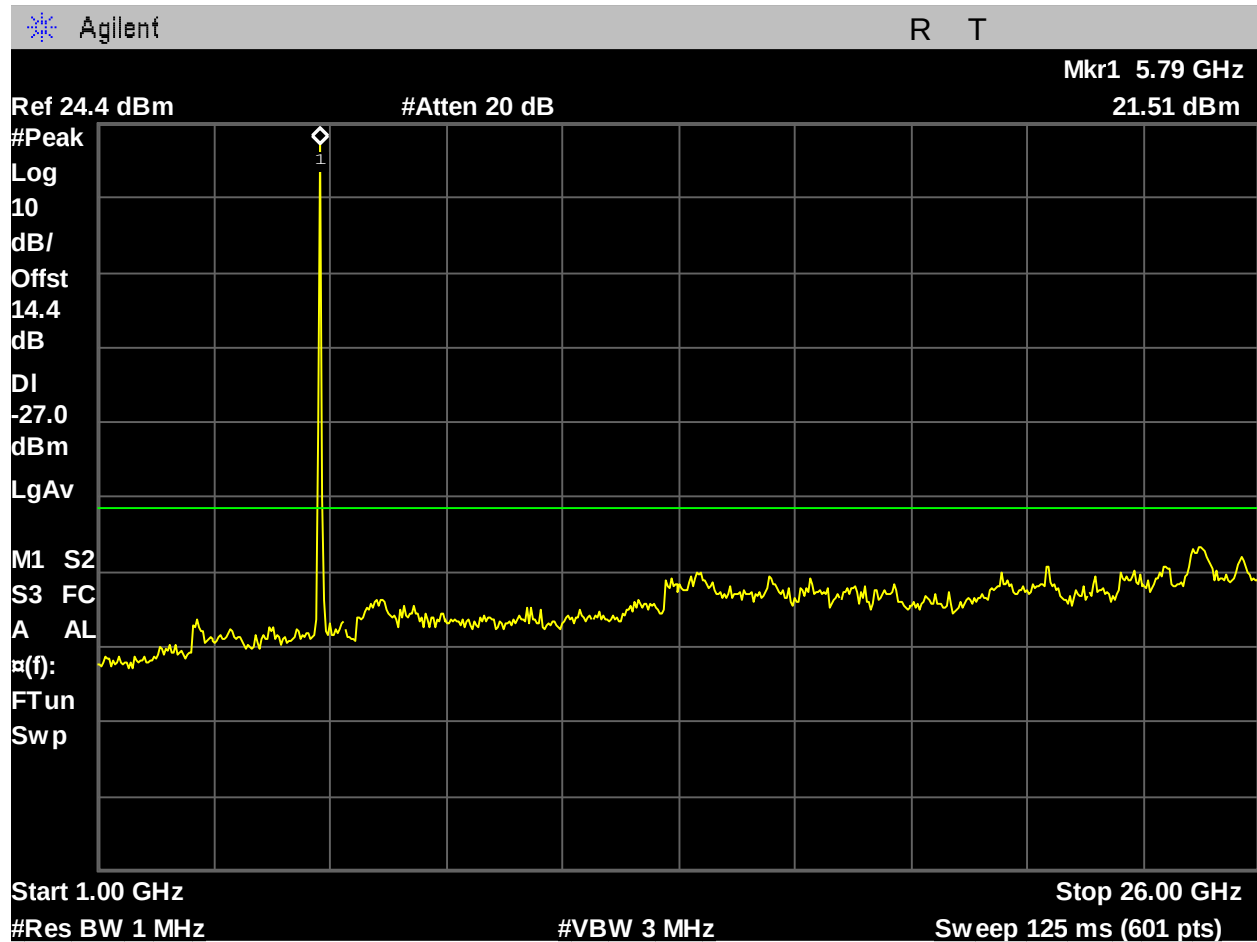


Figure 397: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_n-mode_-27dBm_1-26GHz_Port 1.

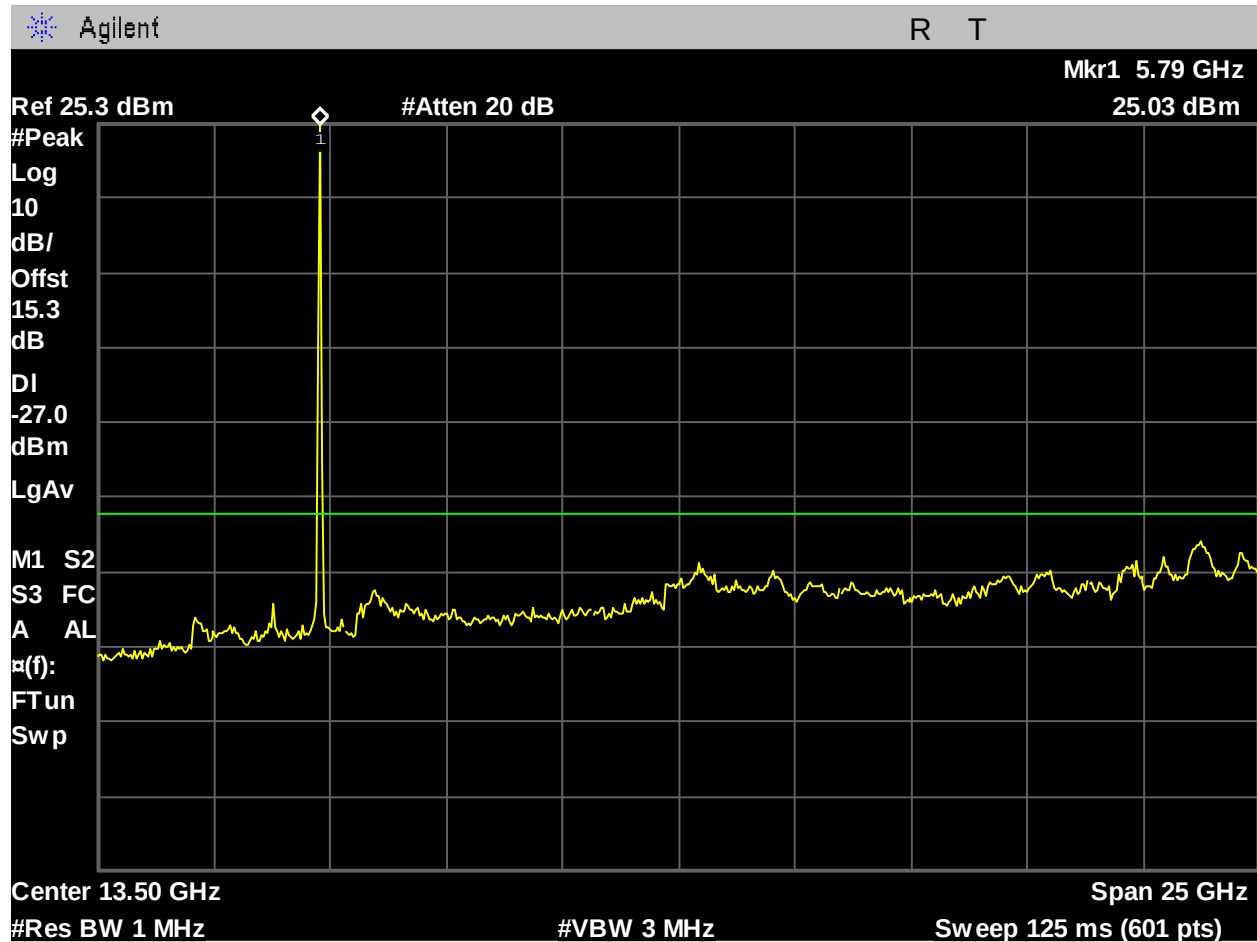


Figure 398: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_n-mode_-27dBm_1-26GHz_Port 2.

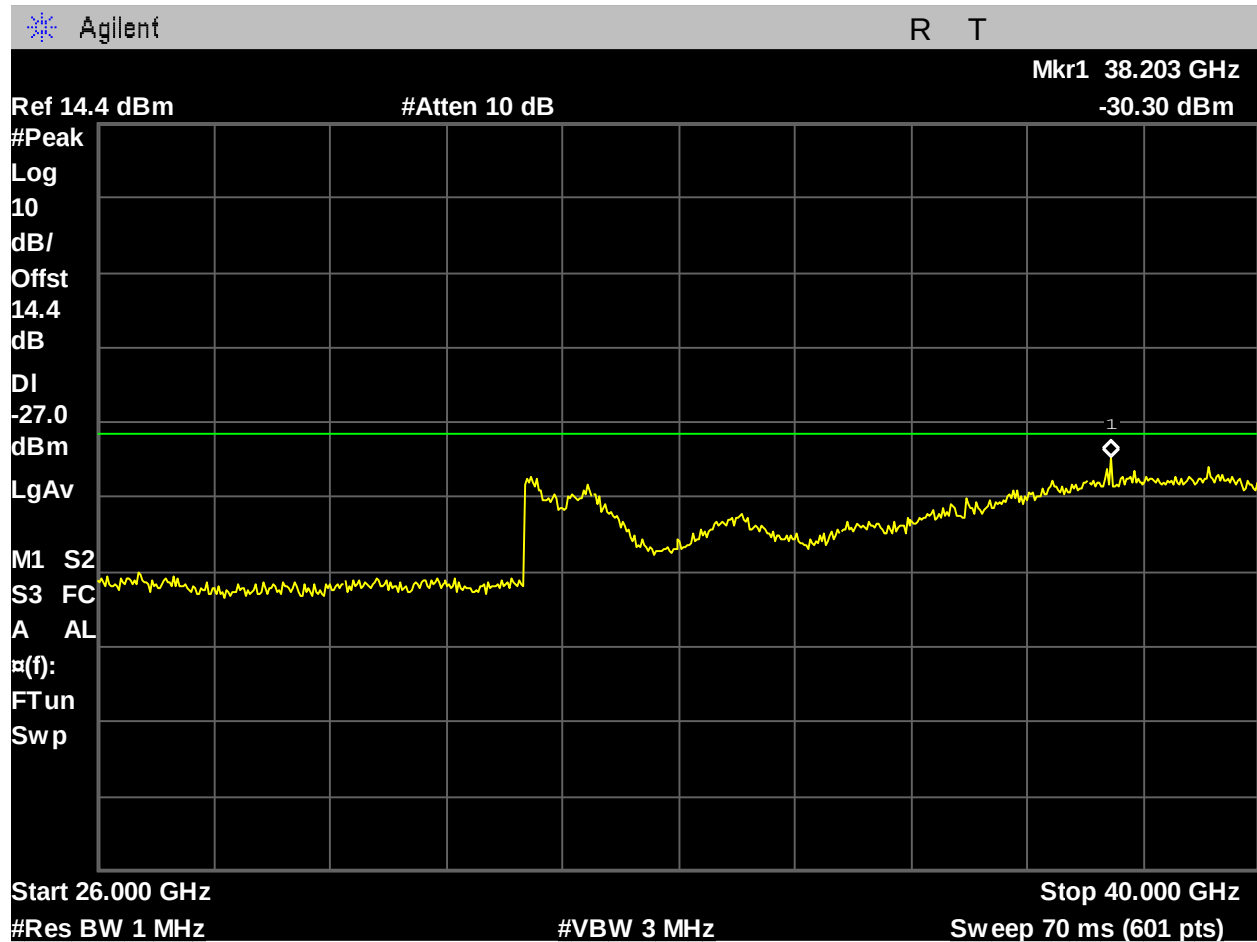


Figure 399: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_n-mode_-27dBm_26-40GHz_Port 1.

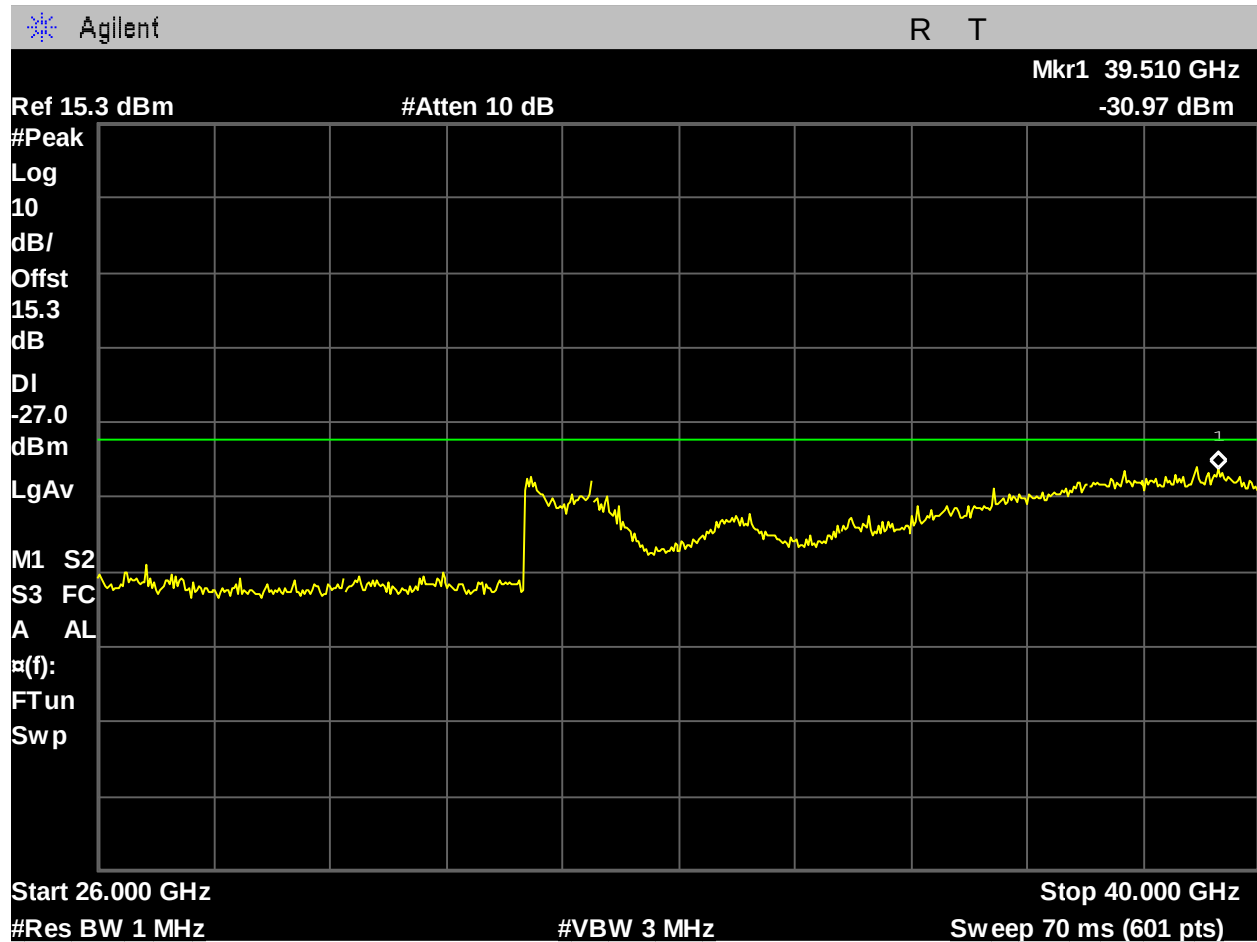


Figure 400: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_n-mode_-27dBm_26-40GHz_Port 2.

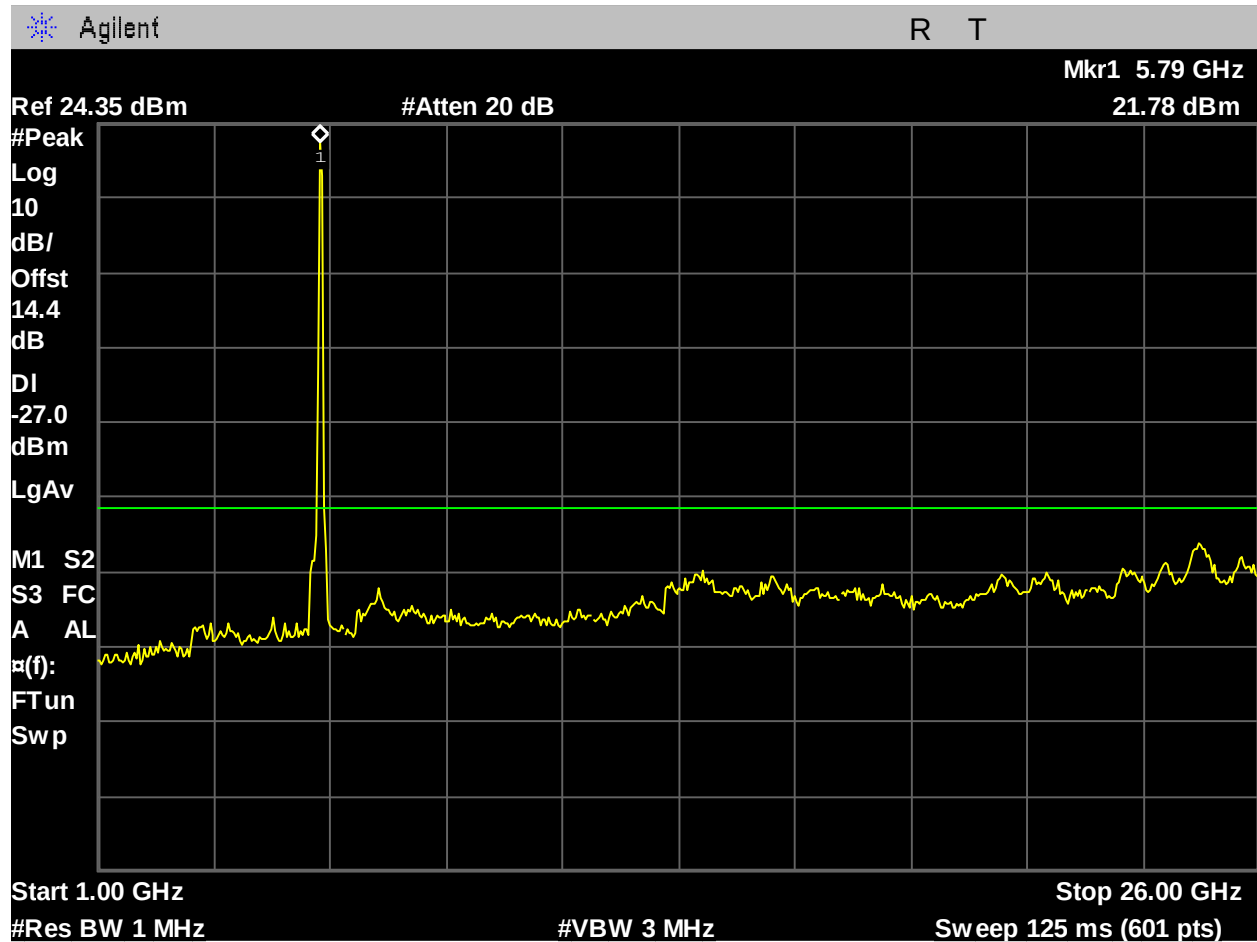


Figure 401: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_ac-mode_-27dBm_1-26GHz_Port 1.

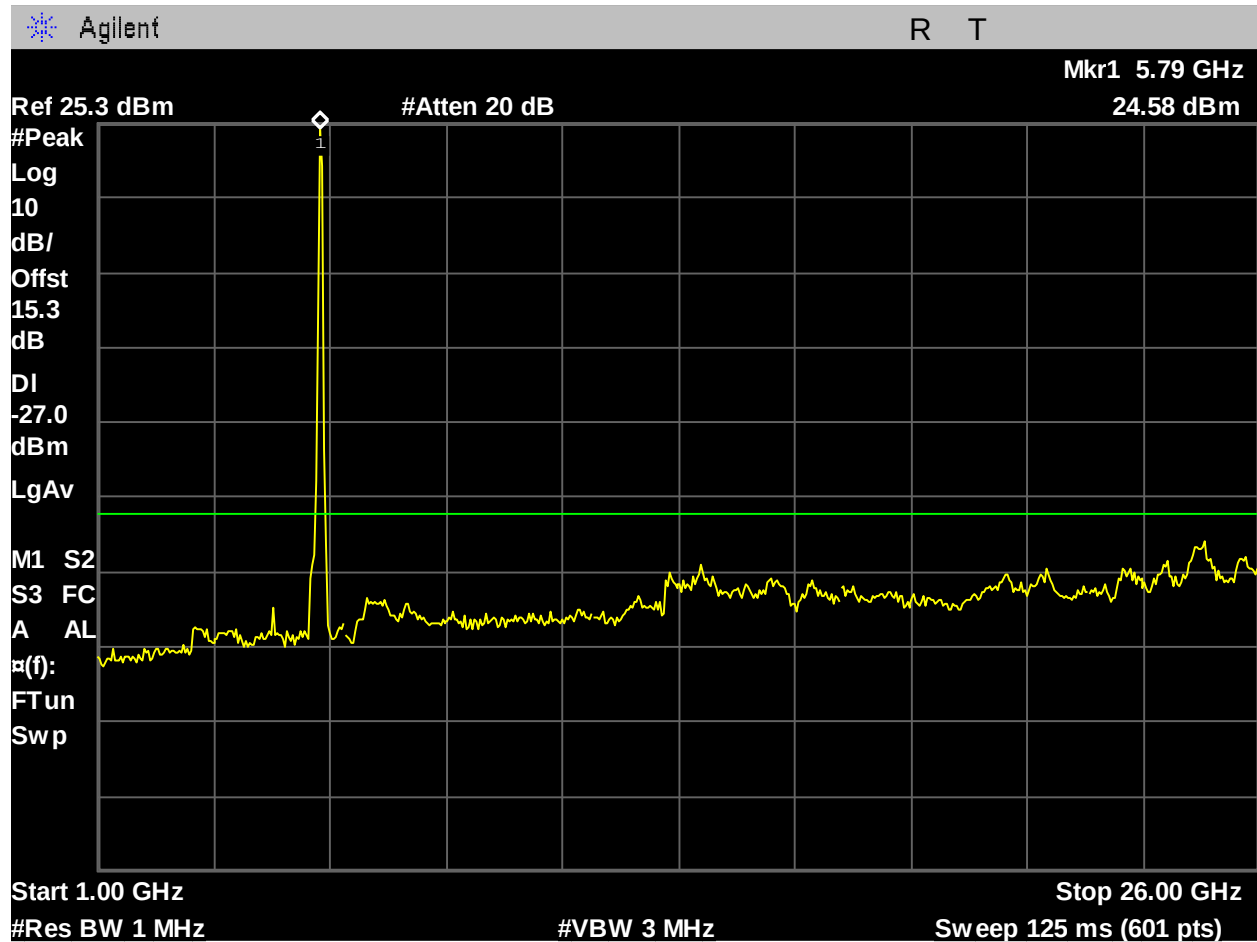


Figure 402: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_ac-mode_-27dBm_1-26GHz_Port 2.

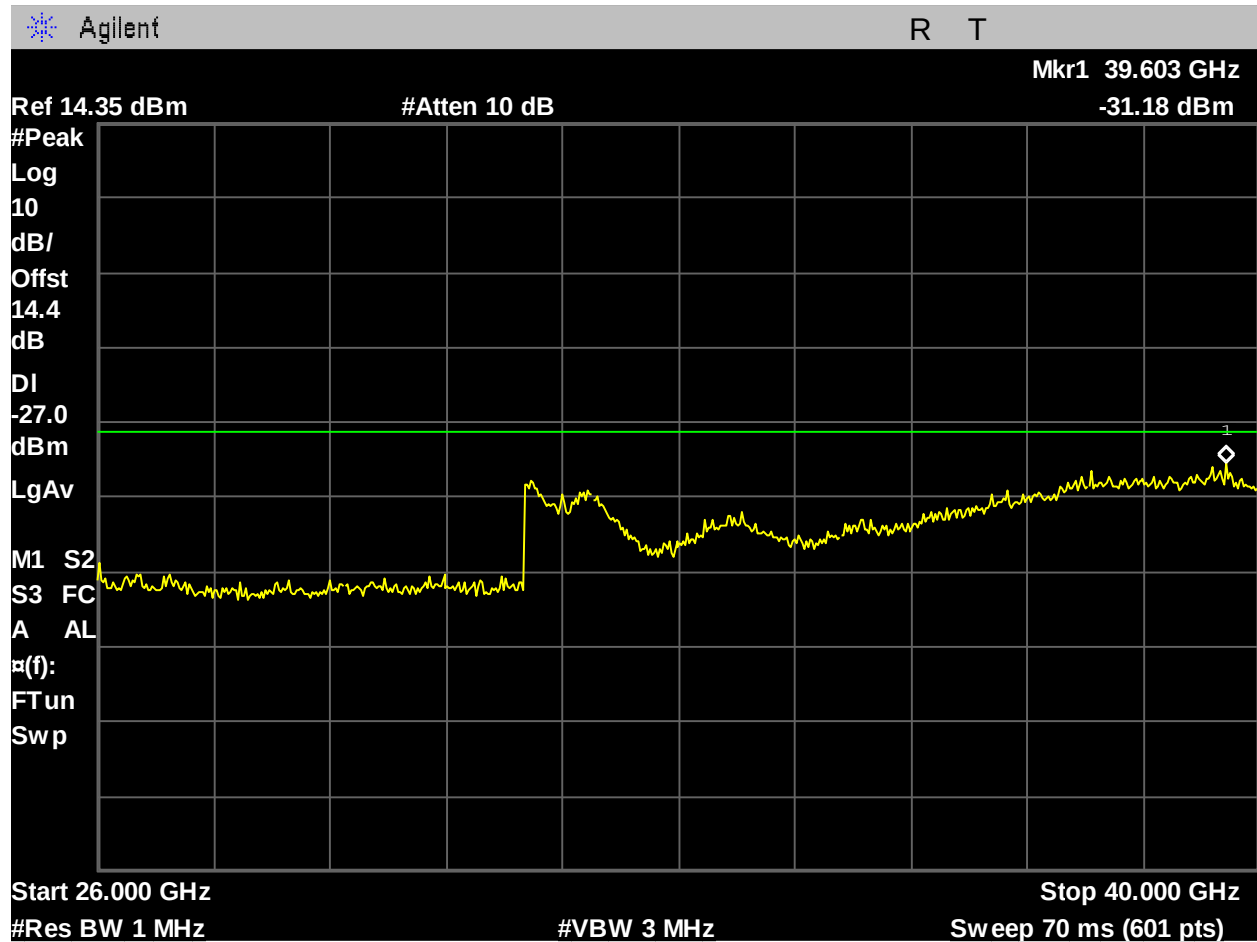


Figure 403: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_ac-mode_-27dBm_26-40GHz_Port 1.

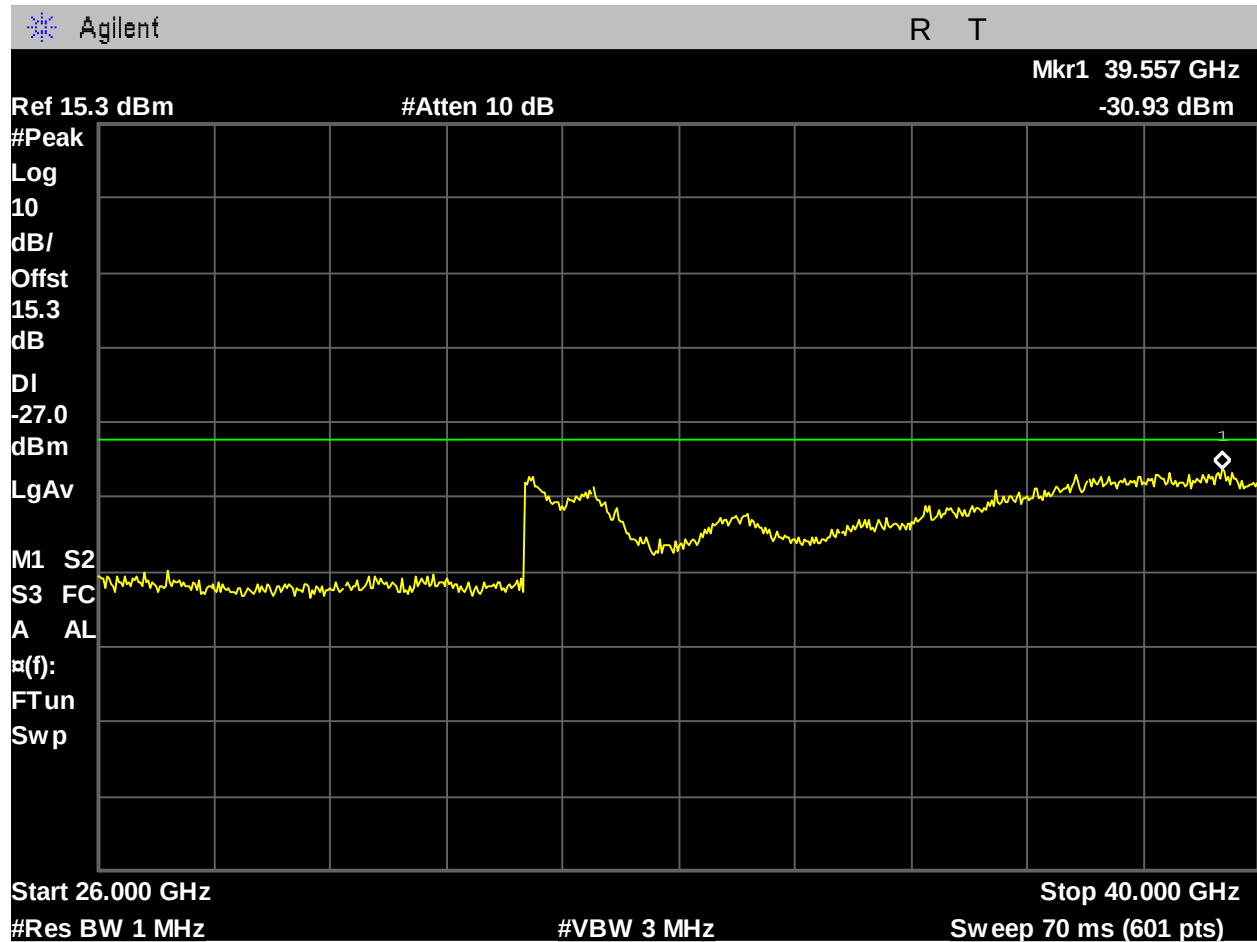


Figure 404: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_ac-mode_-27dBm_26-40GHz_Port 2.

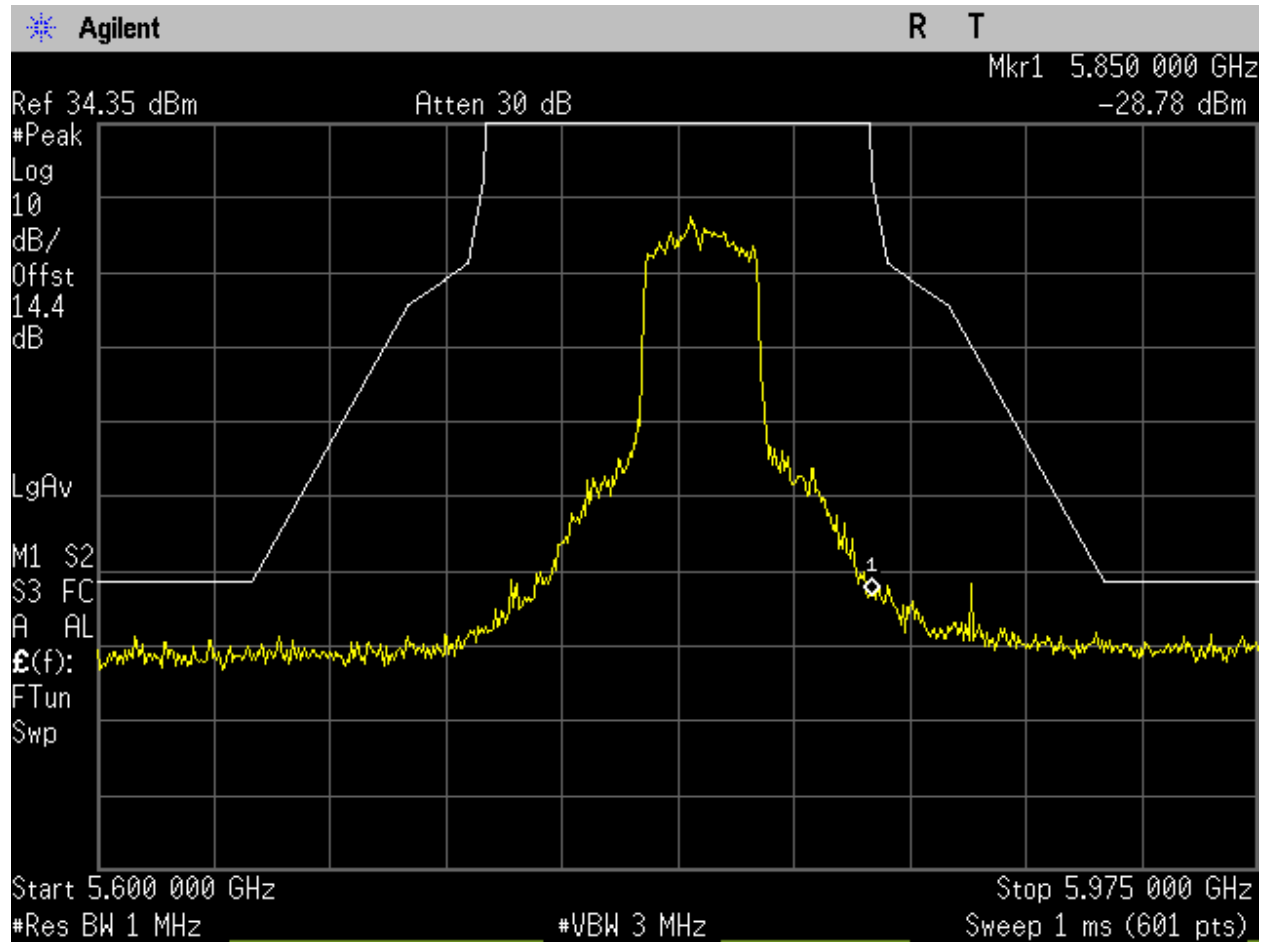


Figure 405: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_ac-mode_Upper Emission Mask_Port 1.

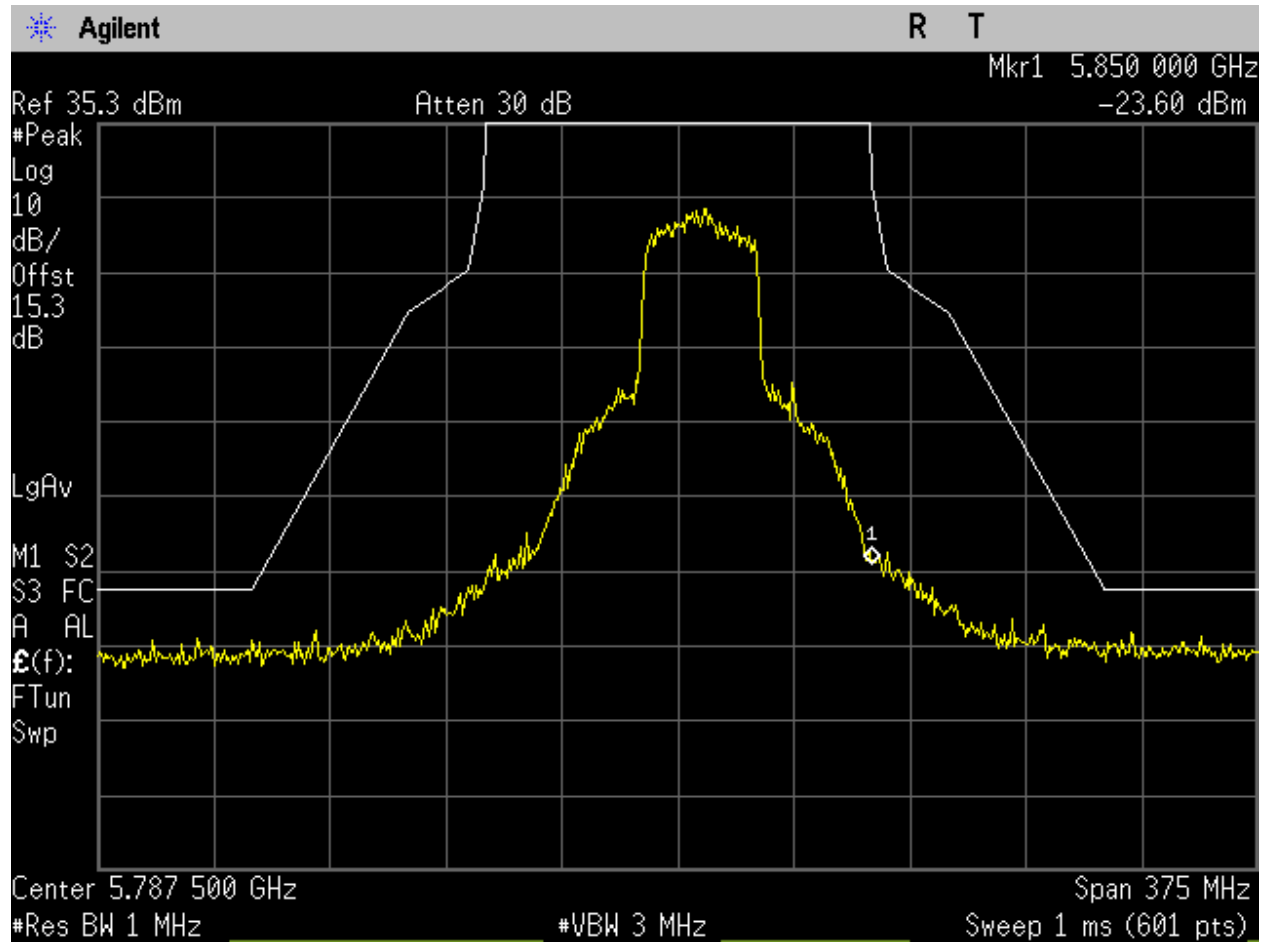


Figure 406: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_ac-mode_Upper Emission Mask_Port 2.

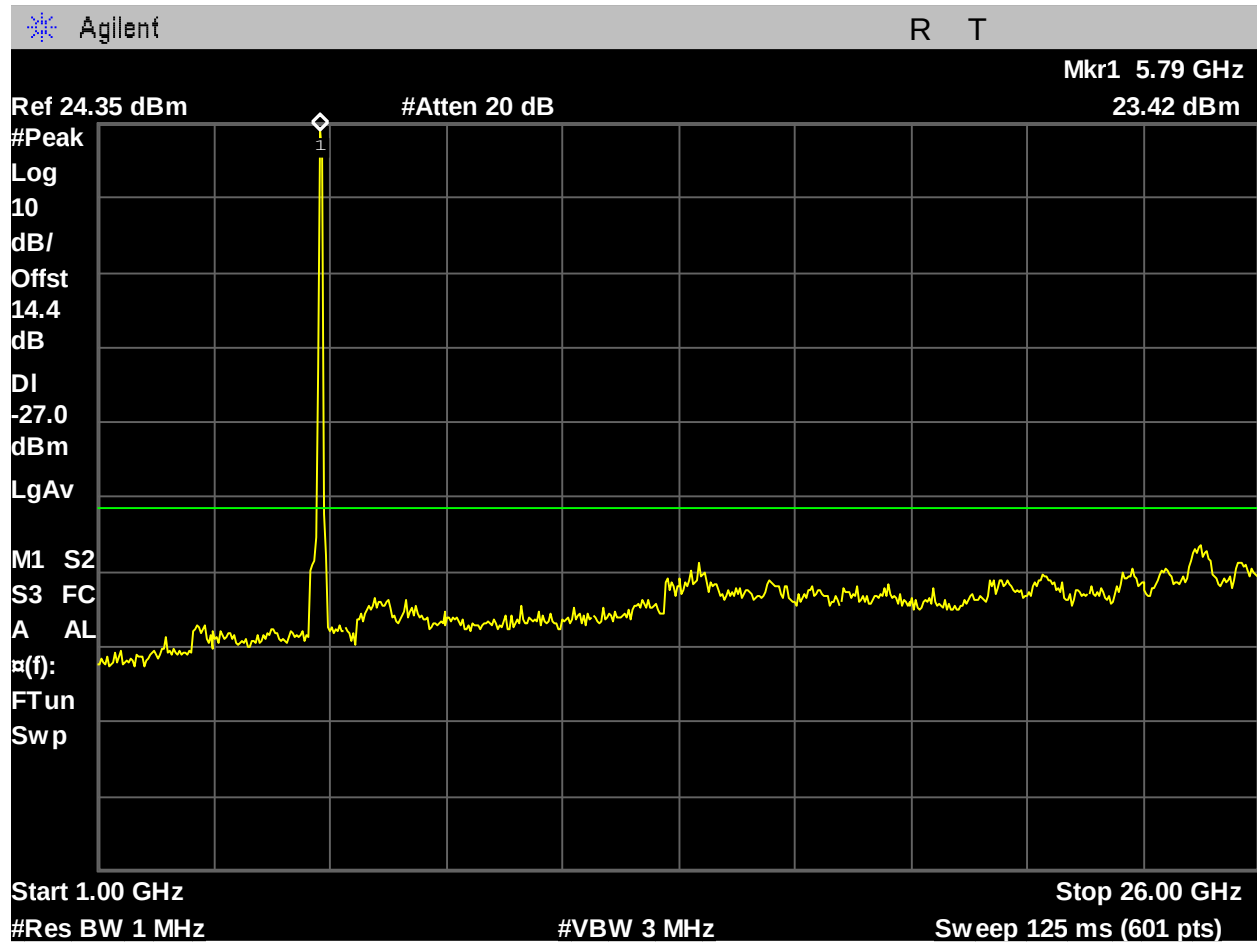


Figure 407: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_ax-mode_-27dBm_1-26GHz_Port 1.

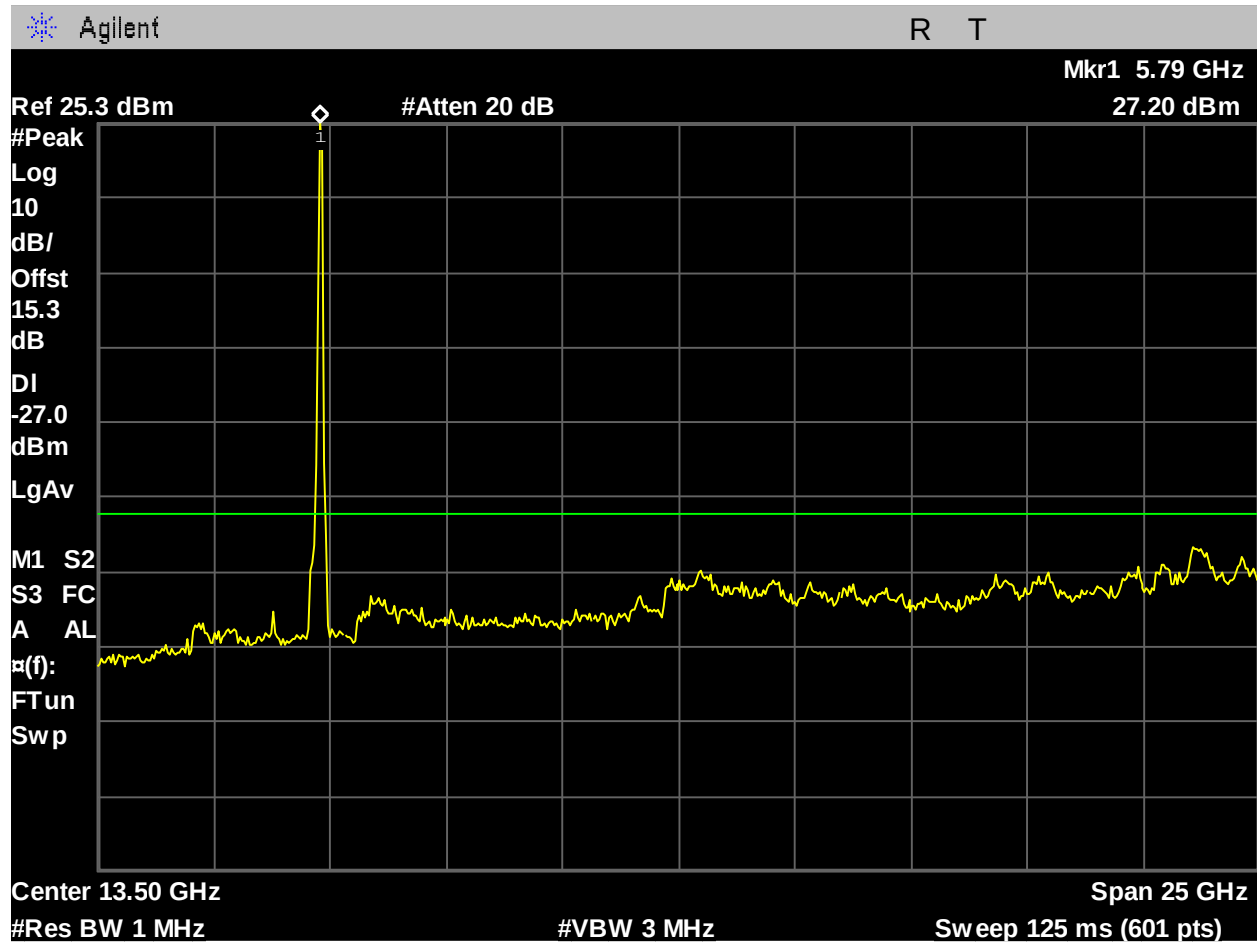


Figure 408: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_ax-mode_-27dBm_1-26GHz_Port 2.



Figure 409: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_ax-mode_-27dBm_26-40GHz_Port 1.



Figure 410: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_ax-mode_-27dBm_26-40GHz_Port 2.

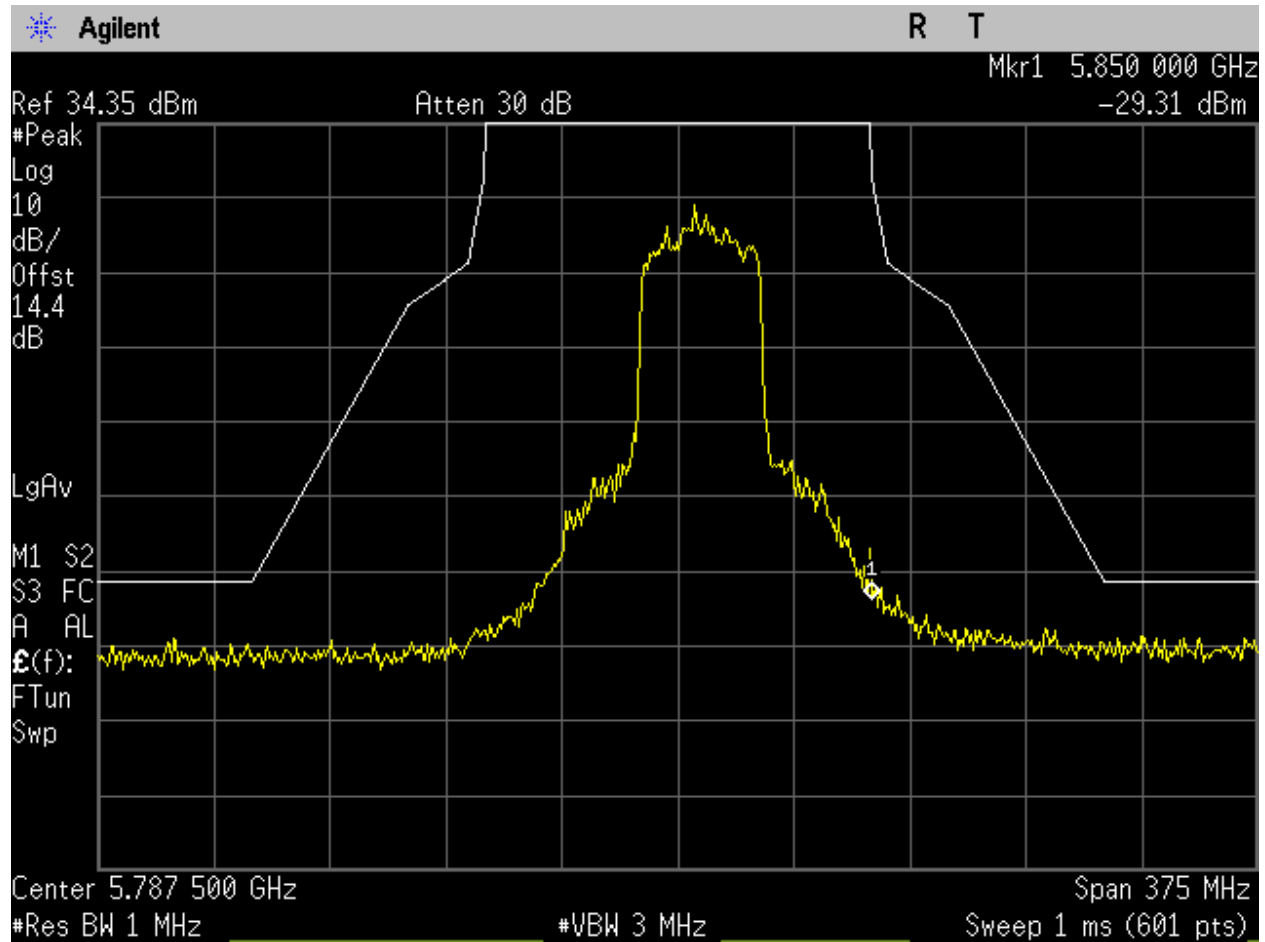


Figure 411: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_ax-mode_Upper Emission Mask_Port 1.

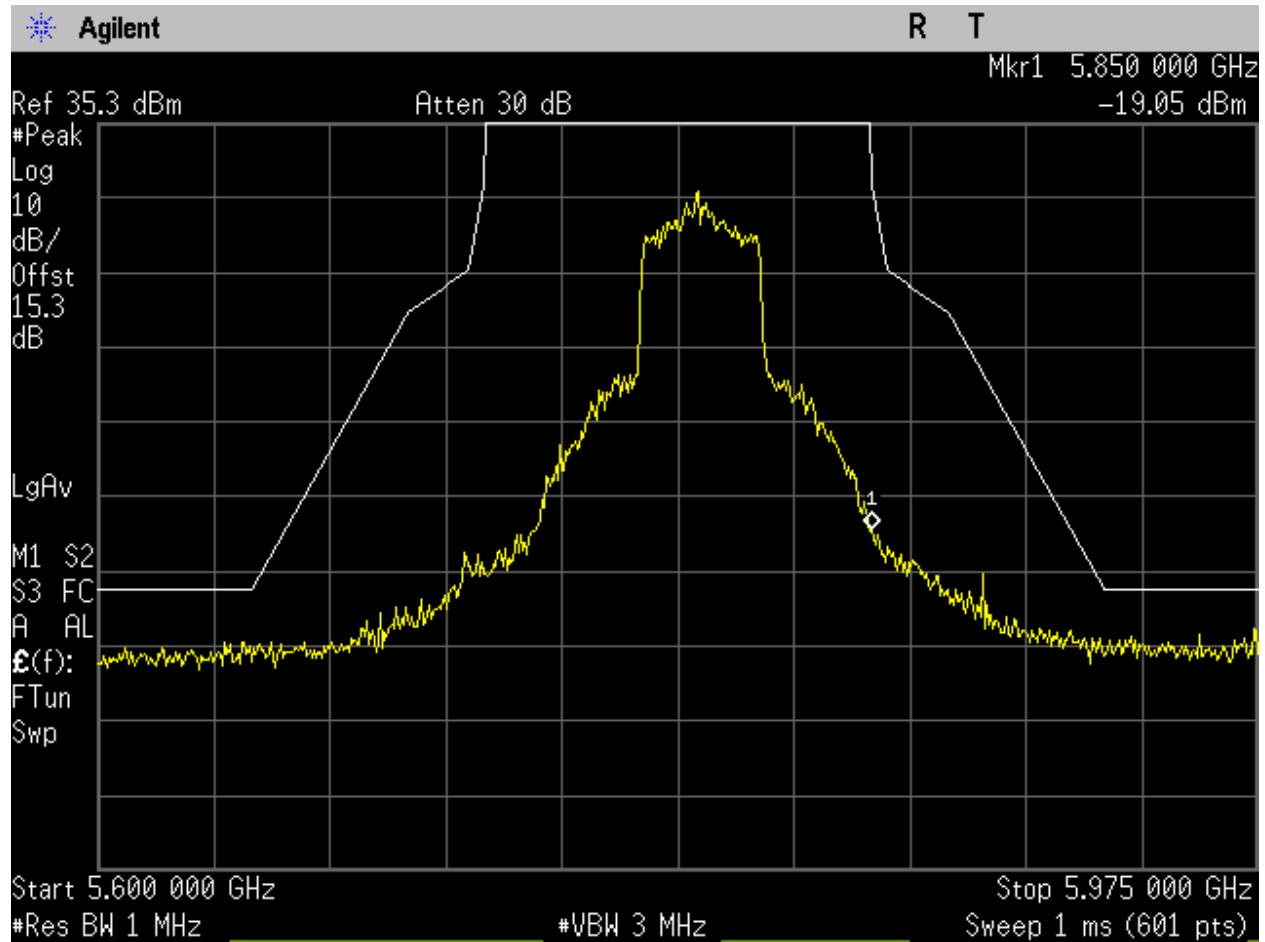


Figure 412: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_ax-mode_Upper Emission Mask_Port 2.

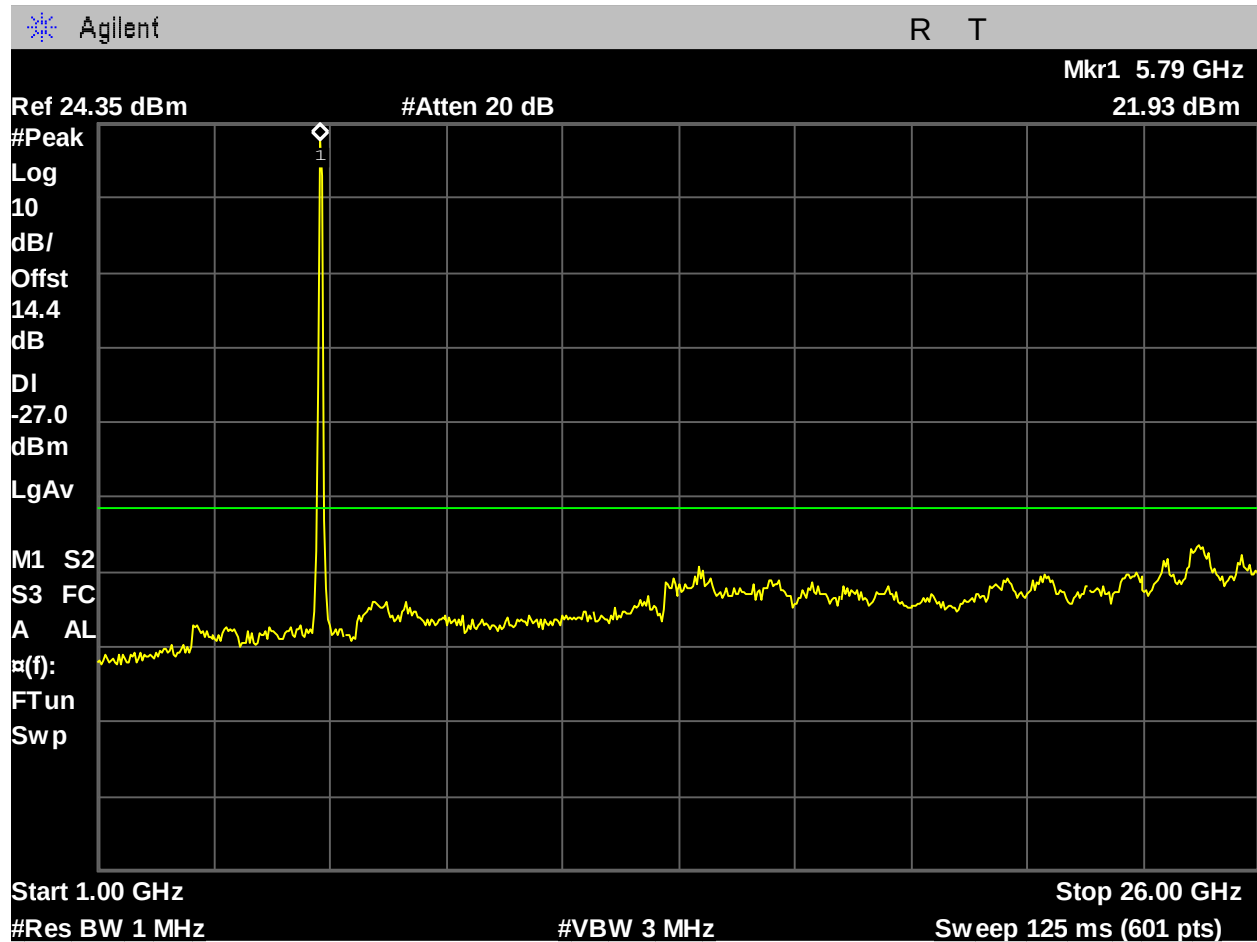


Figure 413: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_n-mode_-27dBm_1-26GHz_Port 1.

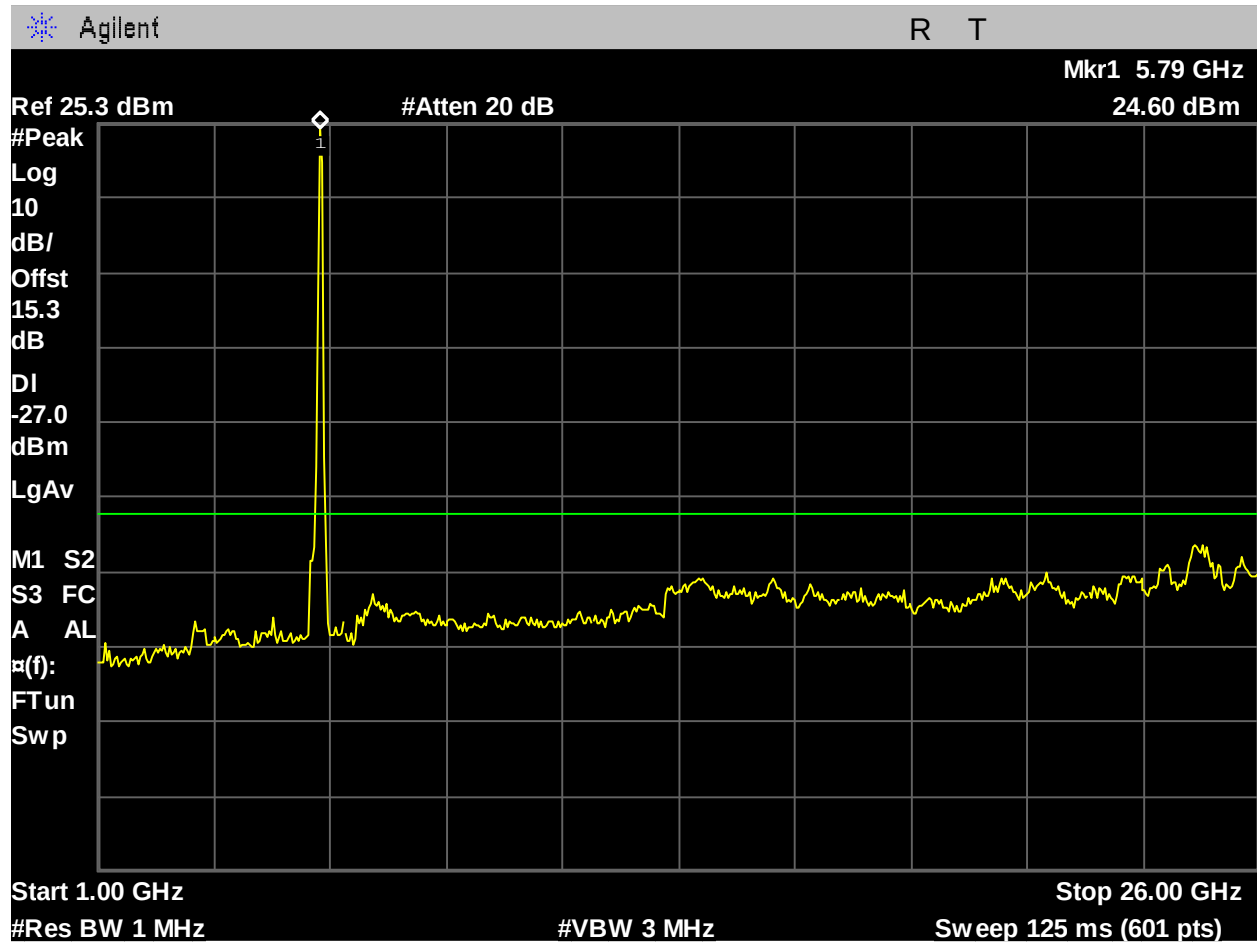


Figure 414: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_n-mode_-27dBm_1-26GHz_Port 2.

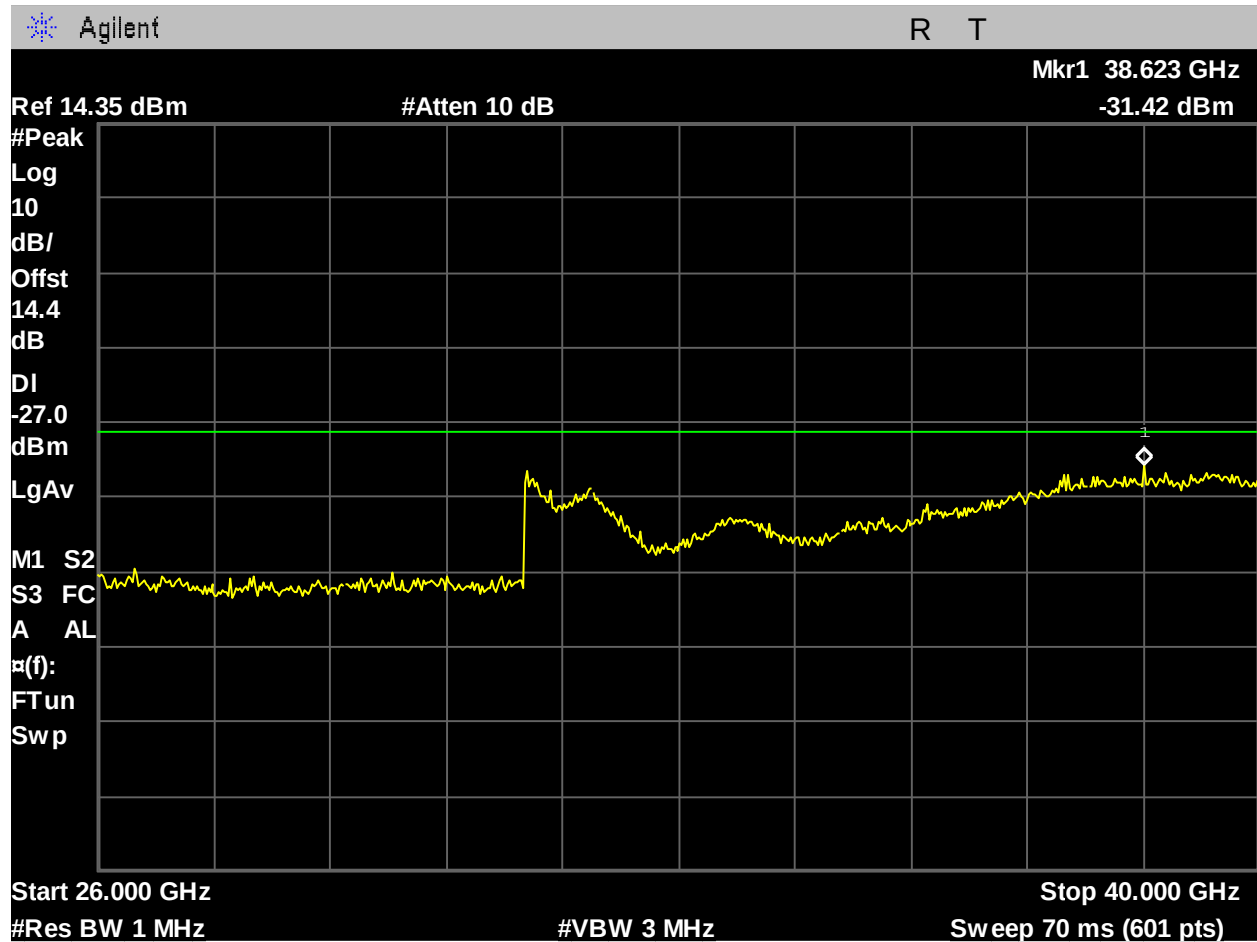


Figure 415: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_n-mode_-27dBm_26-40GHz_Port 1.

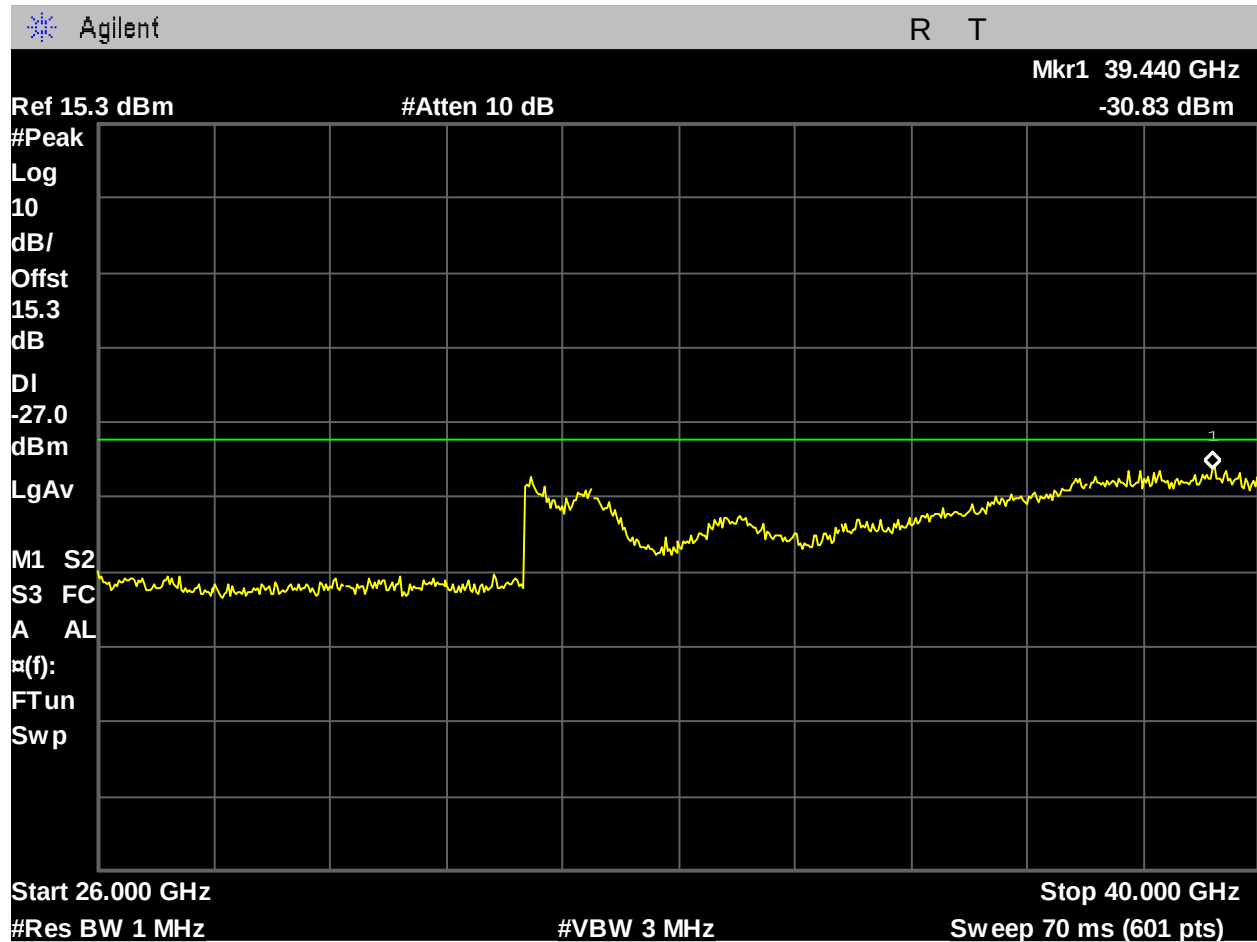


Figure 416: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_n-mode_-27dBm_26-40GHz_Port 2.

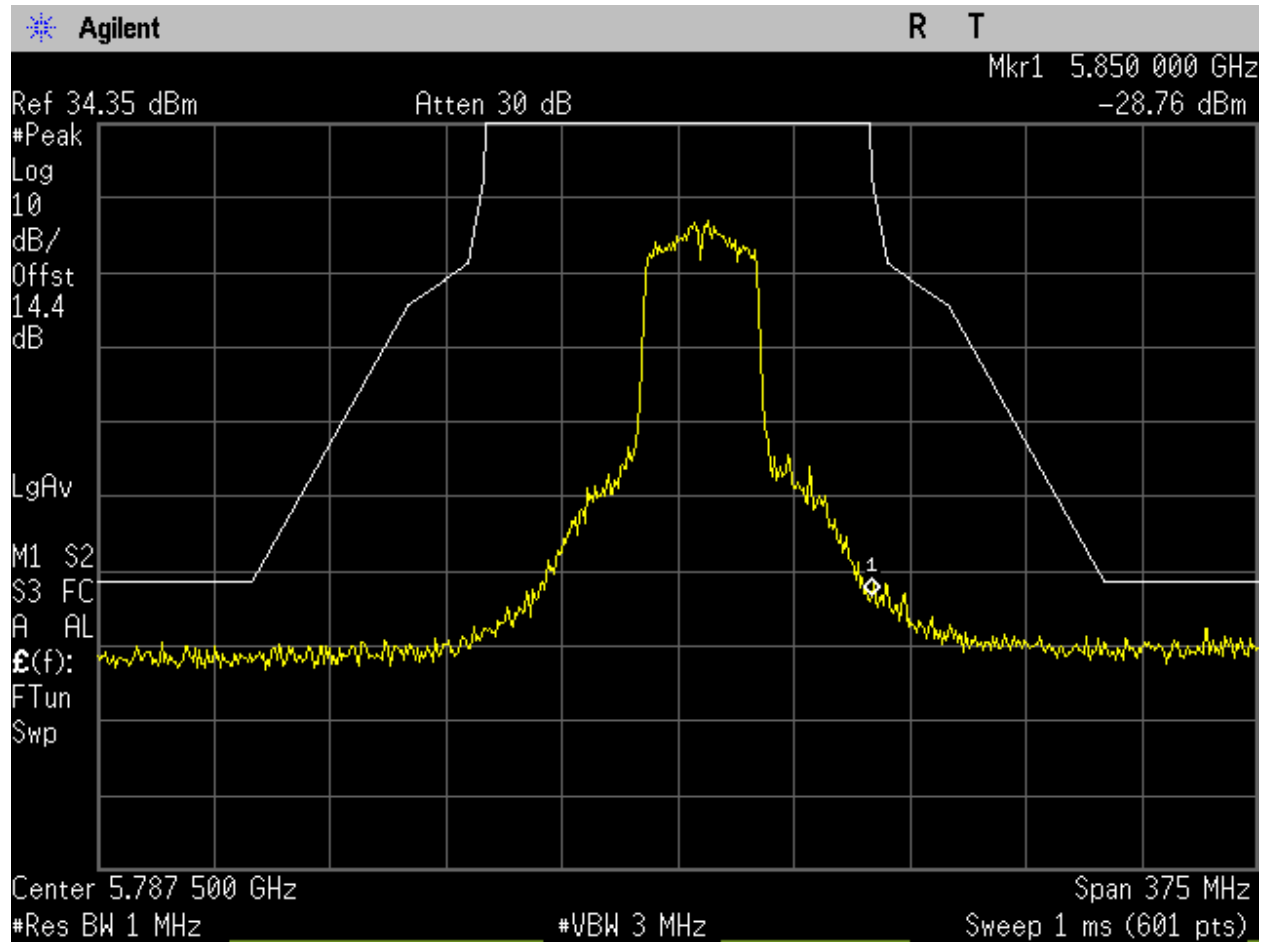


Figure 417: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_n-mode_Upper Emission Mask_Port 1.

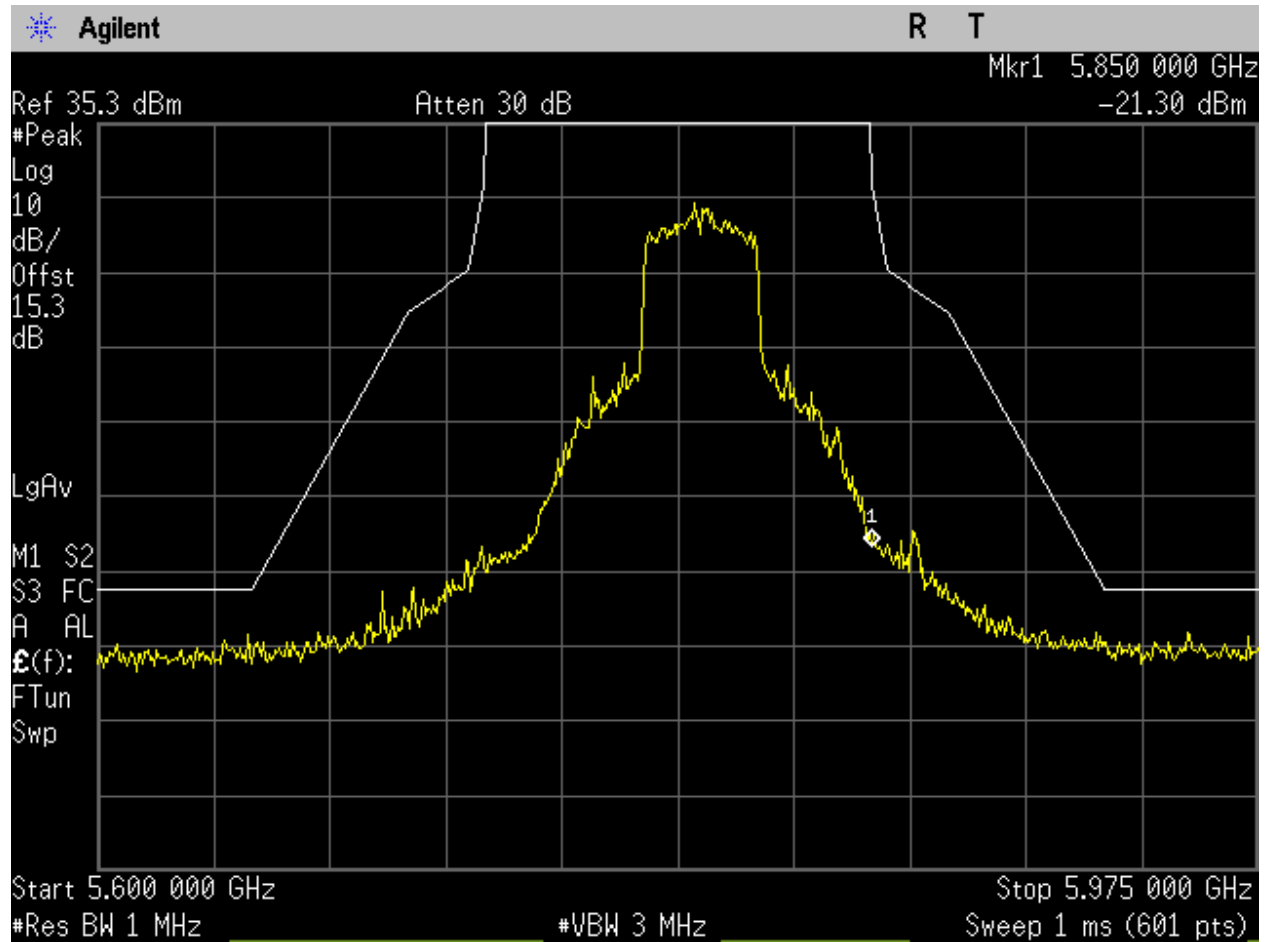


Figure 418: U-NII-3_5795MHz_High_Mid Ch_159_40MHz BW_n-mode_Upper Emission Mask_Port 2.

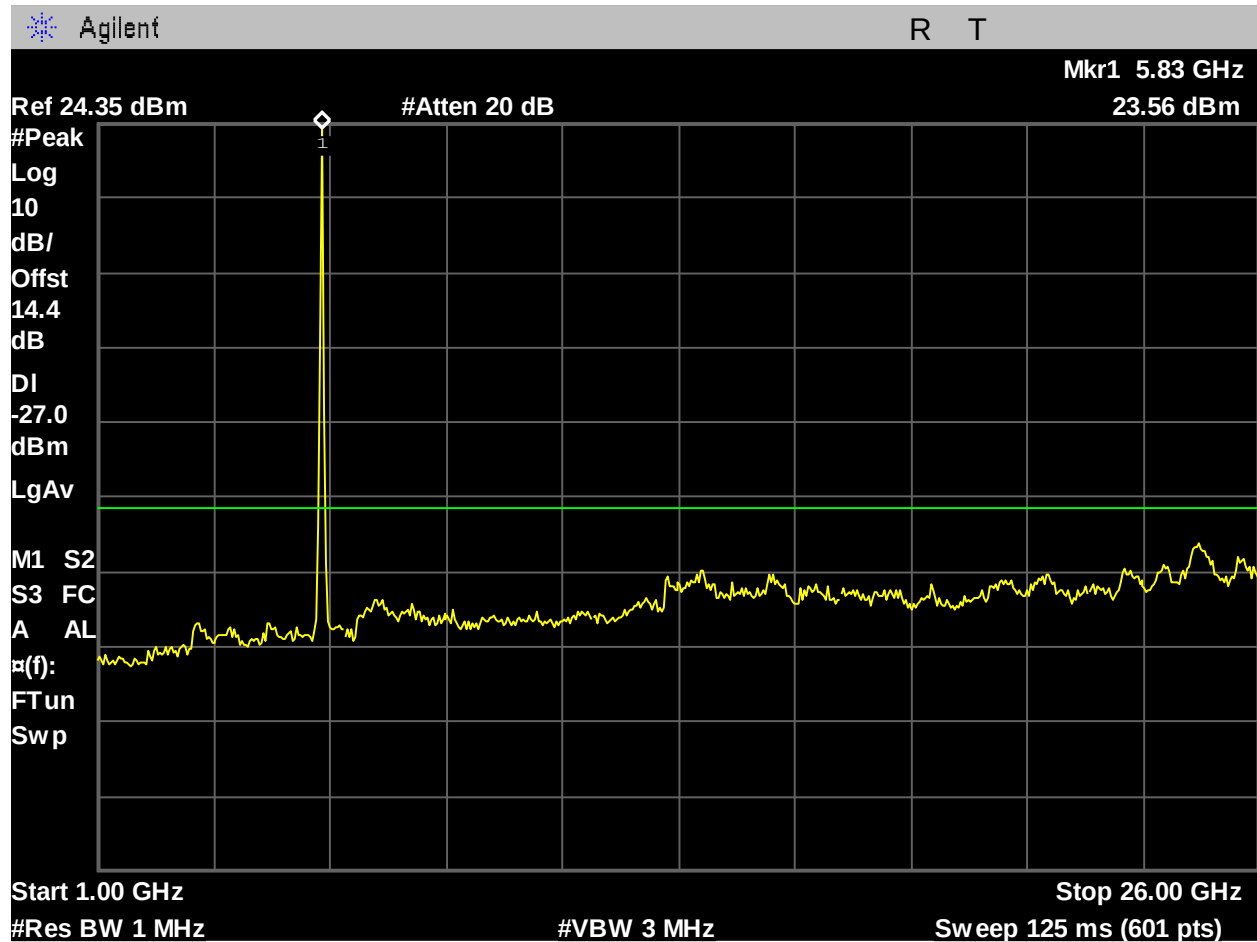


Figure 419: U-NII-3_5825MHz_High Ch_165_20MHz BW_a-mode_-27dBm_1-26GHz_Port 1.

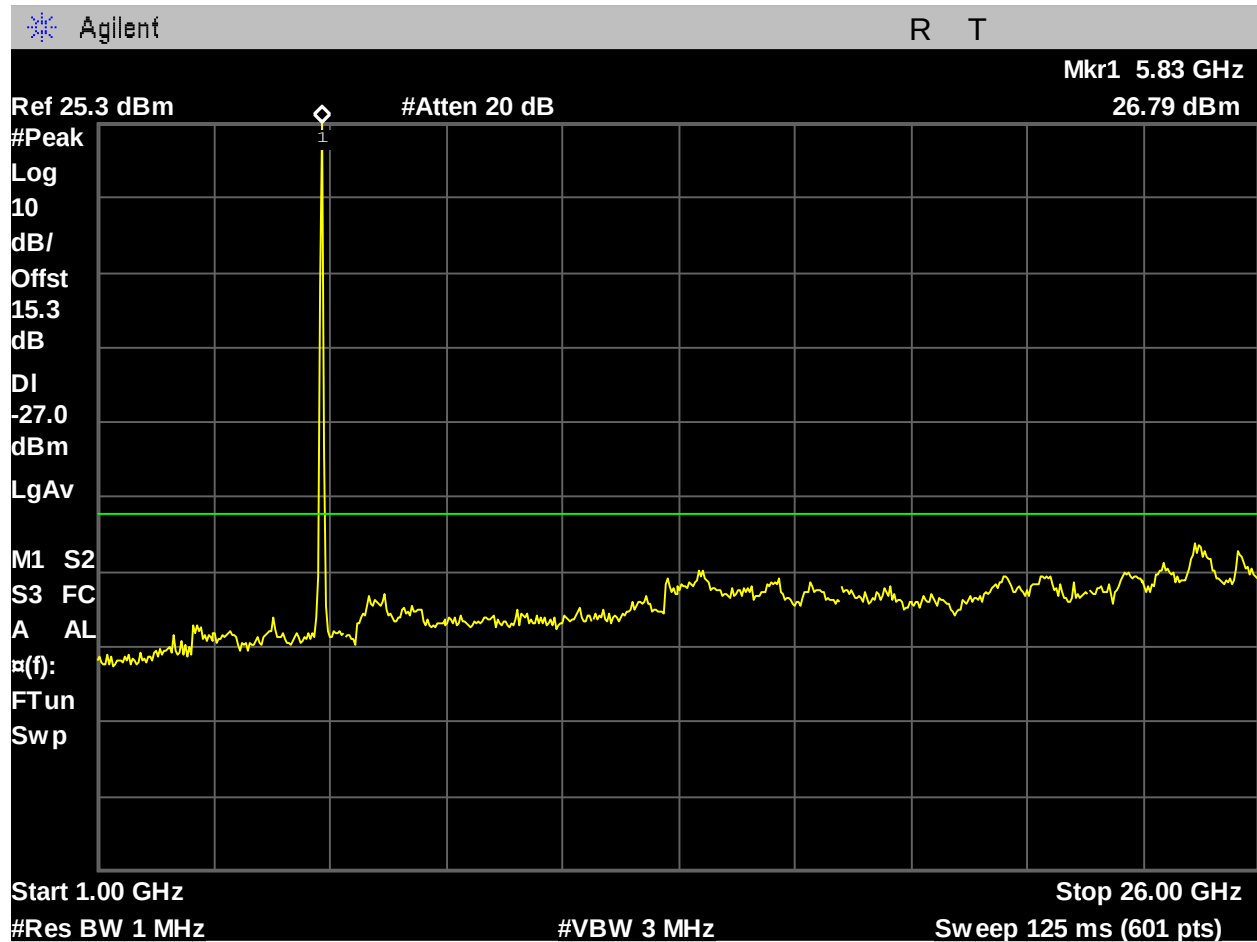


Figure 420: U-NII-3_5825MHz_High Ch_165_20MHz BW_a-mode_-27dBm_1-26GHz_Port 2.

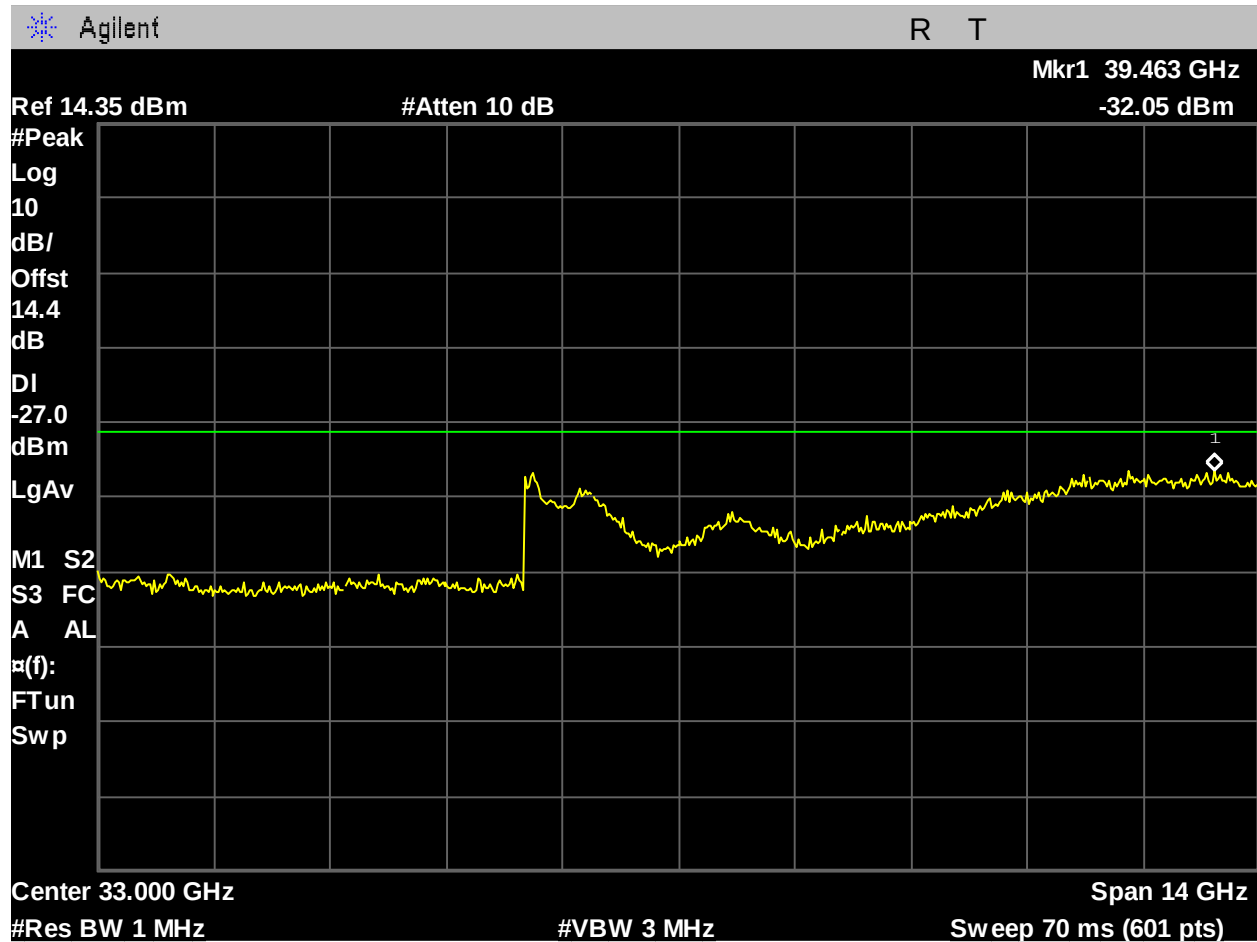


Figure 421: U-NII-3_5825MHz_High Ch_165_20MHz BW_a-mode_-27dBm_26-40GHz_Port 1.

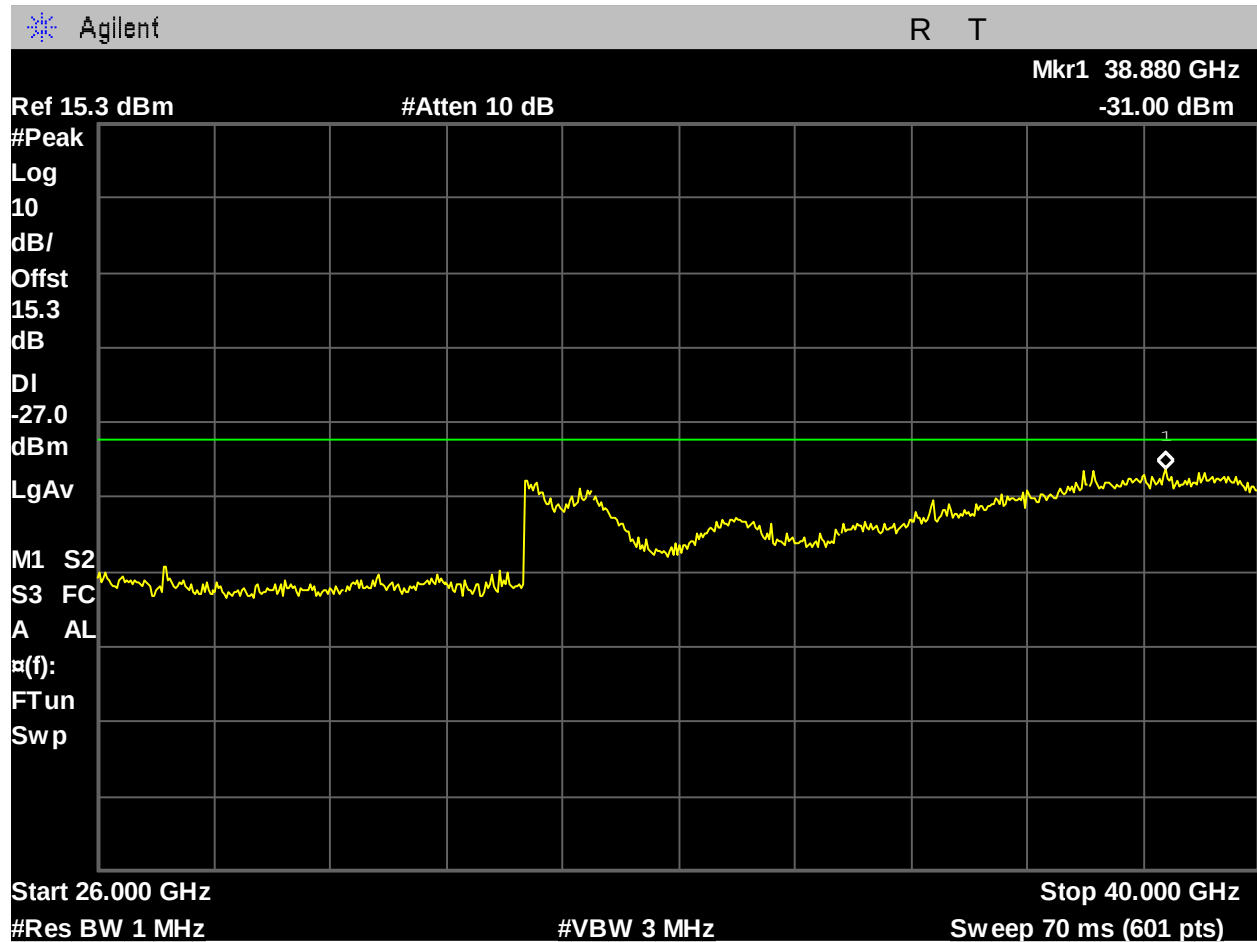


Figure 422: U-NII-3_5825MHz_High Ch_165_20MHz BW_a-mode_-27dBm_26-40GHz_Port 2.

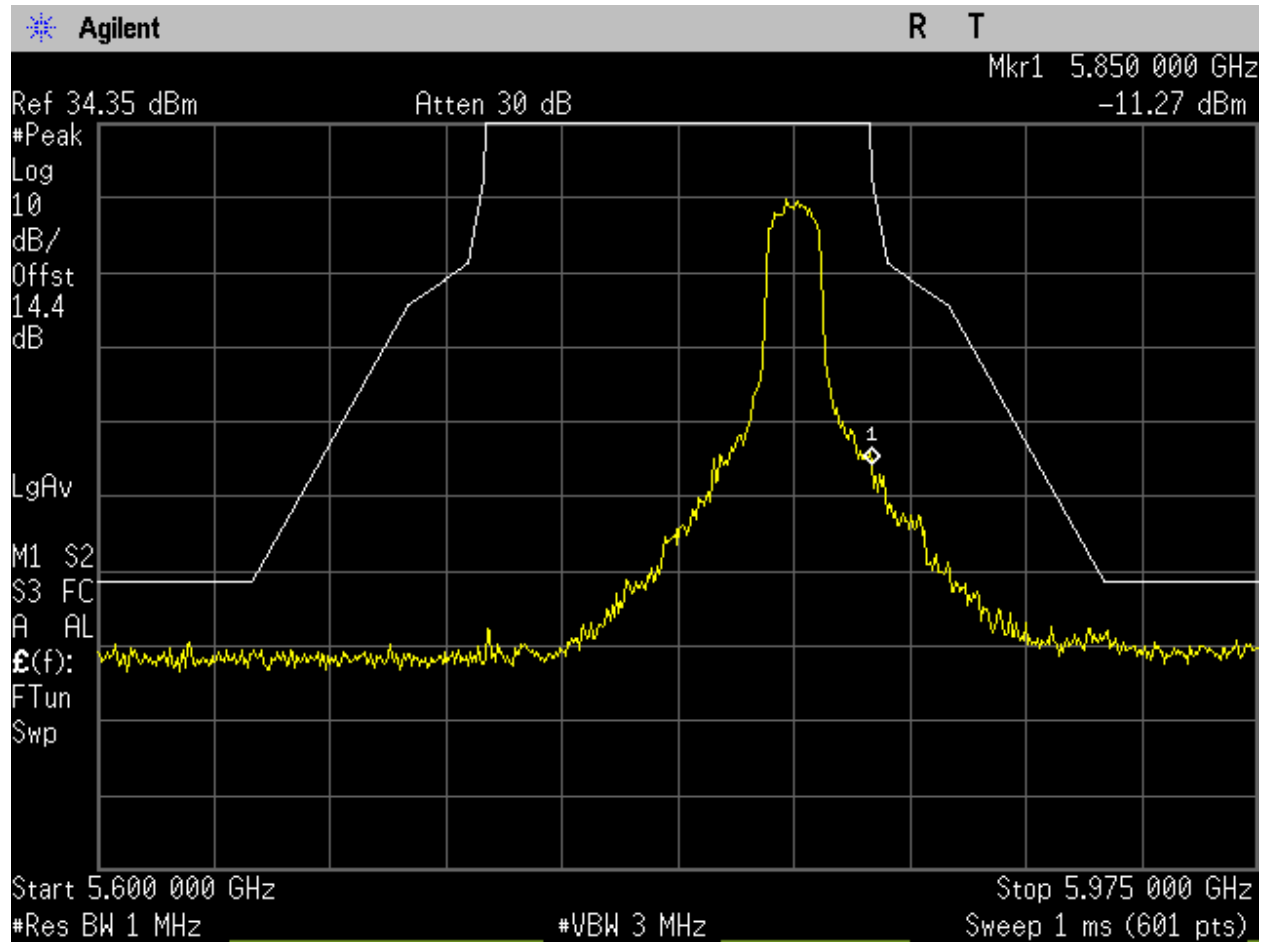


Figure 423: U-NII-3_5825MHz_High Ch_165_20MHz BW_a-mode_Upper Emission Mask_Port 1.

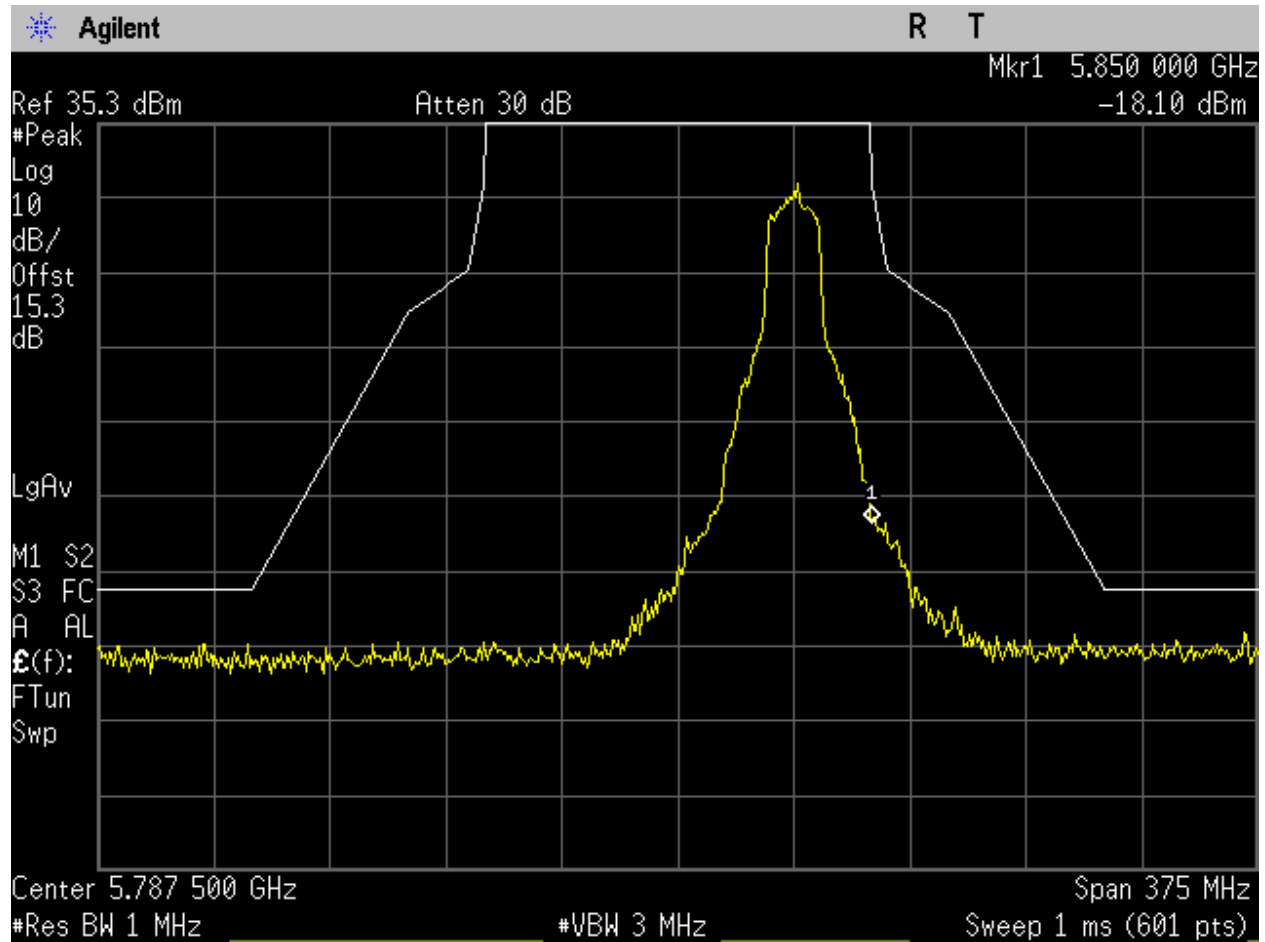


Figure 424: U-NII-3_5825MHz_High Ch_165_20MHz BW_a-mode_Upper Emission Mask_Port 2.

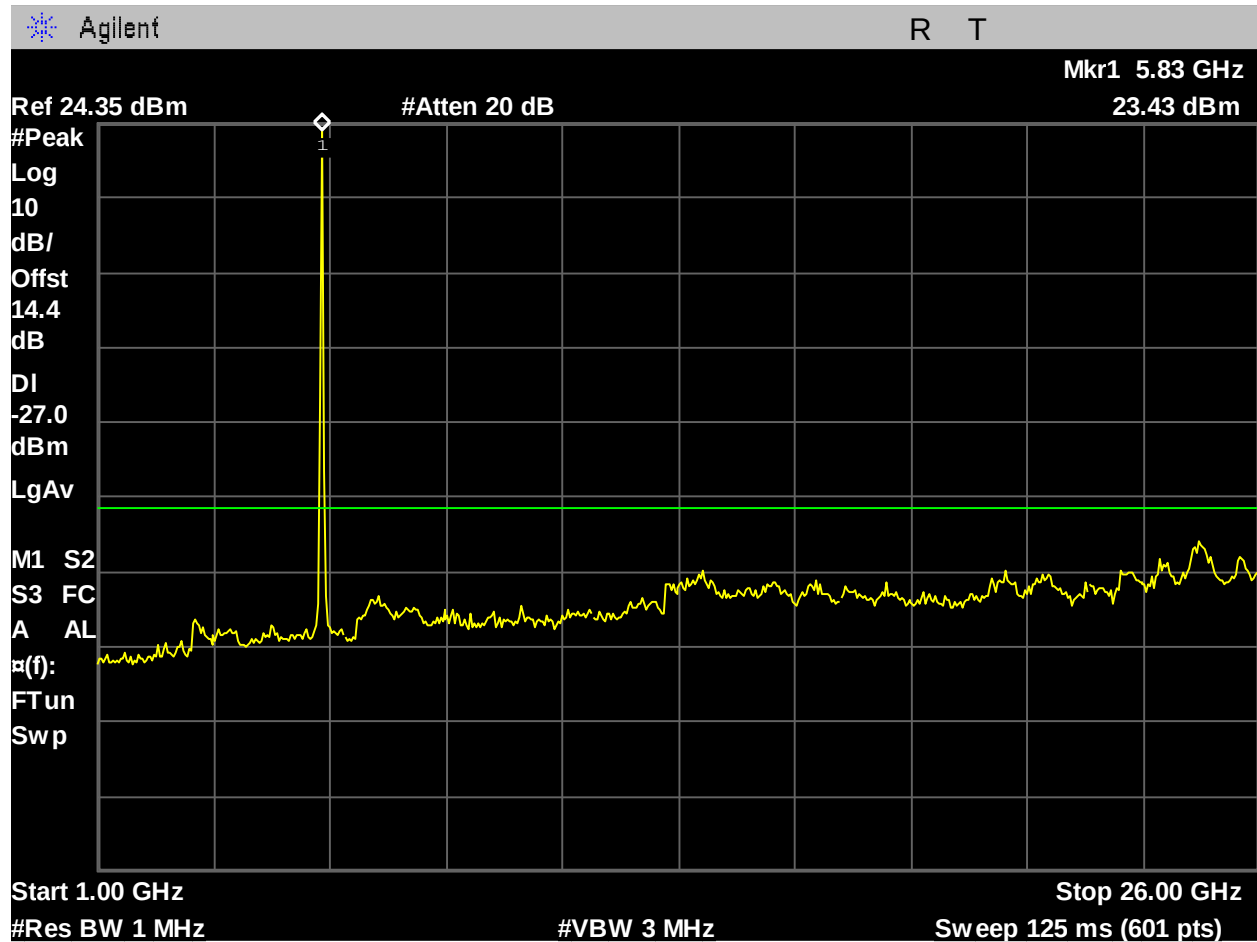


Figure 425: U-NII-3_5825MHz_High Ch_165_20MHz BW_ac-mode_-27dBm_1-26GHz_Port 1.

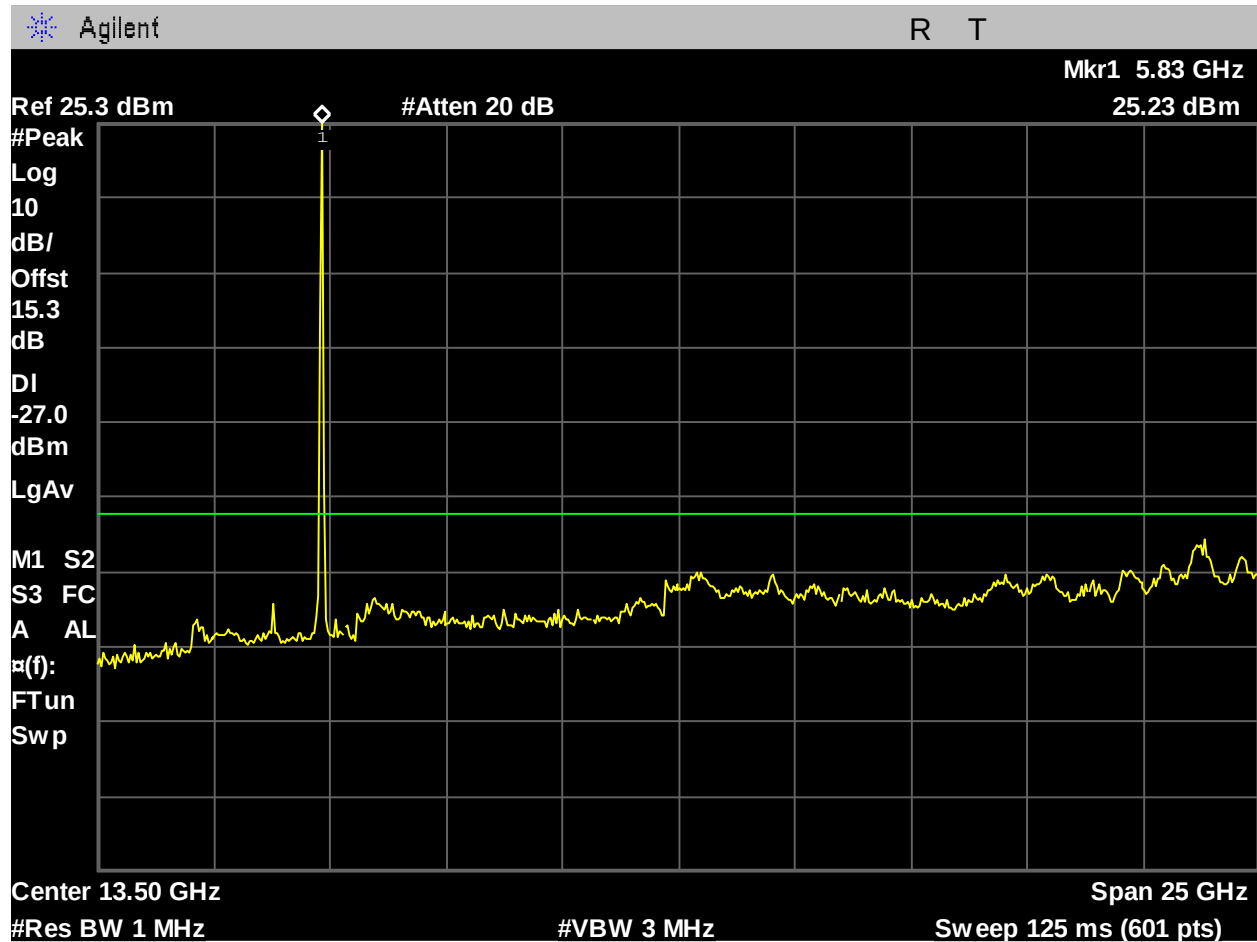


Figure 426: U-NII-3_5825MHz_High Ch_165_20MHz BW_ac-mode_-27dBm_1-26GHz_Port 2.

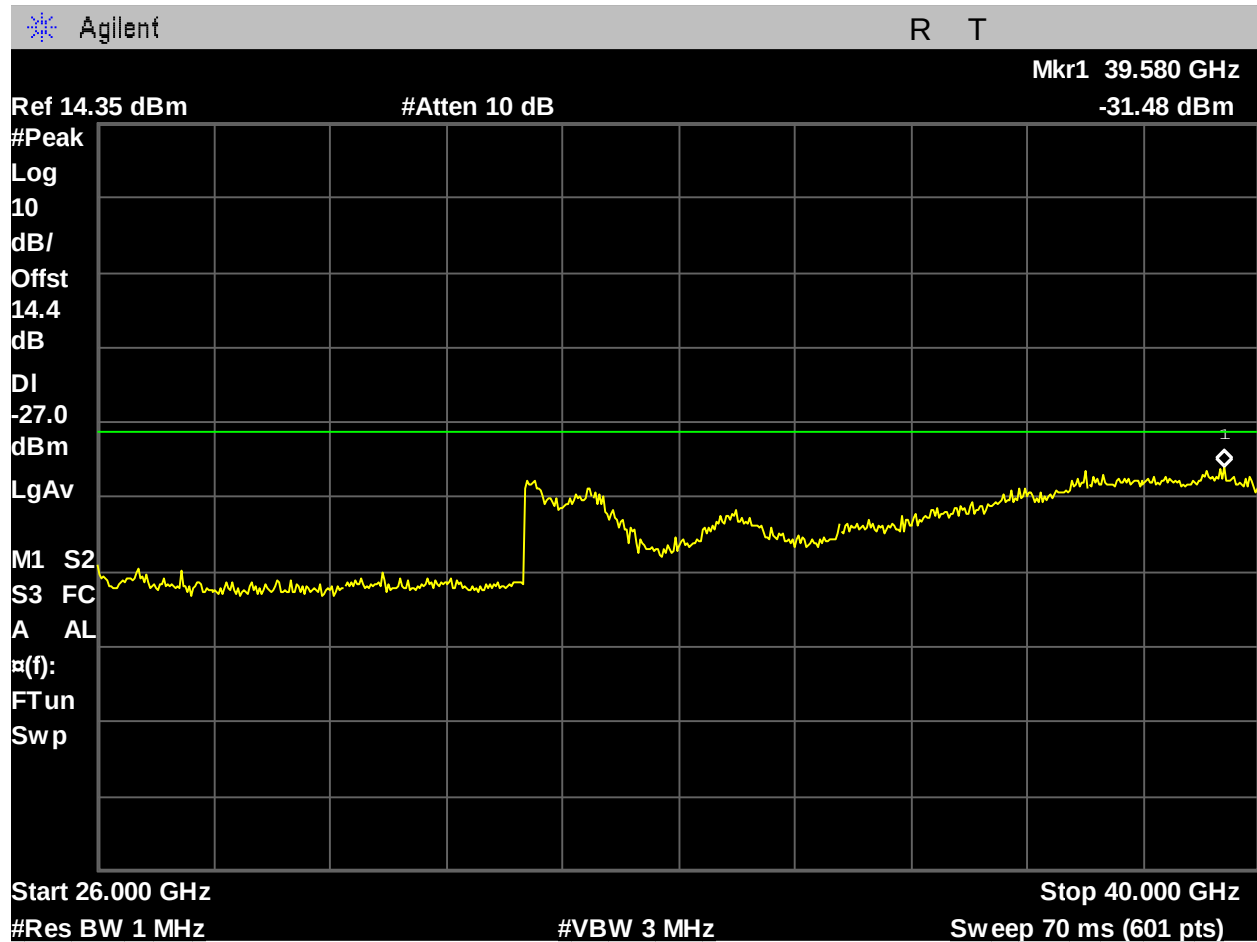


Figure 427: U-NII-3_5825MHz_High Ch_165_20MHz BW_ac-mode_-27dBm_26-40GHz_Port 1.



Figure 428: U-NII-3_5825MHz_High Ch_165_20MHz BW_ac-mode_-27dBm_26-40GHz_Port 2.

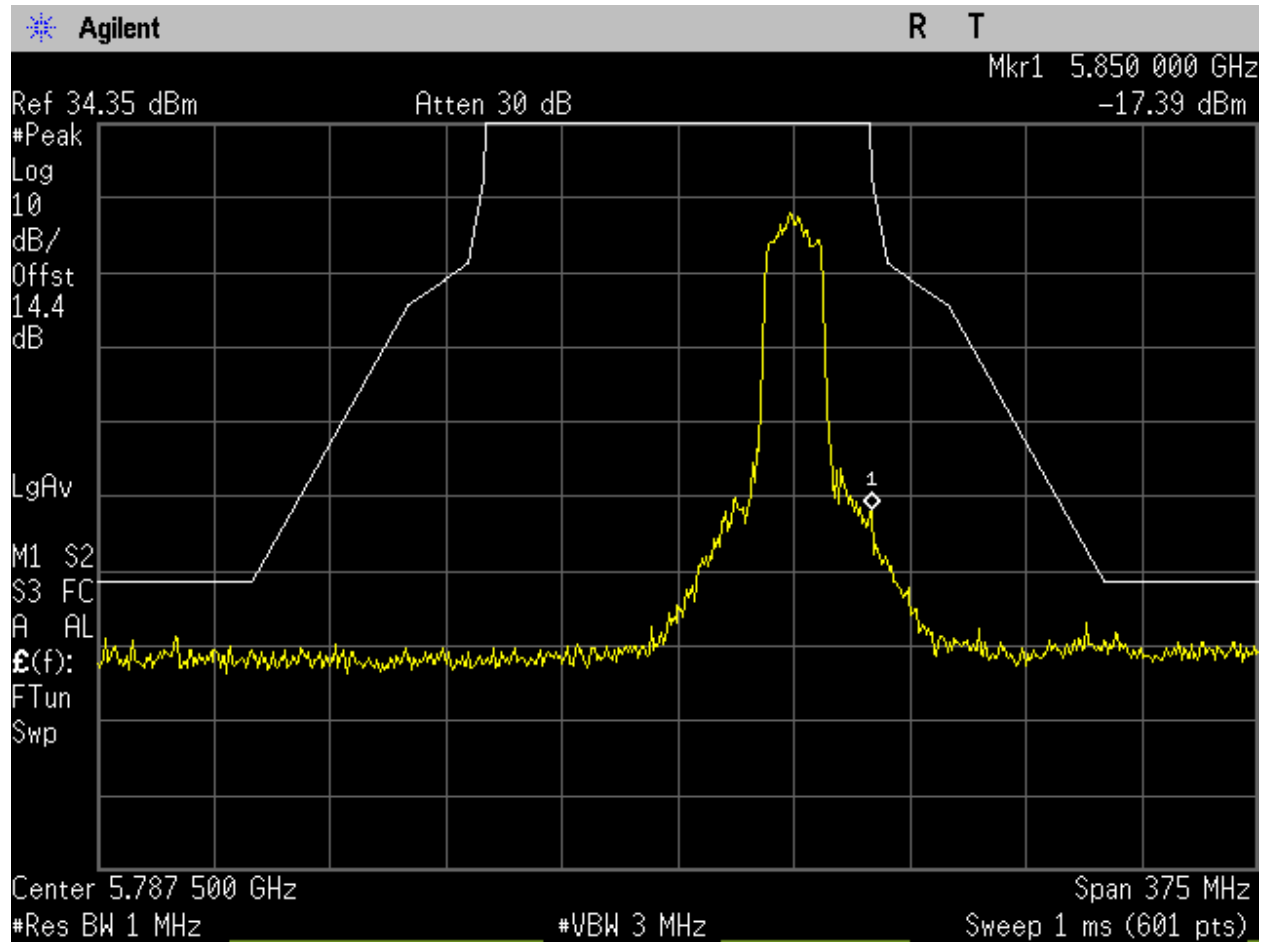


Figure 429: U-NII-3_5825MHz_High Ch_165_20MHz BW_ac-mode_Upper Emission Mask_Port 1.

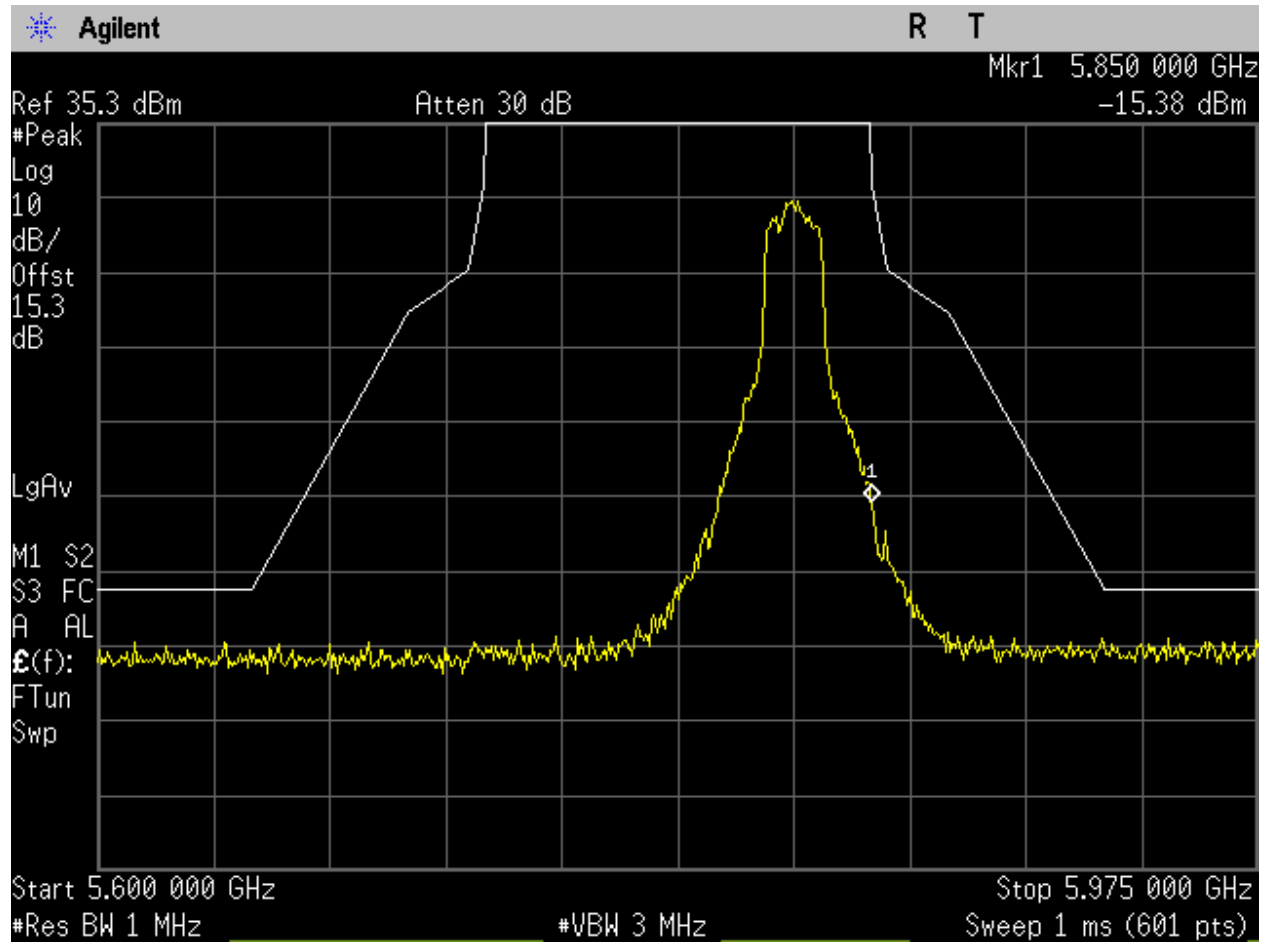


Figure 430: U-NII-3_5825MHz_High Ch_165_20MHz BW_ac-mode_Upper Emission Mask_Port 2.

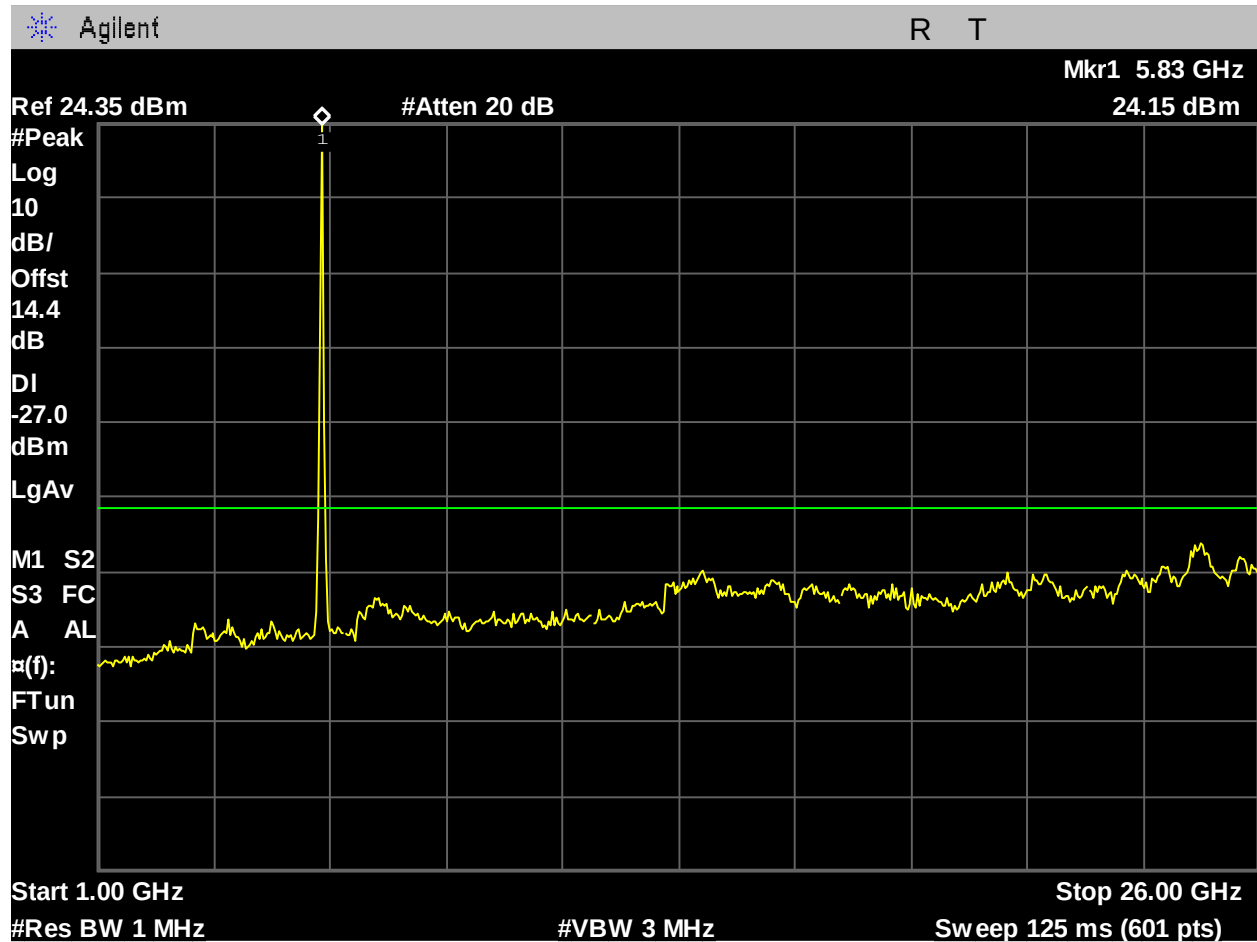


Figure 431: U-NII-3_5825MHz_High Ch_165_20MHz BW_ax-mode_-27dBm_1-26GHz_Port 1.

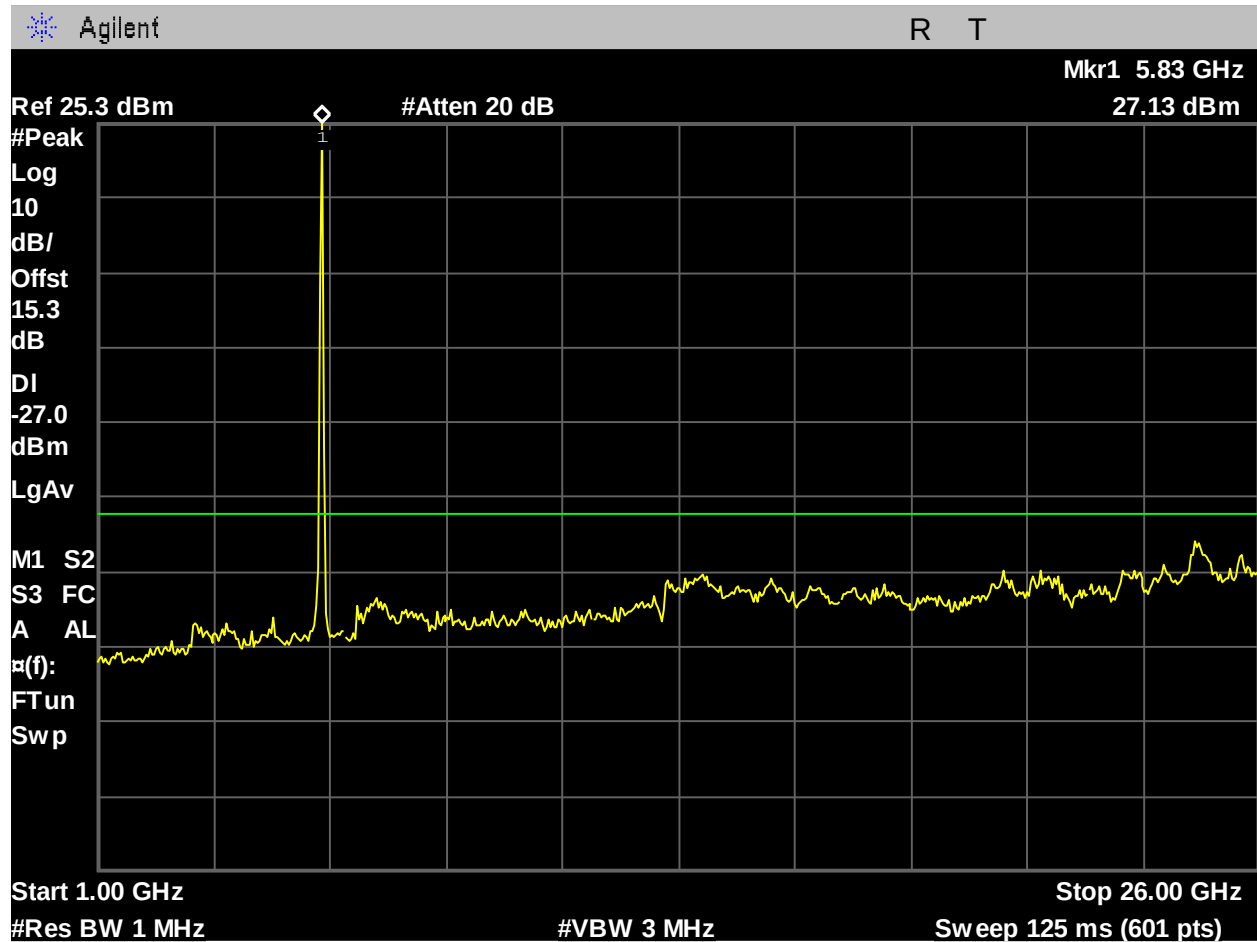


Figure 432: U-NII-3_5825MHz_High Ch_165_20MHz BW_ax-mode_-27dBm_1-26GHz_Port 2.

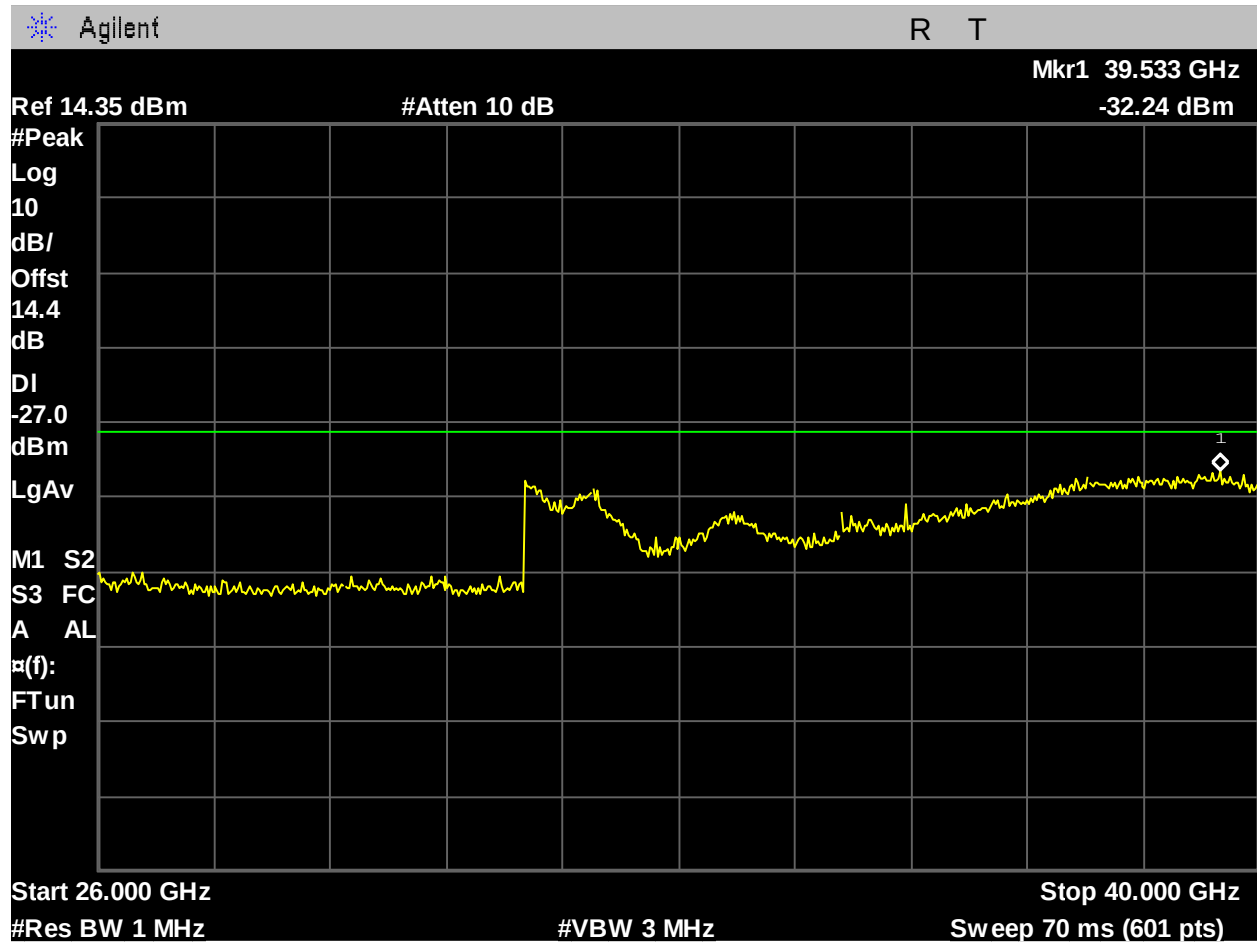


Figure 433: U-NII-3_5825MHz_High Ch_165_20MHz BW_ax-mode_-27dBm_26-40GHz_Port 1.

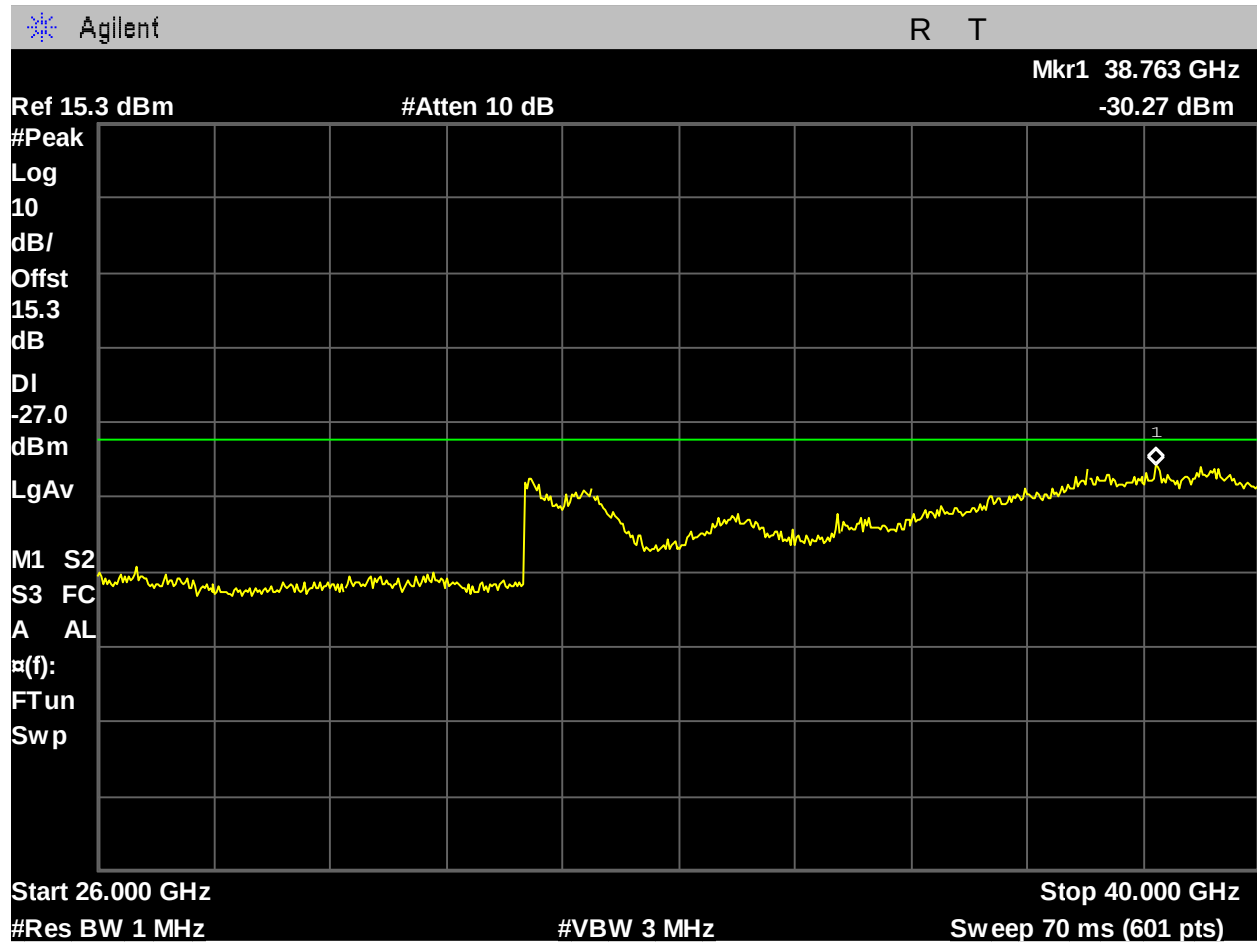


Figure 434: U-NII-3_5825MHz_High Ch_165_20MHz BW_ax-mode_-27dBm_26-40GHz_Port 2.

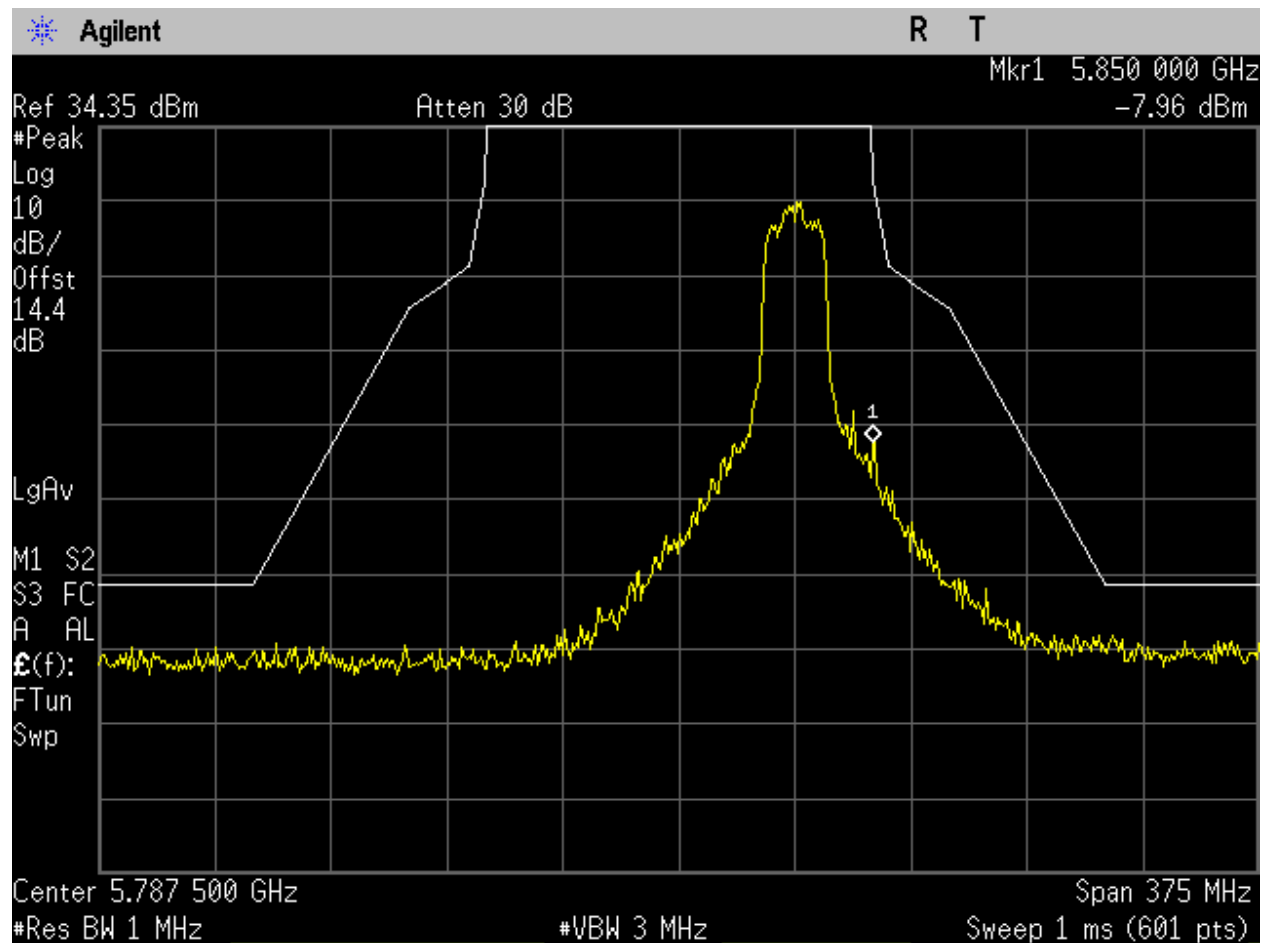


Figure 435: U-NII-3_5825MHz_High Ch_165_20MHz BW_ax-mode_Upper Emission Mask_Port 1.

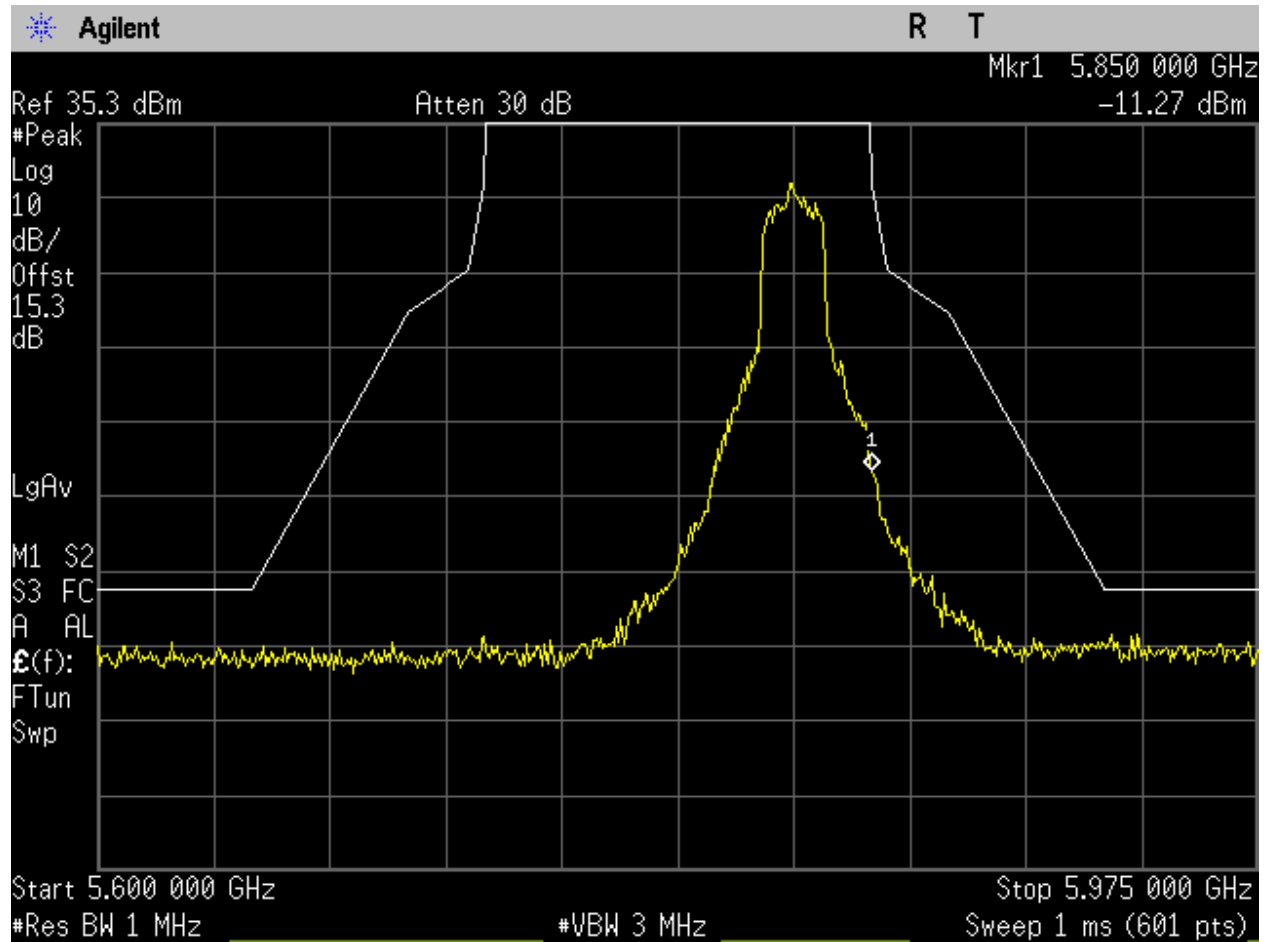


Figure 436: U-NII-3_5825MHz_High Ch_165_20MHz BW_ax-mode_Upper Emission Mask_Port 2.

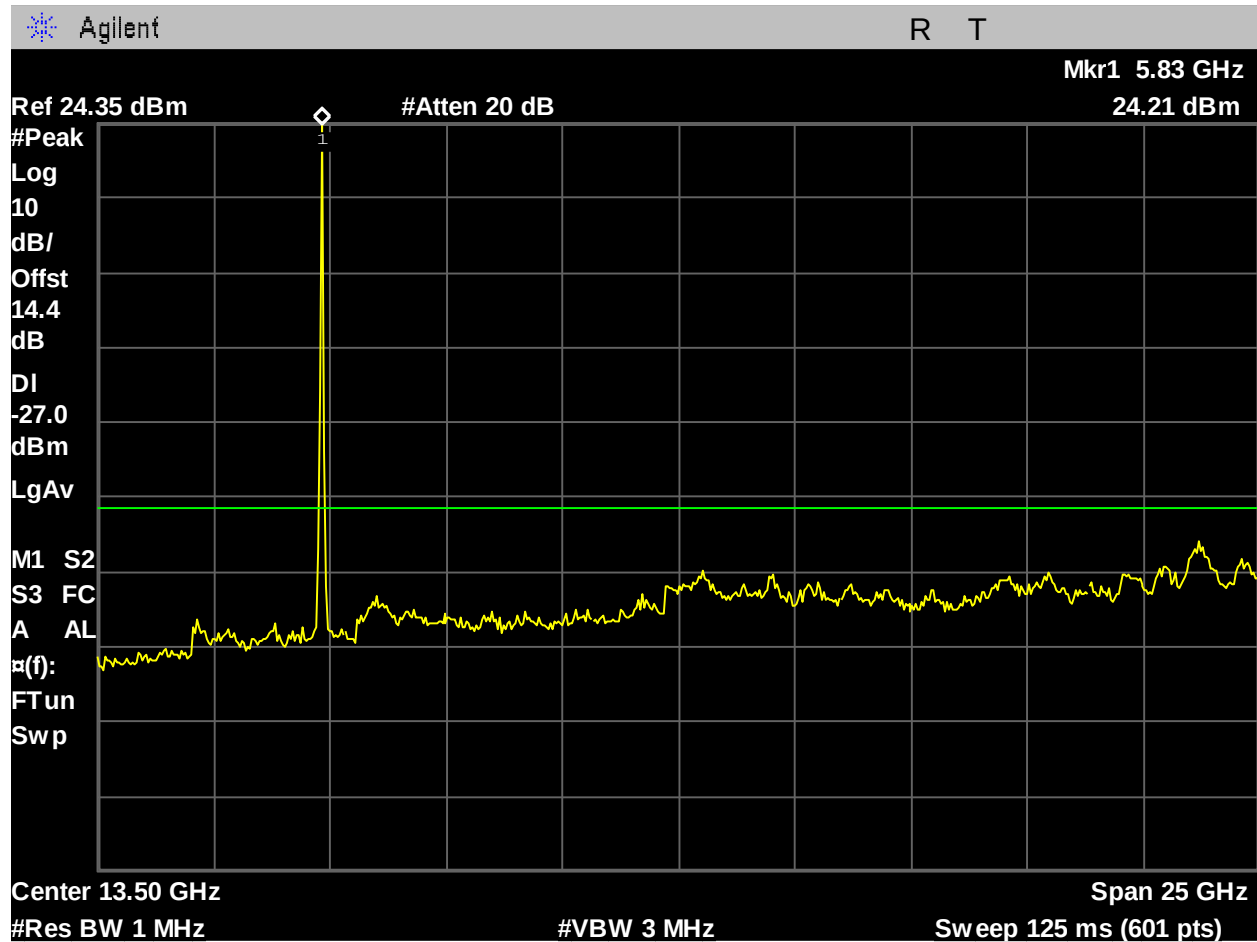


Figure 437: U-NII-3_5825MHz_High Ch_165_20MHz BW_n-mode_-27dBm_1-26GHz_Port 1.

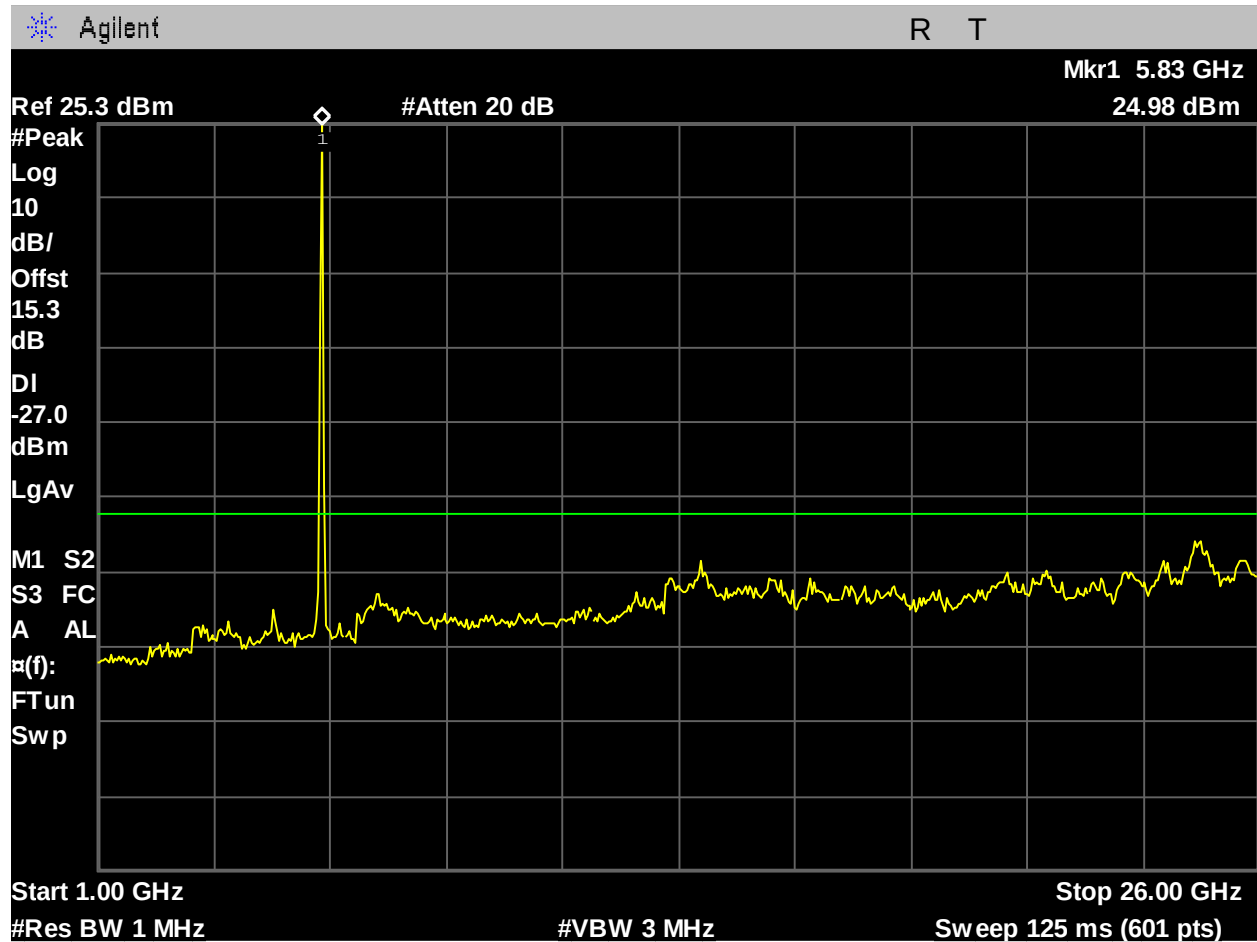


Figure 438: U-NII-3_5825MHz_High Ch_165_20MHz BW_n-mode_-27dBm_1-26GHz_Port 2.

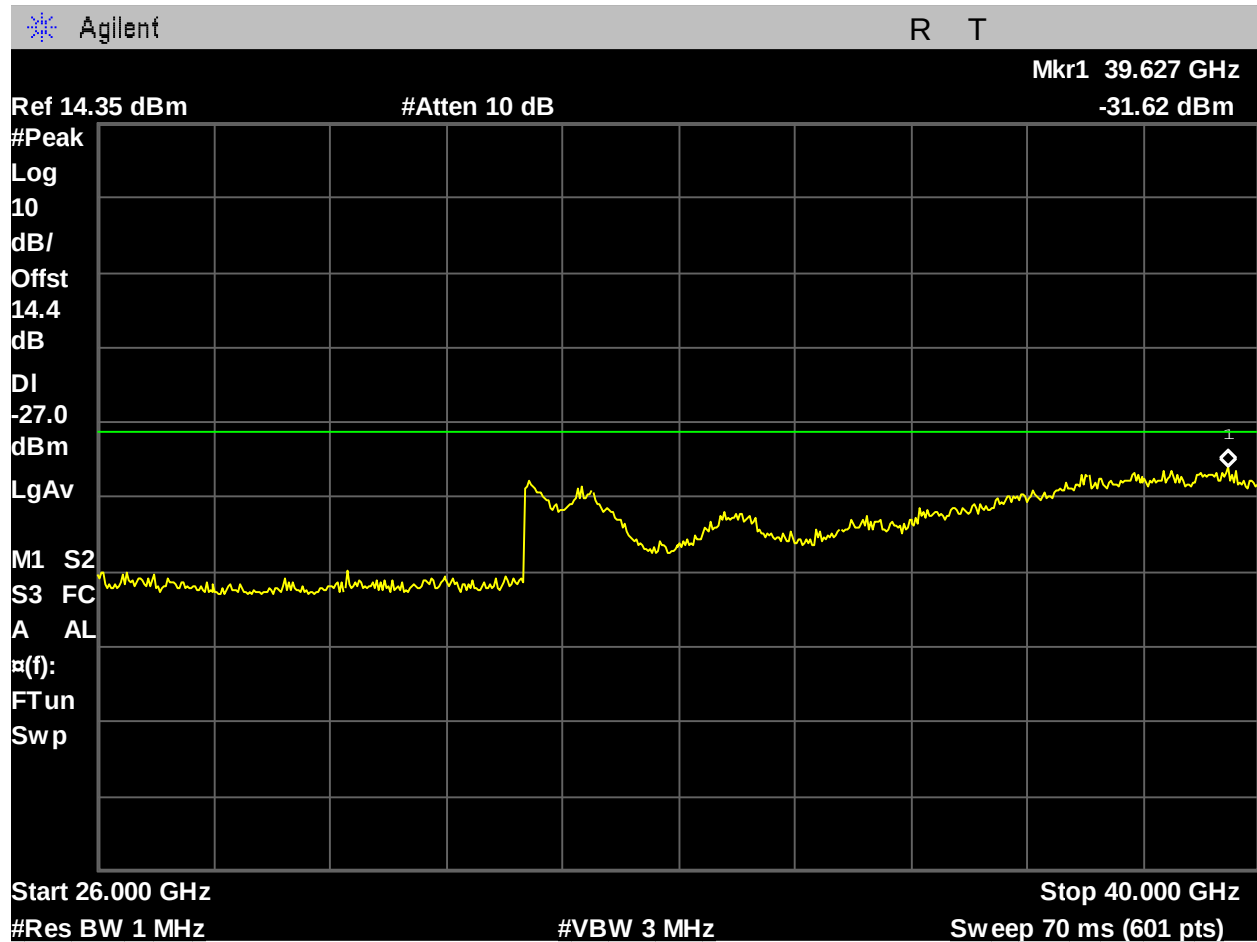


Figure 439: U-NII-3_5825MHz_High Ch_165_20MHz BW_n-mode_-27dBm_26-40GHz_Port 1.

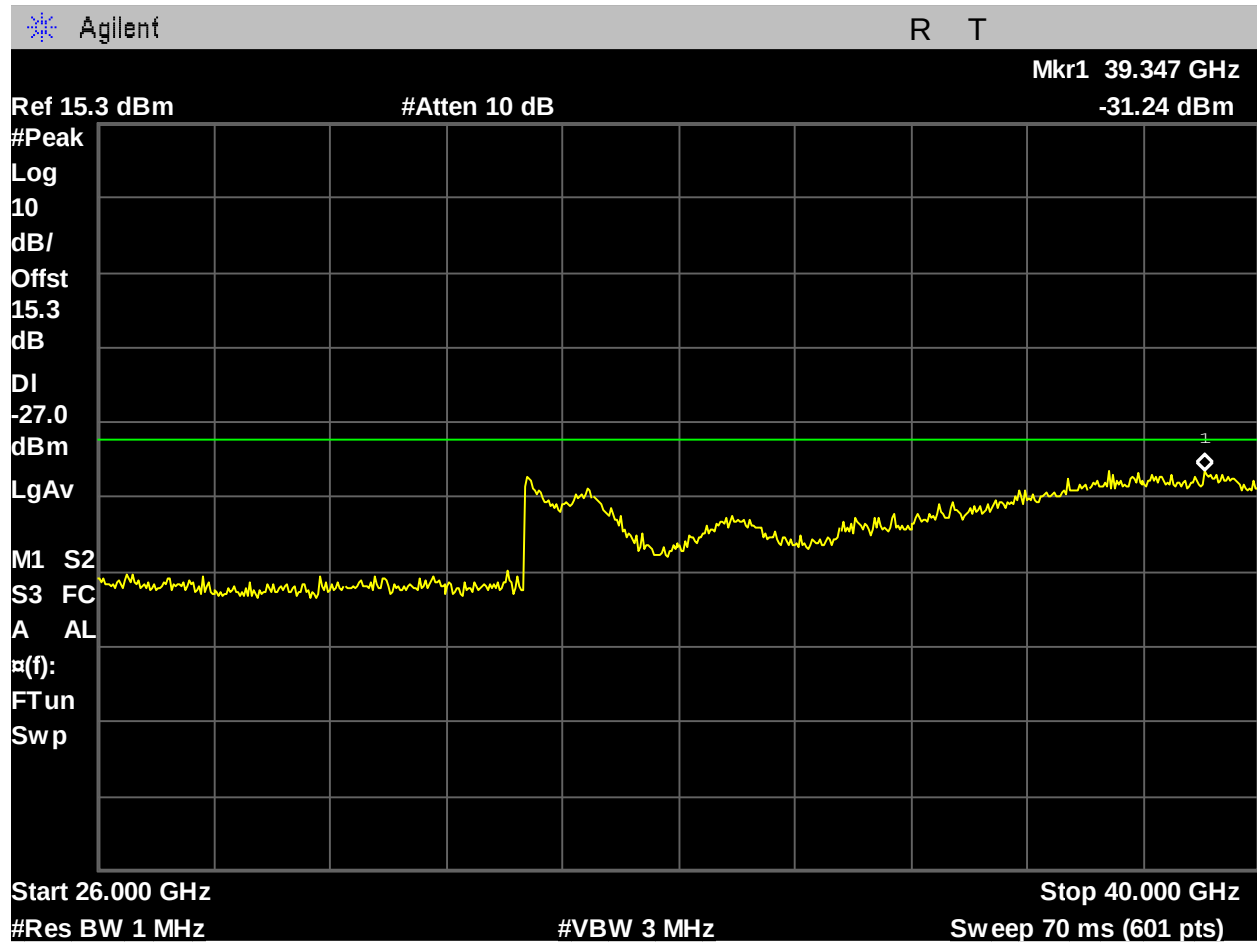


Figure 440: U-NII-3_5825MHz_High Ch_165_20MHz BW_n-mode_-27dBm_26-40GHz_Port 2.

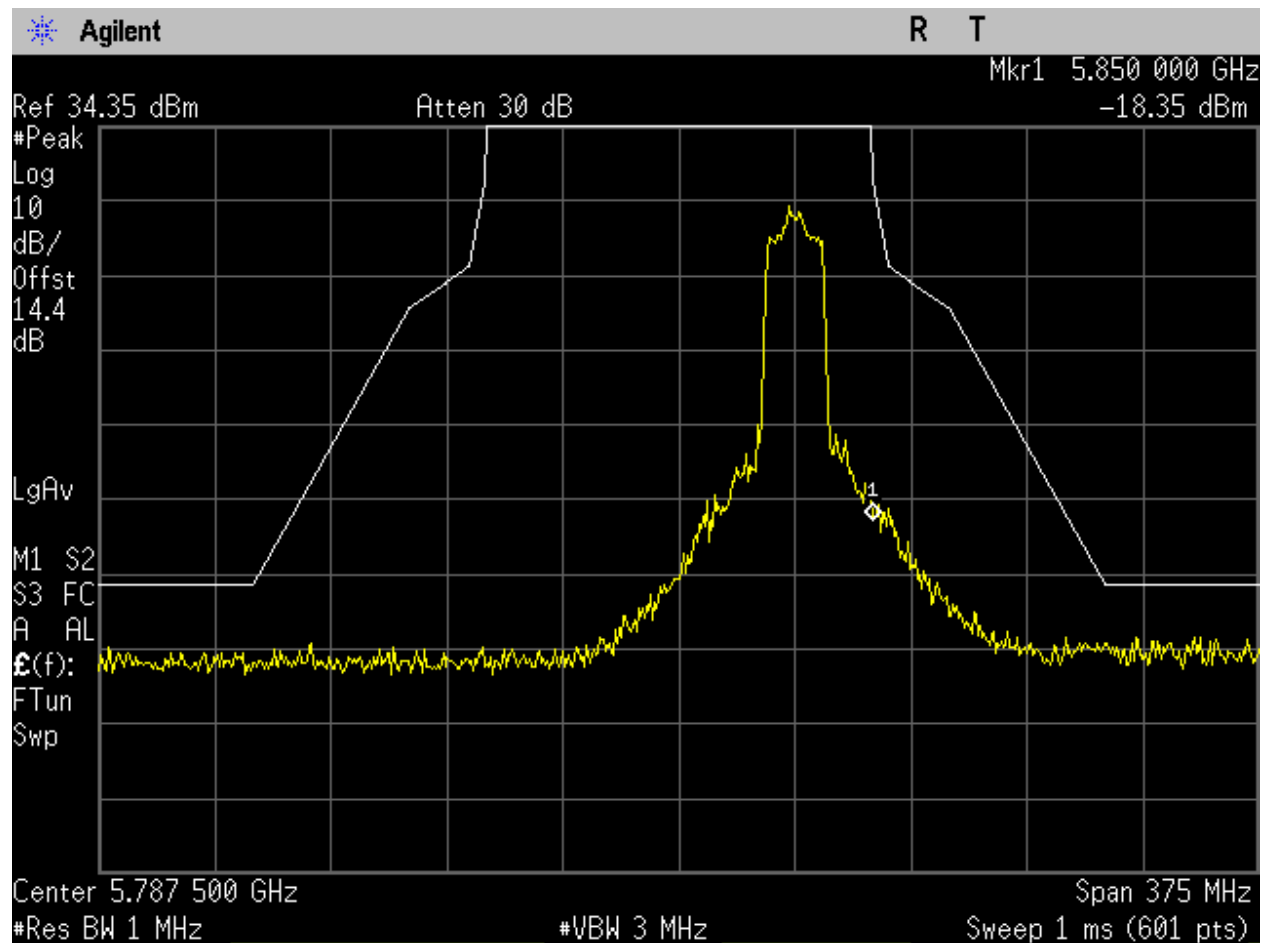


Figure 441: U-NII-3_5825MHz_High Ch_165_20MHz BW_n-mode_Upper Emission Mask_Port 1.

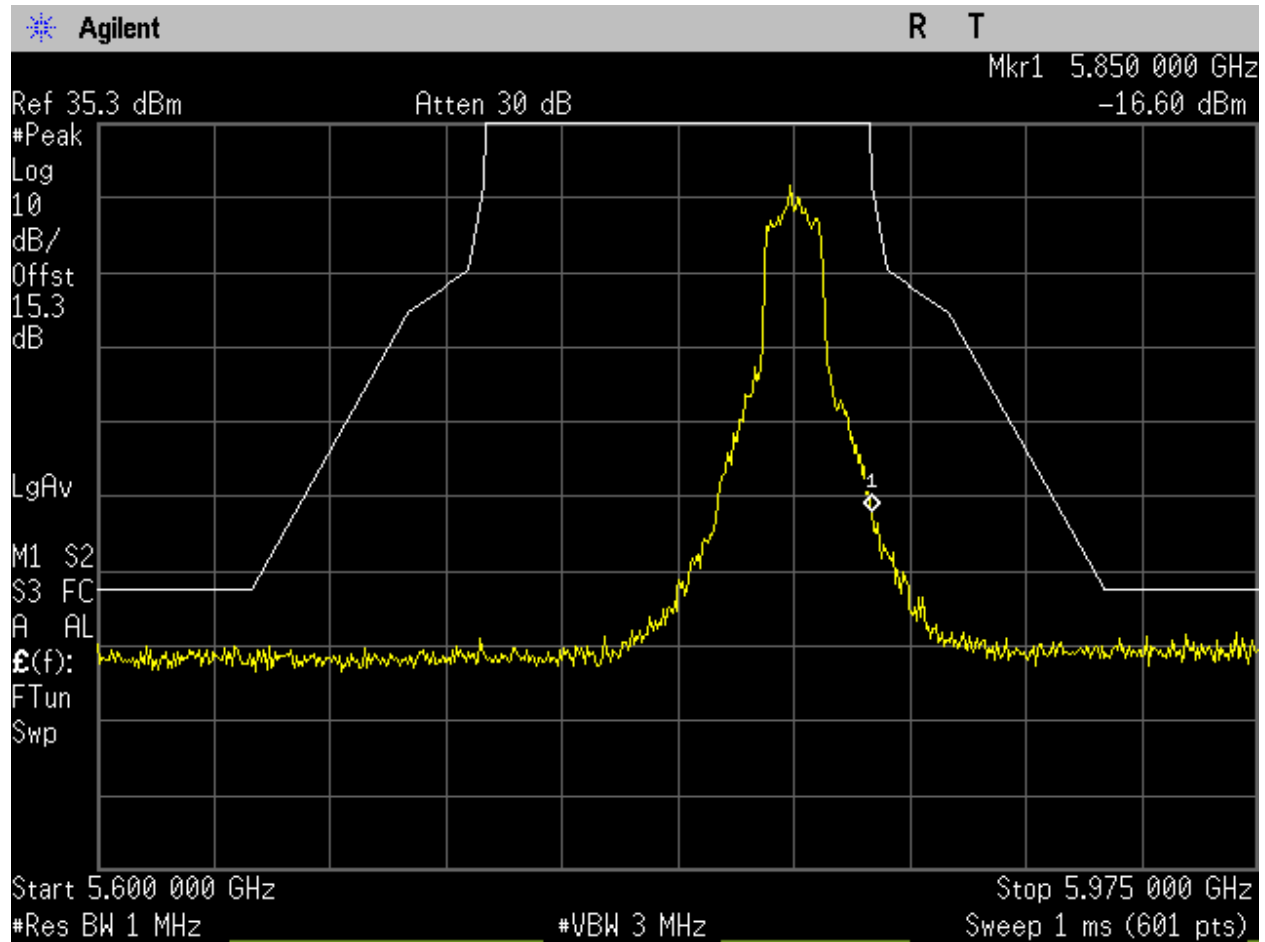


Figure 442: U-NII-3_5825MHz_High Ch_165_20MHz BW_n-mode_Upper Emission Mask_Port 2.

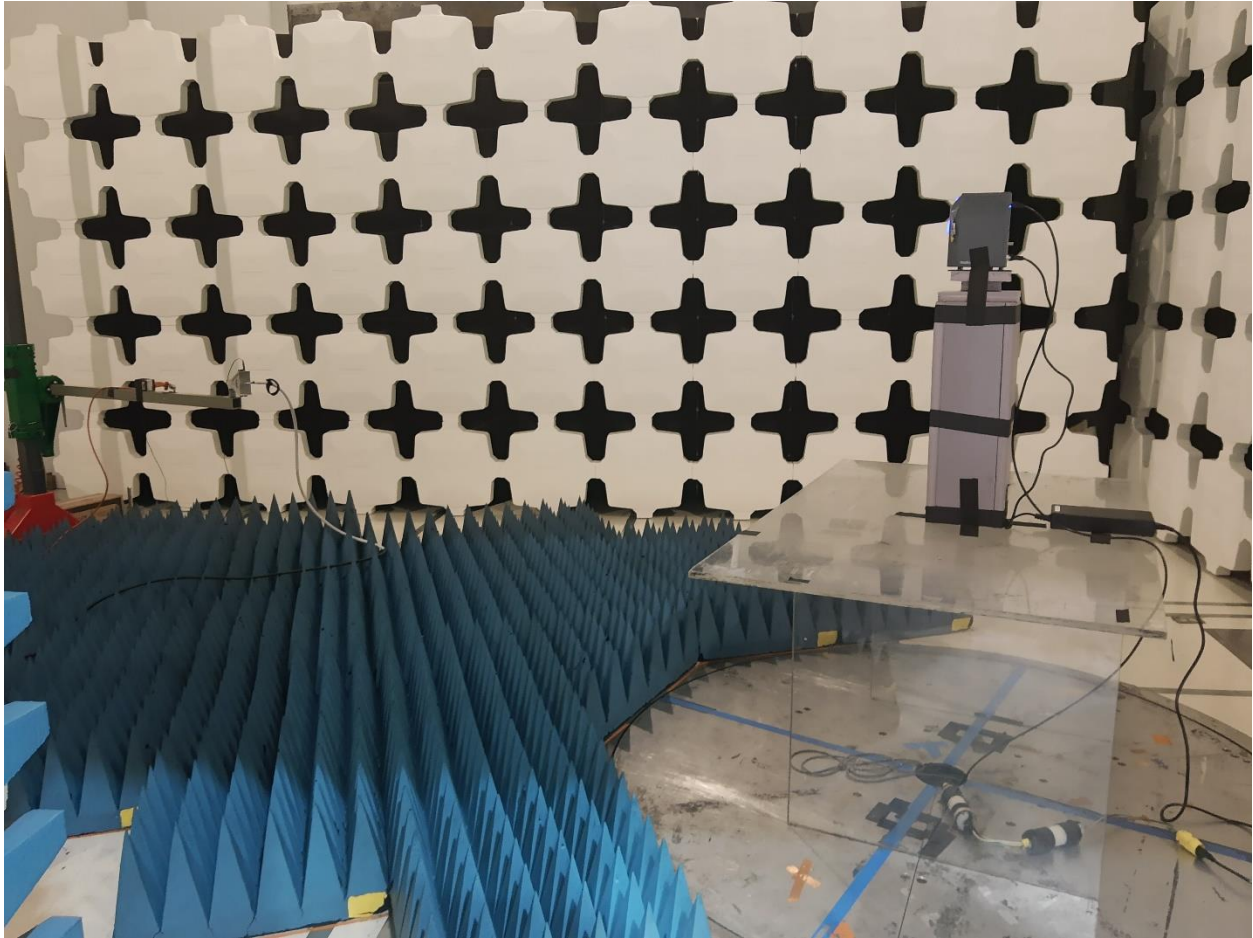


Figure 443: RE Setup [18GHz - 40GHz]

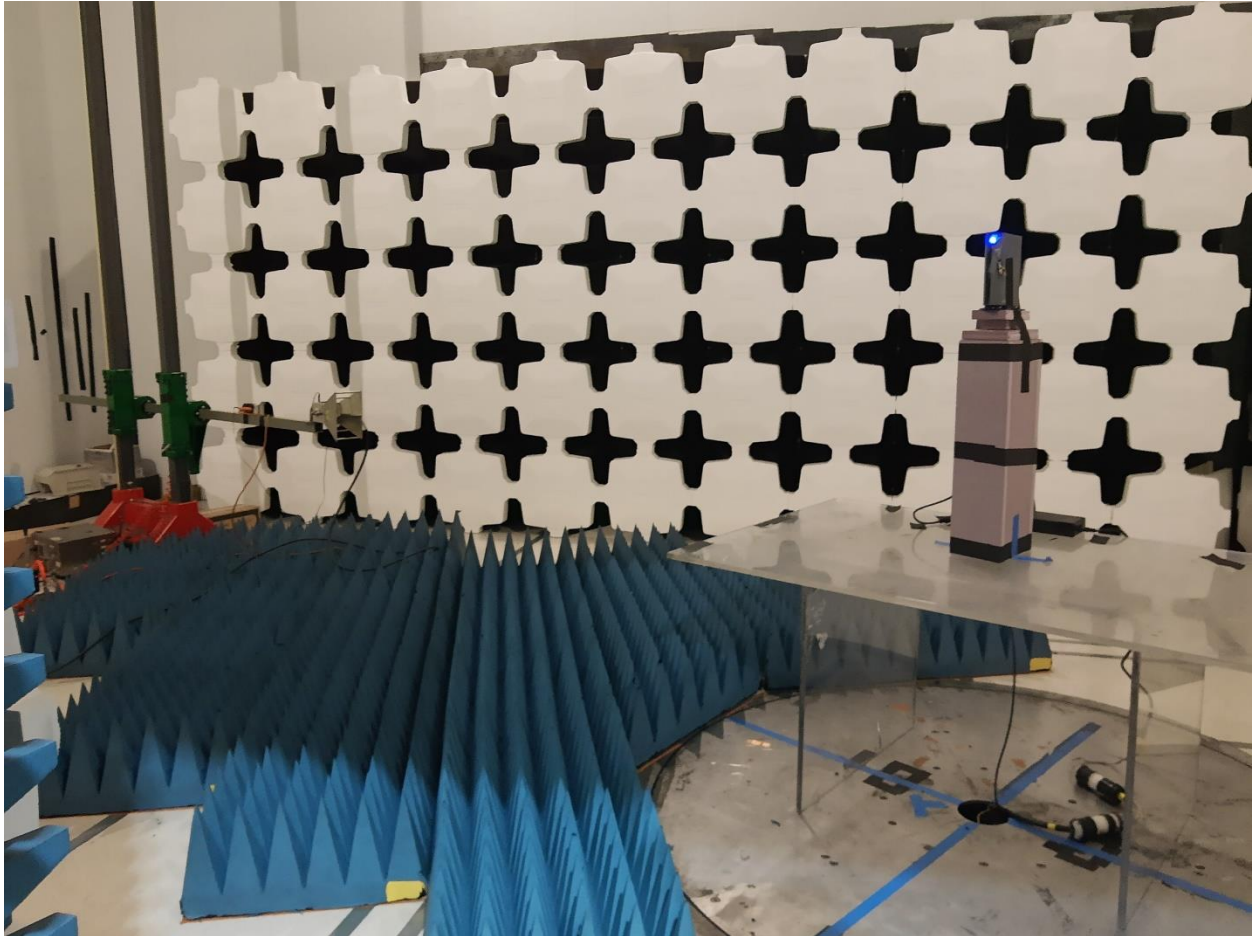


Figure 444: RE Setup [1GHz - 18GHz]

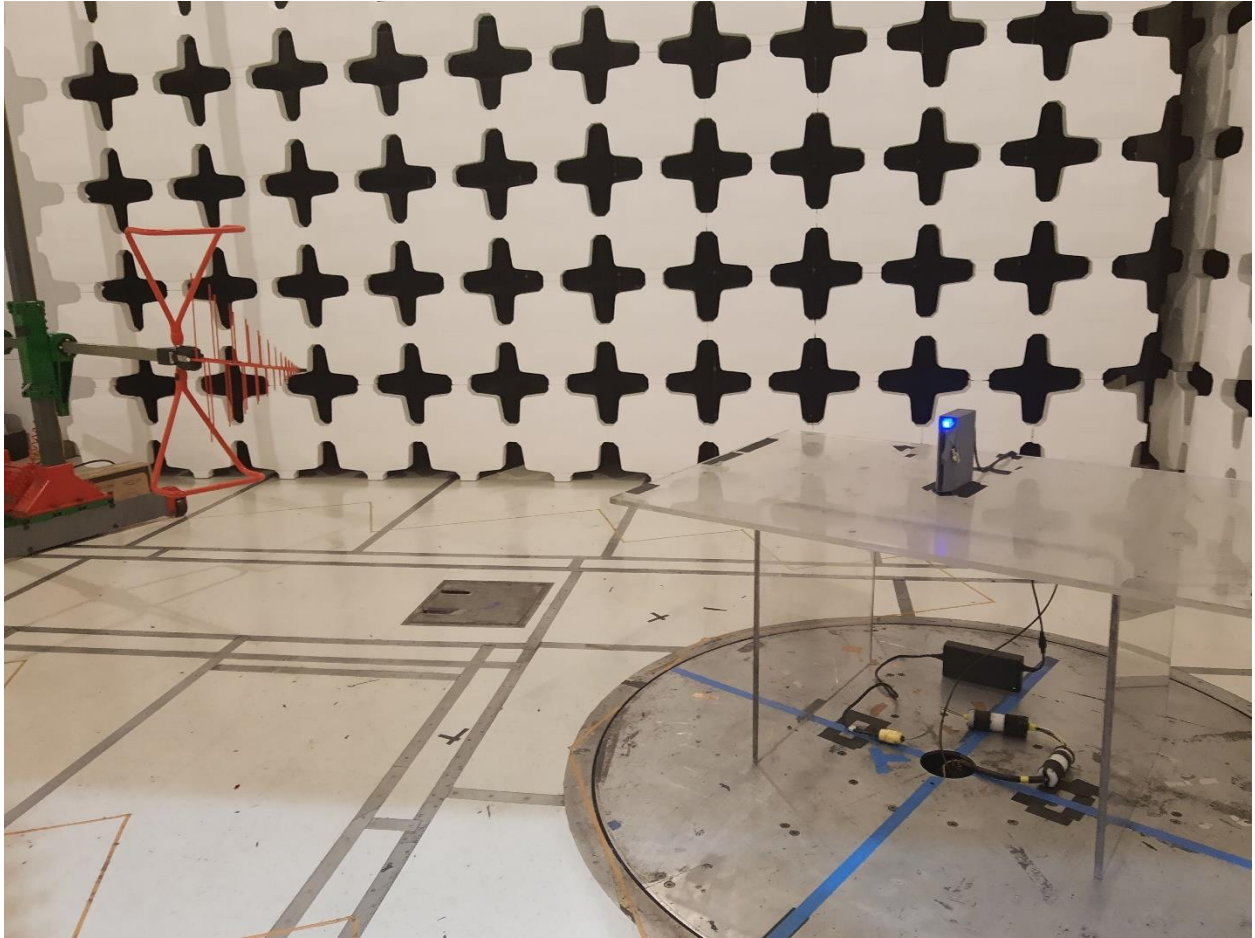


Figure 445: RE Setup [30MHz - 1GHz]

§ 15.407(b)(6) Conducted Emissions

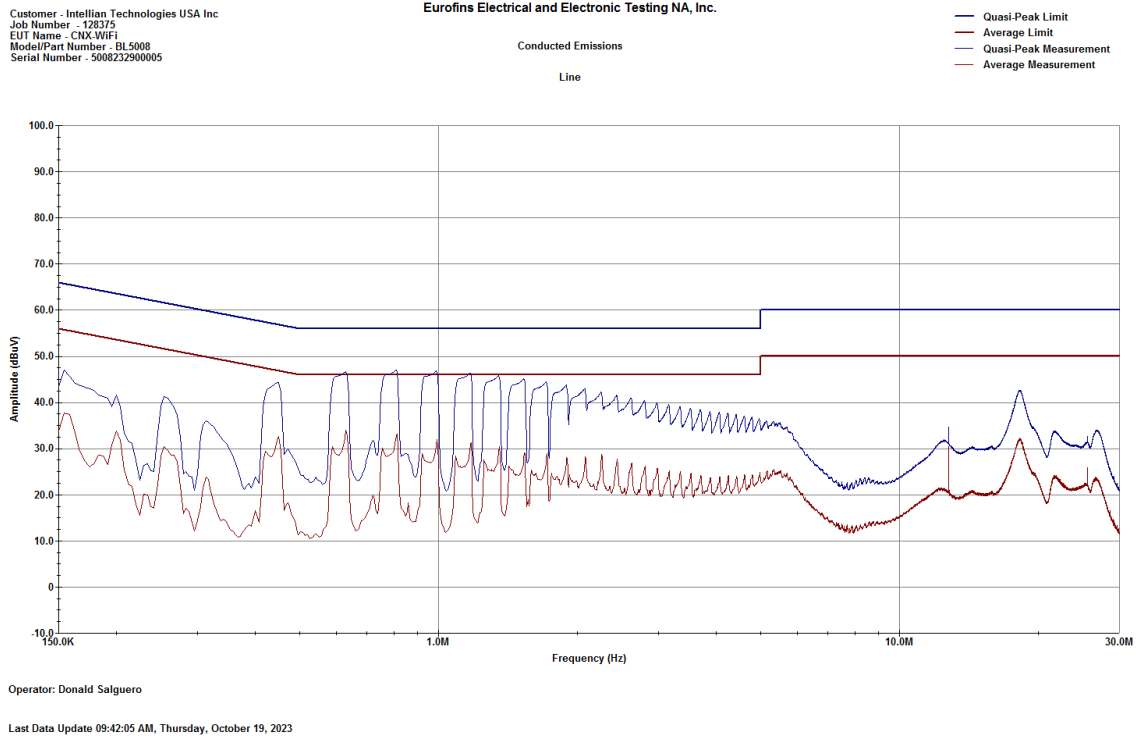


Figure 446: CEV Prescan [Line, 120V60Hz].png

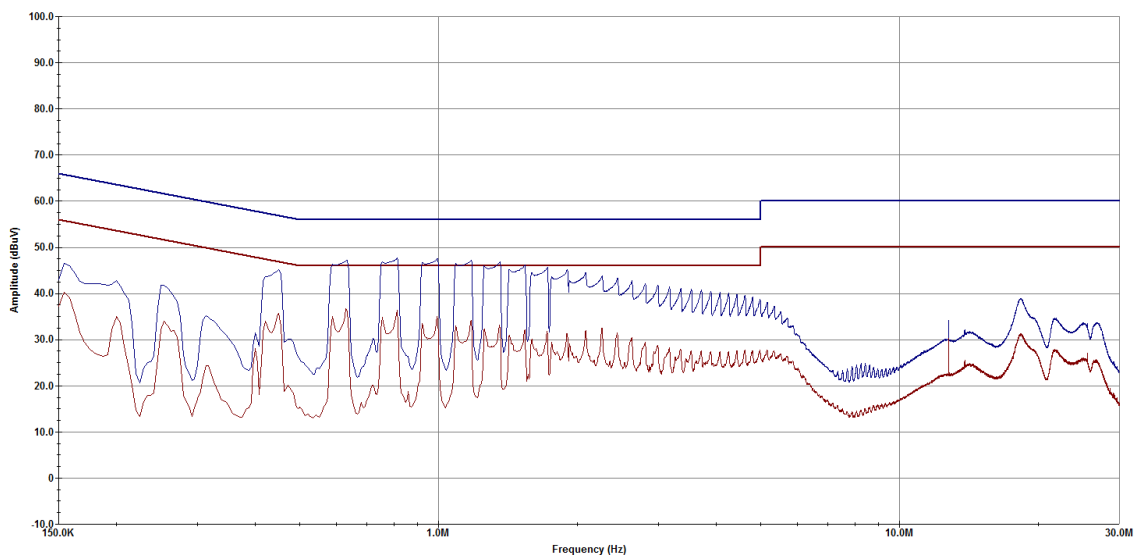
Customer - Intellian Technologies USA Inc
Job Number - 128375
EUT Name - CNX-WiFi
Model/Part Number - BL5008
Serial Number - 5008232900005

Eurofins Electrical and Electronic Testing NA, Inc.

Conducted Emissions

Neutral

— Quasi-Peak Limit
— Average Limit
— Quasi-Peak Measurement
— Average Measurement



Operator: Donald Salguero

Last Data Update 09:39:36 AM, Thursday, October 19, 2023

Figure 447: CEV Prescan [Neutral, 120V60Hz].png

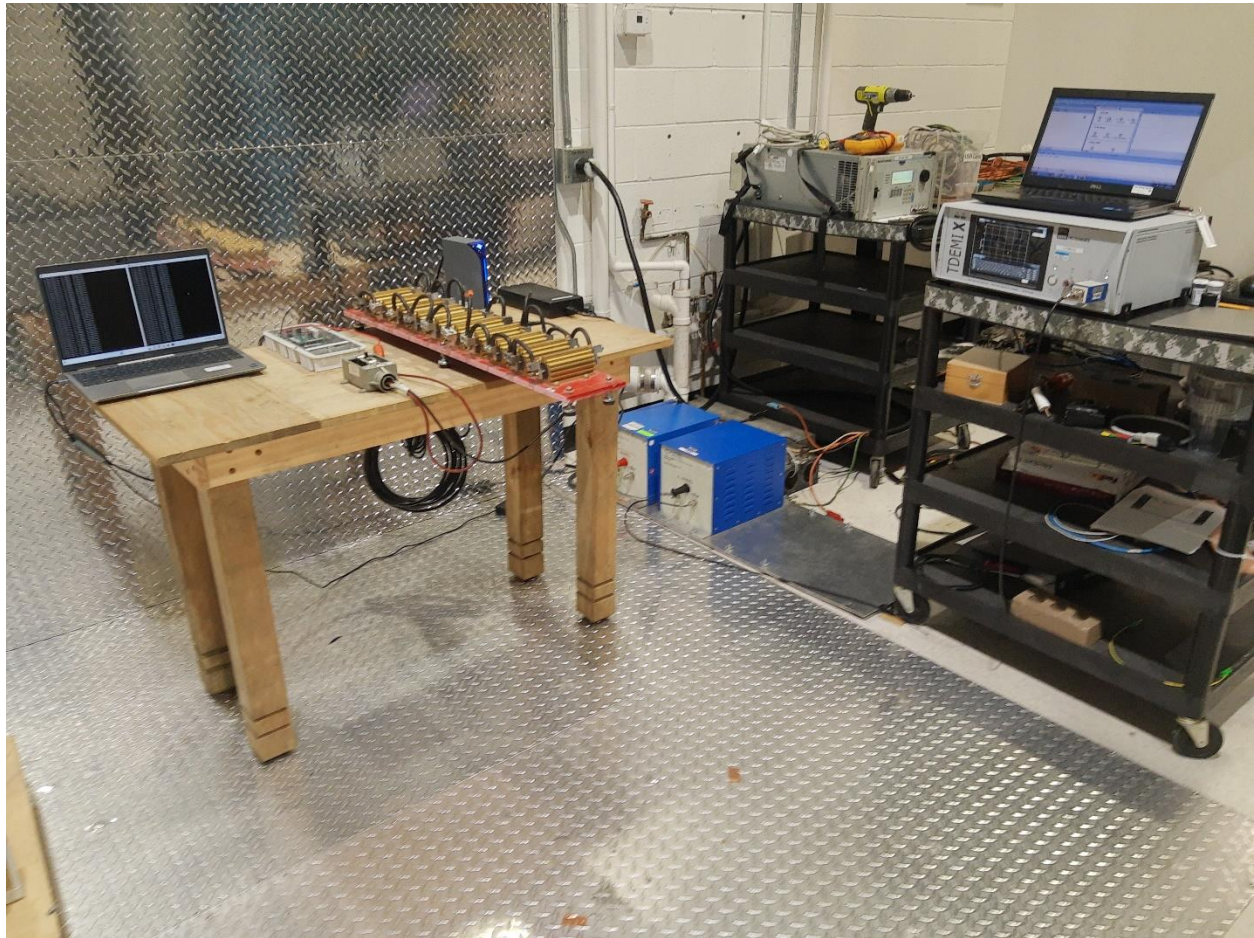


Figure 448: CEV Setup [Front]

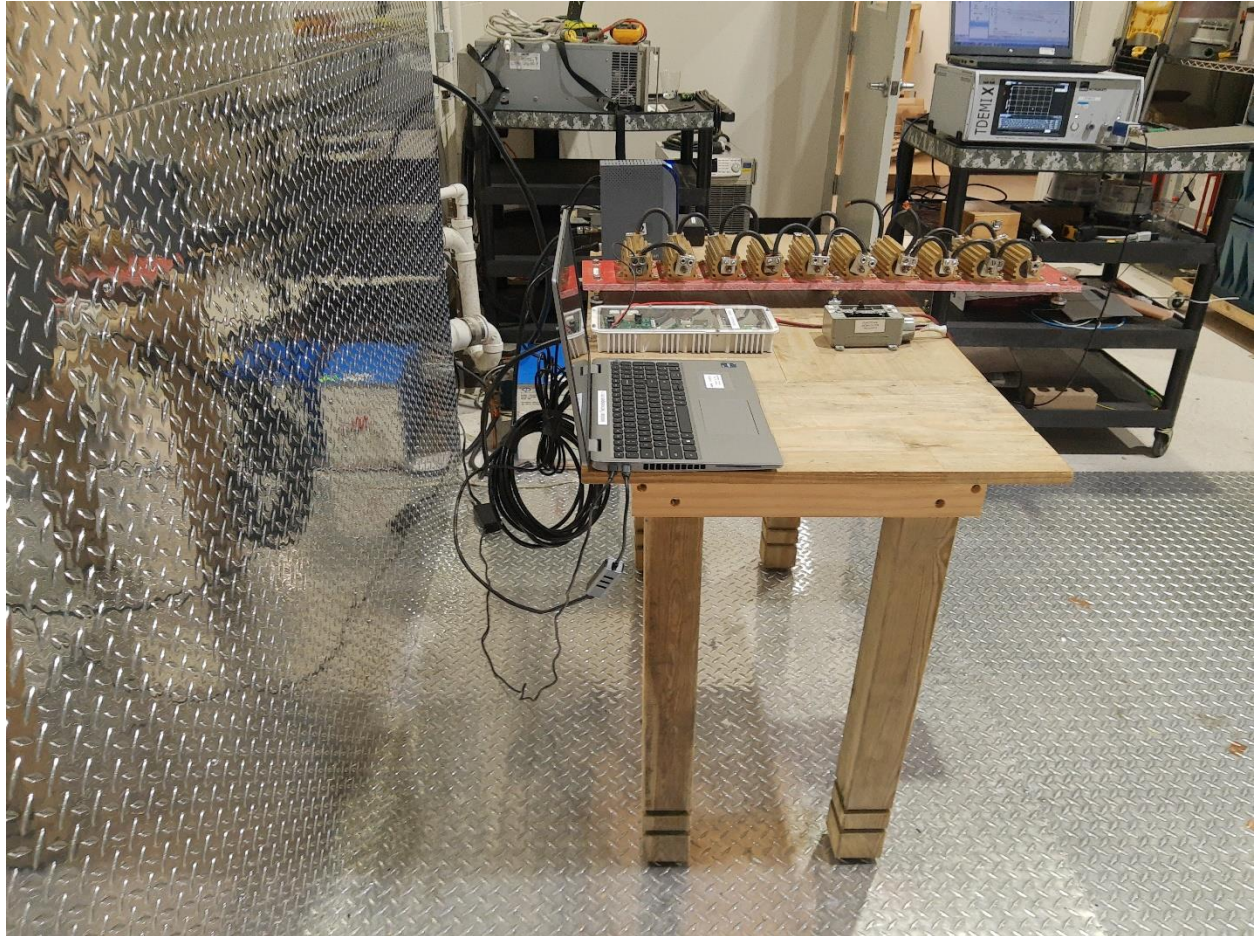


Figure 449: CEV Setup [Rear]

§ 15. 407(e); § 15.403 -6 dB / -26dB Bandwidth

-6dB

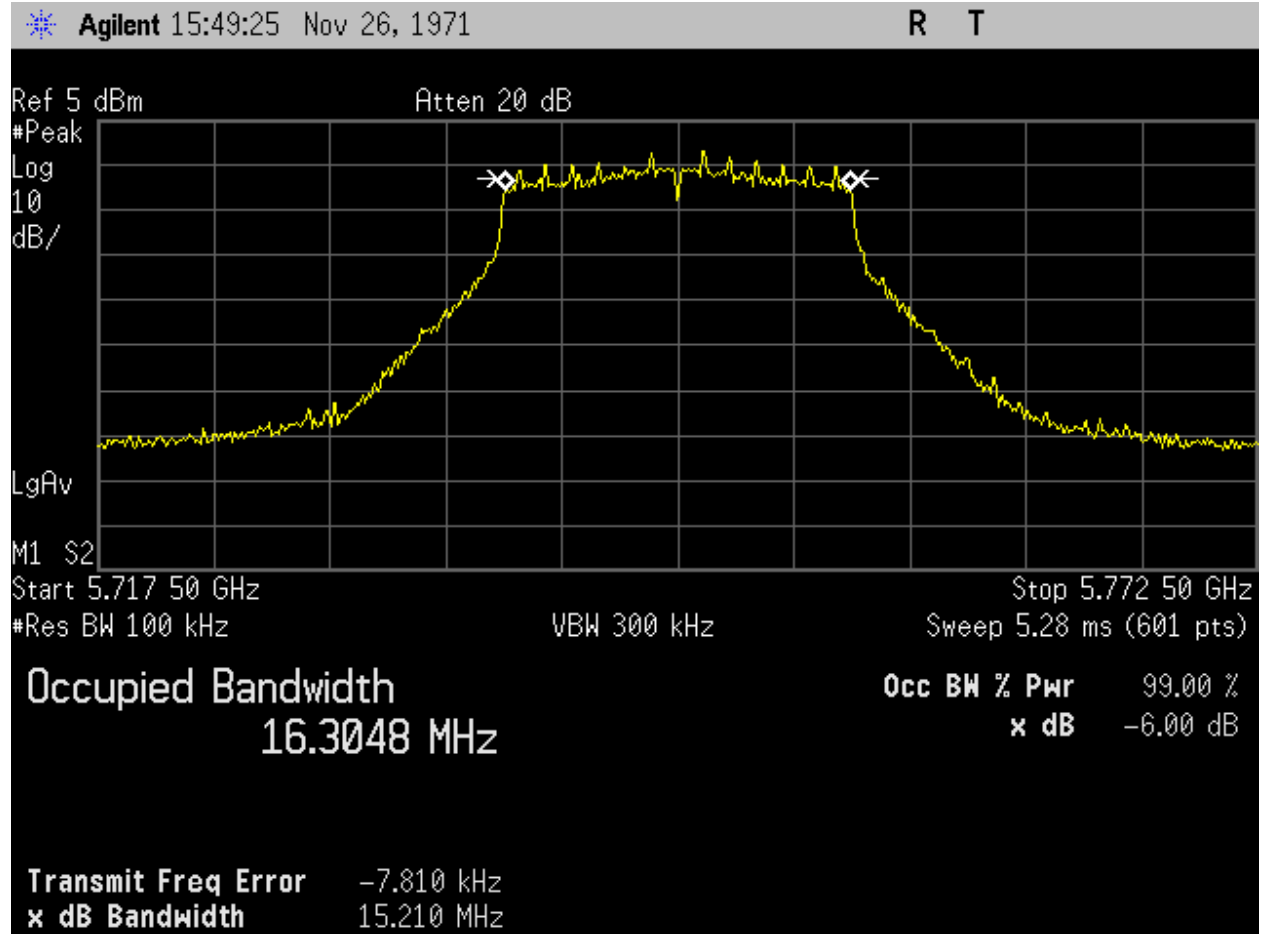


Figure 450: U-NII-3_5745MHz_Low Ch_149_20MHz BW_a-mode_Occupied Bandwidth_-6dB_Port 1.

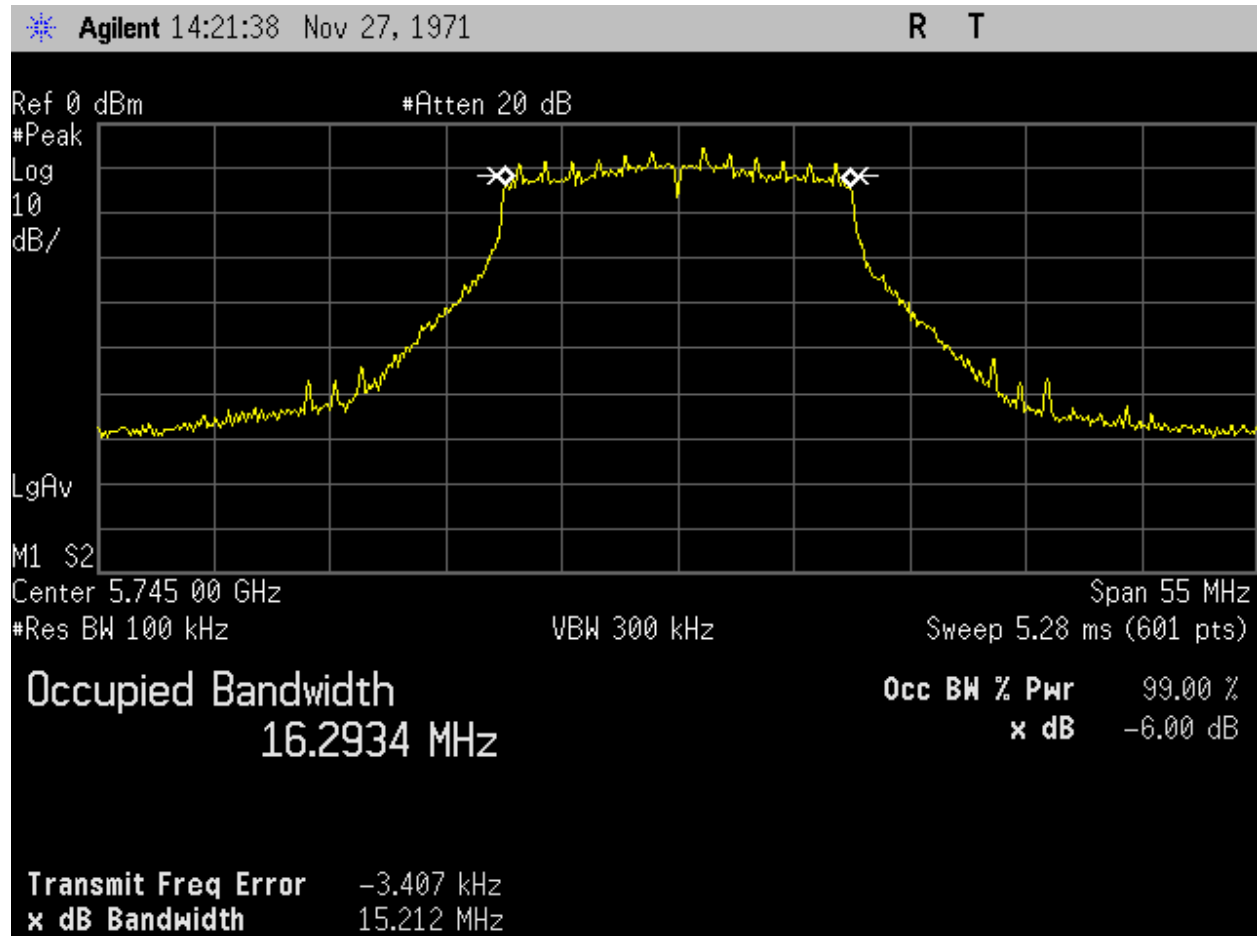


Figure 451: U-NII-3_5745MHz_Low Ch_149_20MHz BW_a-mode_Occupied Bandwidth_-6dB_Port 2.

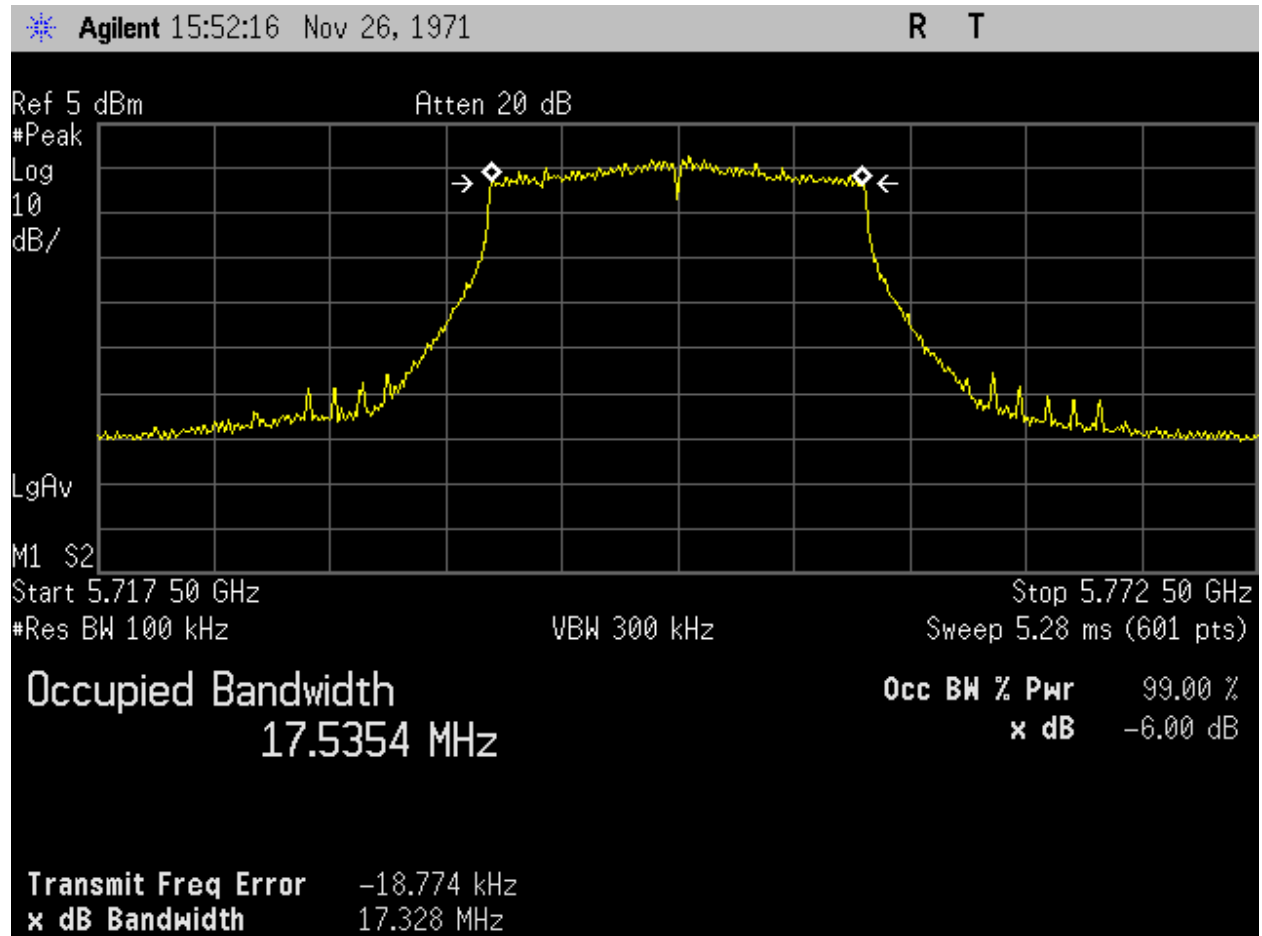


Figure 452: U-NII-3_5745MHz_Low Ch_149_20MHz BW_ac-mode_Occupied Bandwidth_-6dB_Port 1.

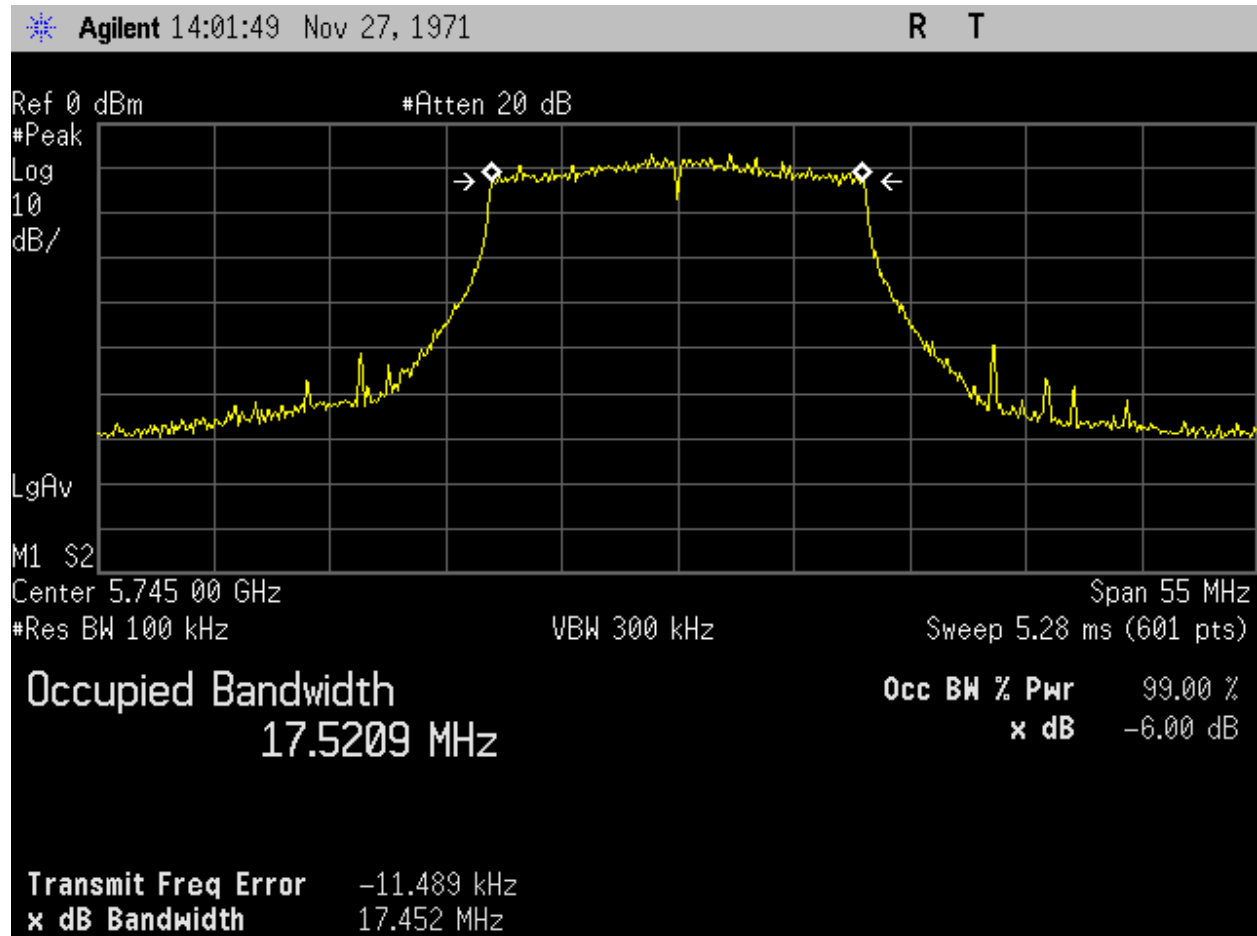


Figure 453: U-NII-3_5745MHz_Low Ch_149_20MHz BW_ac-mode_Occupied Bandwidth_-6dB_Port 2.

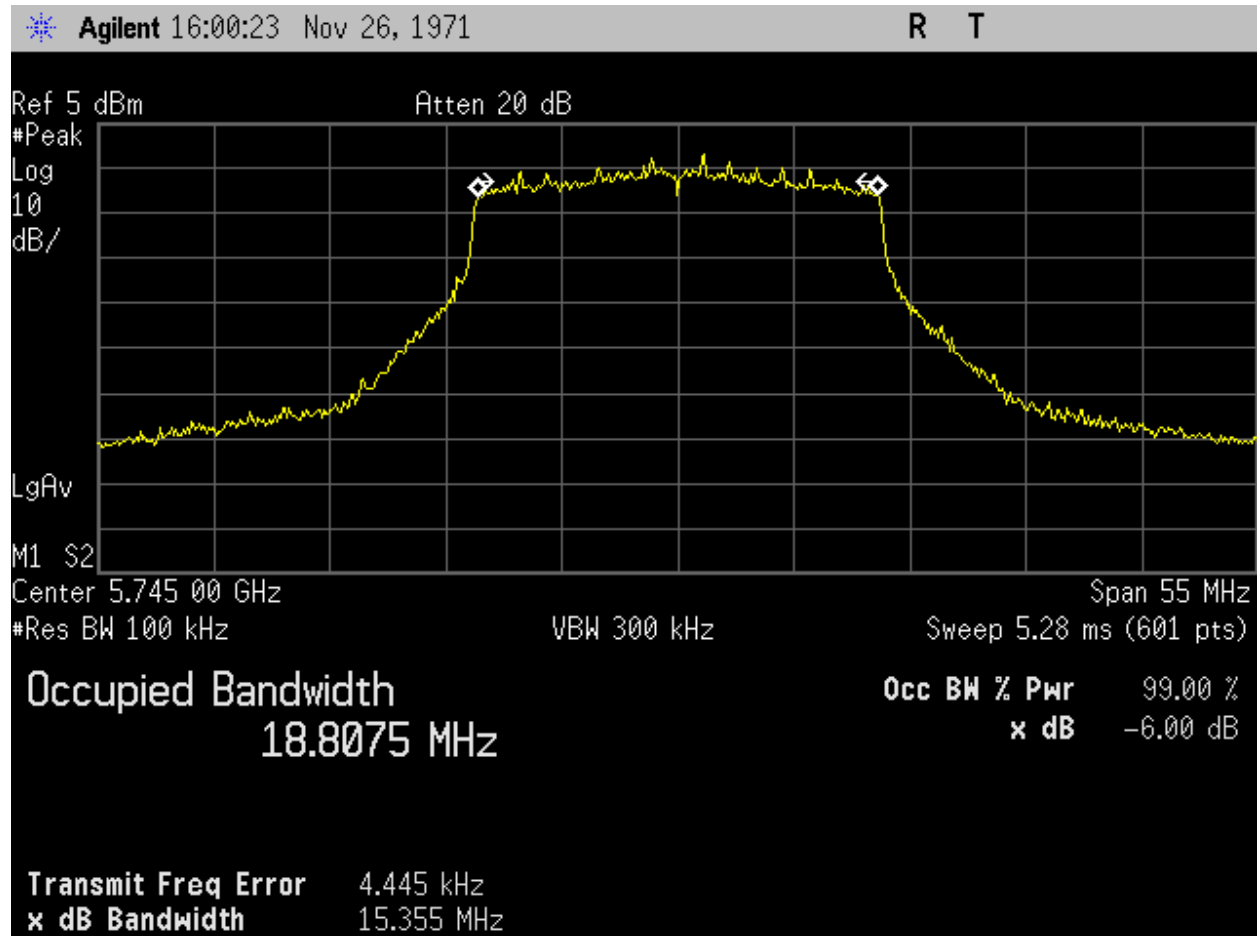


Figure 454: U-NII-3_5745MHz_Low Ch_149_20MHz BW_ax-mode_Occupied Bandwidth_-6dB_Port 1.

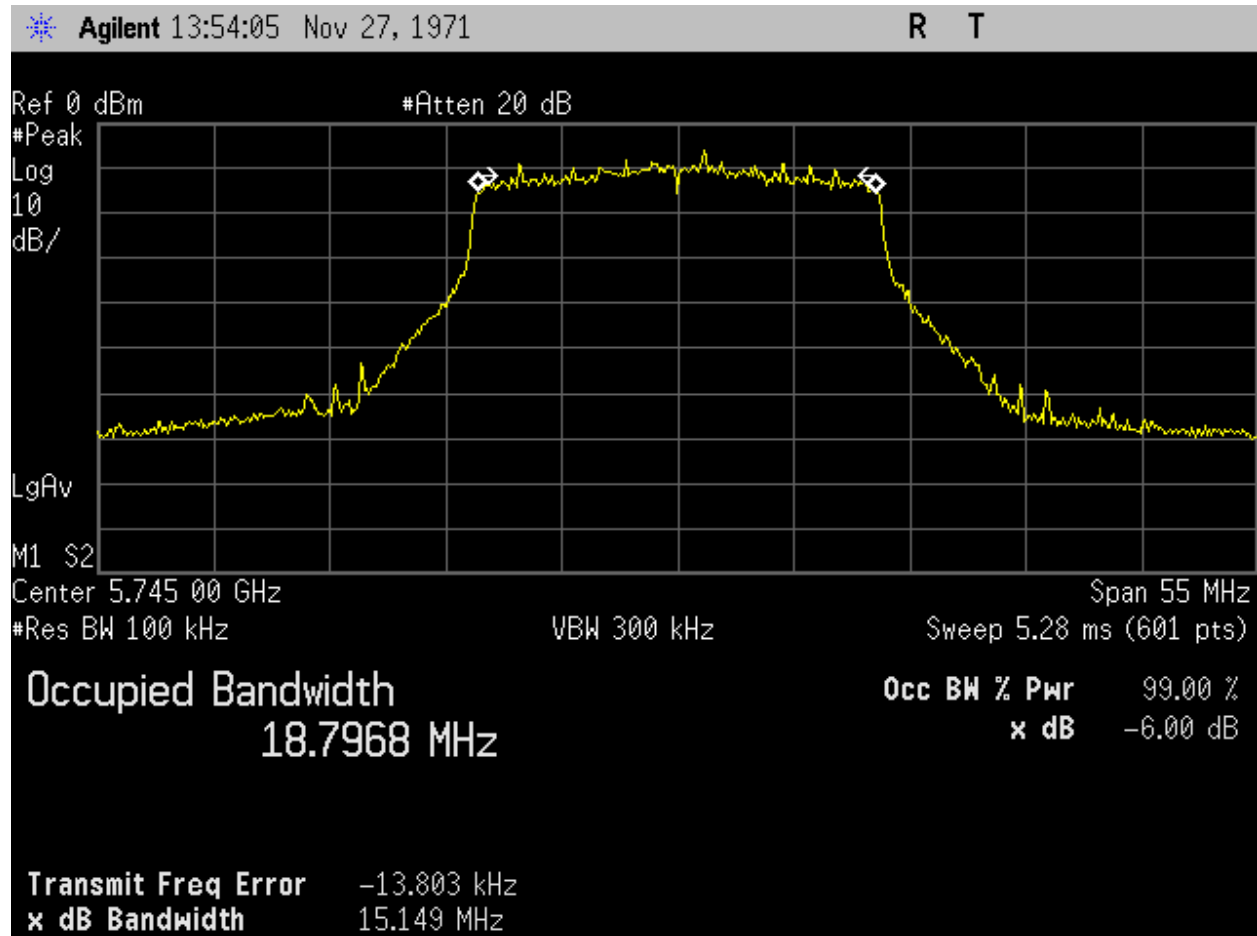


Figure 455: U-NII-3_5745MHz_Low Ch_149_20MHz BW_ax-mode_Occupied Bandwidth_-6dB_Port 2.

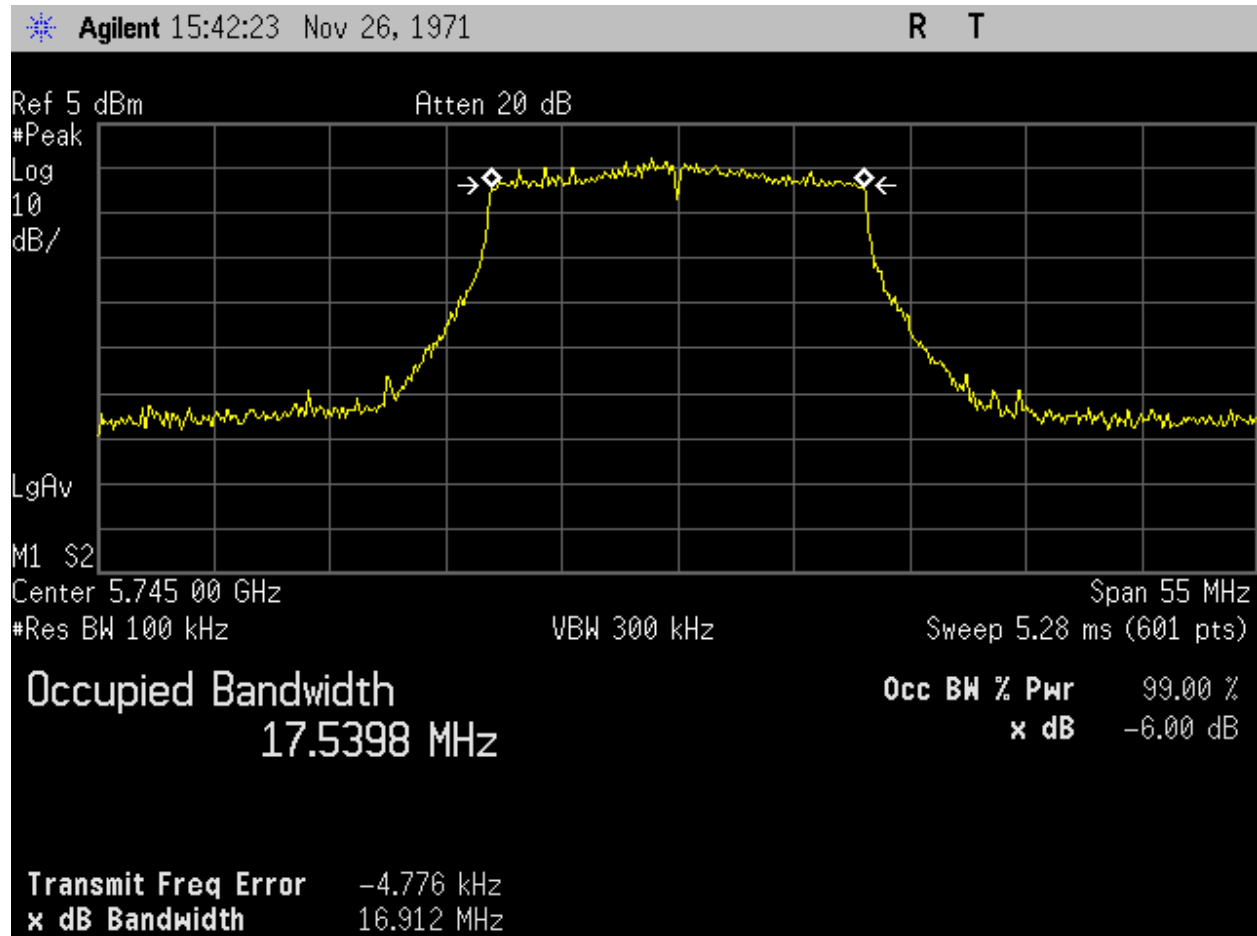


Figure 456: U-NII-3_5745MHz_Low Ch_149_20MHz BW_n-mode_Occupied Bandwidth_-6dB_Port 1.

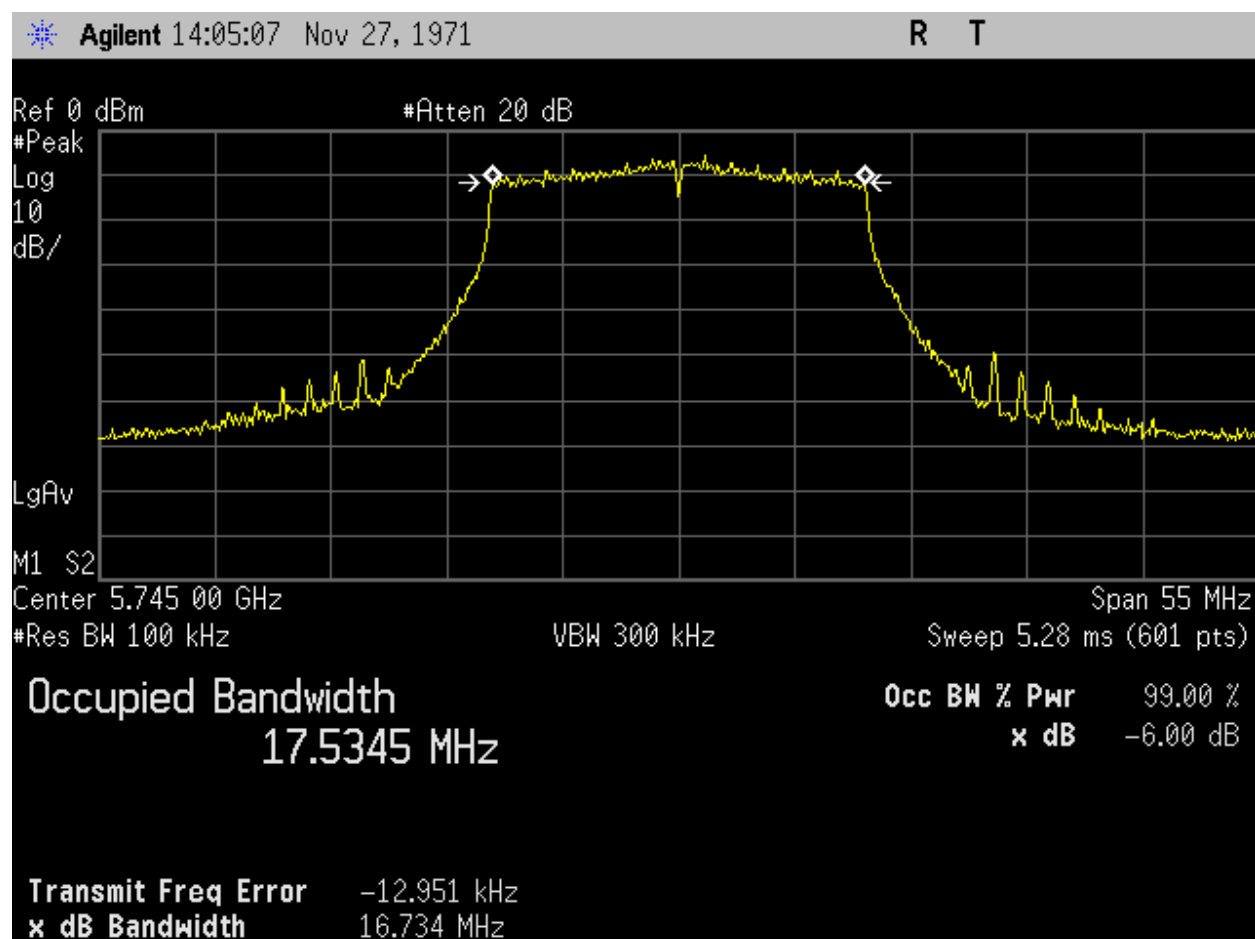


Figure 457: U-NII-3_5745MHz_Low Ch_149_20MHz BW_n-mode_Occupied Bandwidth_-6dB_Port 2.

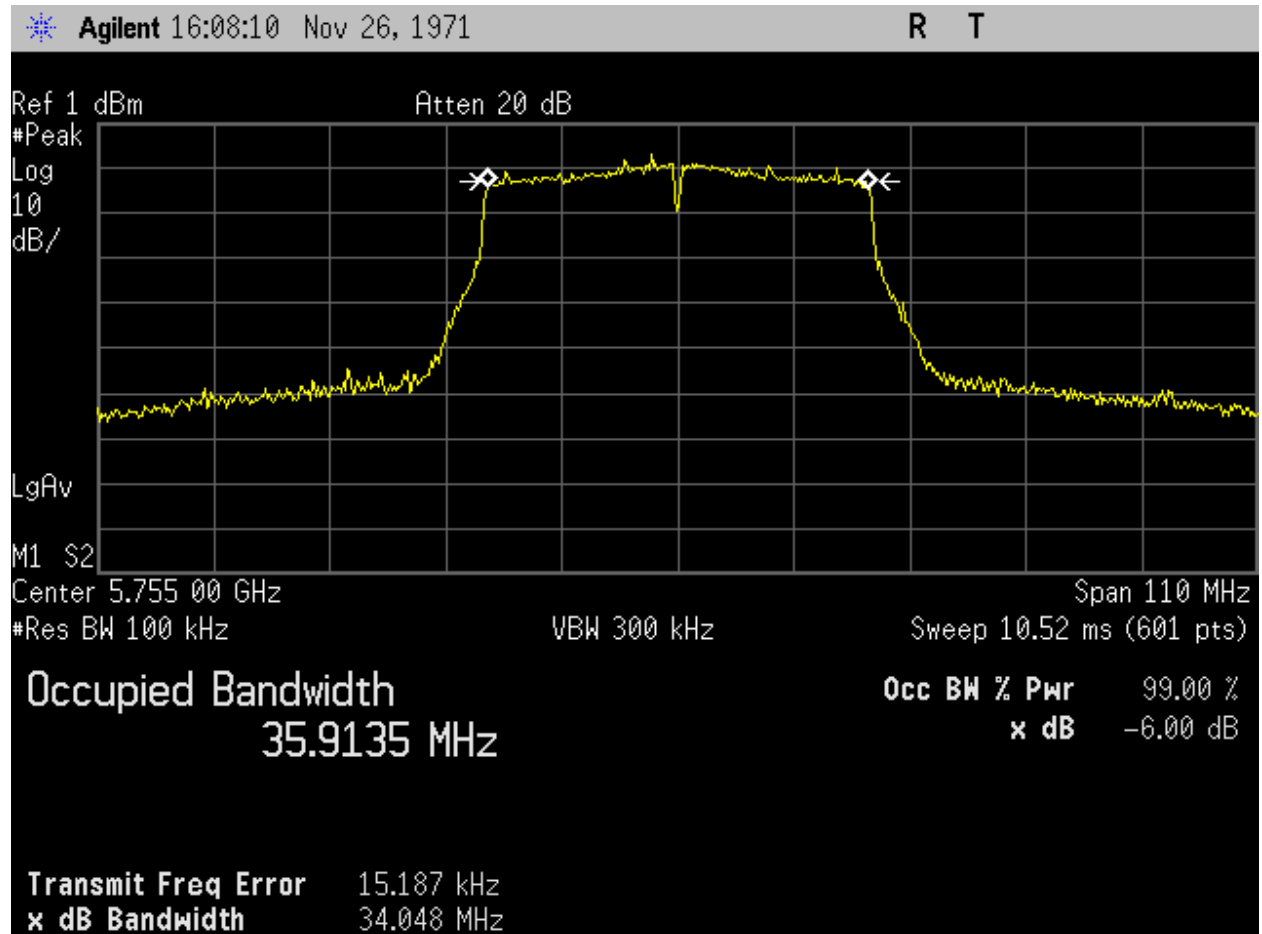


Figure 458: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ac-mode_Occupied Bandwidth_-6dB_Port 1.

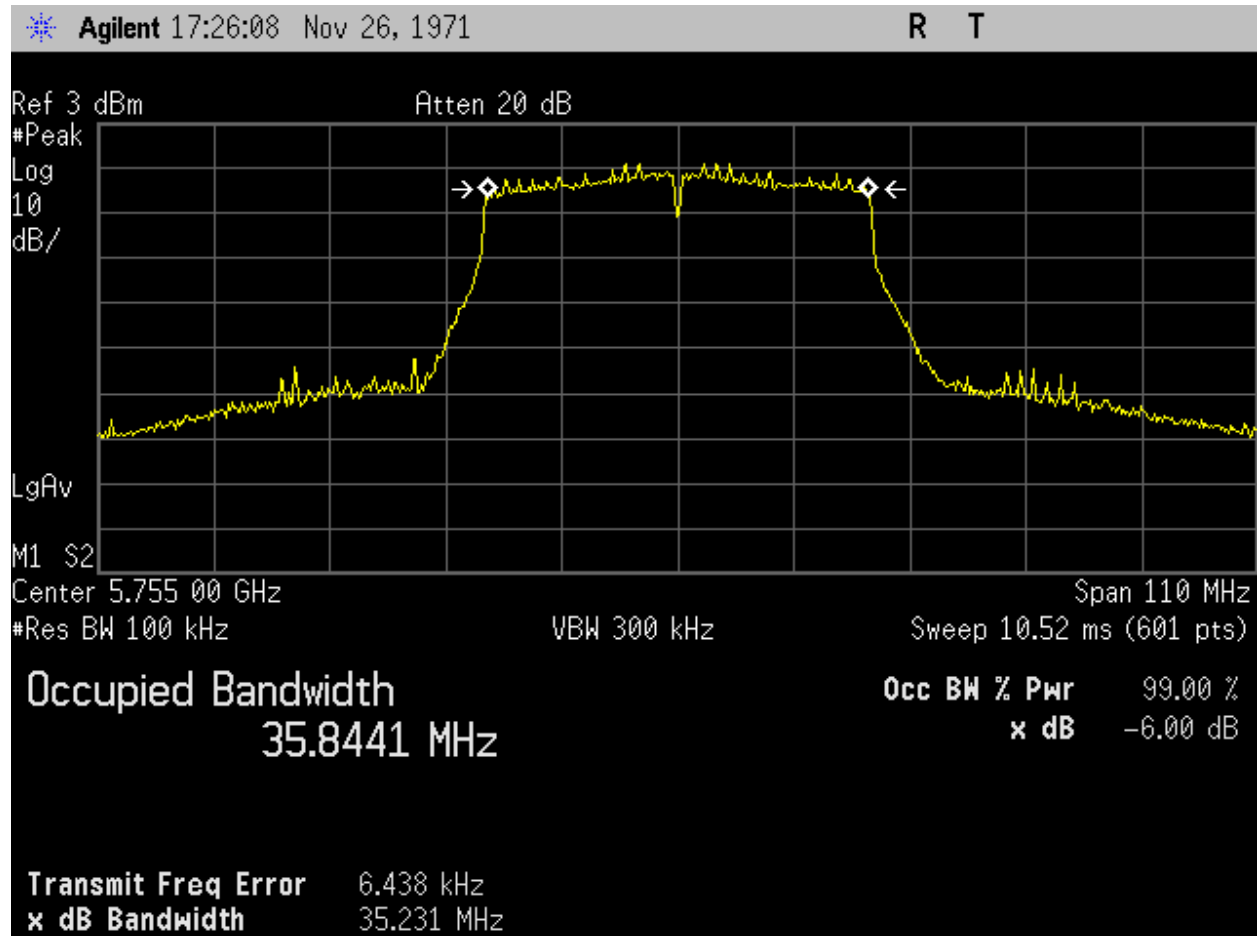


Figure 459: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ac-mode_Occupied Bandwidth_-6dB_Port 2.

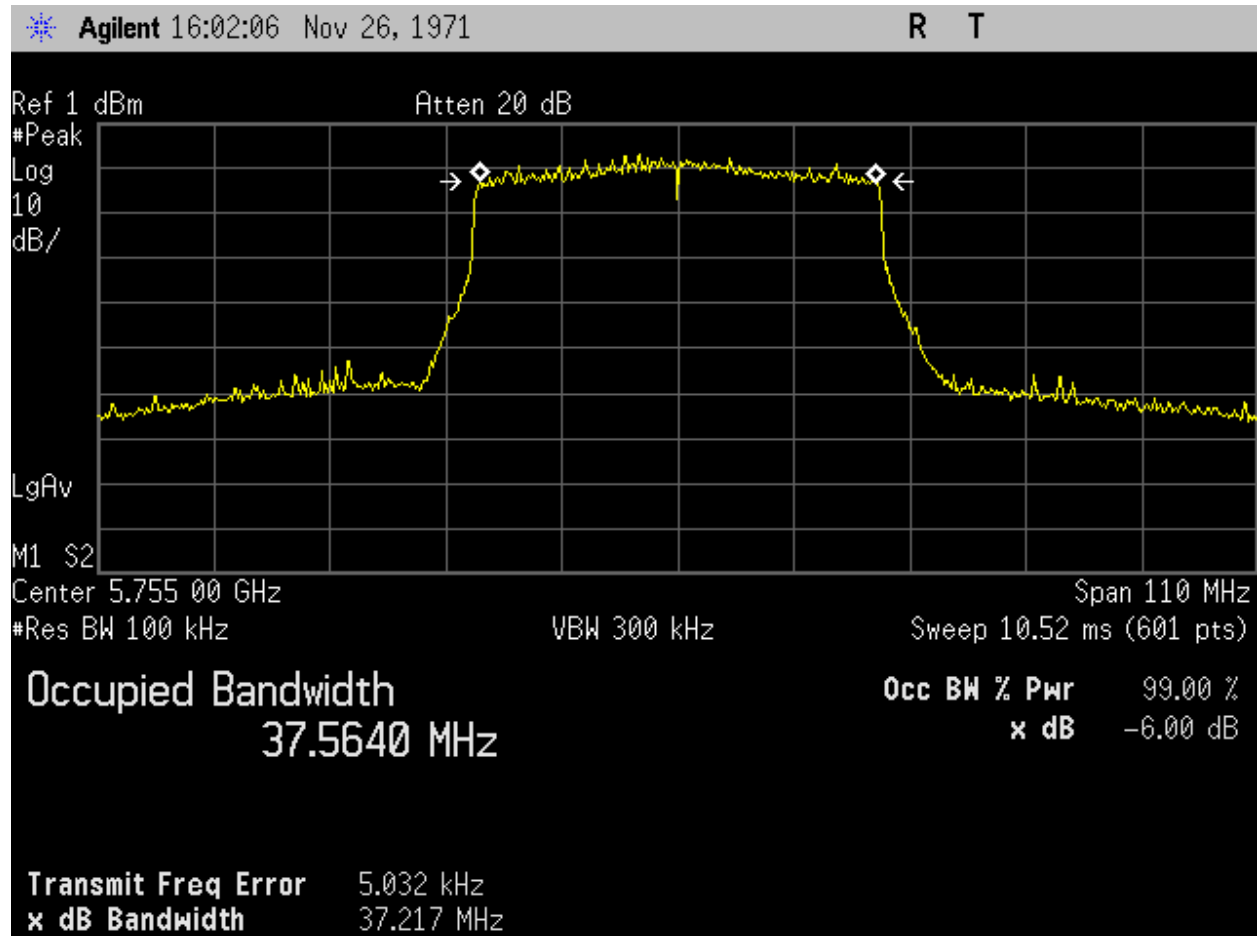


Figure 460: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ax-mode_Occupied Bandwidth_-6dB_Port 1.

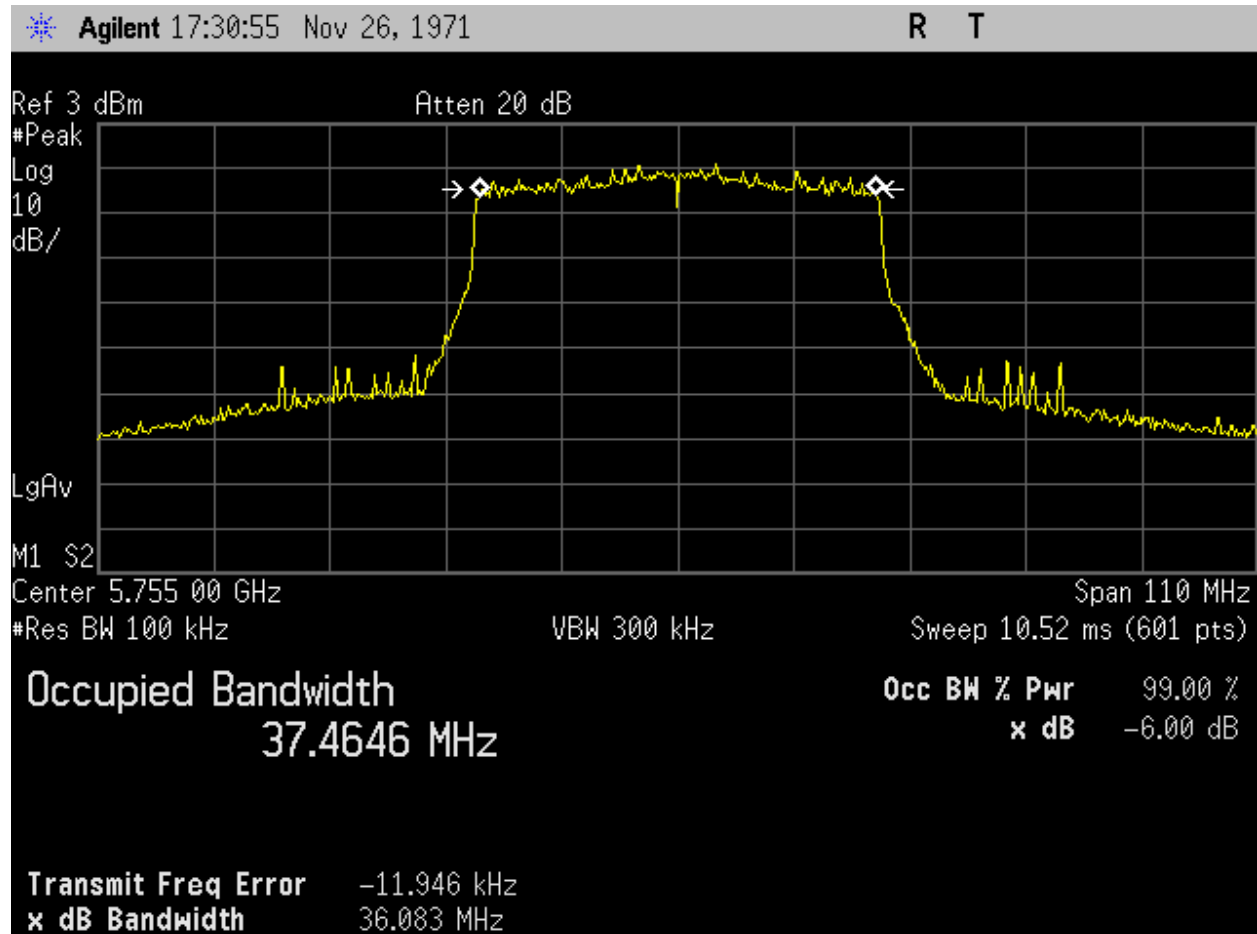


Figure 461: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ax-mode_Occupied Bandwidth_-6dB_Port 2.

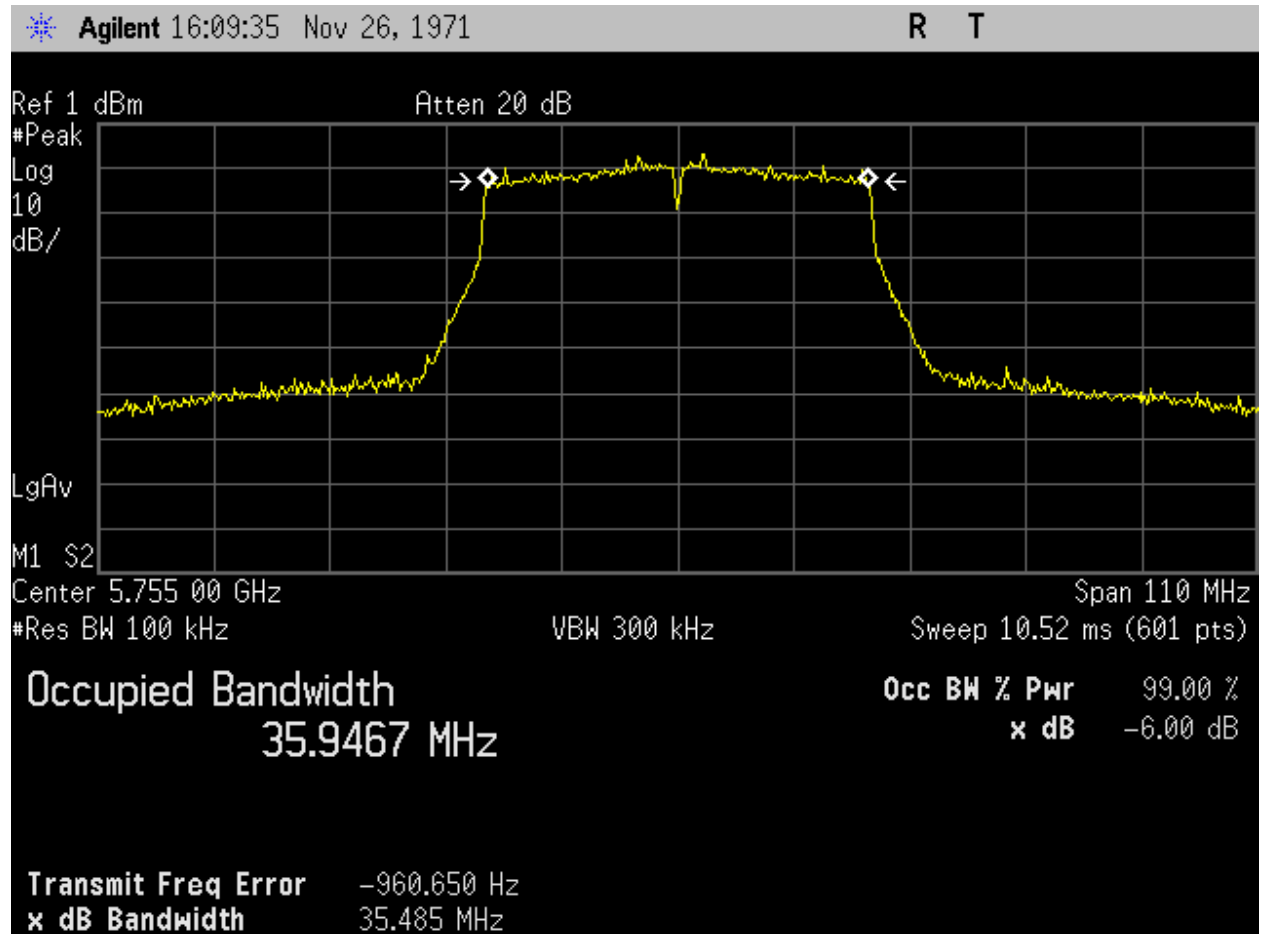


Figure 462: U-NII-3_5755MHz_Low Ch_151_40MHz BW_n-mode_Occupied Bandwidth_-6dB_Port 1.

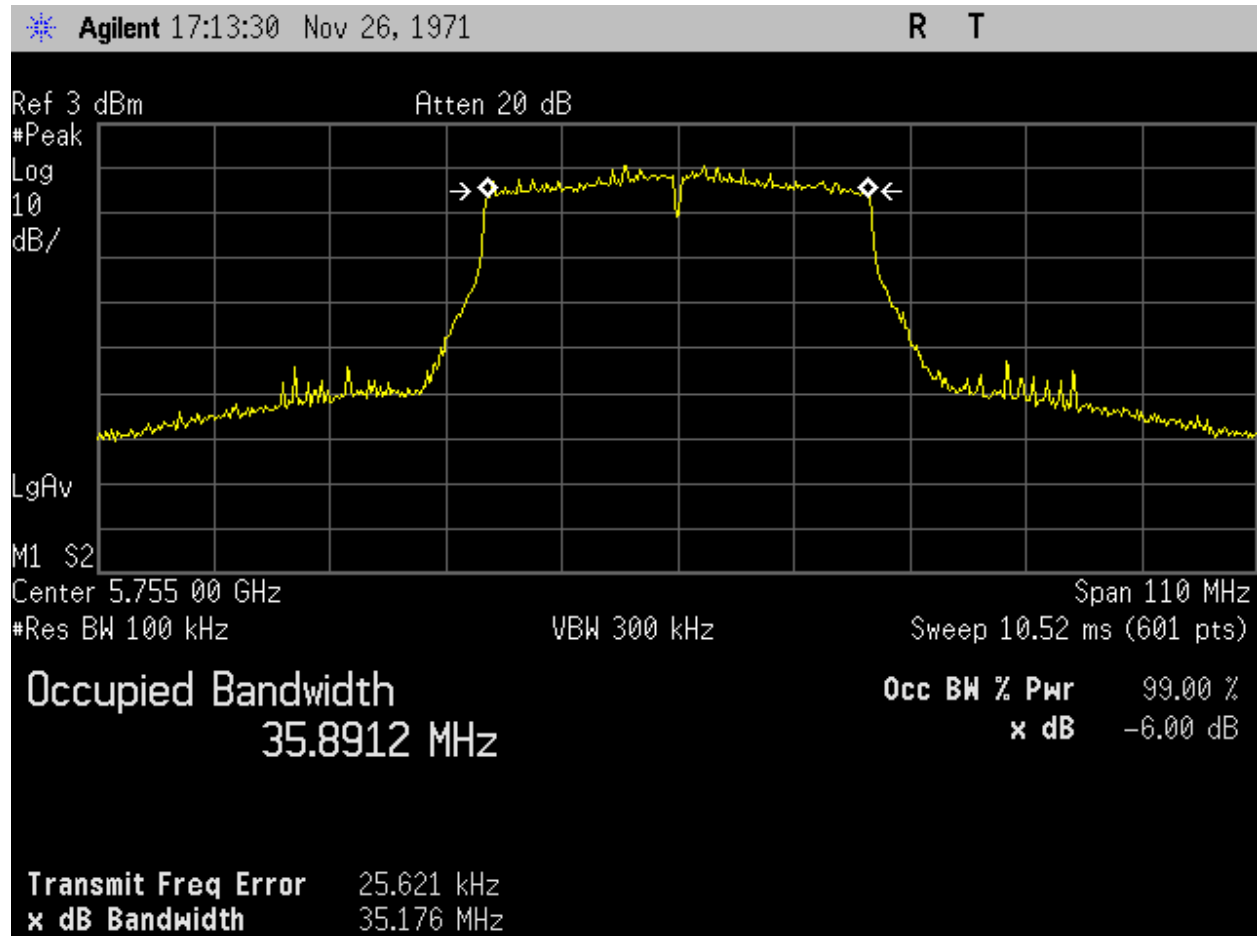


Figure 463: U-NII-3_5755MHz_Low Ch_151_40MHz BW_n-mode_Occupied Bandwidth_-6dB_Port 2.

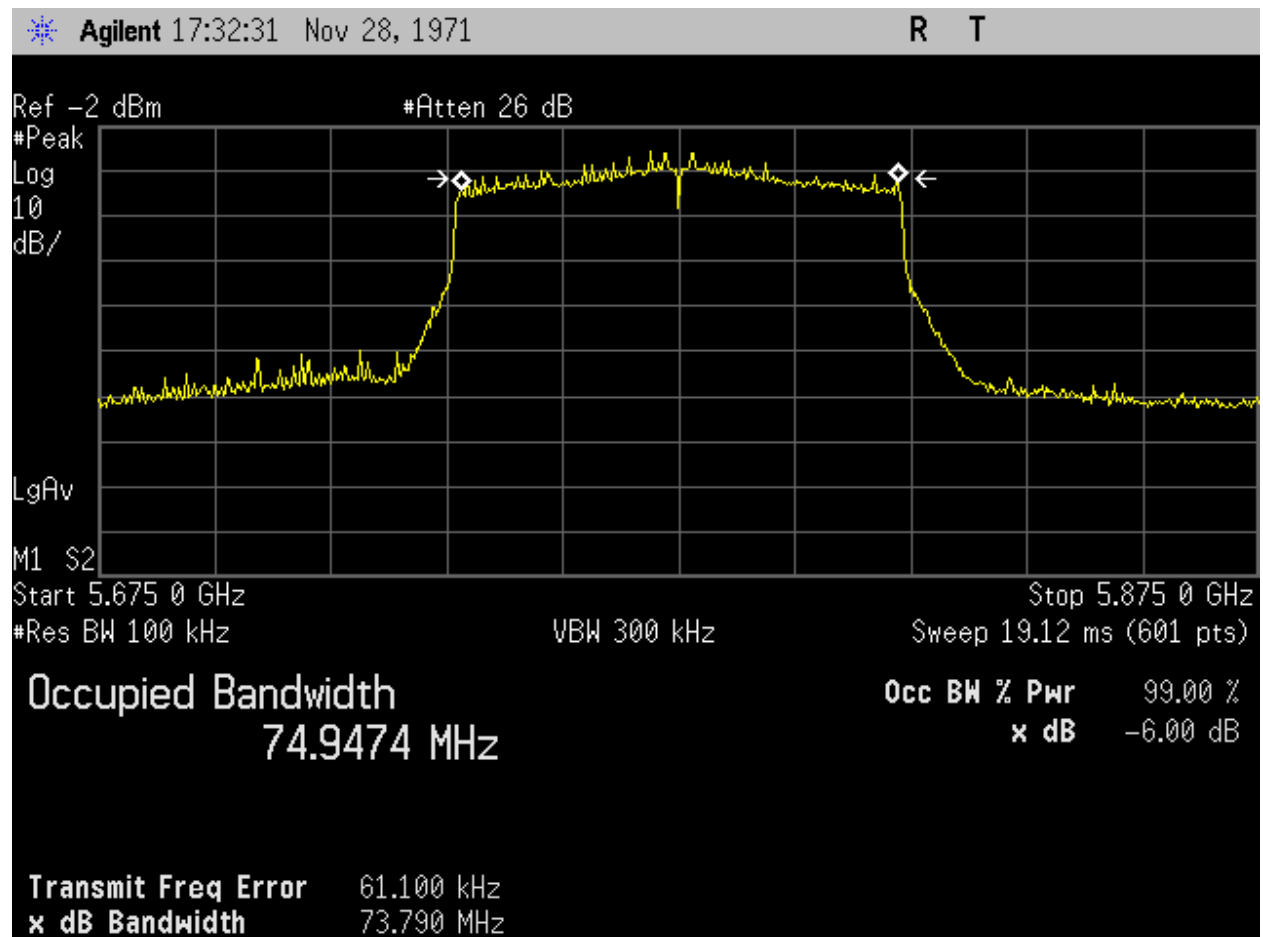


Figure 464: U-NII-3_5775MHz_Mid Ch_155_80MHz BW_ac-mode_Occupied Bandwidth_-6dB_Port 1.

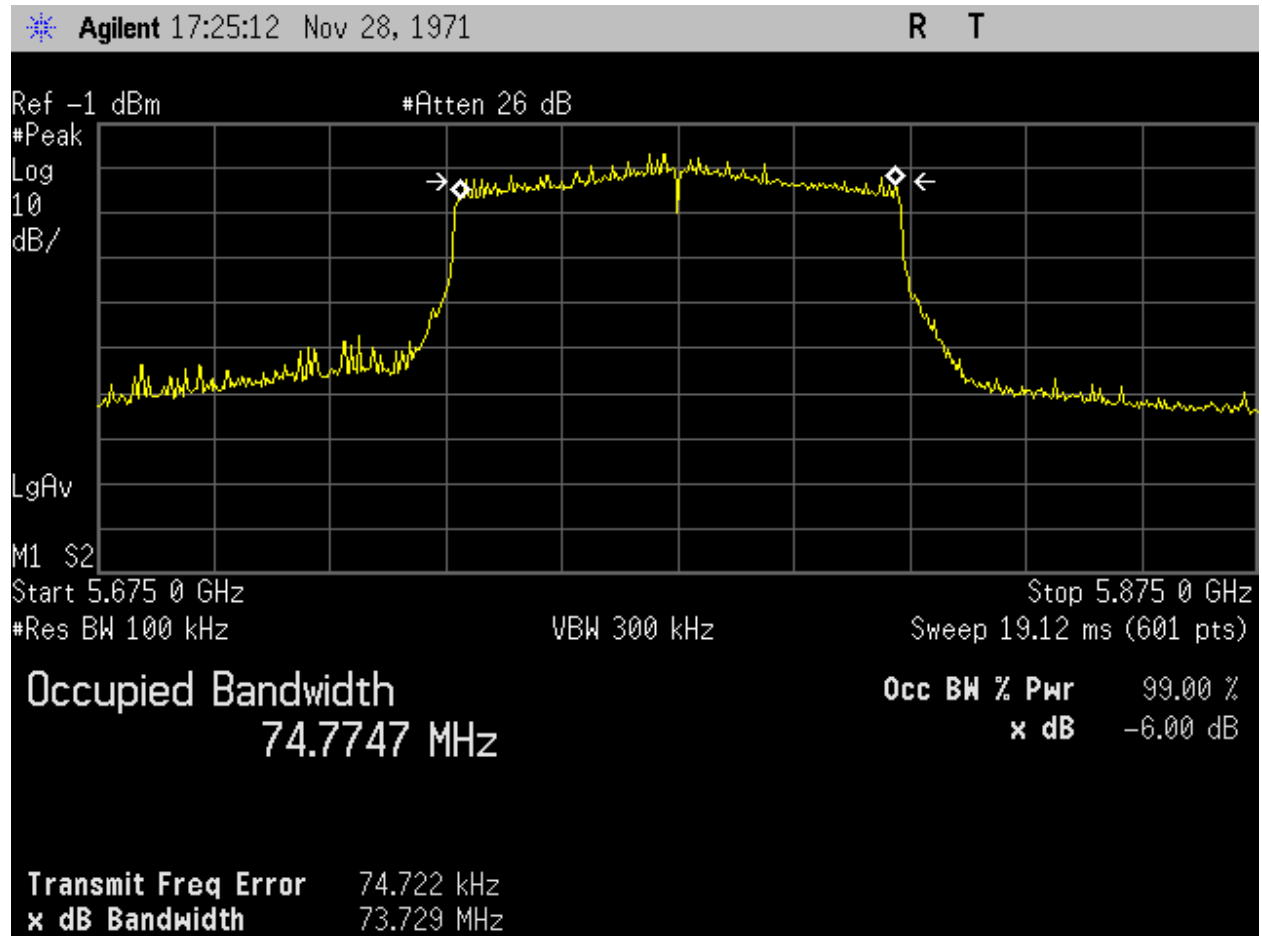


Figure 465: U-NII-3_5775MHz_Mid Ch_155_80MHz BW_ac-mode_Occupied Bandwidth_-6dB_Port 2.

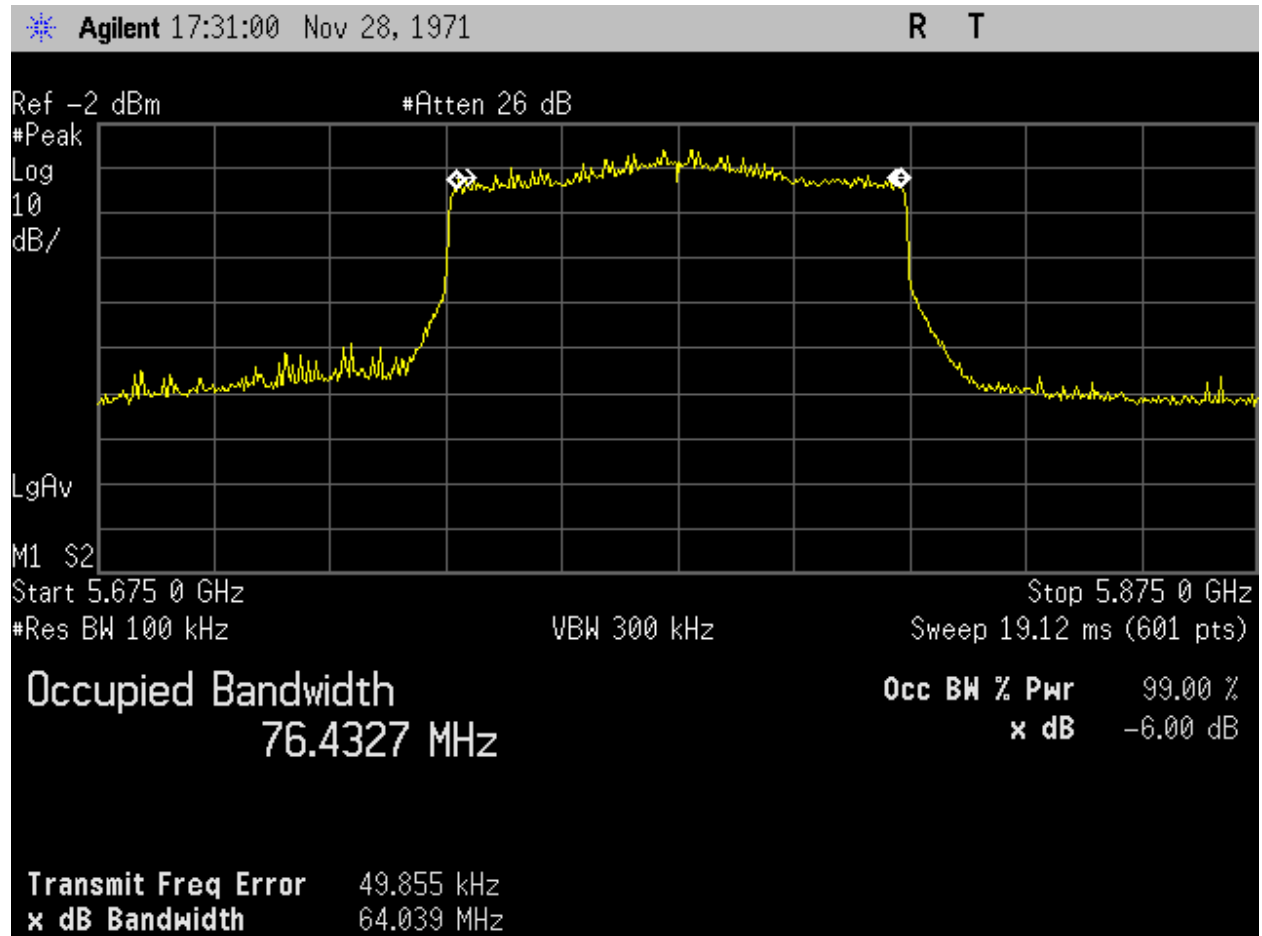


Figure 466: U-NII-3_5775MHz_Mid Ch_155_80MHz BW_ax-mode_Occupied Bandwidth_-6dB_Port 1.

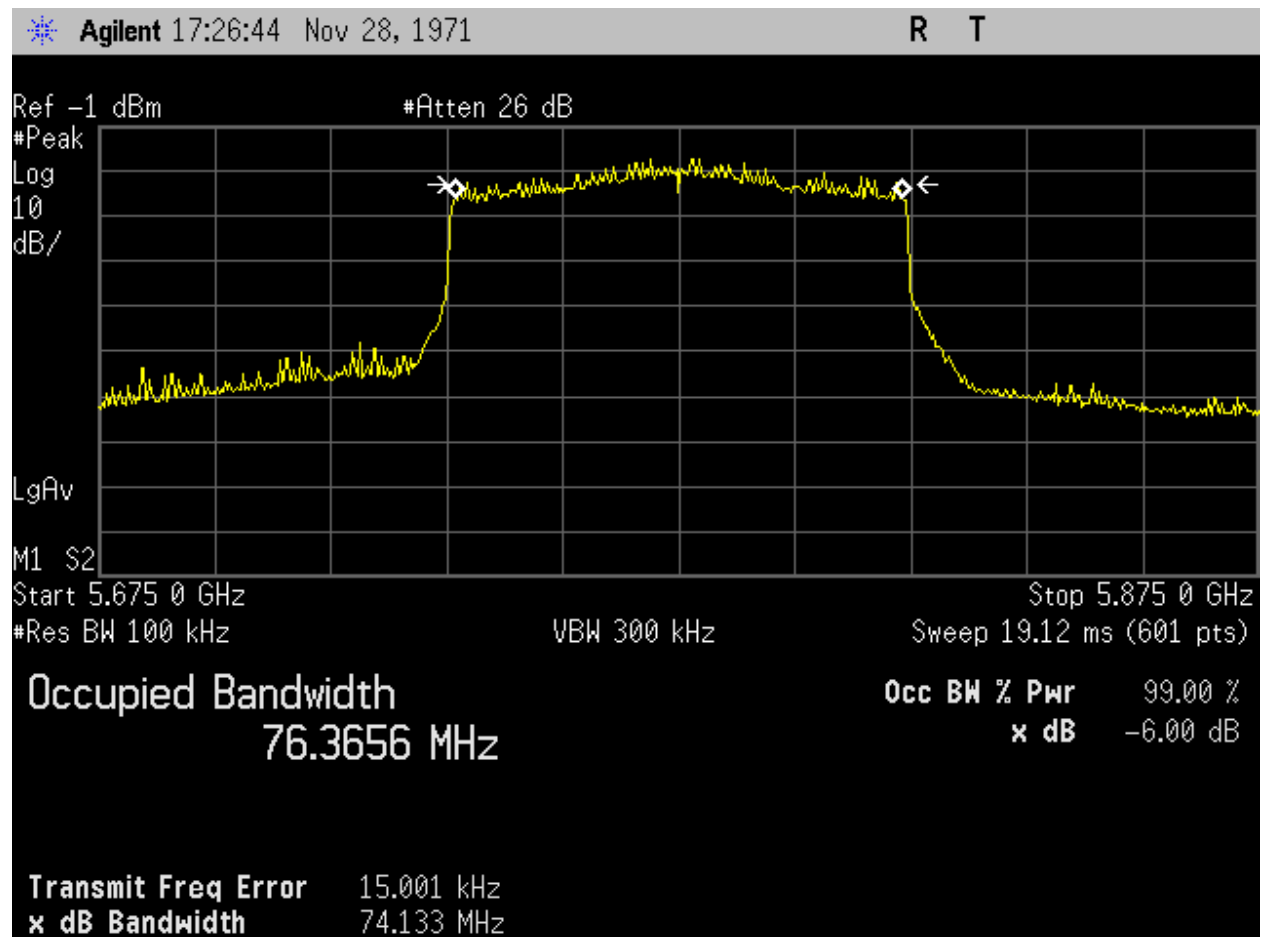


Figure 467: U-NII-3_5775MHz_Mid Ch_155_80MHz BW_ax-mode_Occupied Bandwidth_-6dB_Port 2.

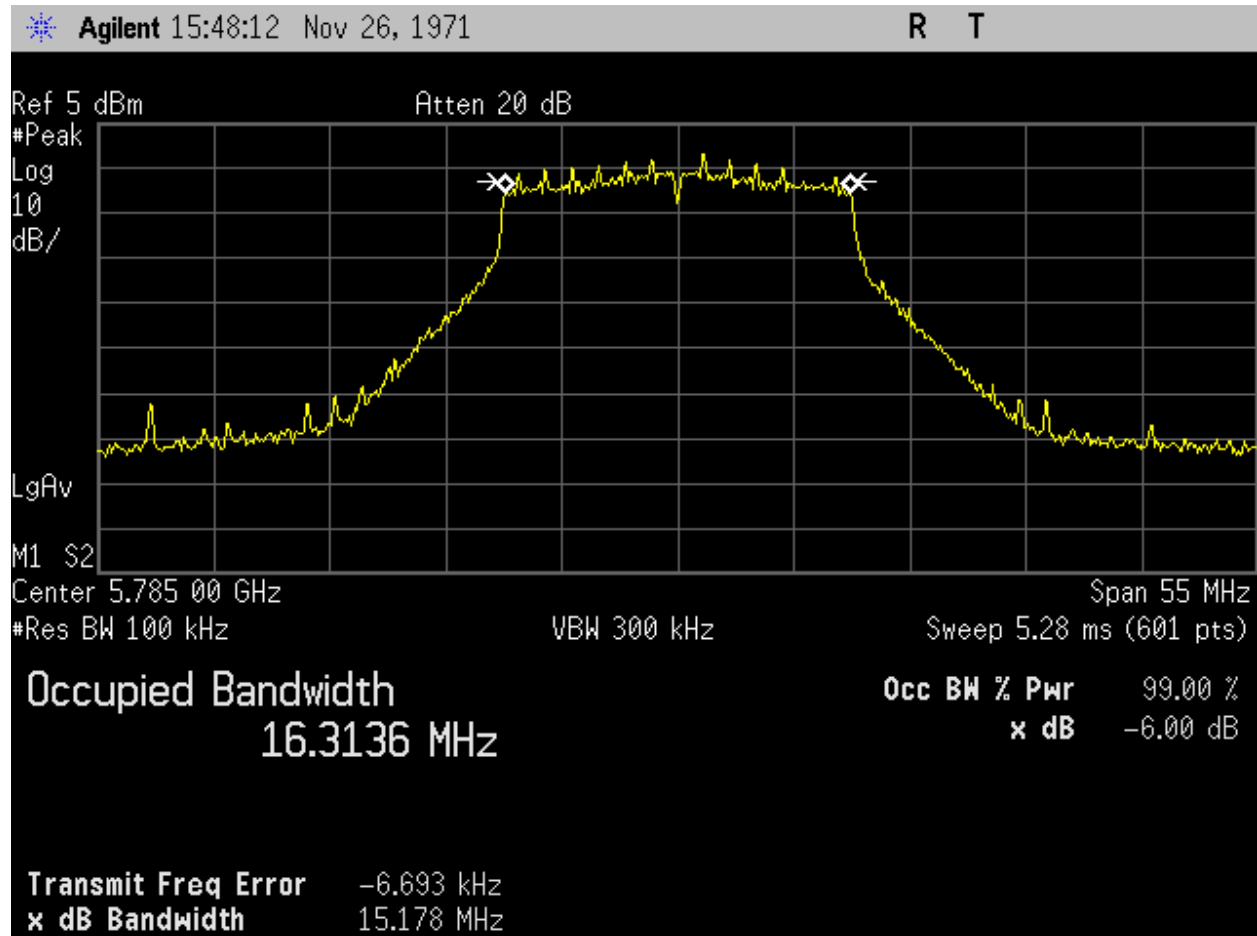


Figure 468: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_a-mode_Occupied Bandwidth_-6dB_Port 1.

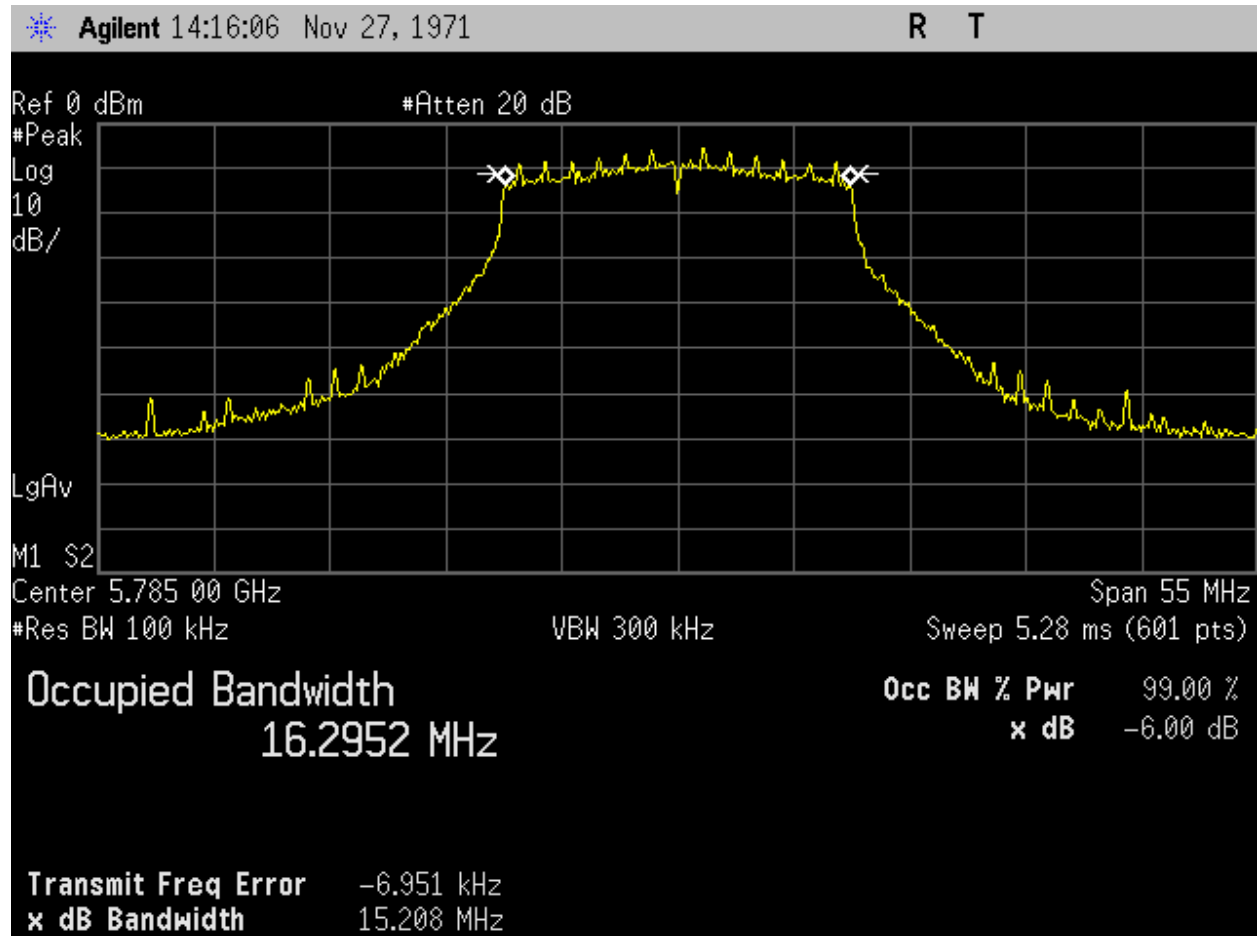


Figure 469: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_a-mode_Occupied Bandwidth_-6dB_Port 2.

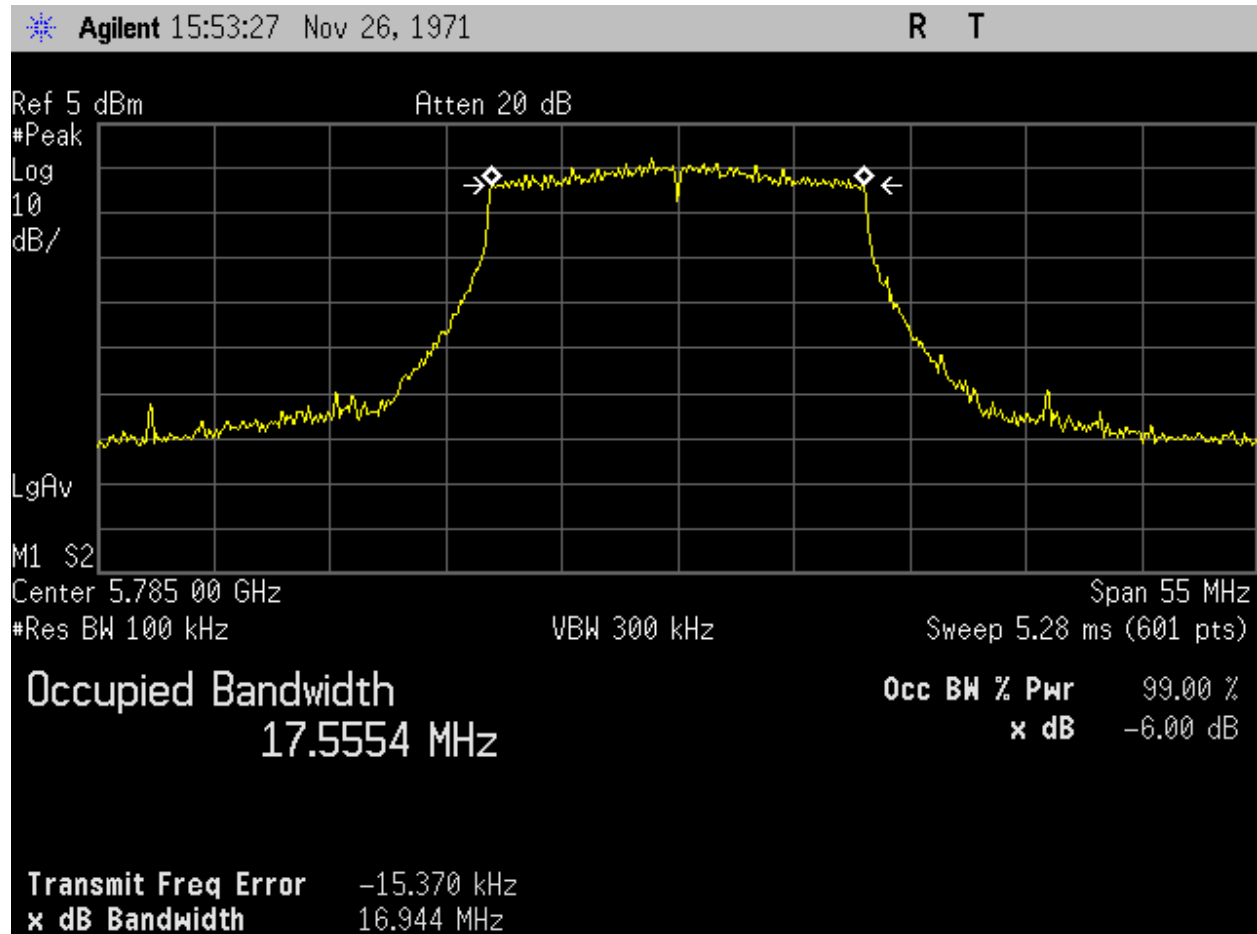


Figure 470: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_ac-mode_Occupied Bandwidth_-6dB_Port 1.

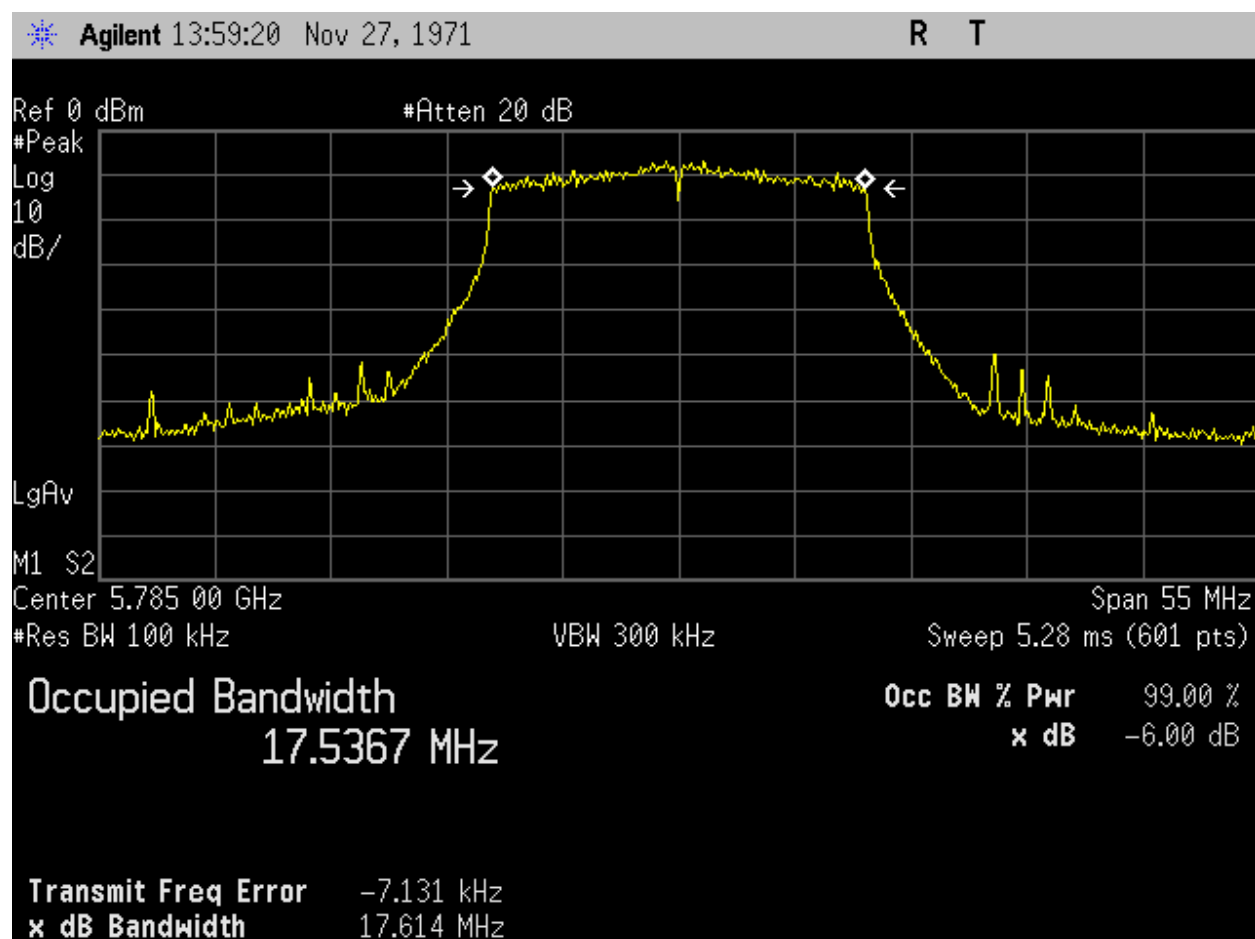


Figure 471: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_ac-mode_Occupied Bandwidth_-6dB_Port 2.

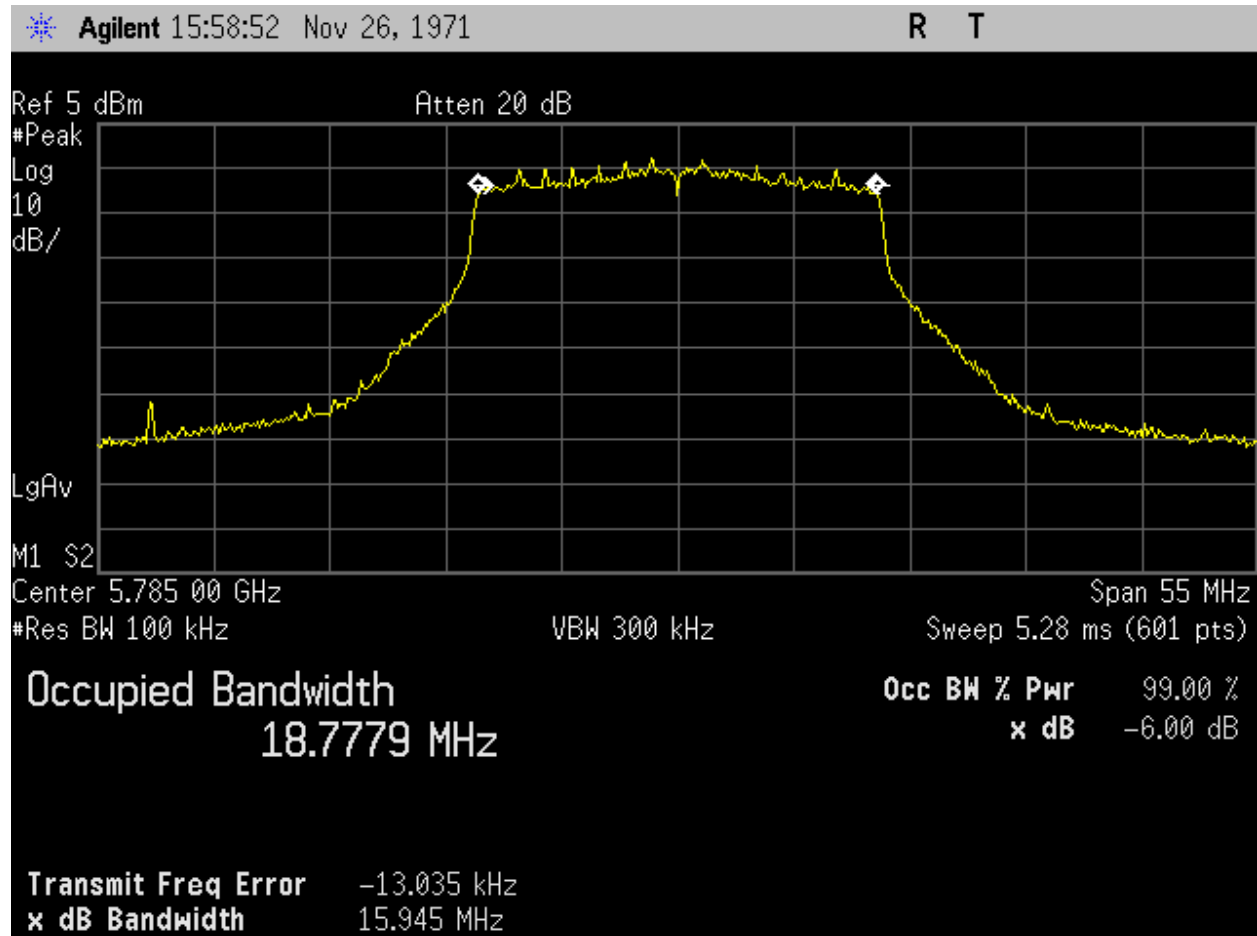


Figure 472: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_ax-mode_Occupied Bandwidth_-6dB_Port 1.

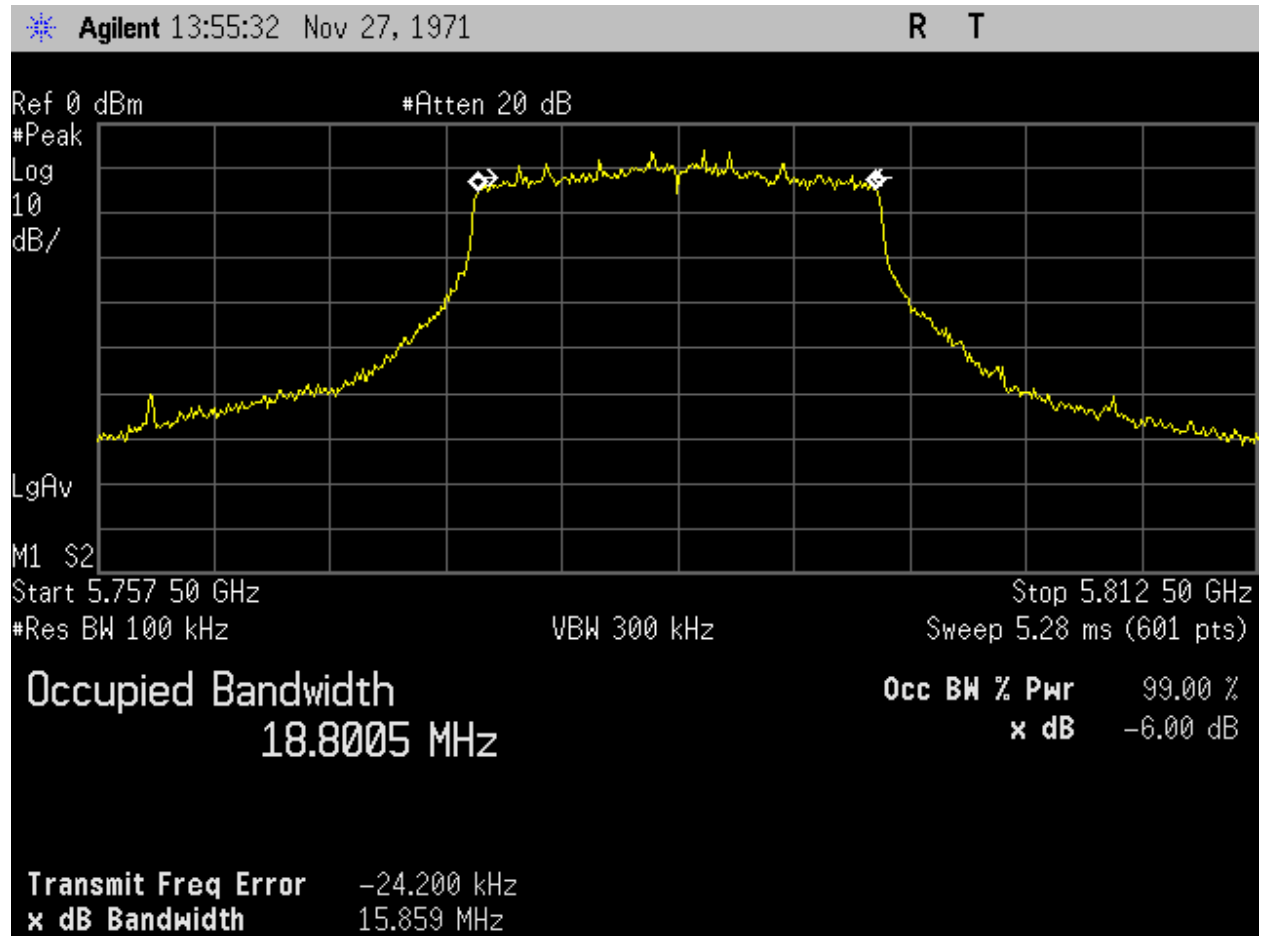


Figure 473: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_ax-mode_Occupied Bandwidth_-6dB_Port 2.

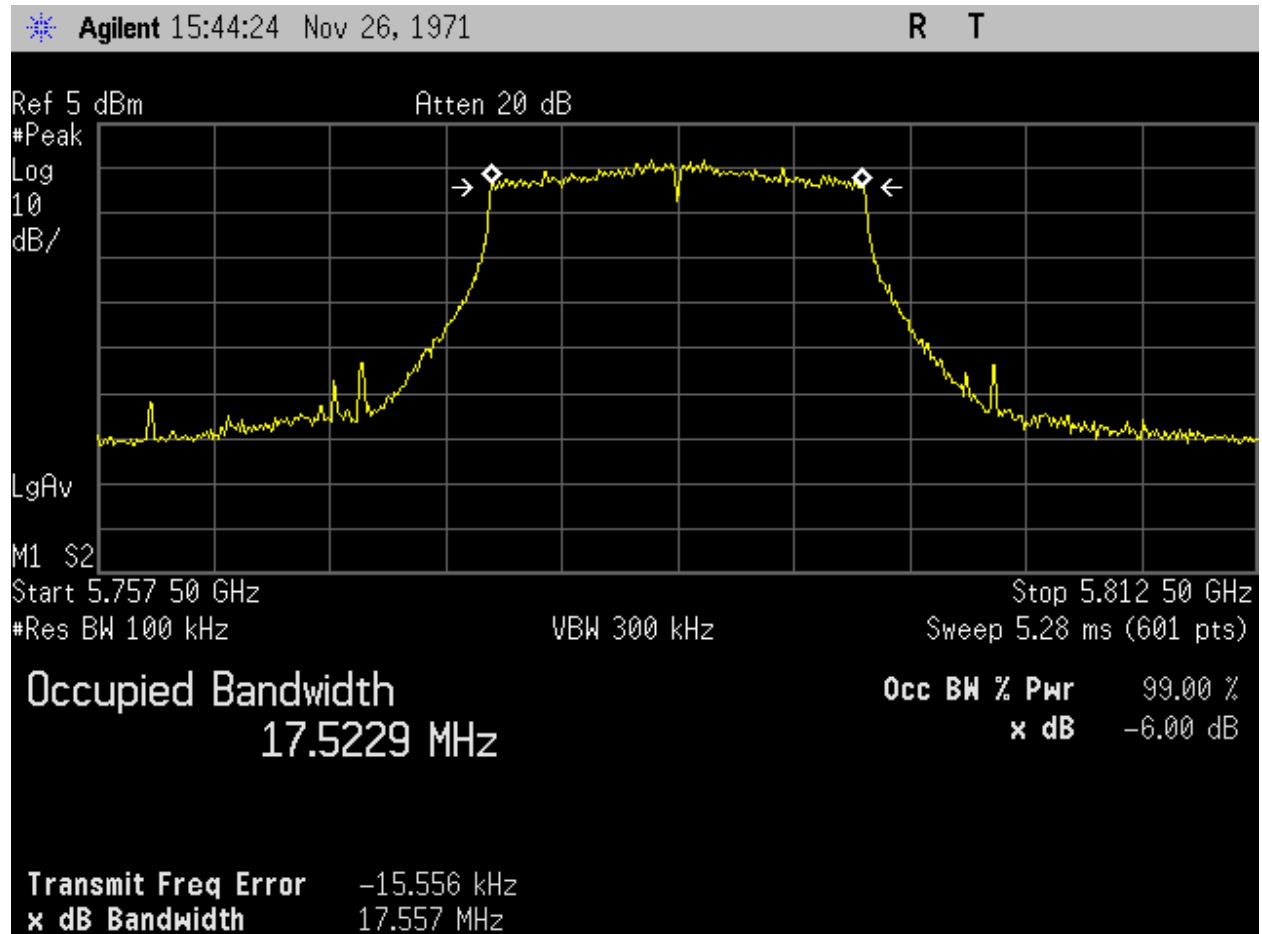


Figure 474: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_n-mode_Occupied Bandwidth_-6dB_Port 1.

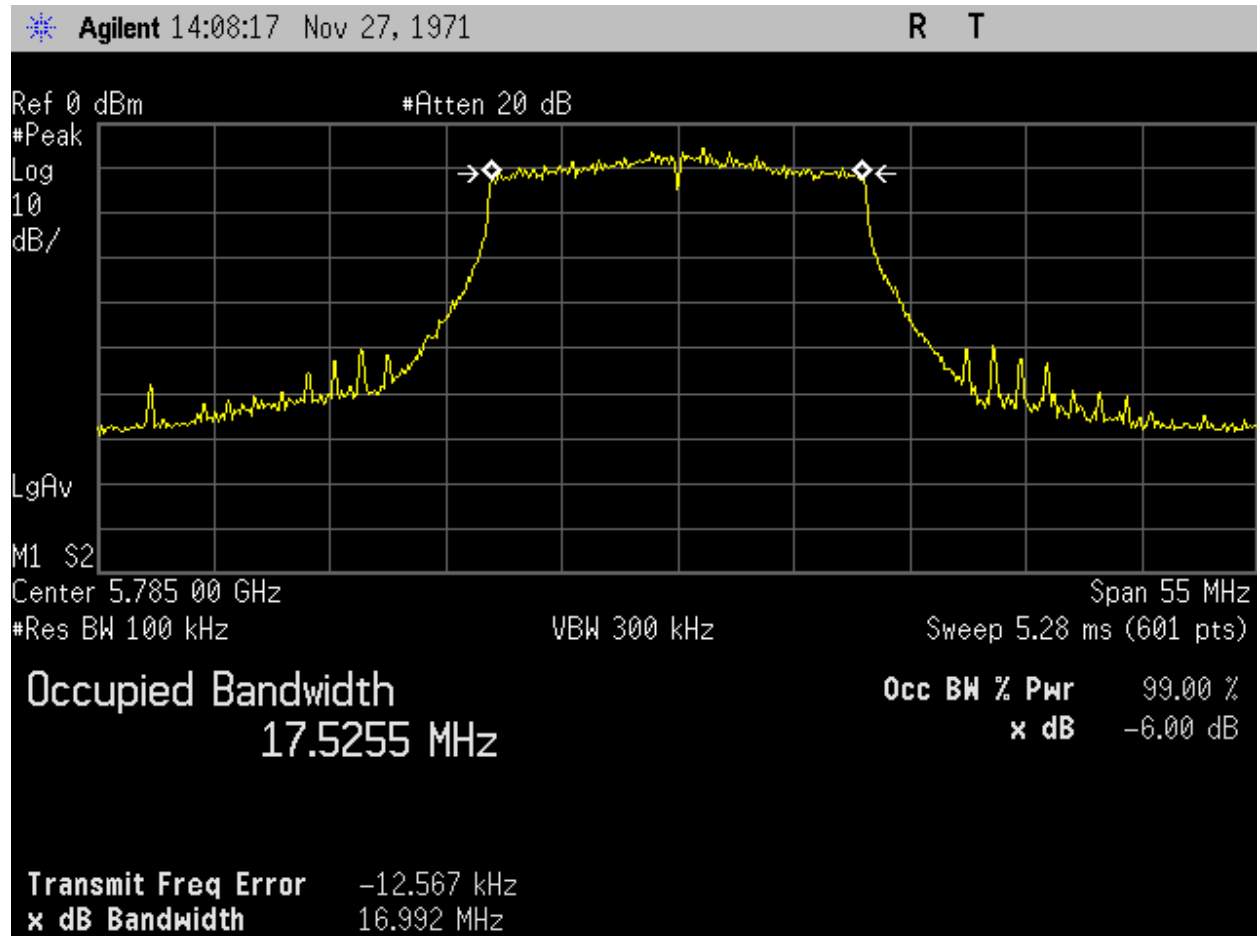


Figure 475: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_n-mode_Occupied Bandwidth_-6dB_Port 2.

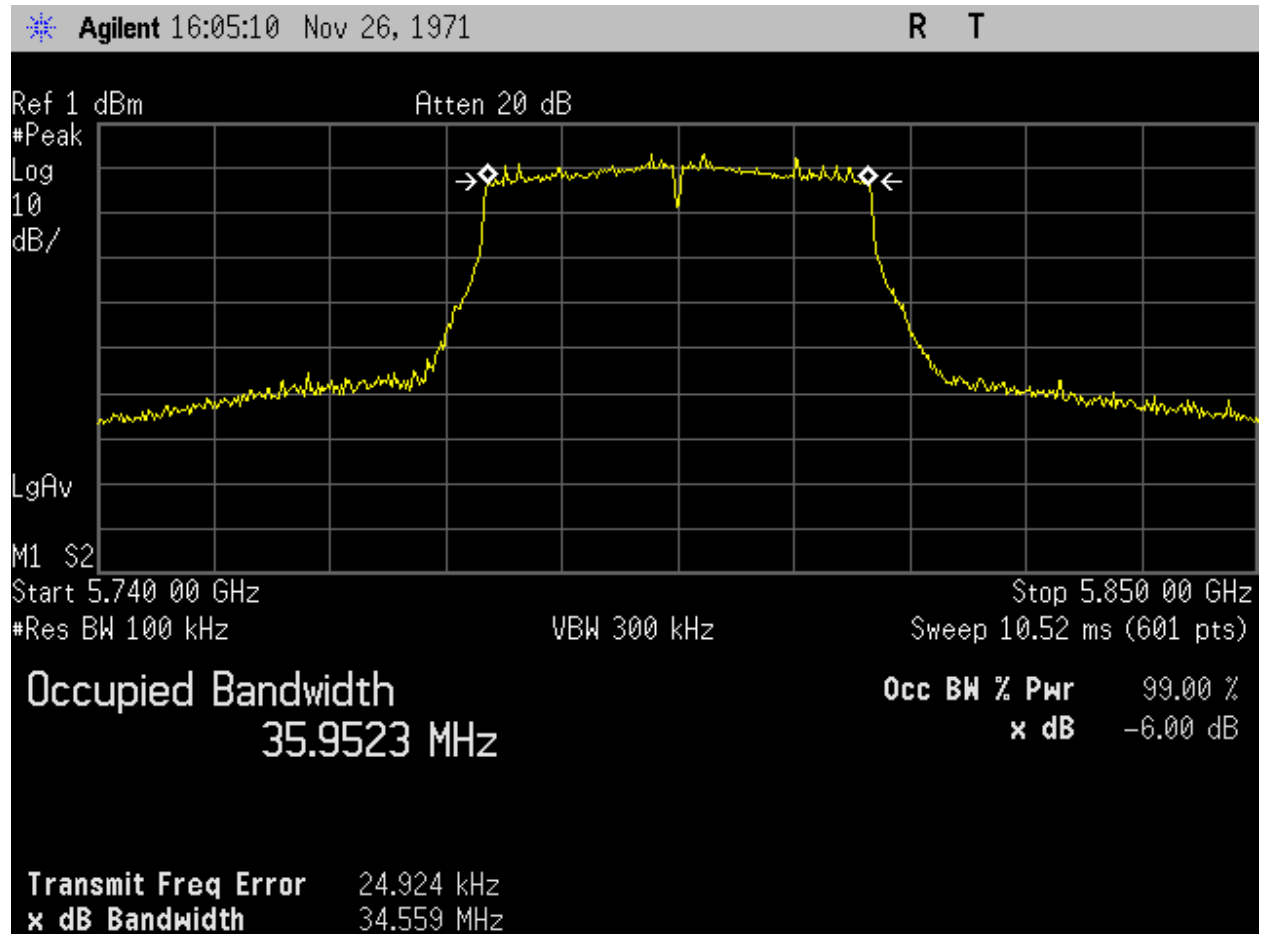


Figure 476: U-NII-3_5795MHz_High Ch_159_40MHz BW_ac-mode_Occupied Bandwidth_-6dB_Port 1.

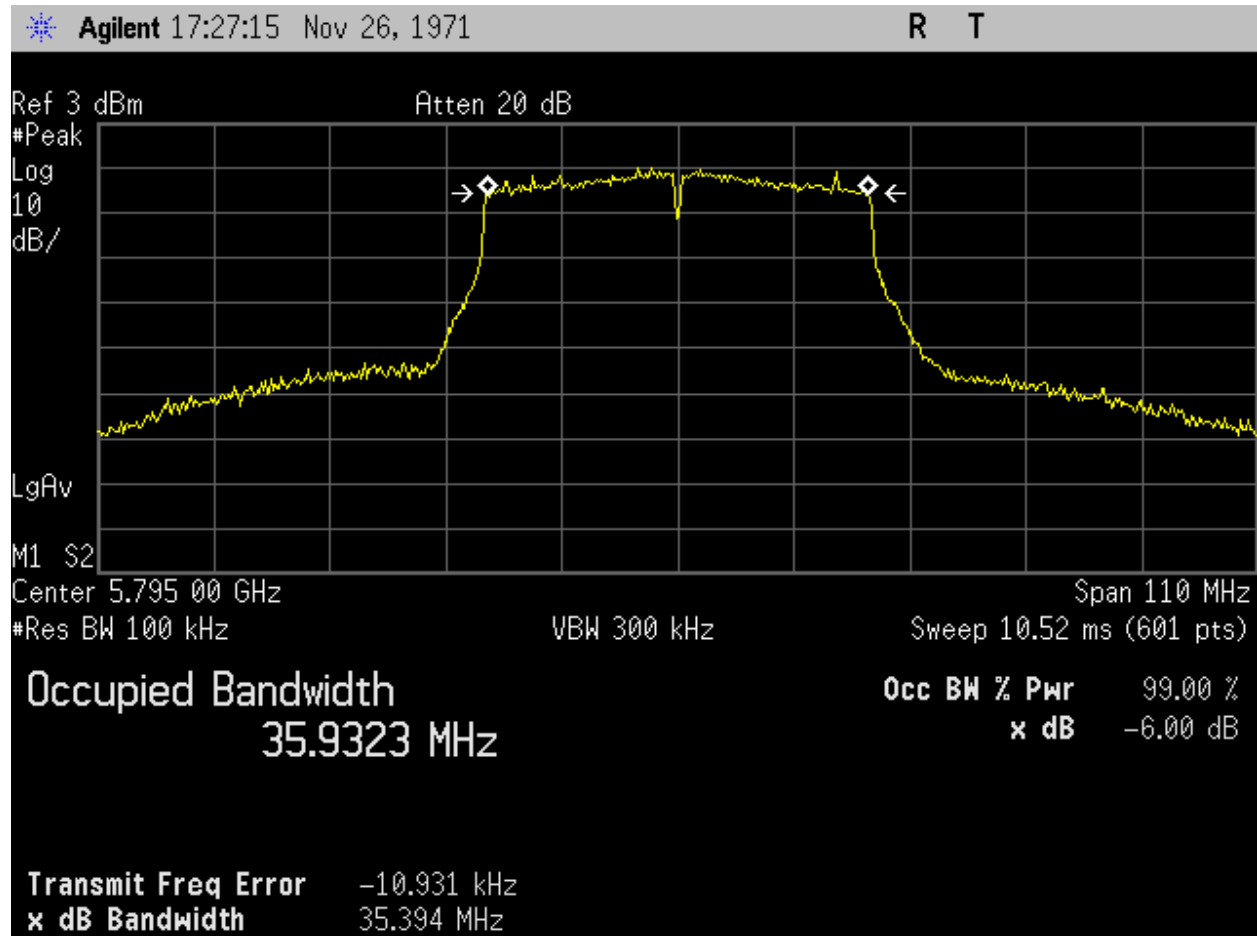


Figure 477: U-NII-3_5795MHz_High Ch_159_40MHz BW_ac-mode_Occupied Bandwidth_-6dB_Port 2.

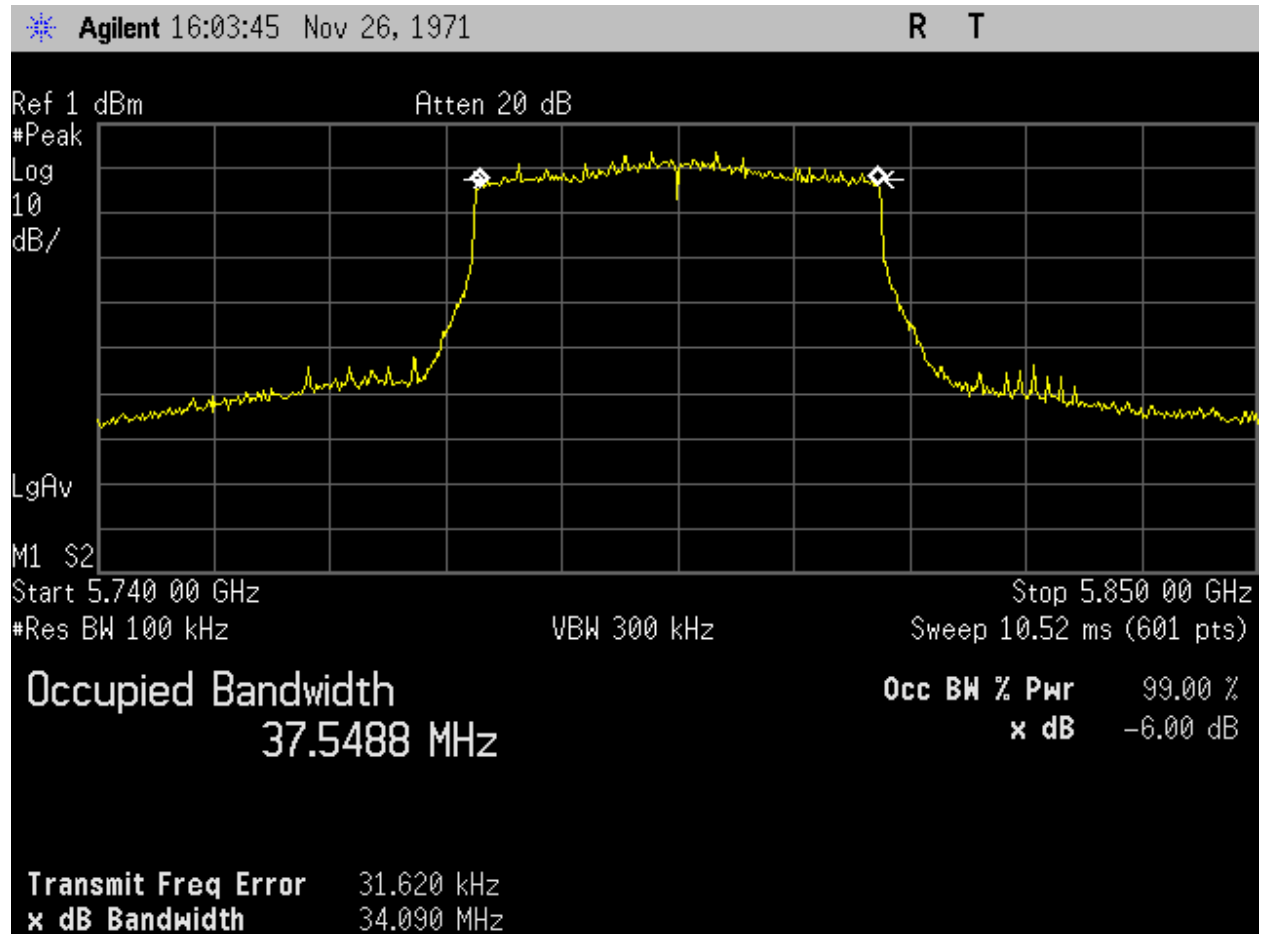


Figure 478: U-NII-3_5795MHz_High Ch_159_40MHz BW_ax-mode_Occupied Bandwidth_-6dB_Port 1.

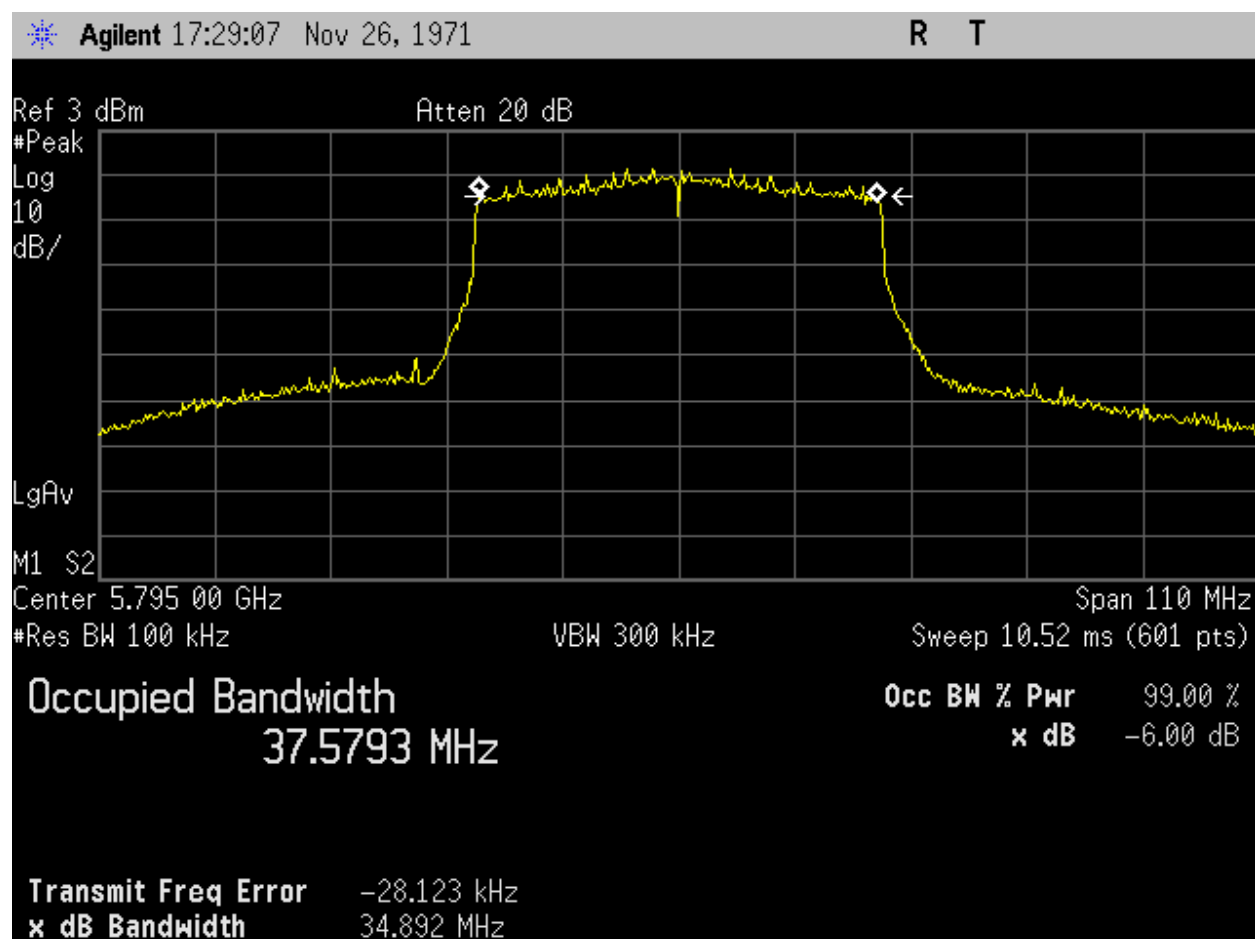


Figure 479: U-NII-3_5795MHz_High Ch_159_40MHz BW_ax-mode_Occupied Bandwidth_-6dB_Port 2.

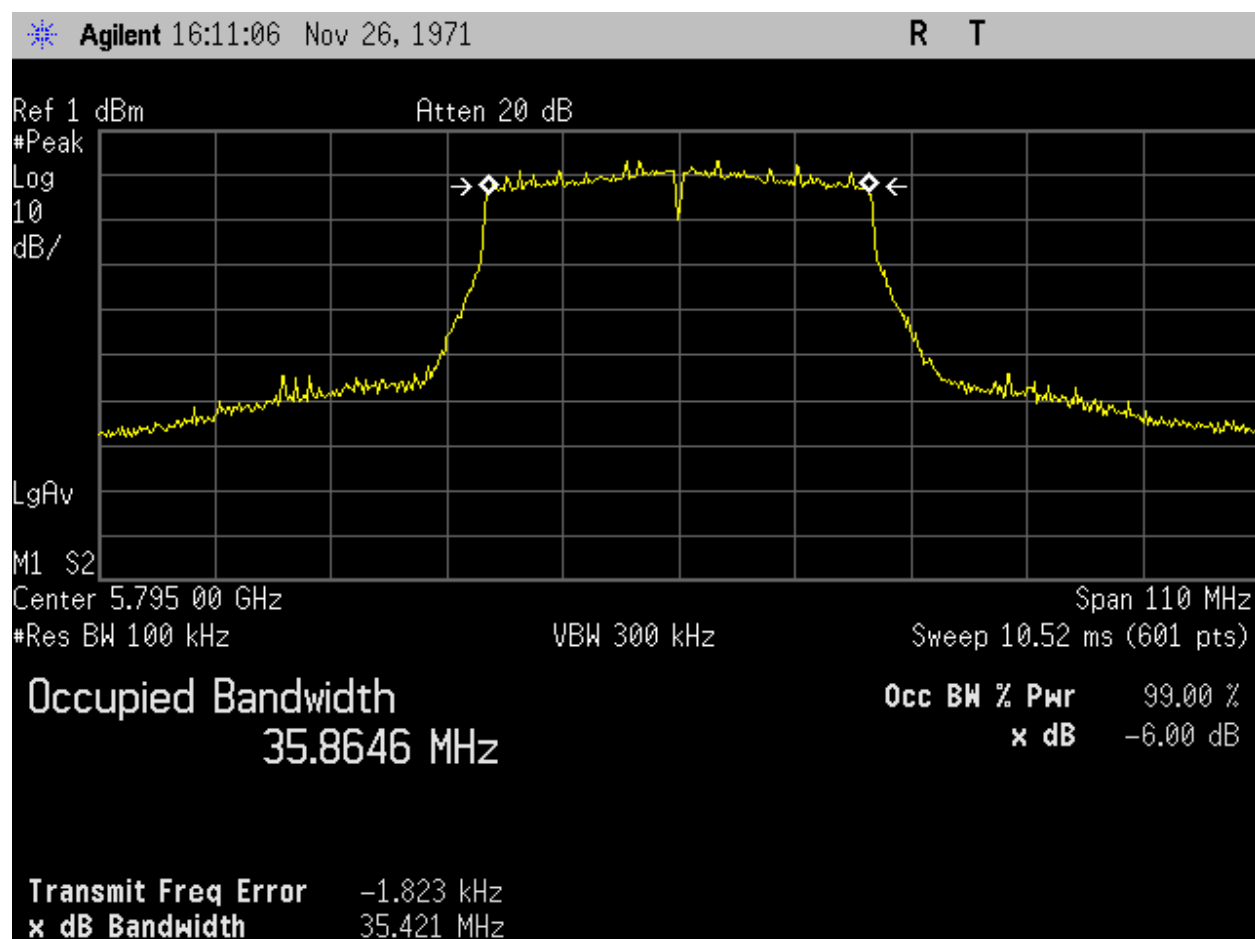


Figure 480: U-NII-3_5795MHz_High Ch_159_40MHz BW_n-mode_Occupied Bandwidth_-6dB_Port 1.

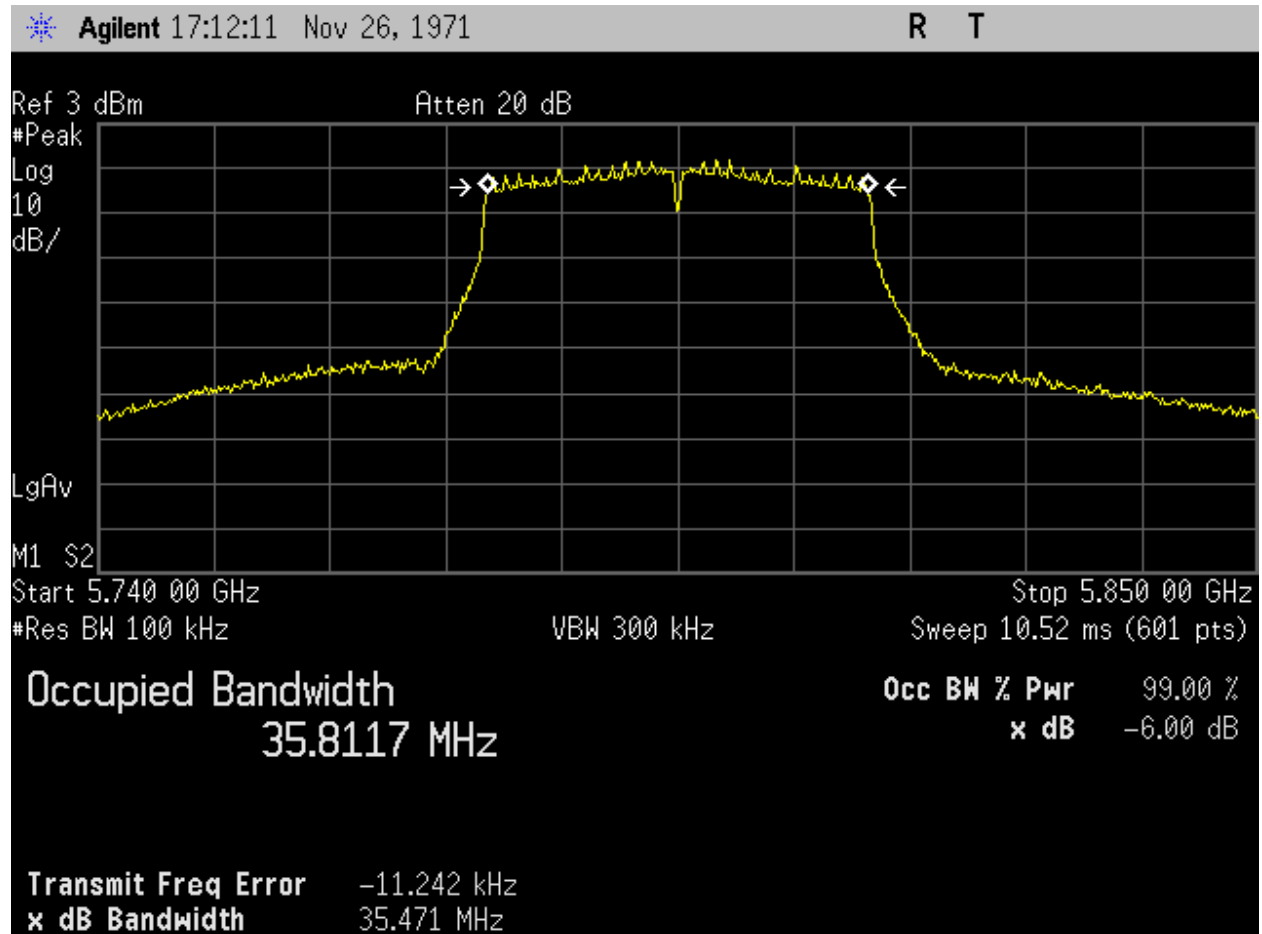


Figure 481: U-NII-3_5795MHz_High Ch_159_40MHz BW_n-mode_Occupied Bandwidth_-6dB_Port 2.

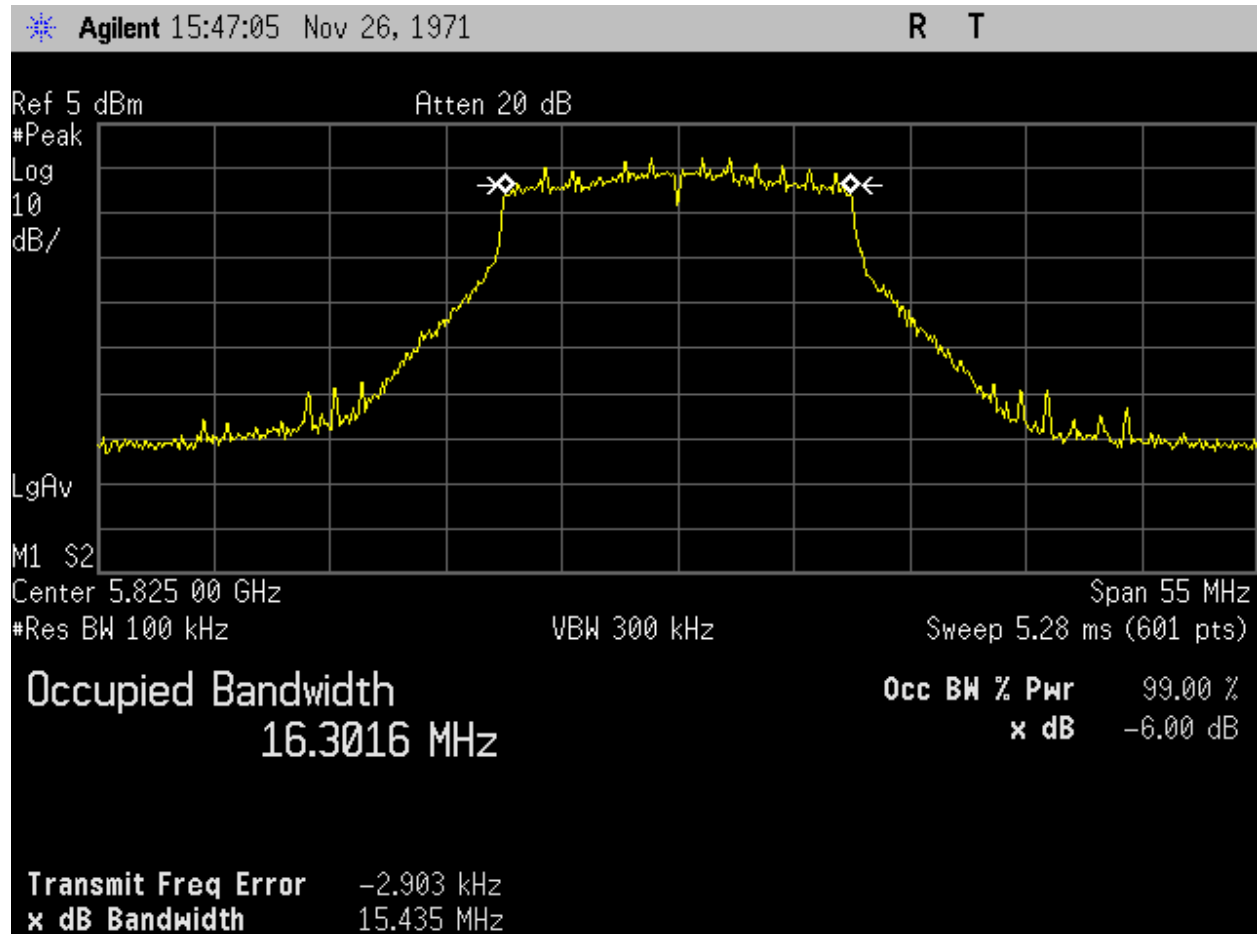


Figure 482: U-NII-3_5825MHz_High Ch_165_20MHz BW_a-mode_Occupied Bandwidth_-6dB_Port 1.

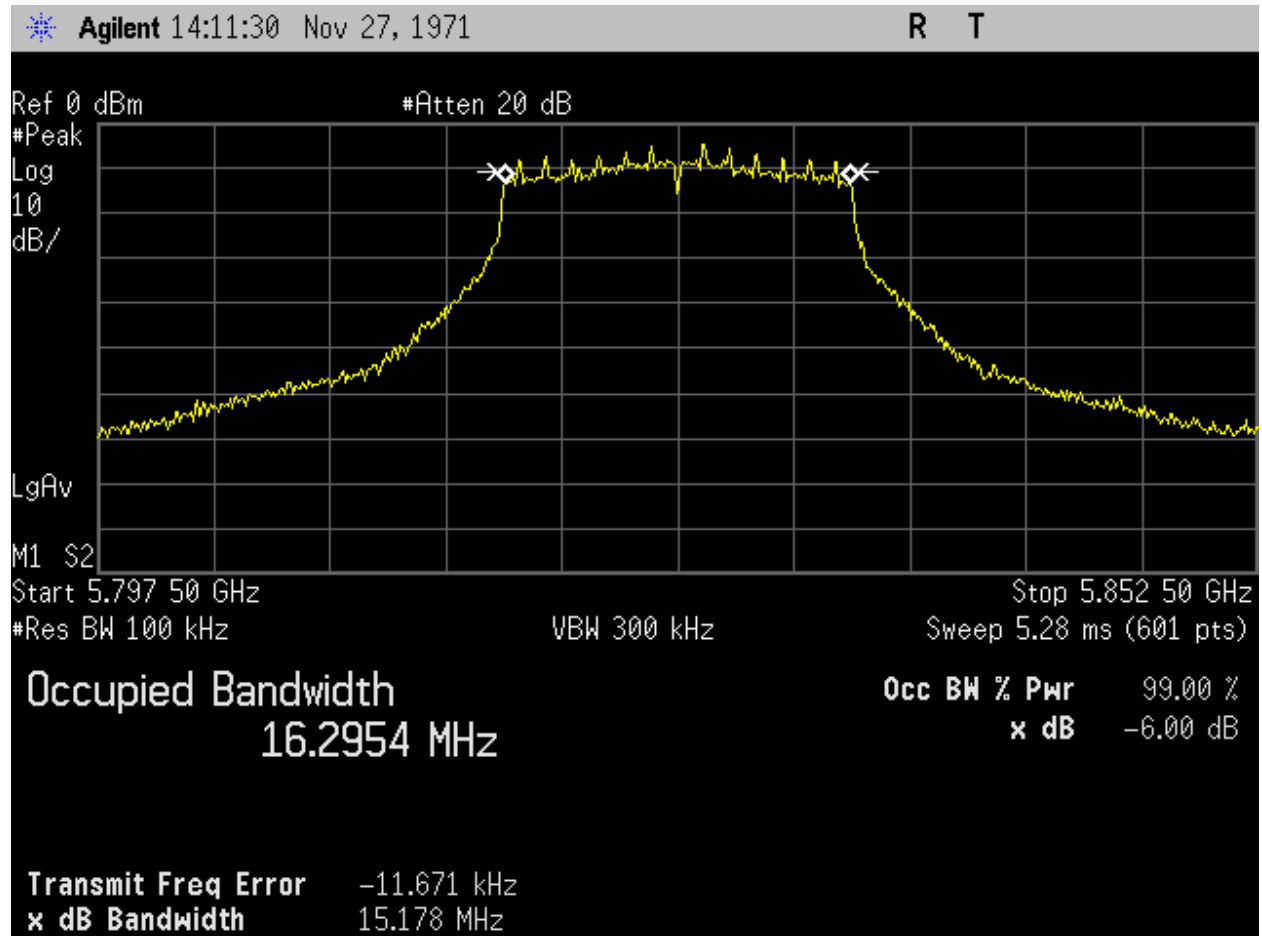


Figure 483: U-NII-3_5825MHz_High Ch_165_20MHz BW_a-mode_Occupied Bandwidth_-6dB_Port 2.

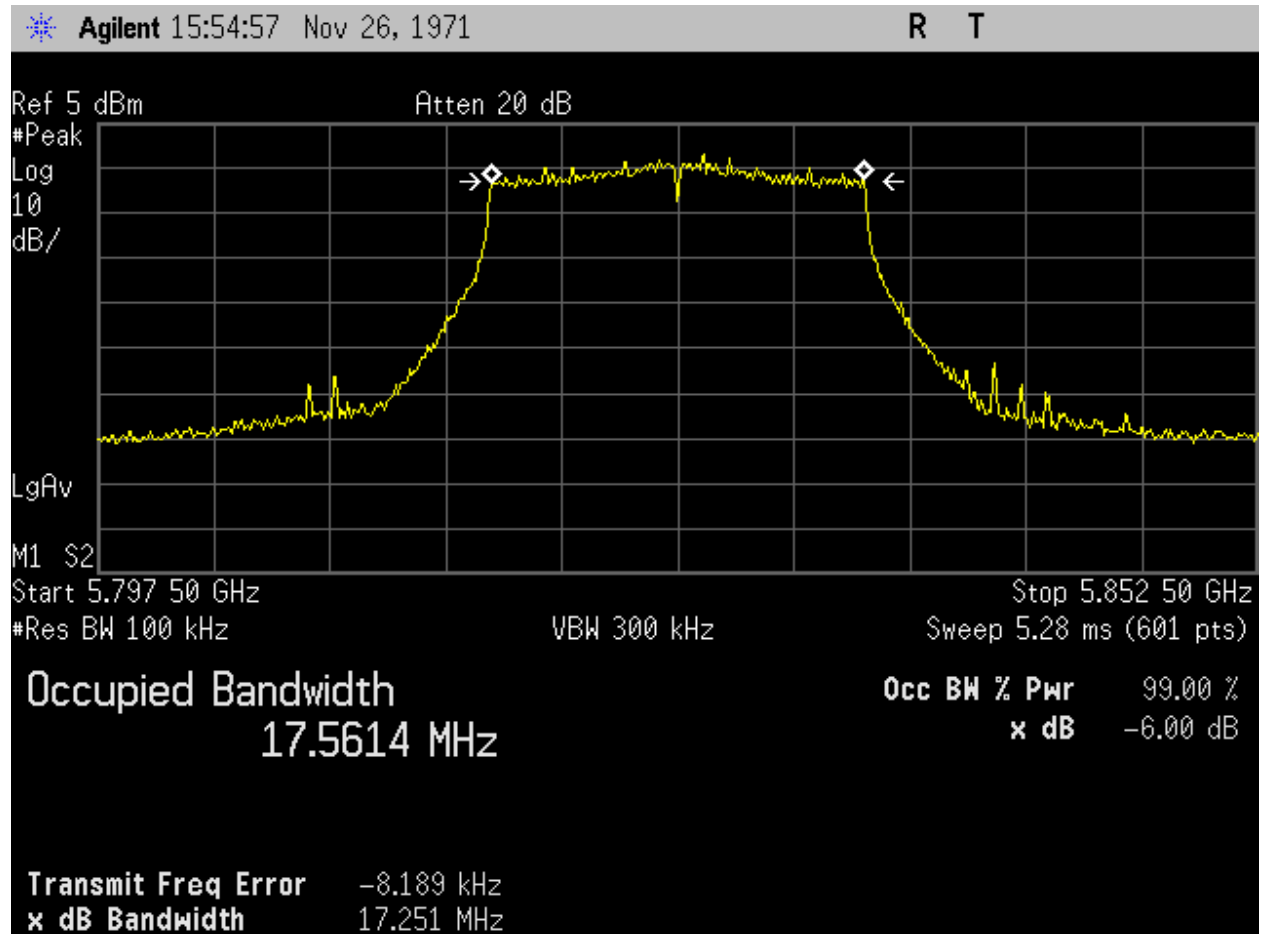


Figure 484: U-NII-3_5825MHz_High Ch_165_20MHz BW_ac-mode_Occupied Bandwidth_-6dB_Port 1.

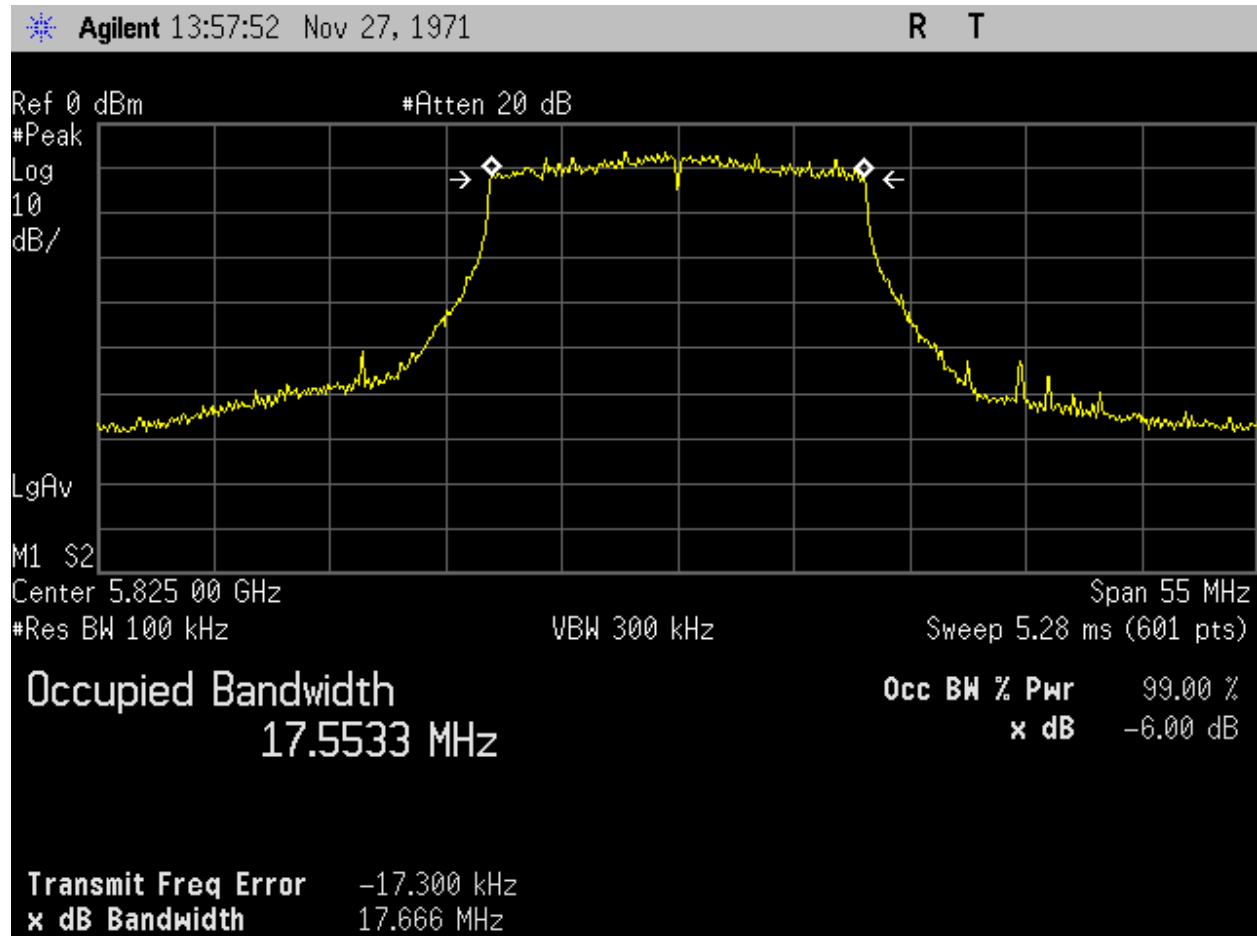


Figure 485: U-NII-3_5825MHz_High Ch_165_20MHz BW_ac-mode_Occupied Bandwidth_-6dB_Port 2.

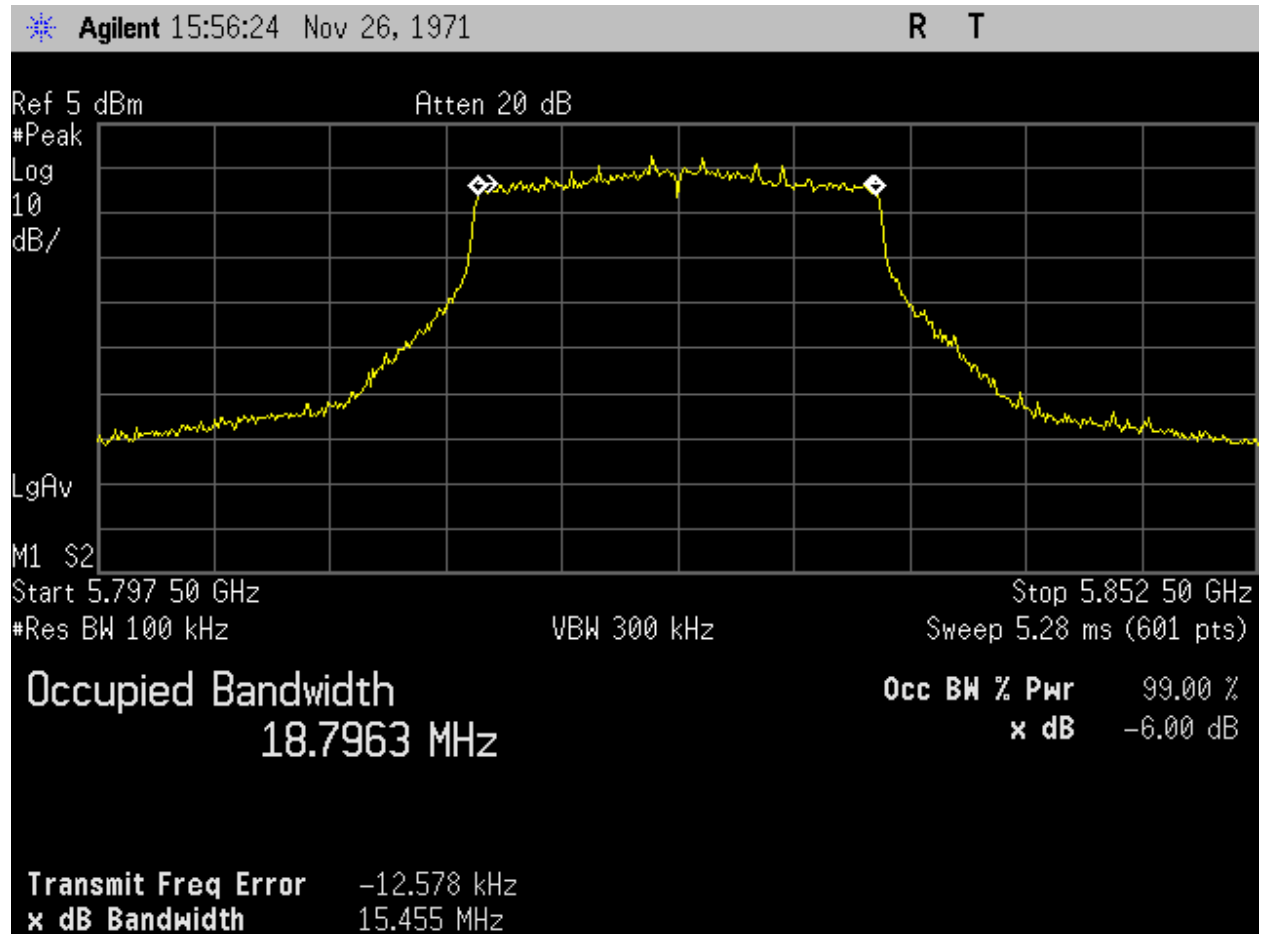


Figure 486: U-NII-3_5825MHz_High Ch_165_20MHz BW_ax-mode_Occupied Bandwidth_-6dB_Port 1.

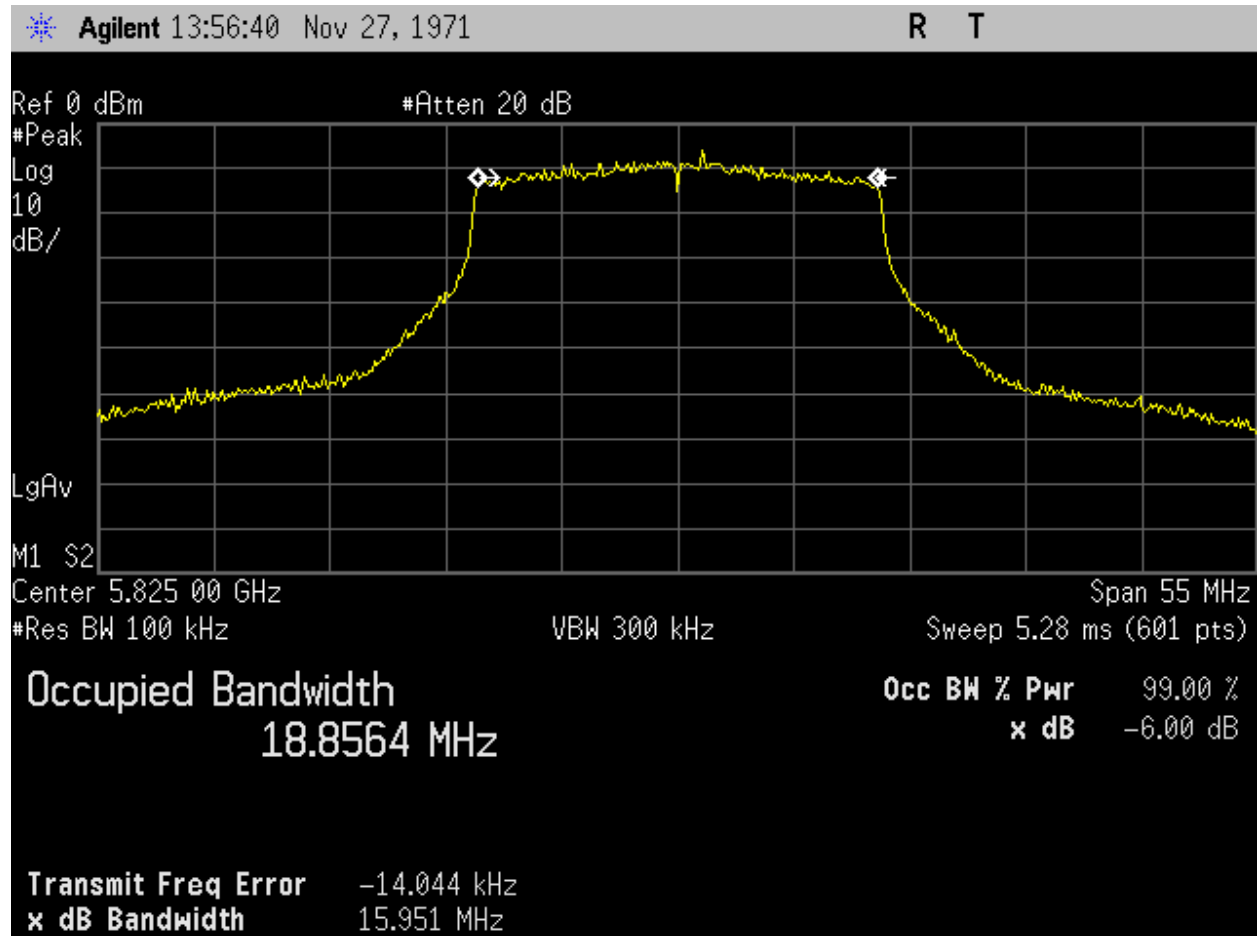


Figure 487: U-NII-3_5825MHz_High Ch_165_20MHz BW_ax-mode_Occupied Bandwidth_-6dB_Port 2.

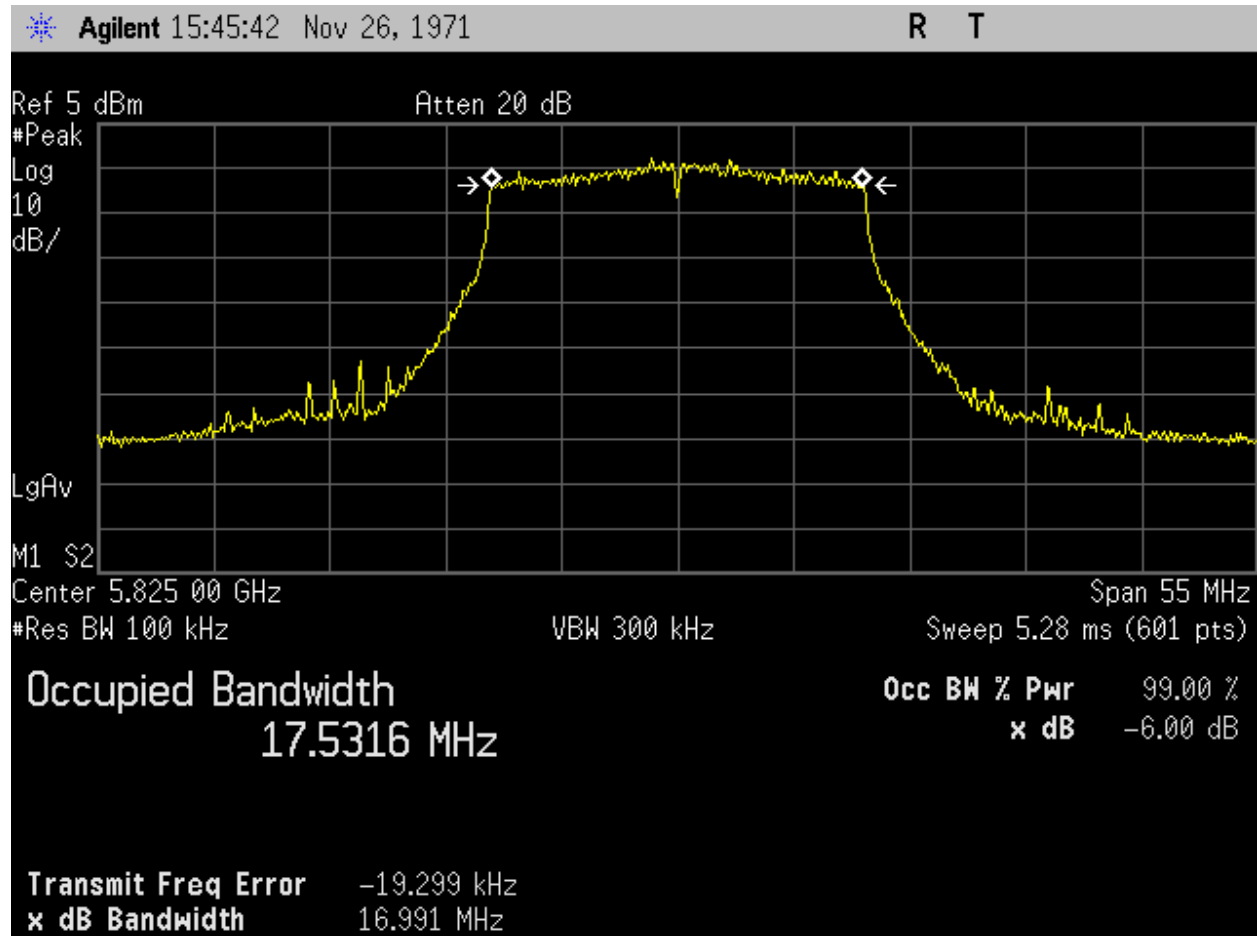


Figure 488: U-NII-3_5825MHz_High Ch_165_20MHz BW_n-mode_Occupied Bandwidth_-6dB_Port 1.

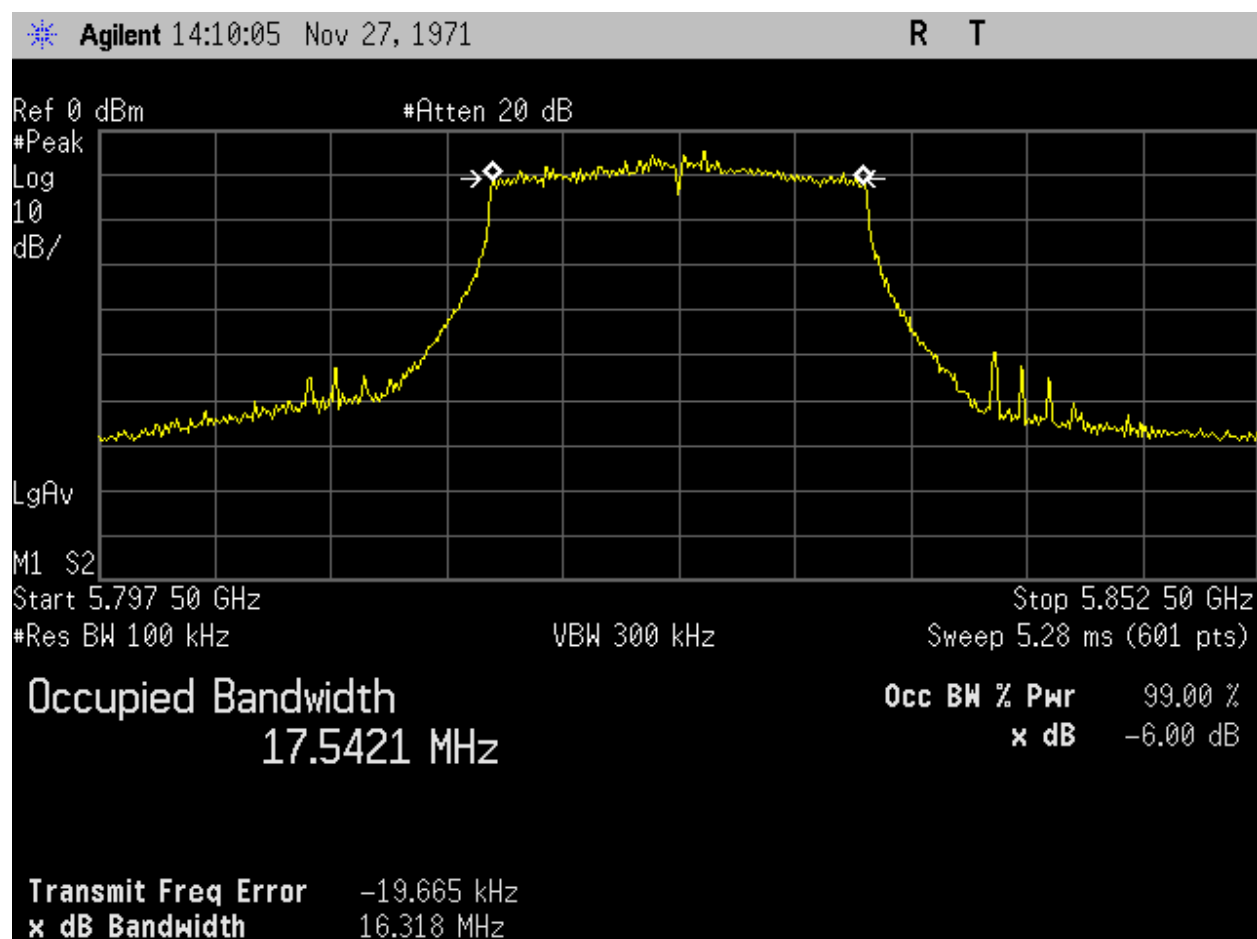


Figure 489: U-NII-3_5825MHz_High Ch_165_20MHz BW_n-mode_Occupied Bandwidth_-6dB_Port 2.

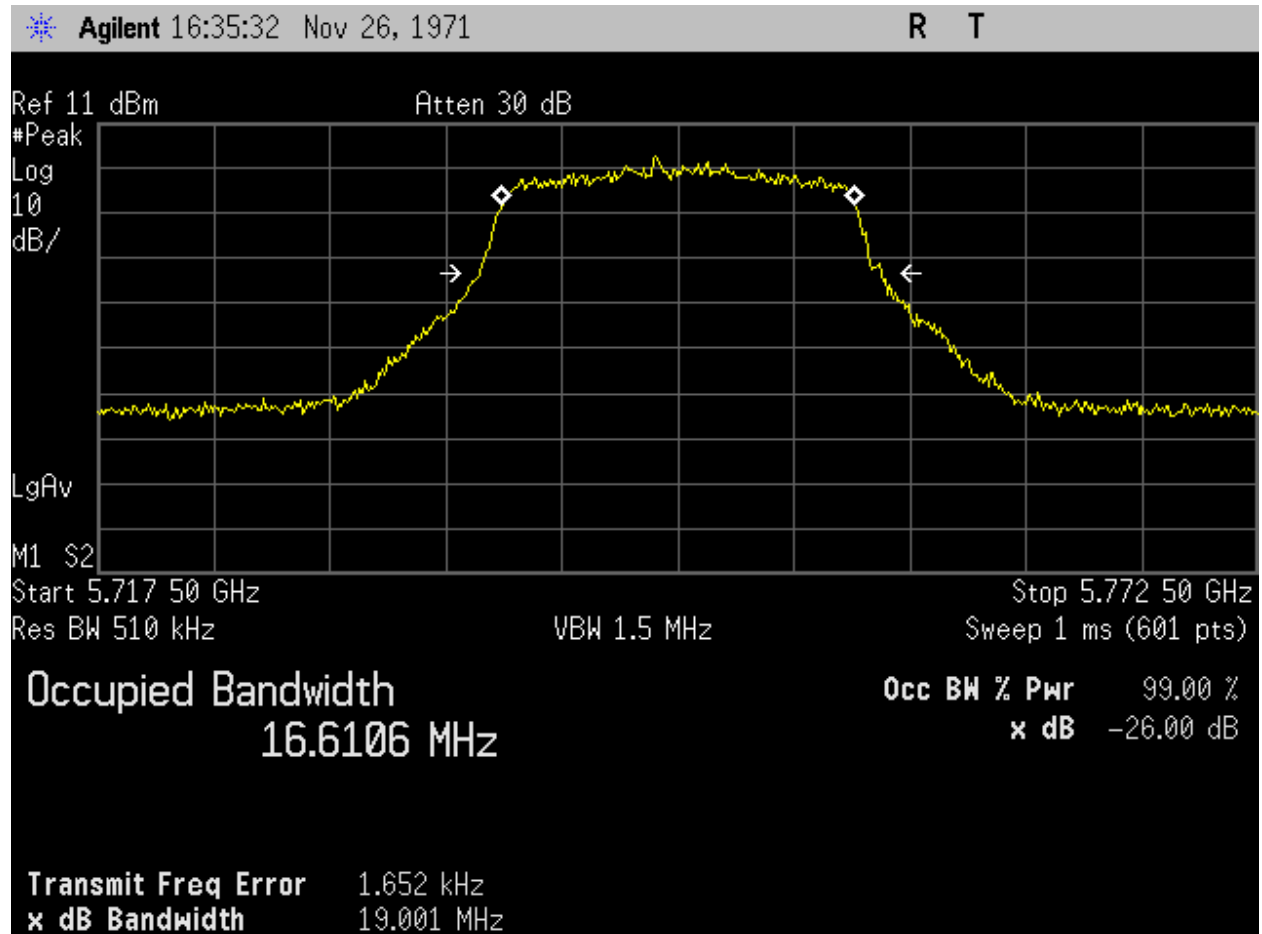


Figure 490: U-NII-3_5745MHz_Low Ch_149_20MHz BW_a-mode_Occupied Bandwidth_99 Percent_Port 1.

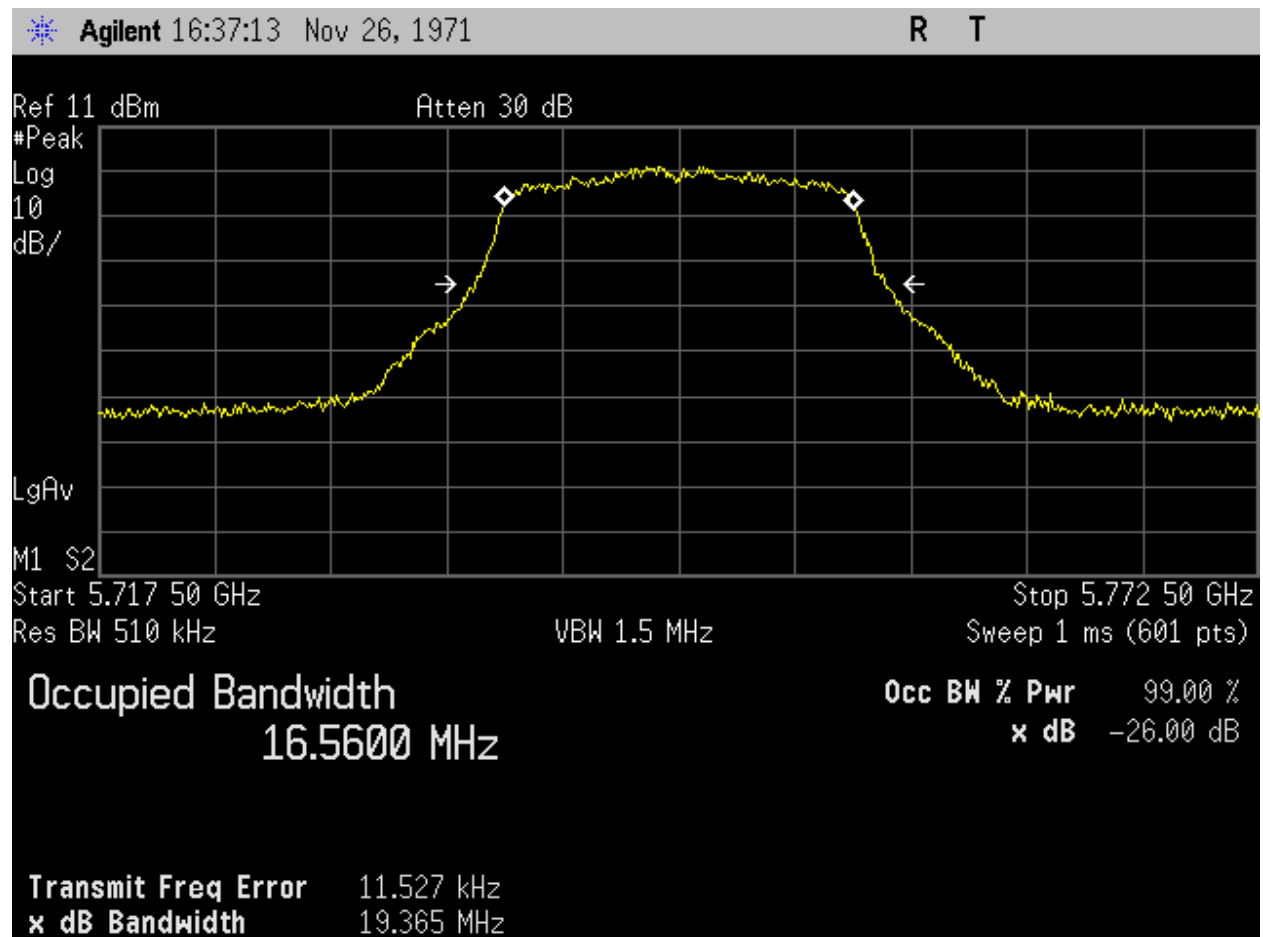


Figure 491: U-NII-3_5745MHz_Low Ch_149_20MHz BW_a-mode_Occupied Bandwidth_99 Percent_Port 2.

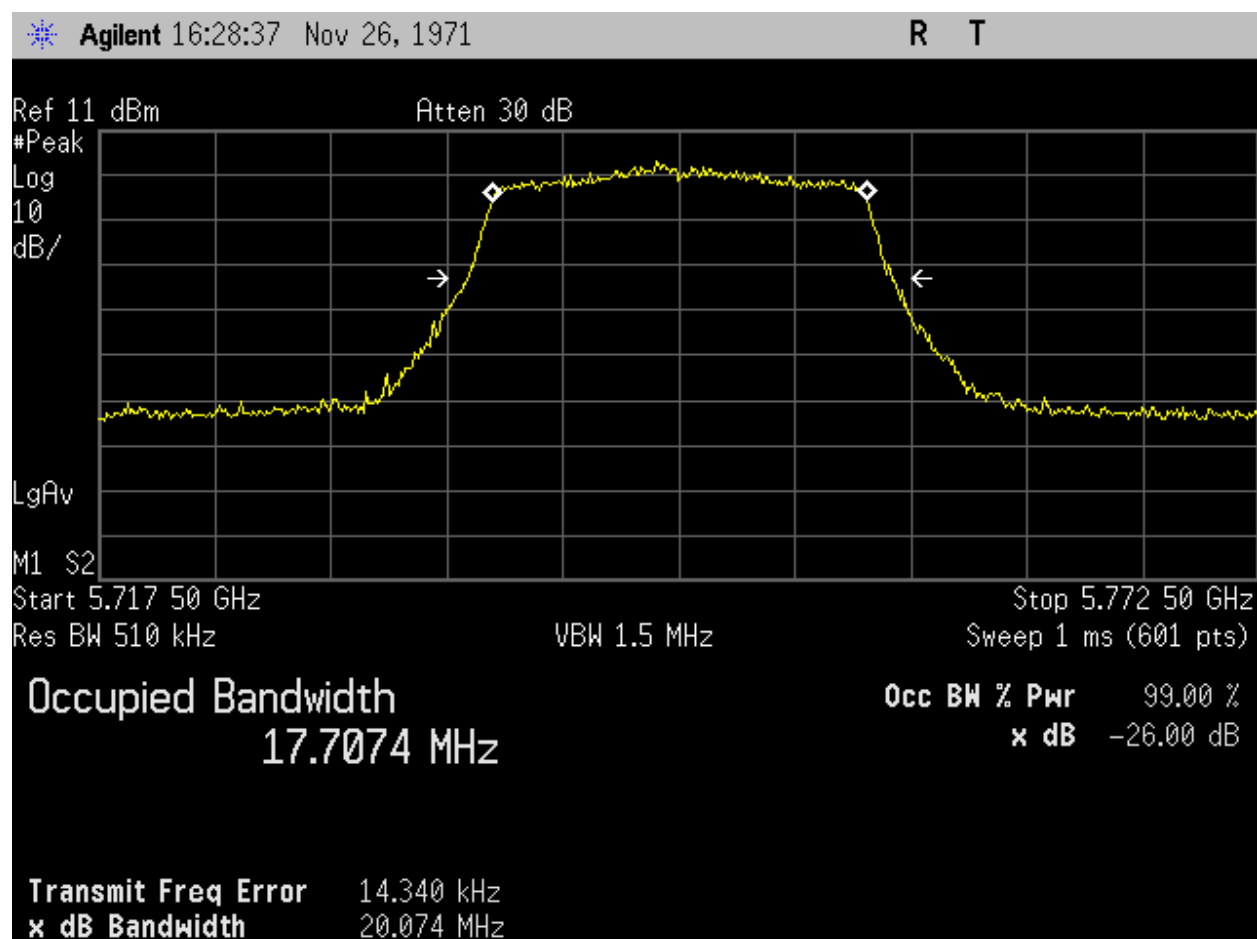


Figure 492: U-NII-3_5745MHz_Low Ch_149_20MHz BW_ac-mode_Occupied Bandwidth_99 Percent_Port 1.

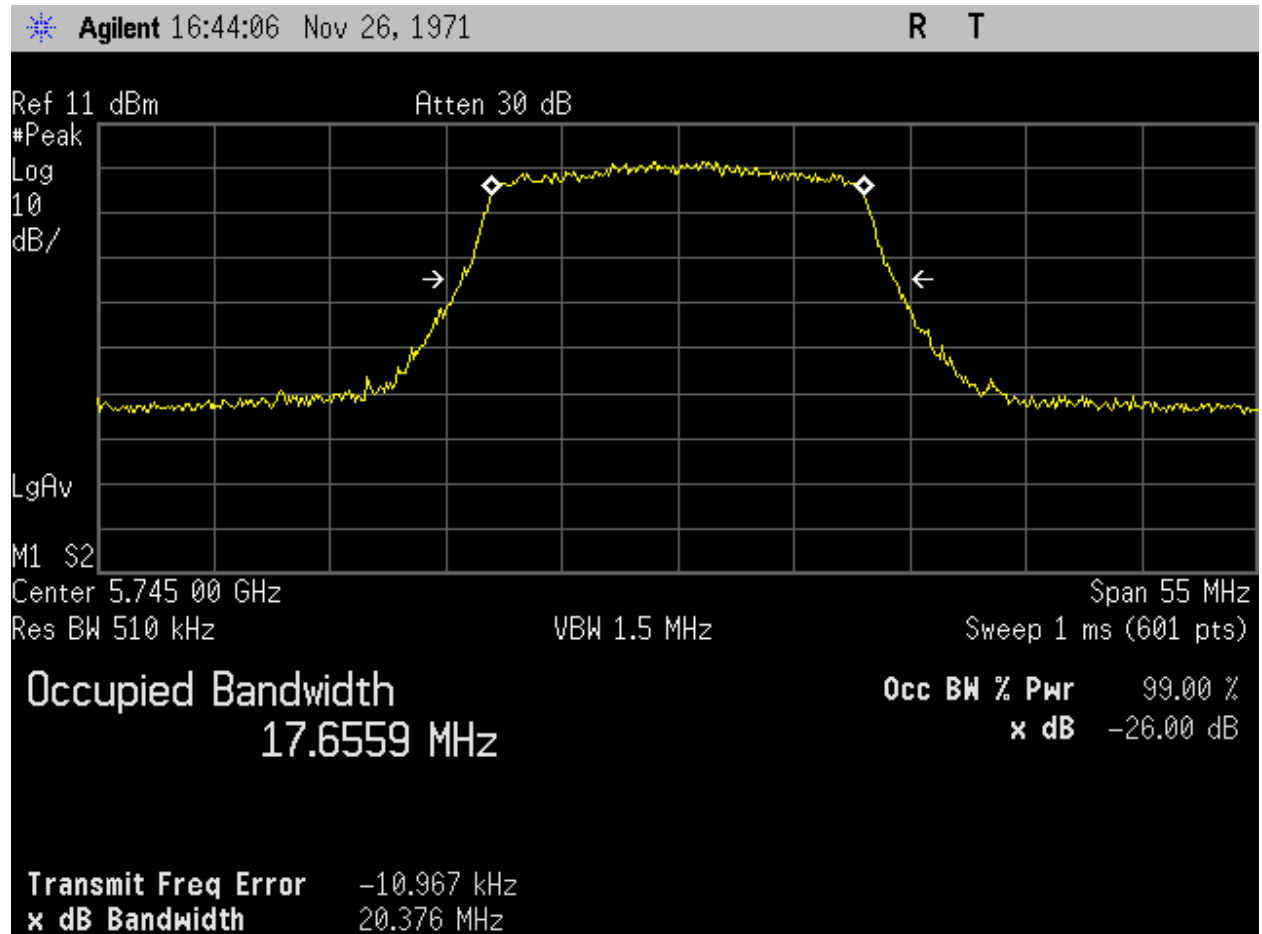


Figure 493: U-NII-3_5745MHz_Low Ch_149_20MHz BW_ac-mode_Occupied Bandwidth_99 Percent_Port 2.

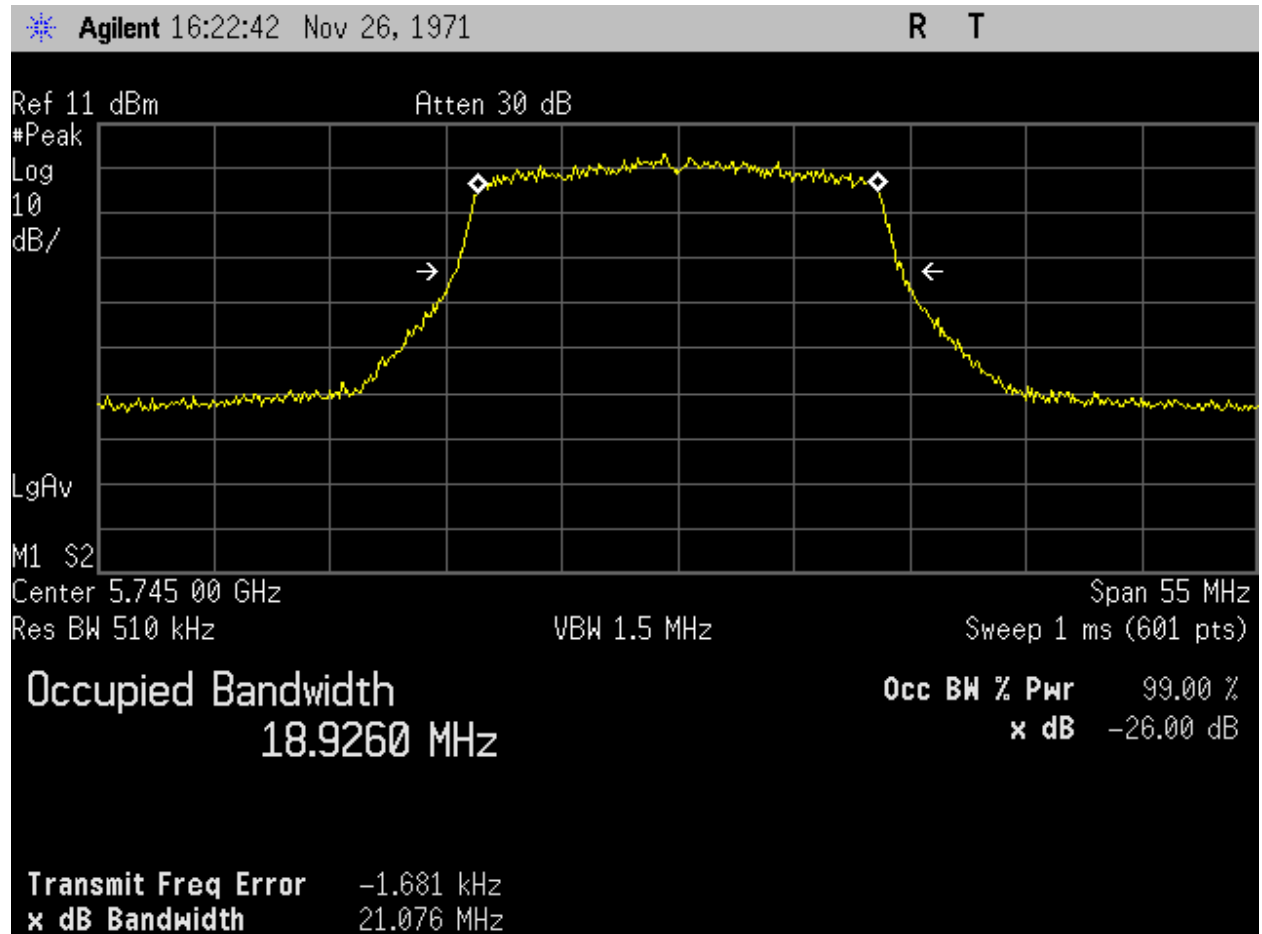


Figure 494: U-NII-3_5745MHz_Low Ch_149_20MHz BW_ax-mode_Occupied Bandwidth_99 Percent_Port 1.

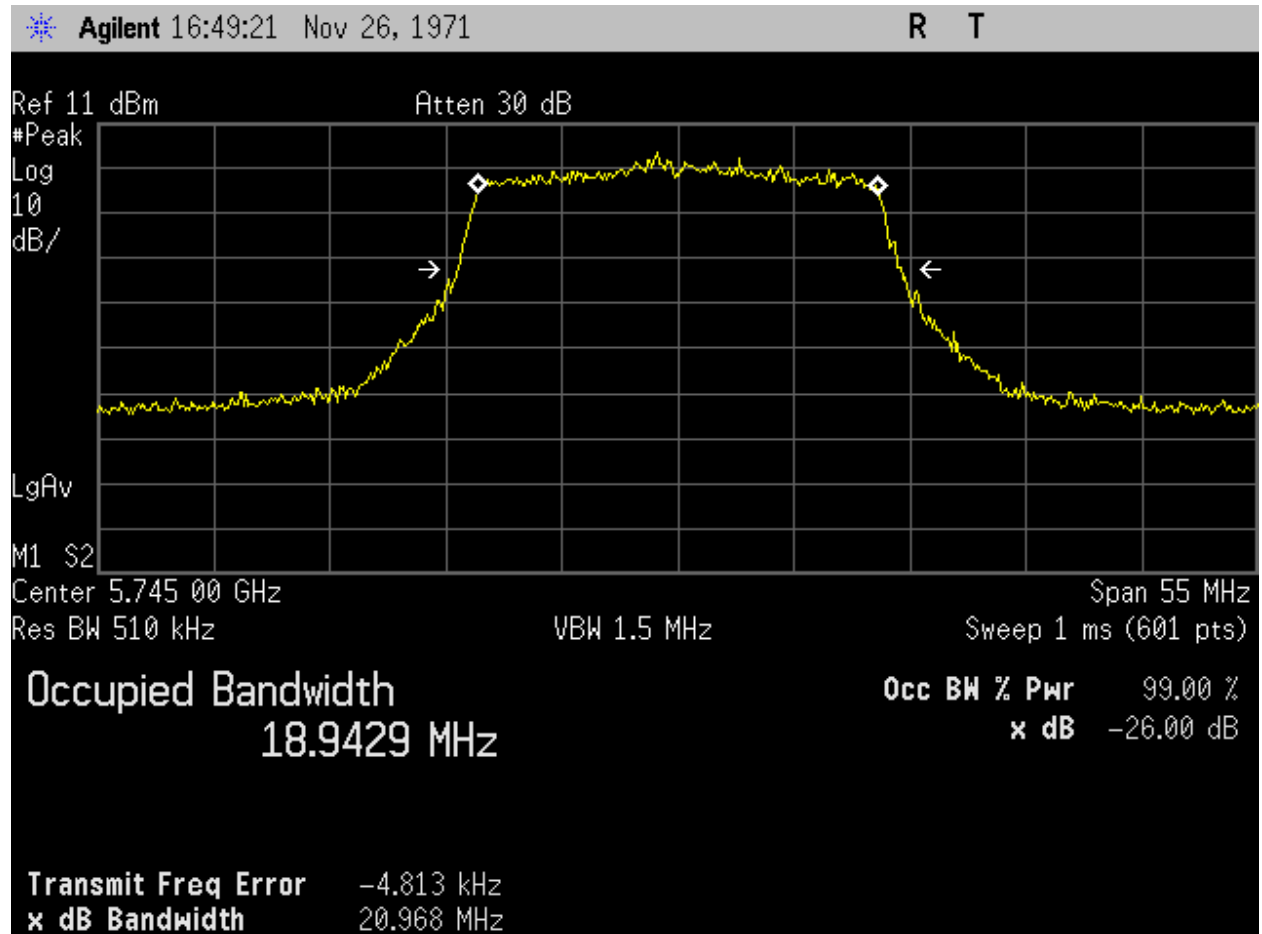


Figure 495: U-NII-3_5745MHz_Low Ch_149_20MHz BW_ax-mode_Occupied Bandwidth_99 Percent_Port 2.

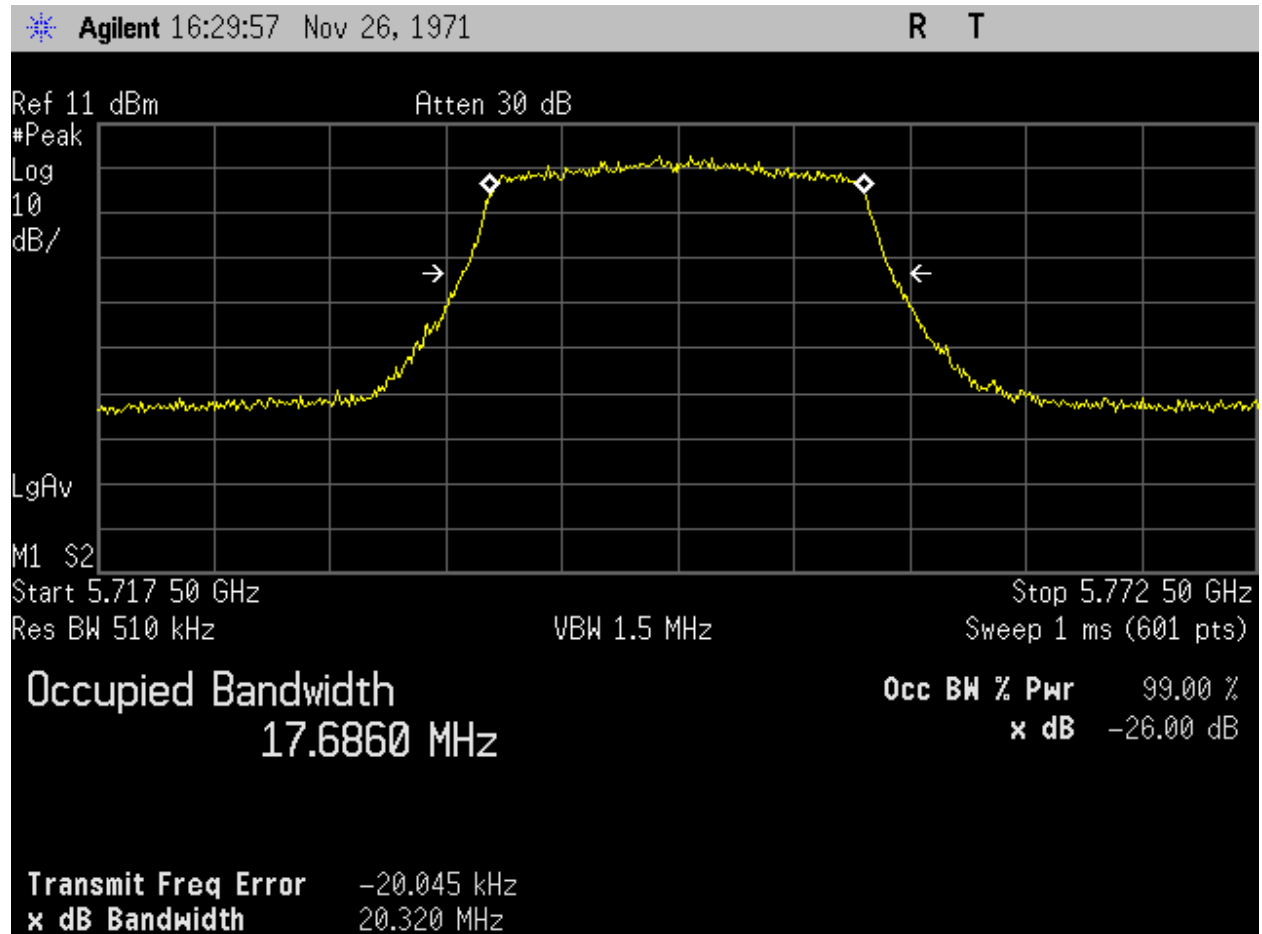


Figure 496: U-NII-3_5745MHz_Low Ch_149_20MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 1.

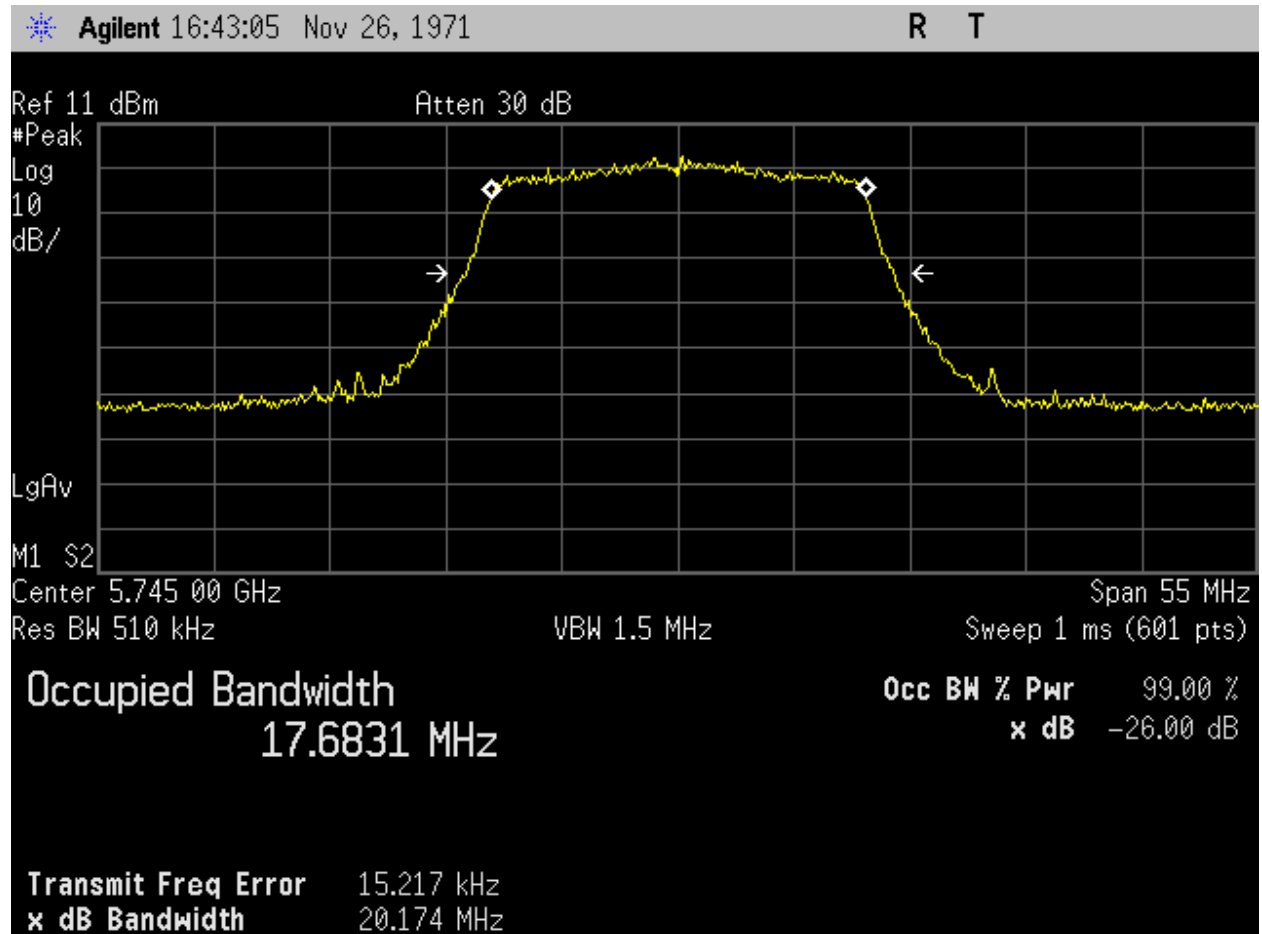


Figure 497: U-NII-3_5745MHz_Low Ch_149_20MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 2.

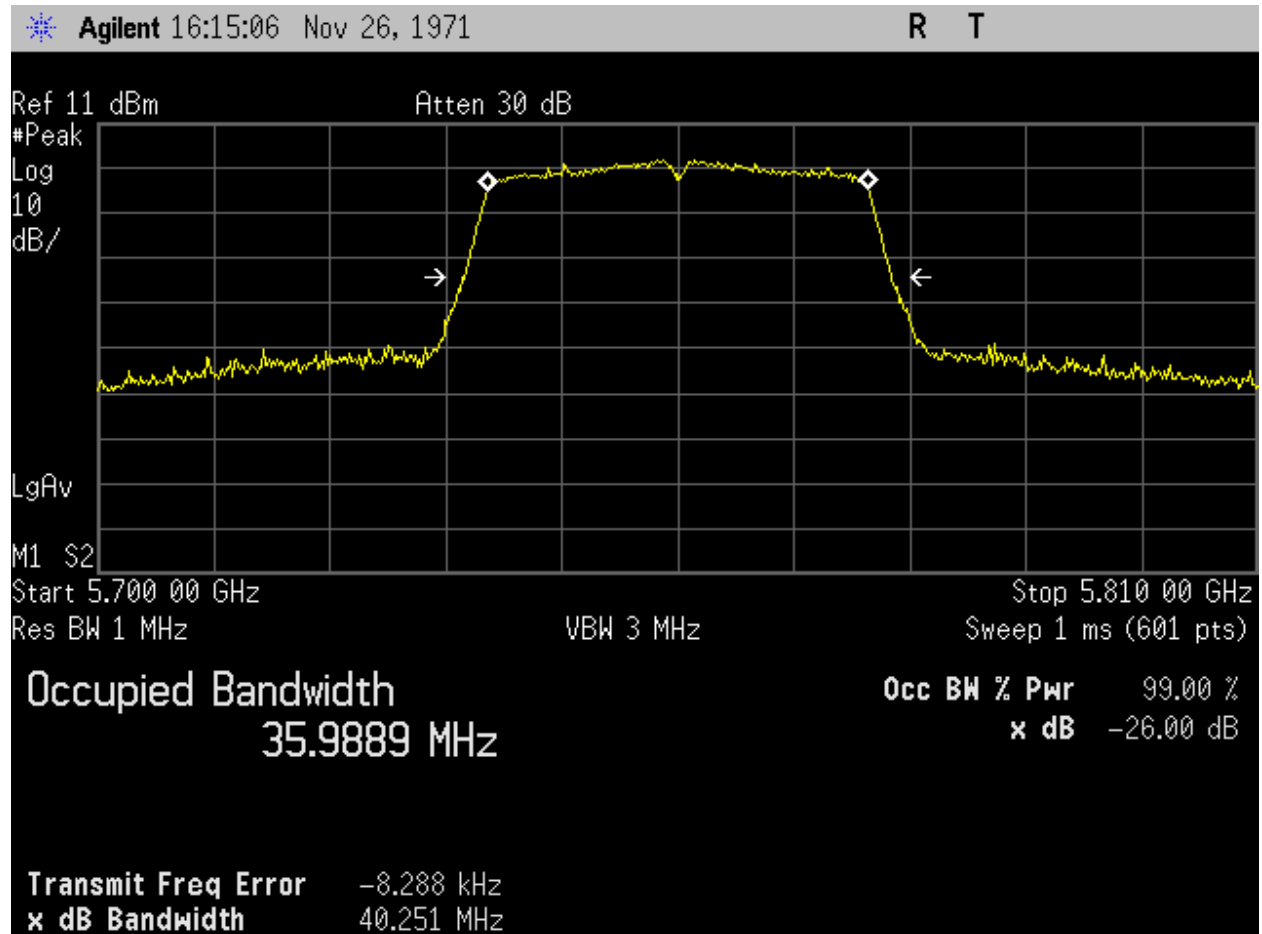


Figure 498: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ac-mode_Occupied Bandwidth_99 Percent_Port 1.

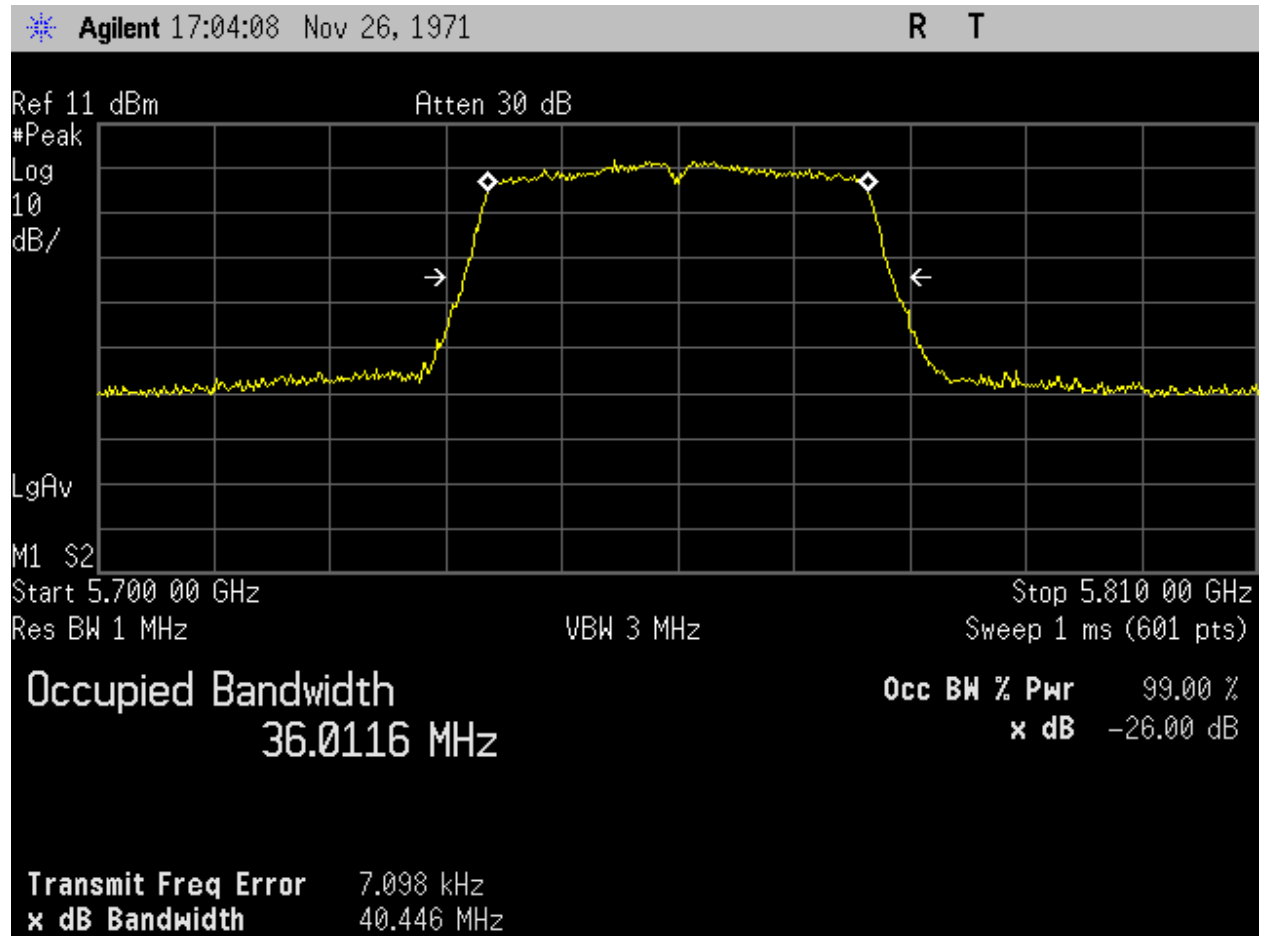


Figure 499: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ac-mode_Occupied Bandwidth_99 Percent_Port 2.

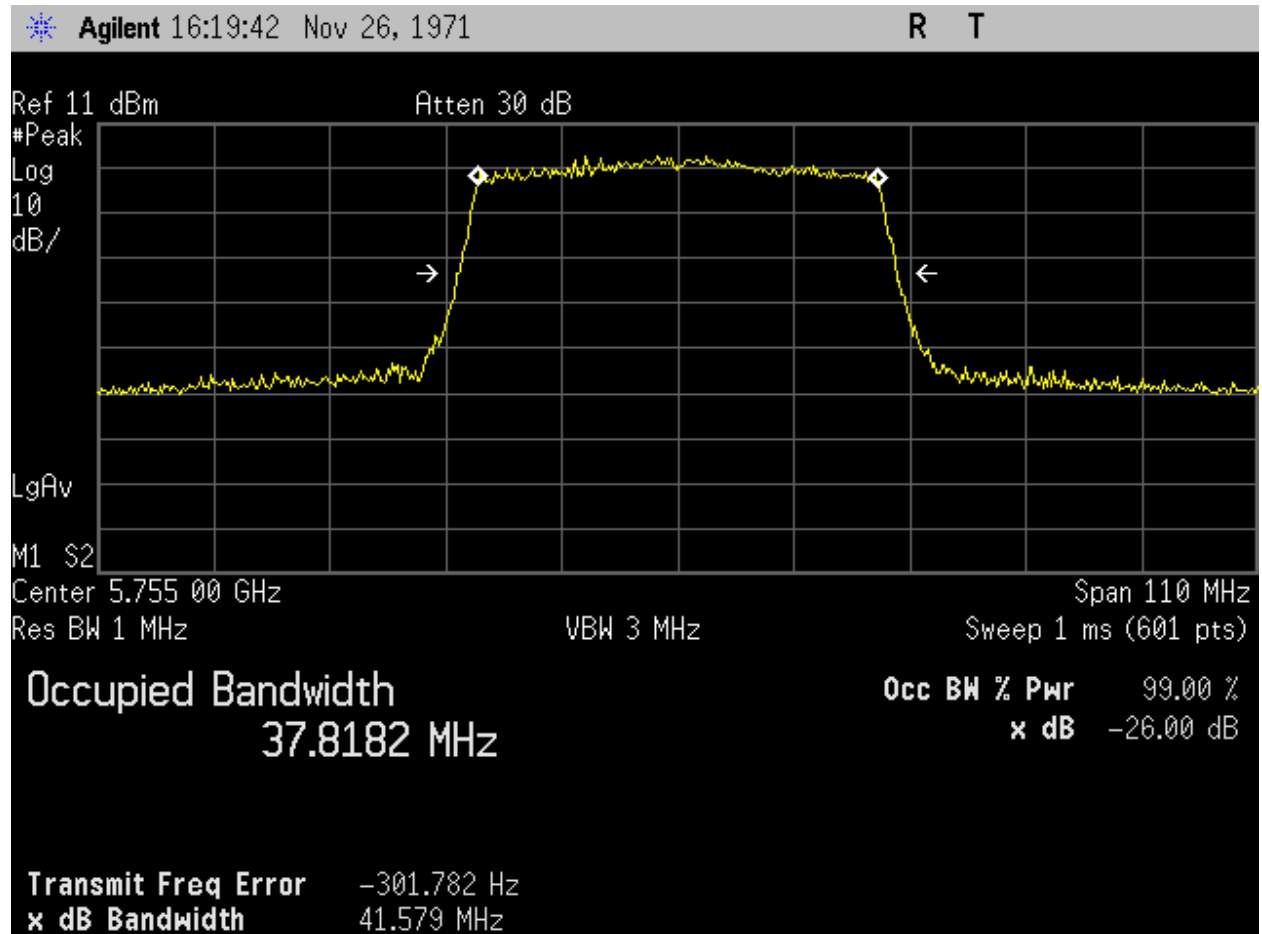


Figure 500: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ax-mode_Occupied Bandwidth_99 Percent_Port 1.

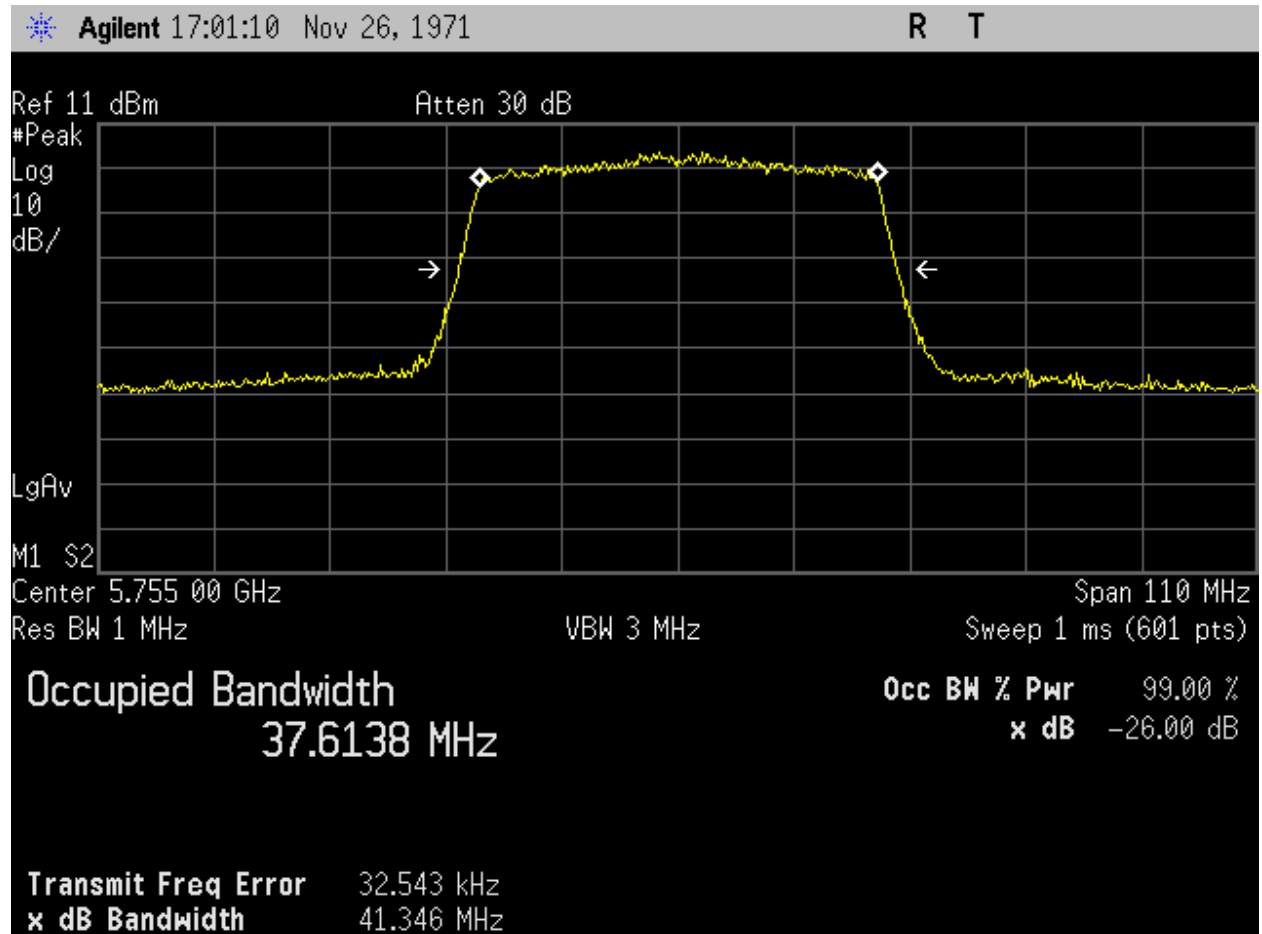


Figure 501: U-NII-3_5755MHz_Low Ch_151_40MHz BW_ax-mode_Occupied Bandwidth_99 Percent_Port 2.

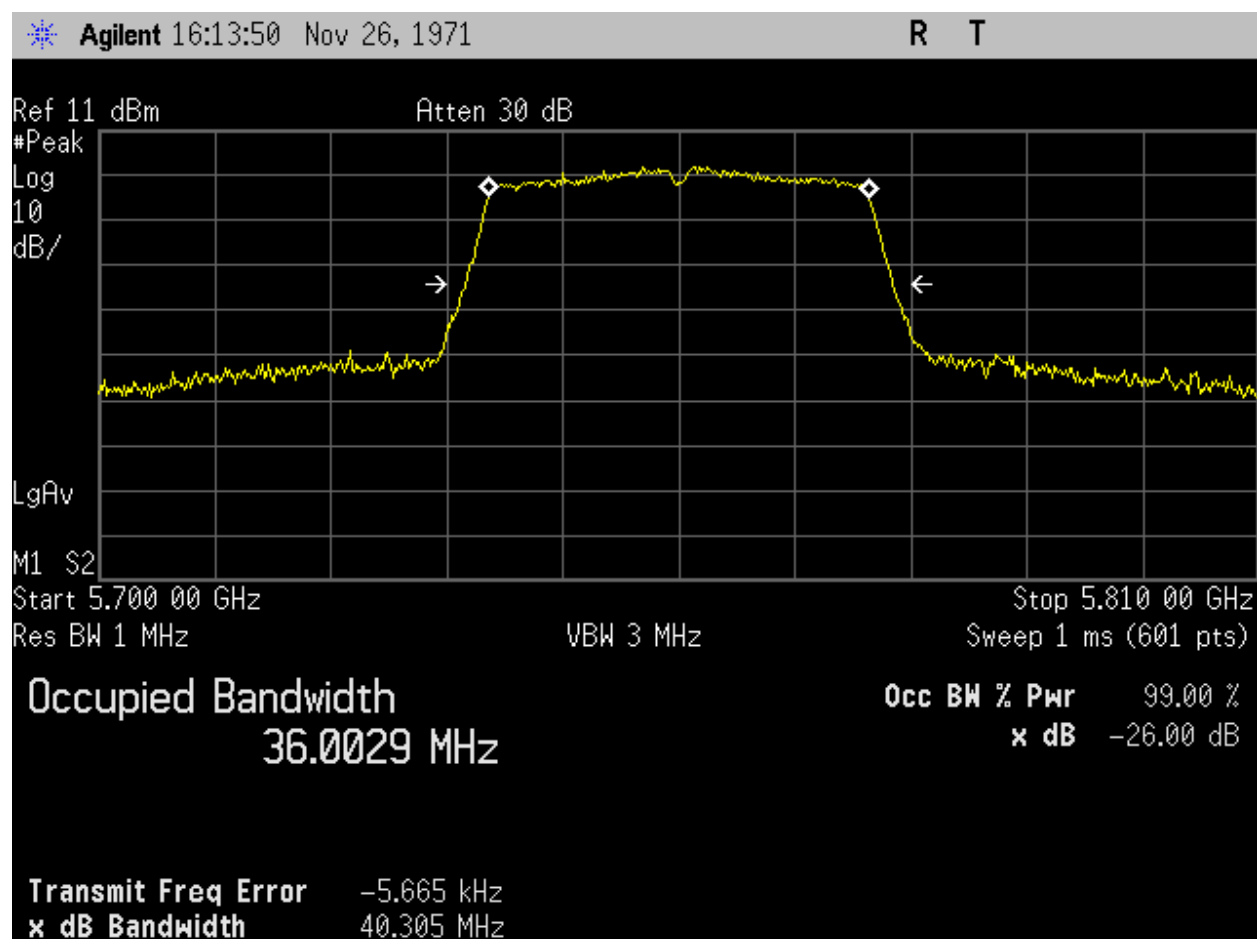


Figure 502: U-NII-3_5755MHz_Low Ch_151_40MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 1.

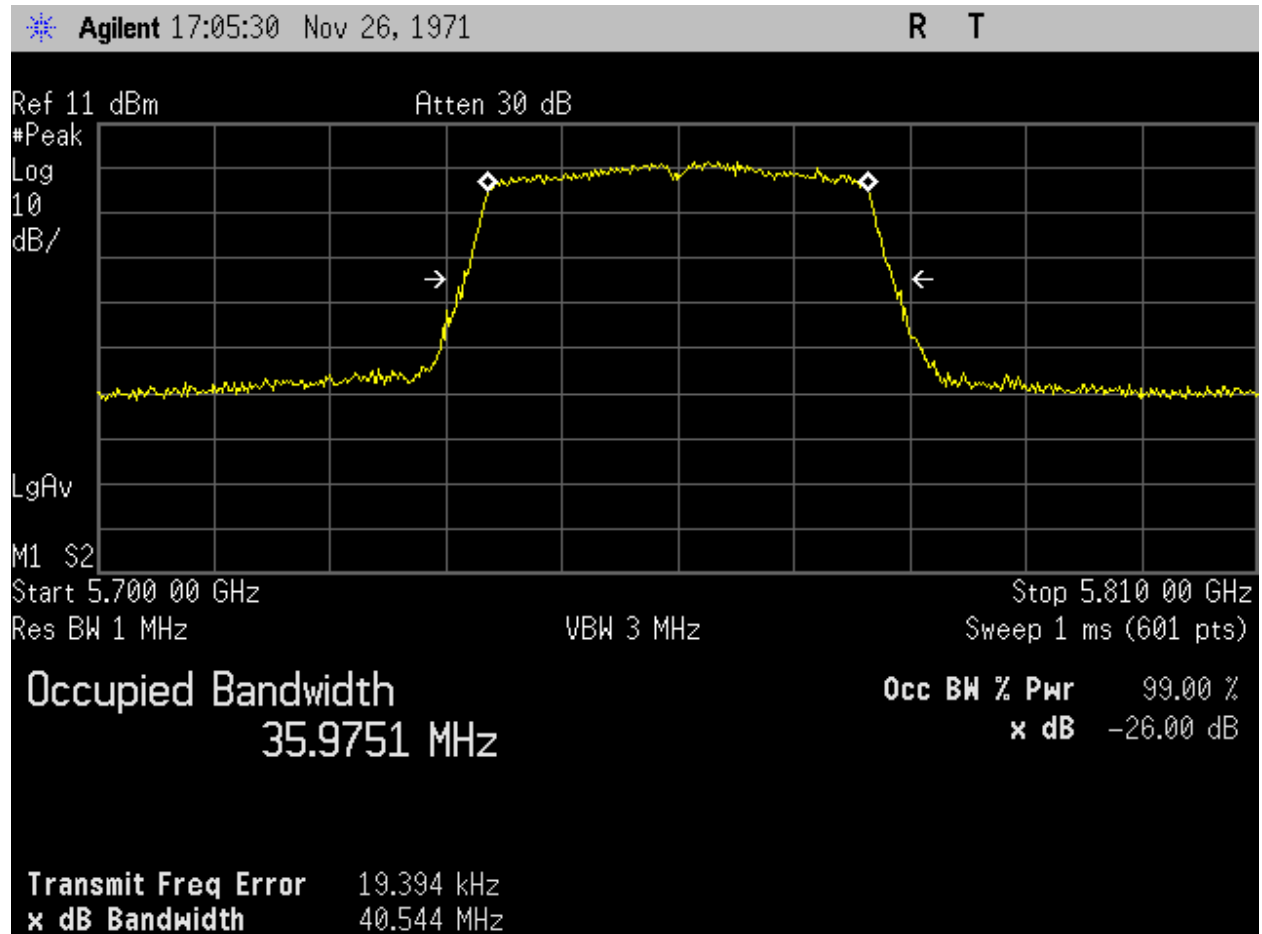


Figure 503: U-NII-3_5755MHz_Low Ch_151_40MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 2.

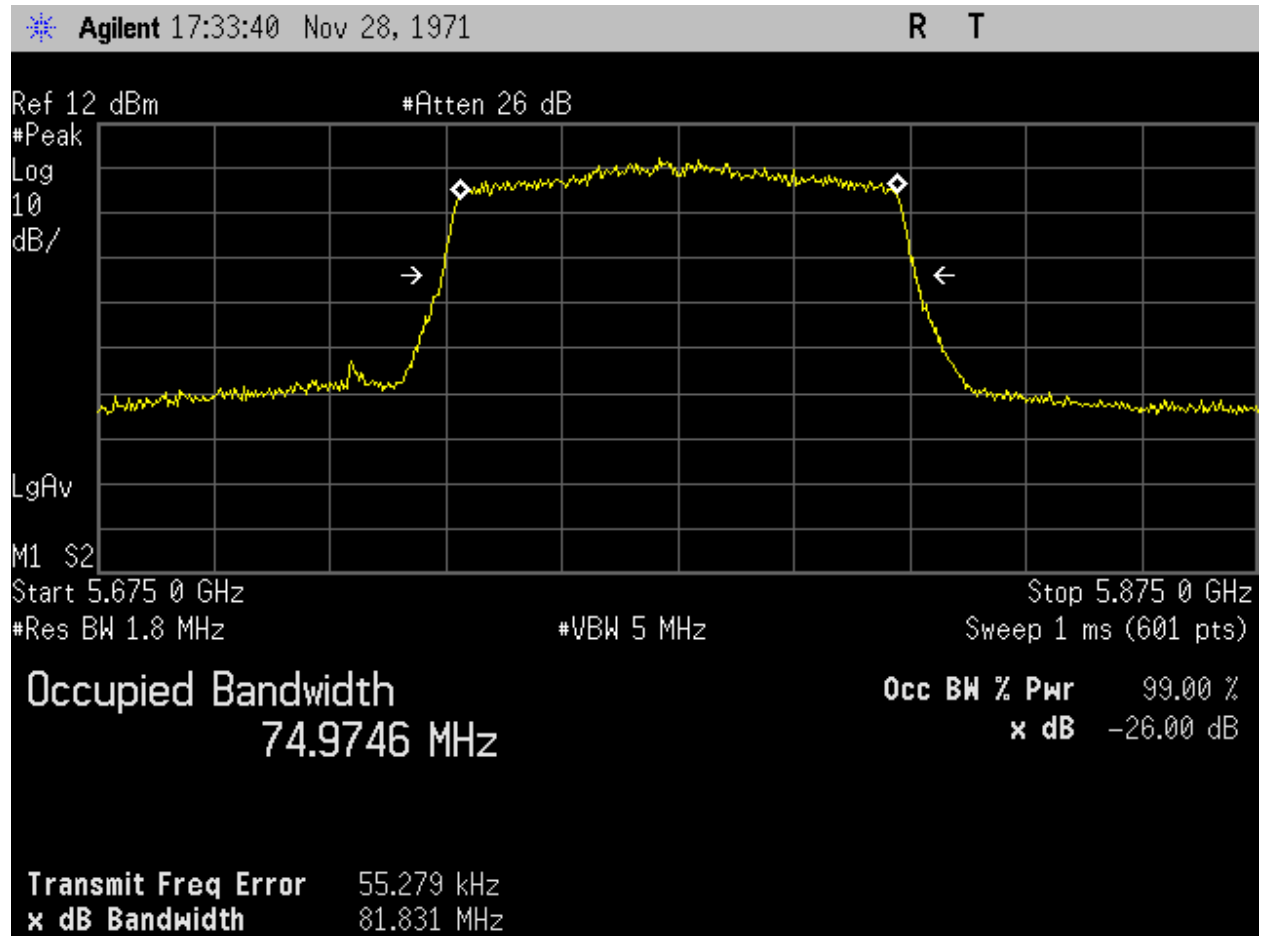


Figure 504: U-NII-3_5775MHz_Mid Ch_155_80MHz BW_ac-mode_Occupied Bandwidth_99 Percent_Port 1.

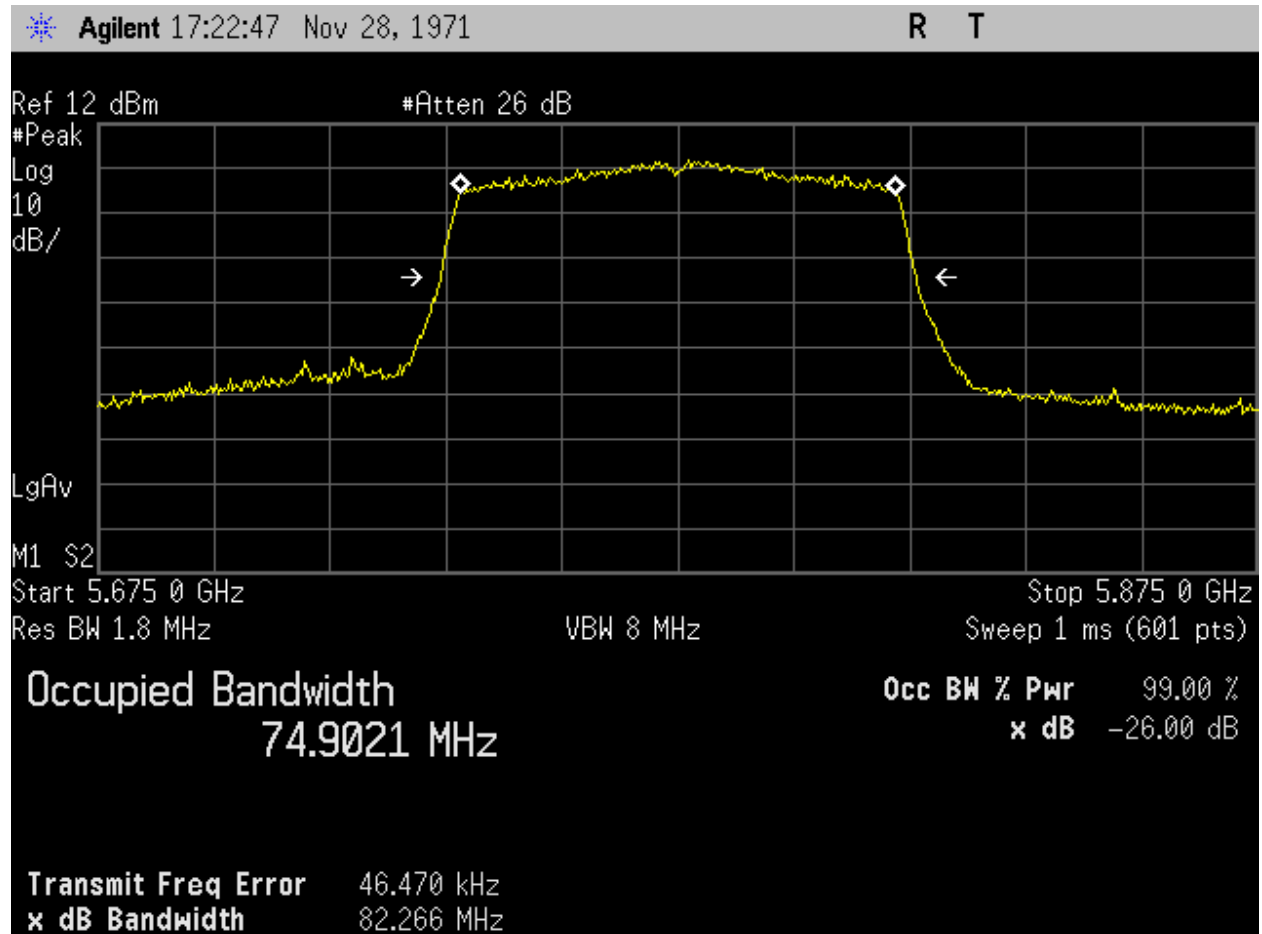


Figure 505: U-NII-3_5775MHz_Mid Ch_155_80MHz BW_ac-mode_Occupied Bandwidth_99 Percent_Port 2.

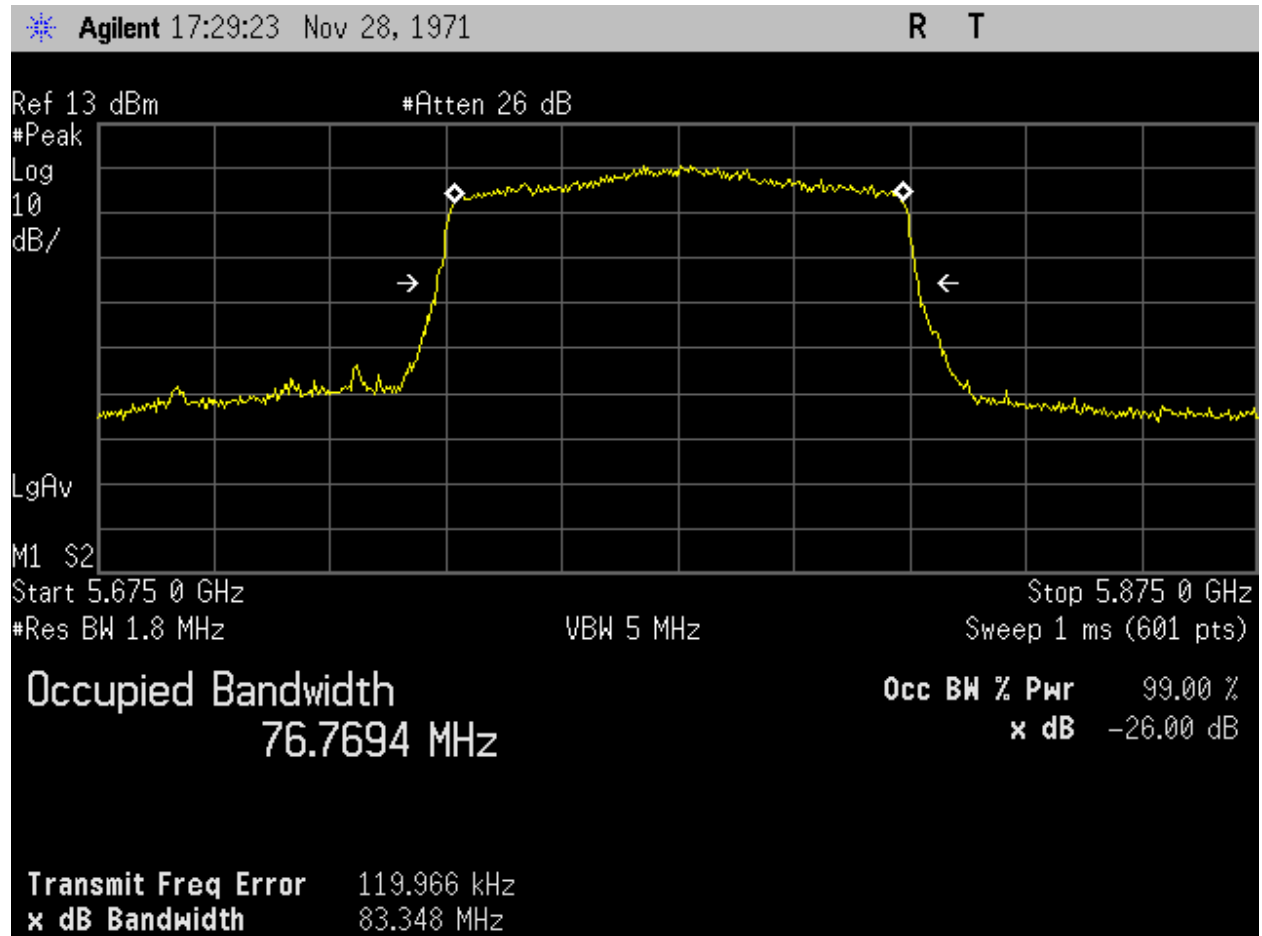


Figure 506: U-NII-3_5775MHz_Mid Ch_155_80MHz BW_ax-mode_Occupied Bandwidth_99 Percent_Port 1.

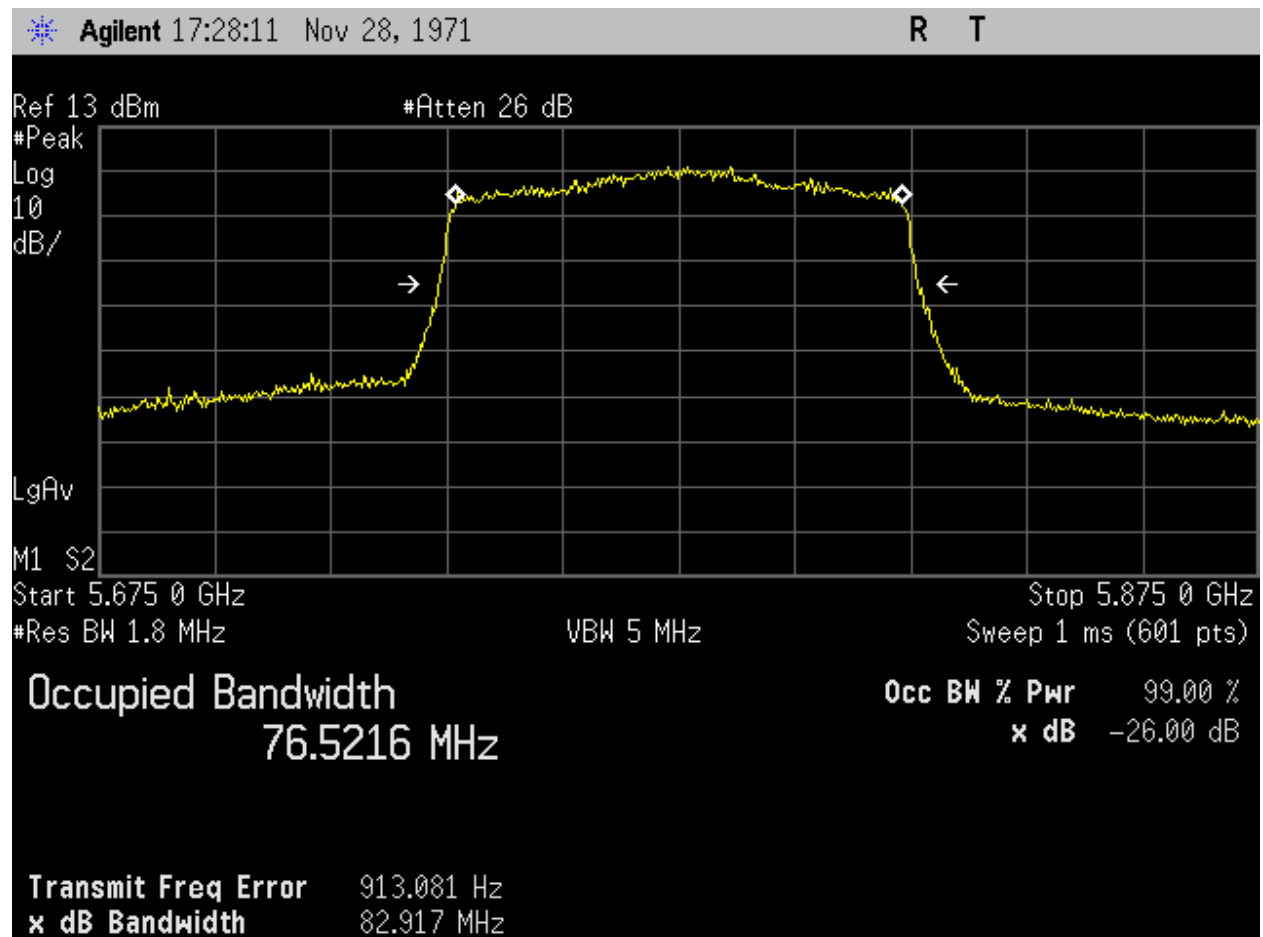


Figure 507: U-NII-3_5775MHz_Mid Ch_155_80MHz BW_ax-mode_Occupied Bandwidth_99 Percent_Port 2.

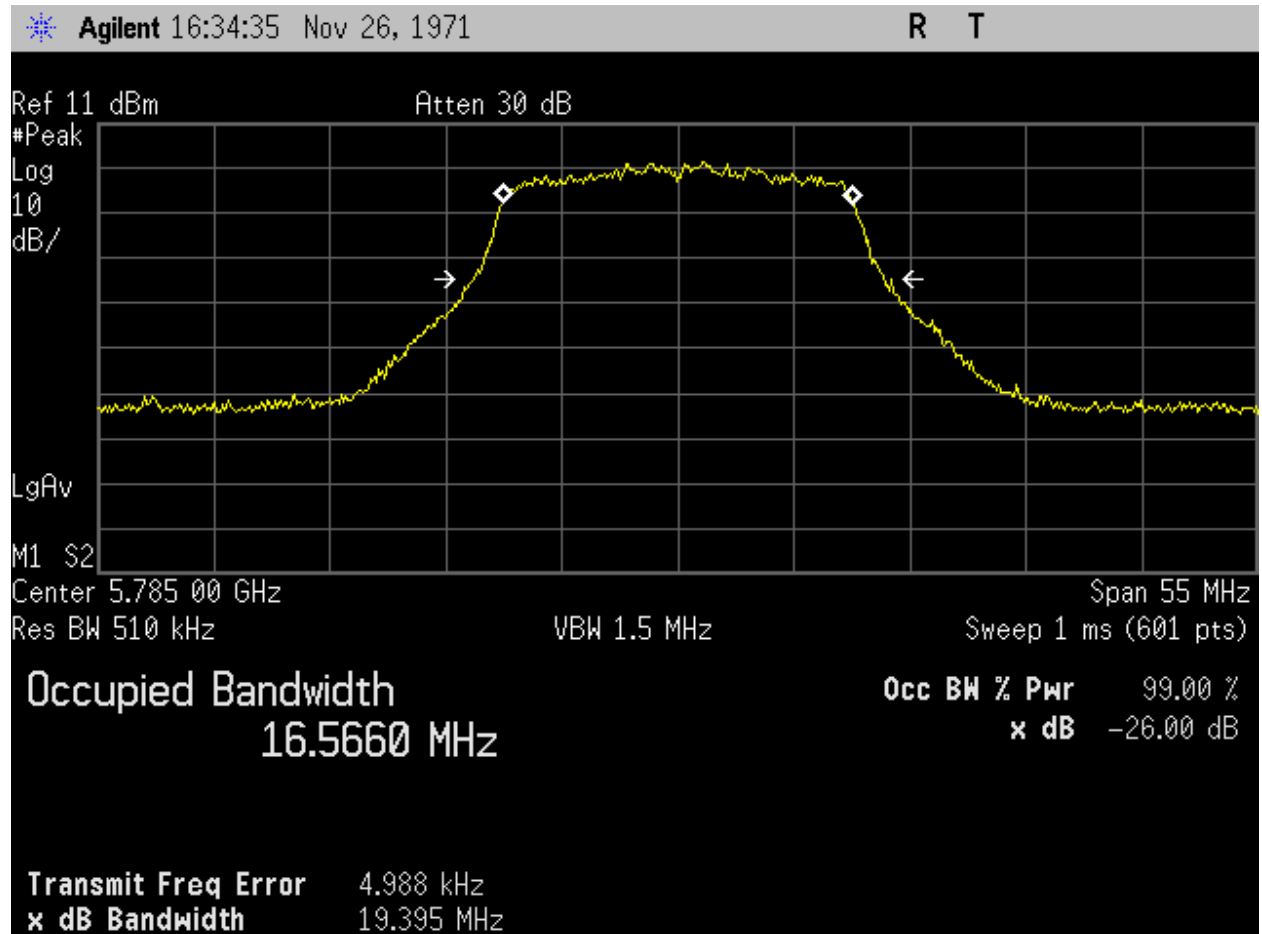


Figure 508: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_a-mode_Occupied Bandwidth_99 Percent_Port 1.

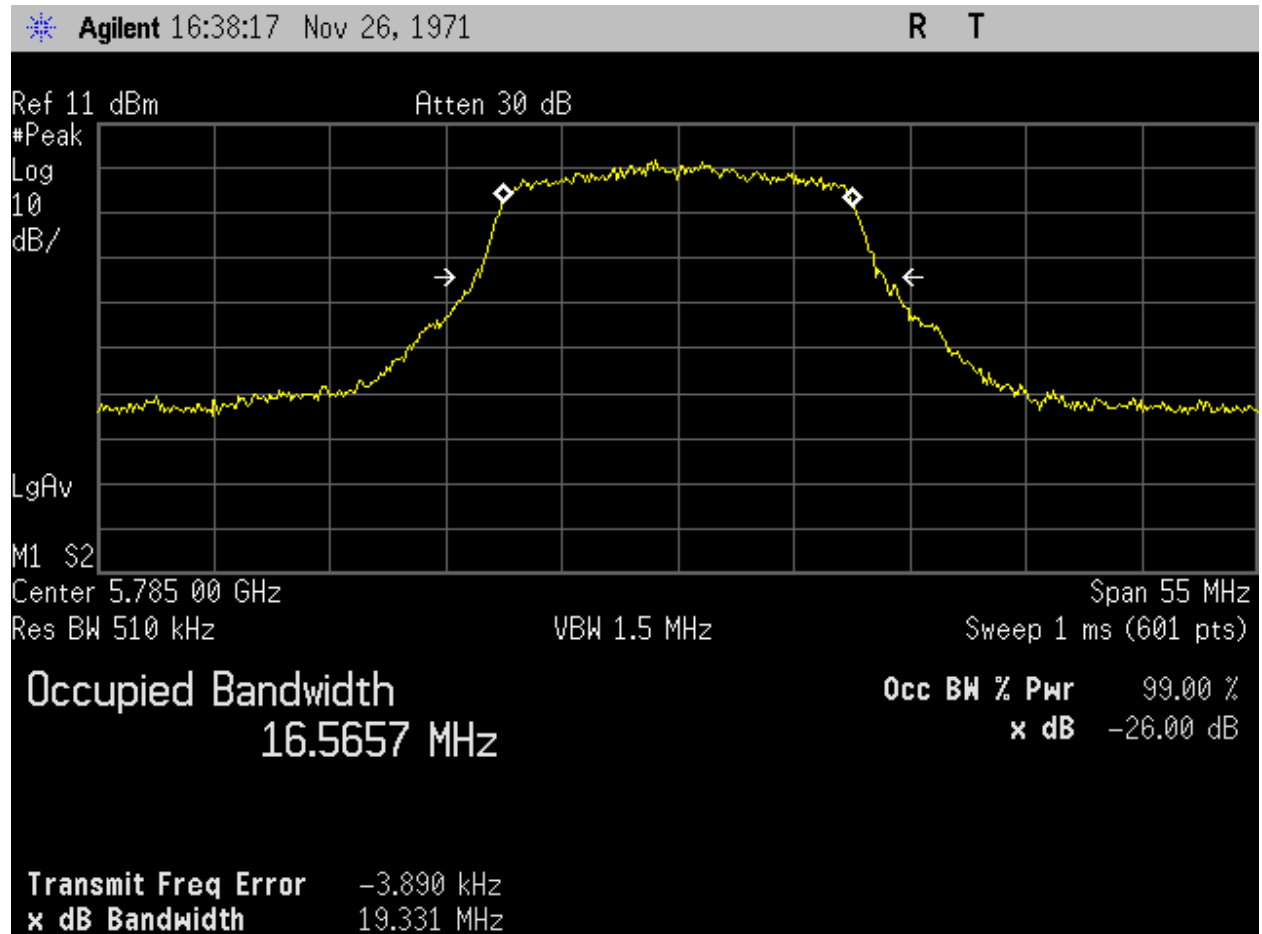


Figure 509: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_a-mode_Occupied Bandwidth_99 Percent_Port 2.

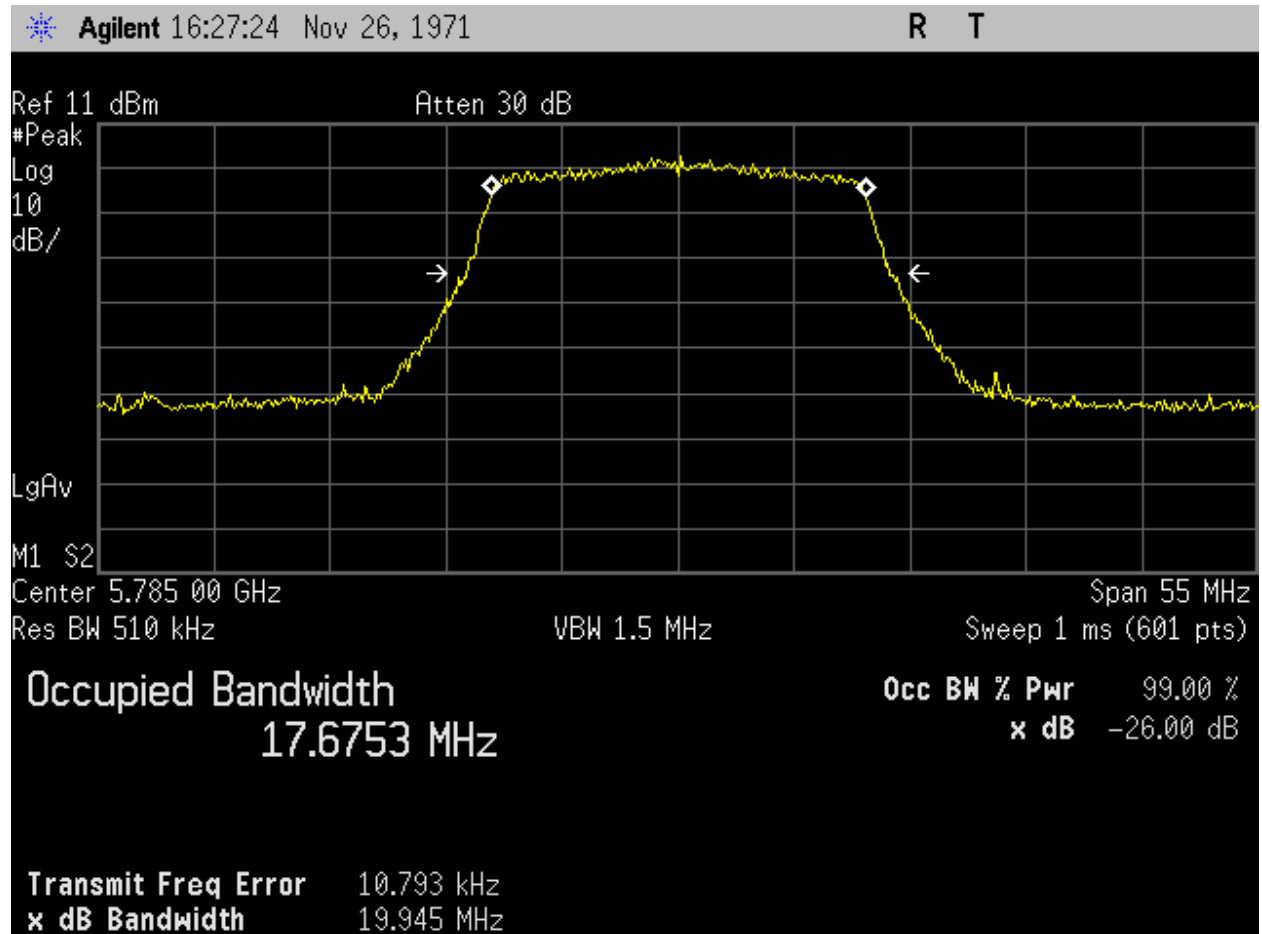


Figure 510: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_ac-mode_Occupied Bandwidth_99 Percent_Port 1.

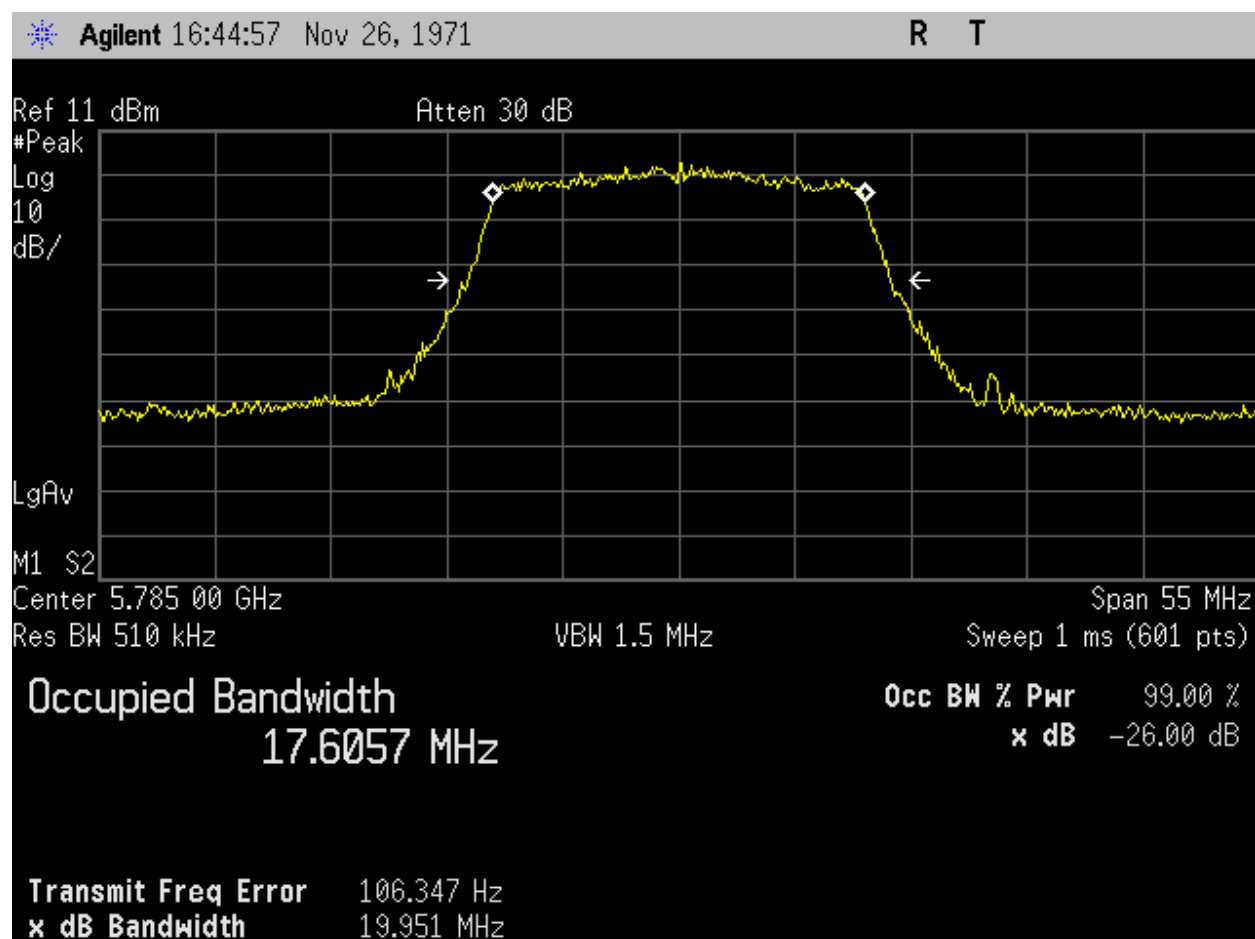


Figure 511: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_ac-mode_Occupied Bandwidth_99 Percent_Port 2.

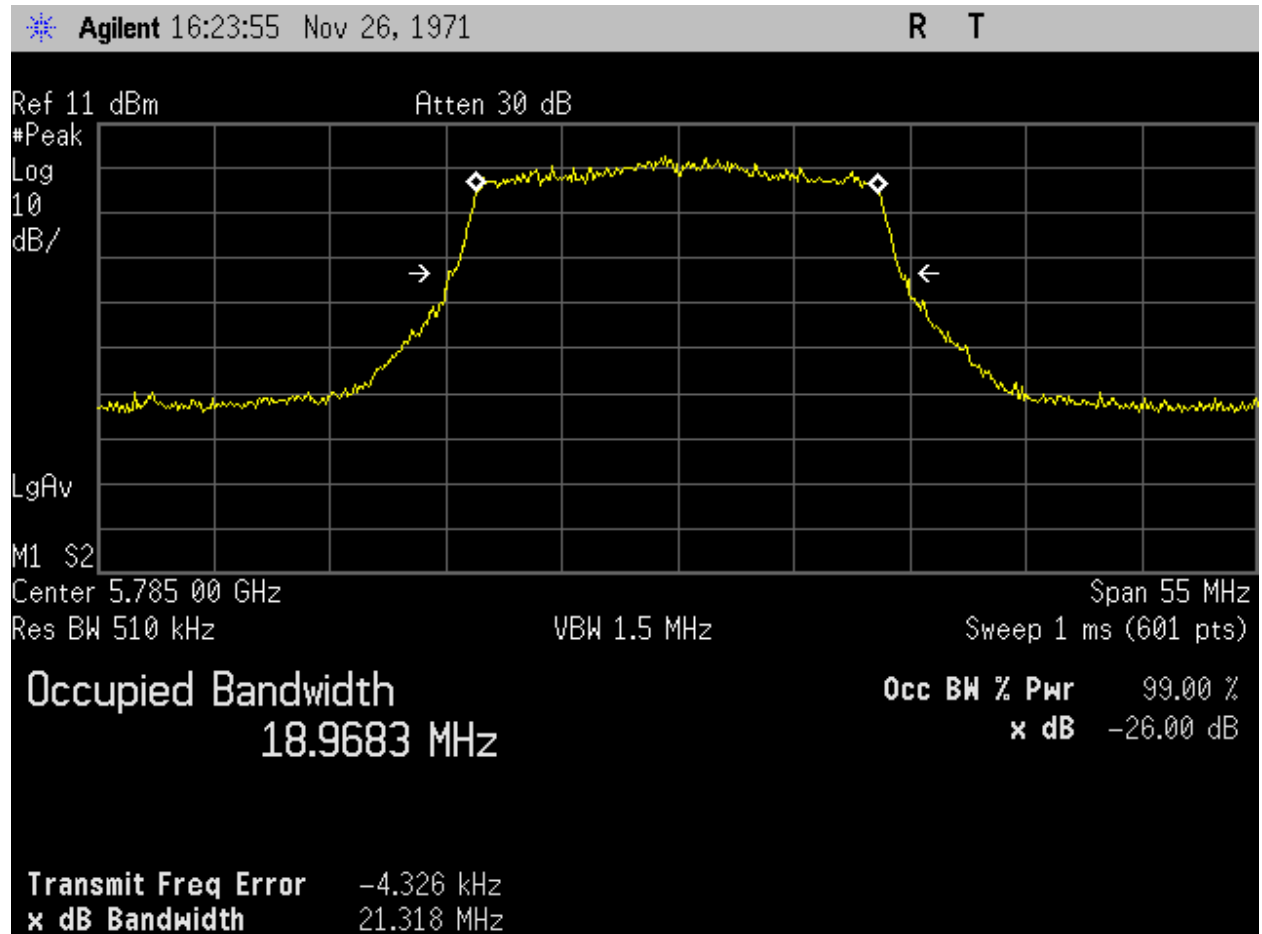


Figure 512: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_ax-mode_Occupied Bandwidth_99 Percent_Port 1.

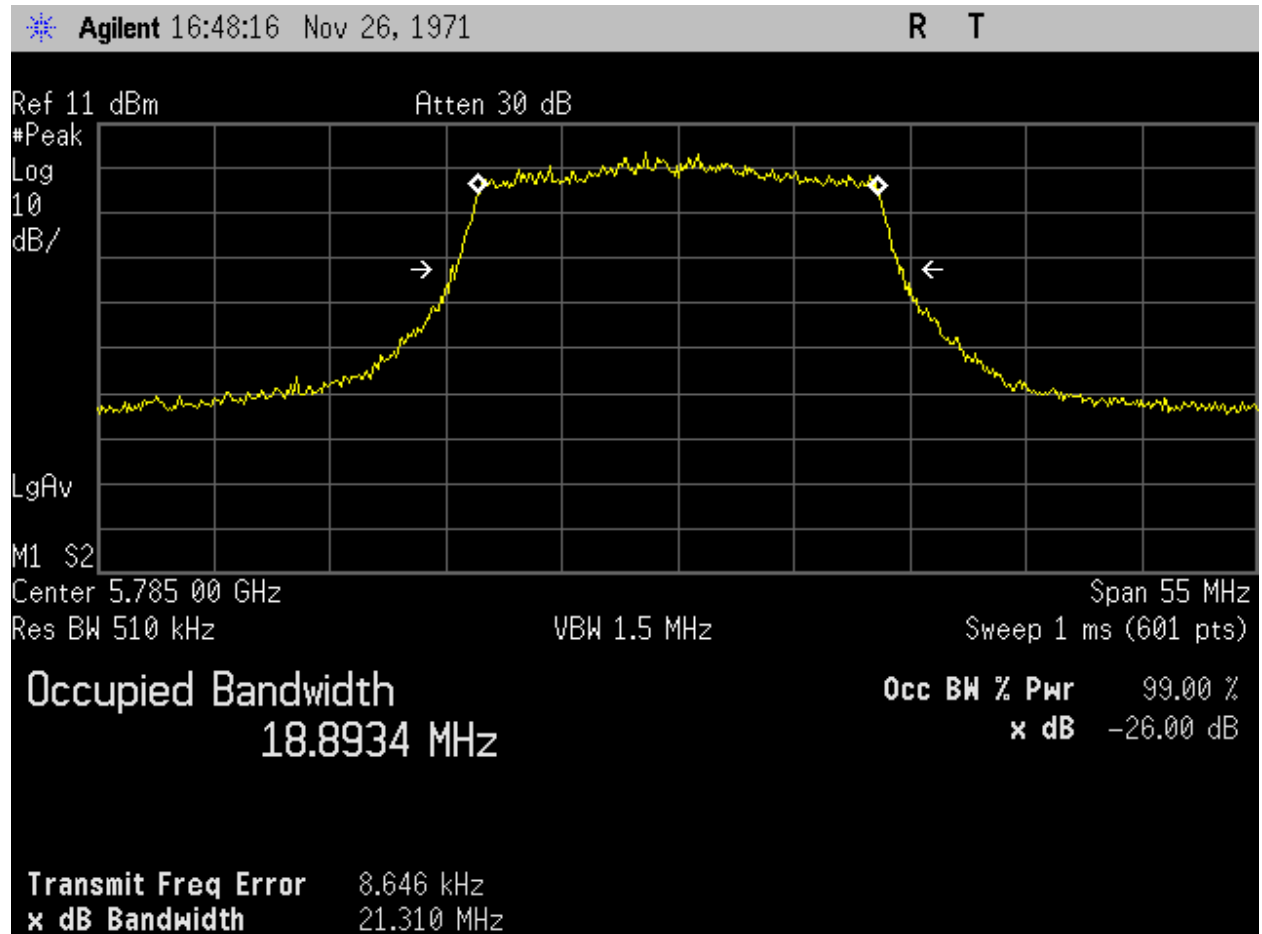


Figure 513: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_ax-mode_Occupied Bandwidth_99 Percent_Port 2.

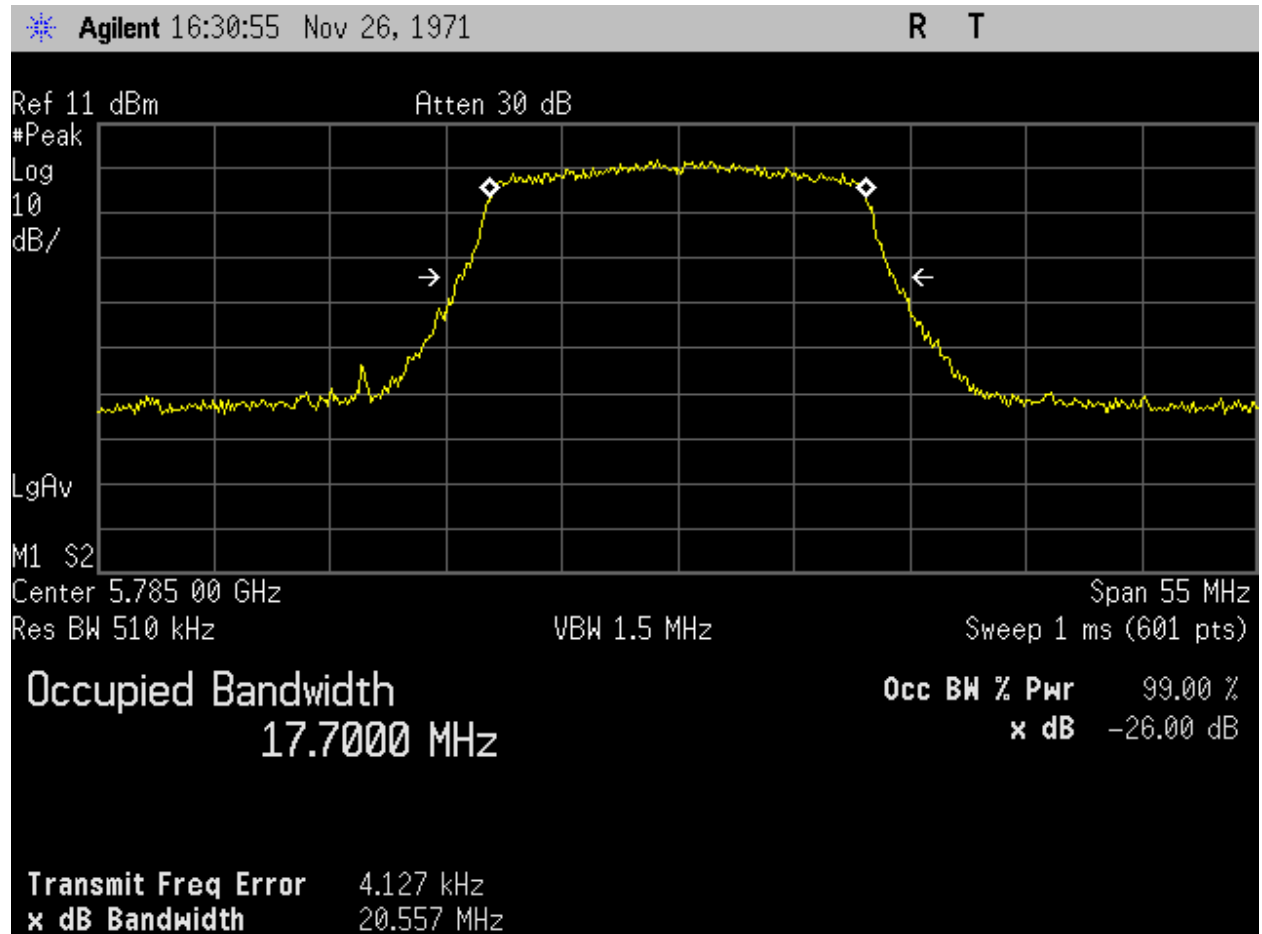


Figure 514: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 1.

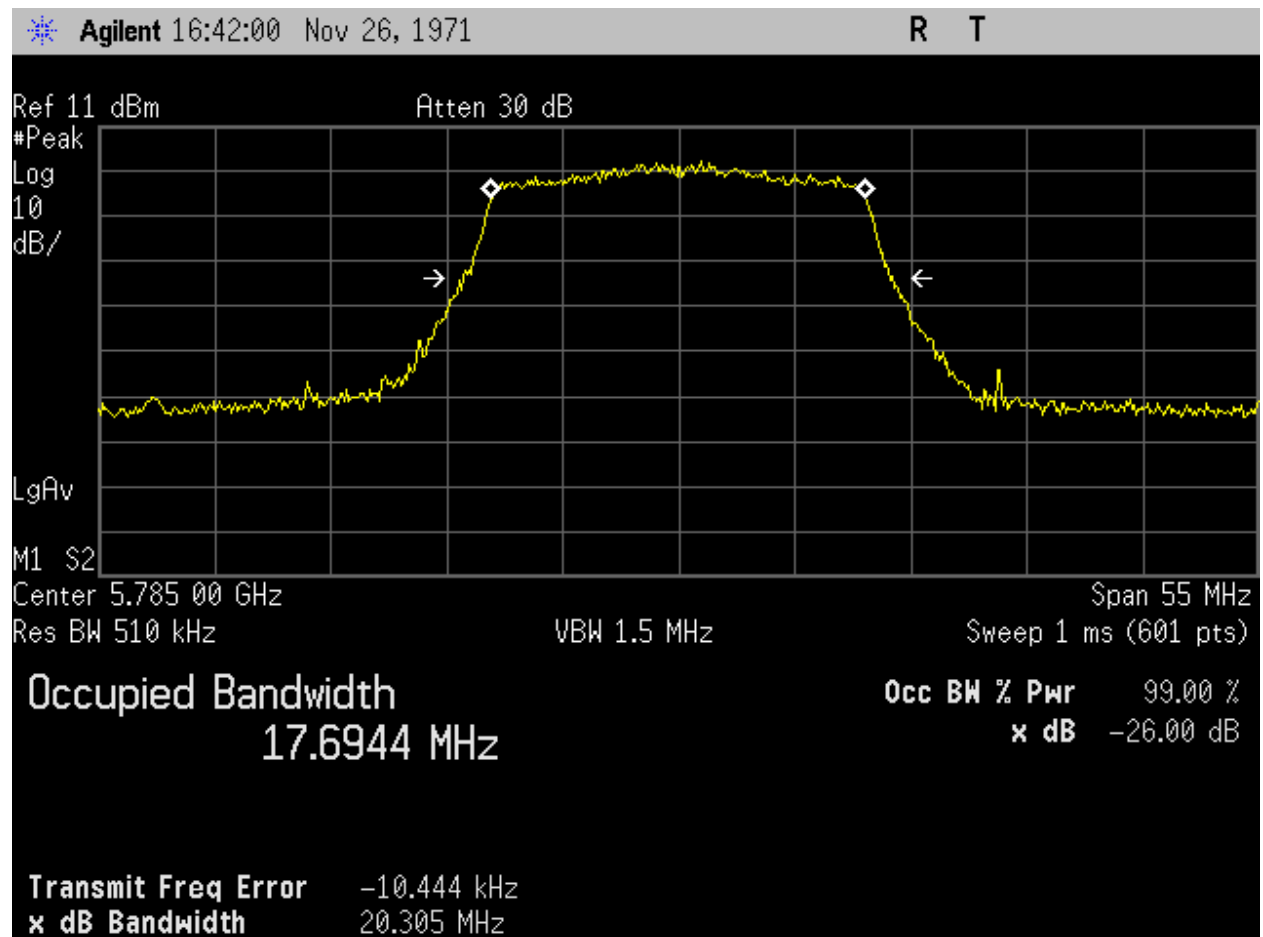


Figure 515: U-NII-3_5785MHz_Mid Ch_157_20MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 2.

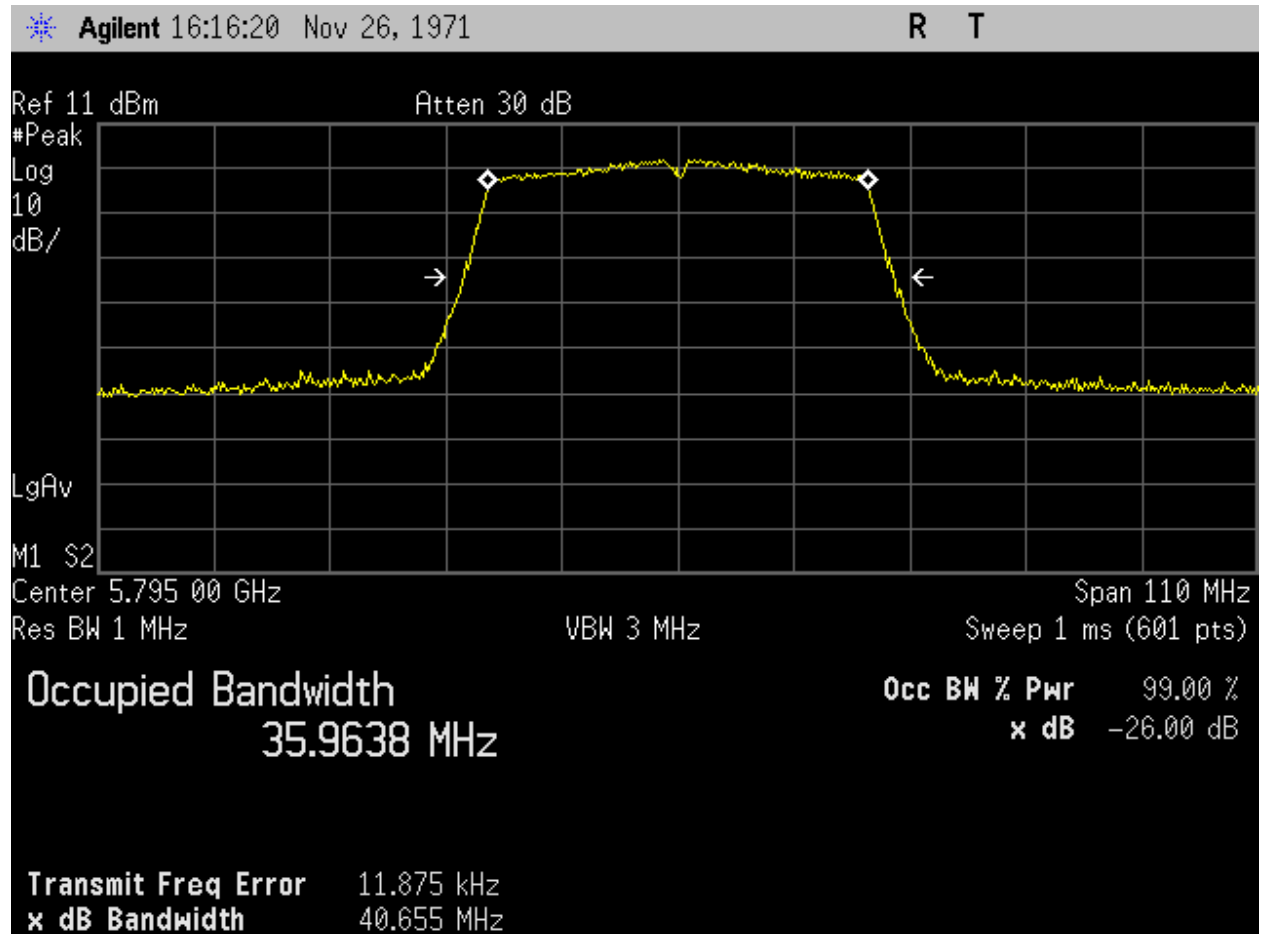


Figure 516: U-NII-3_5795MHz_High Ch_159_40MHz BW_ac-mode_Occupied Bandwidth_99 Percent_Port 1.

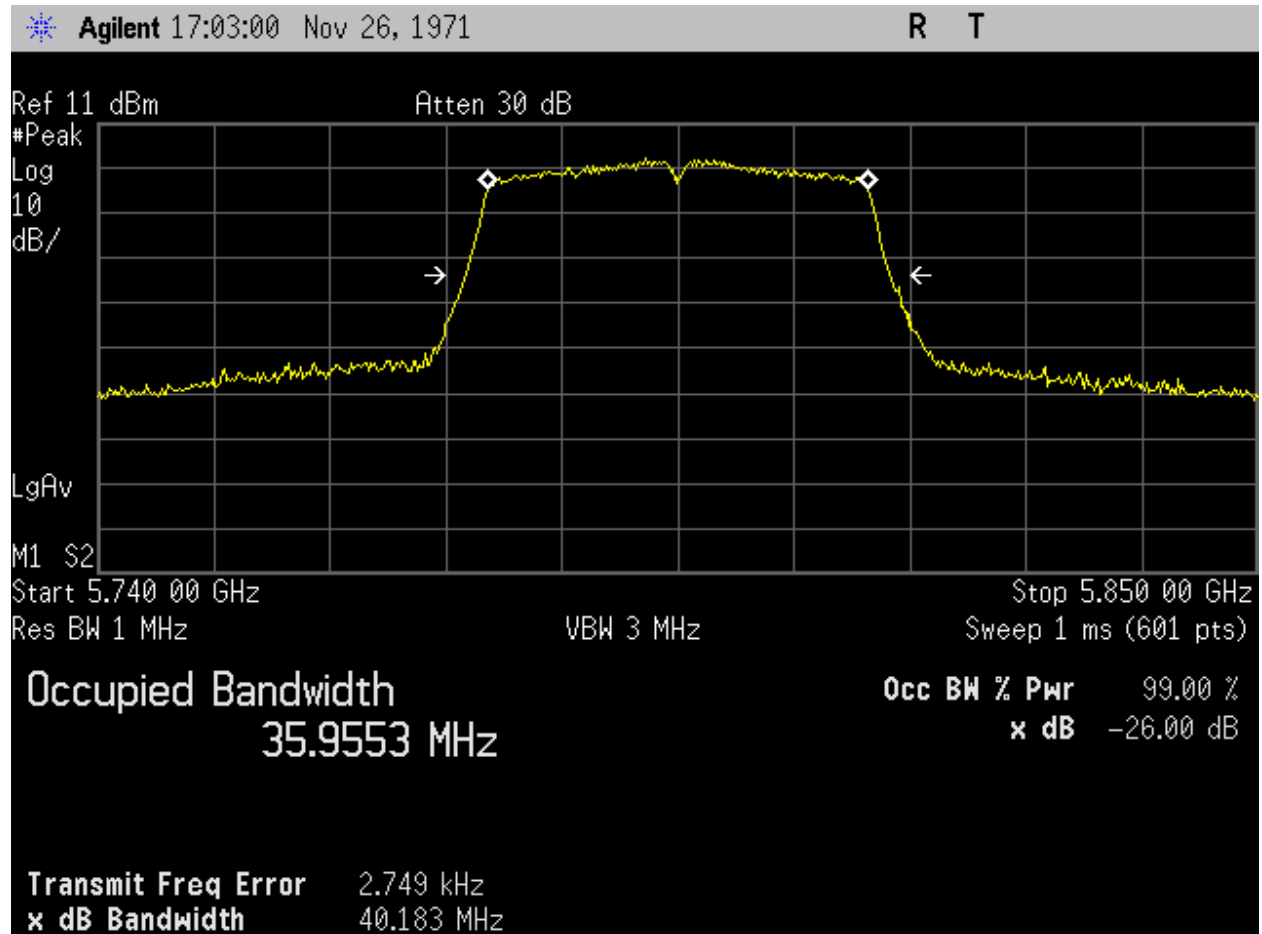


Figure 517: U-NII-3_5795MHz_High Ch_159_40MHz BW_ac-mode_Occupied Bandwidth_99 Percent_Port 2.

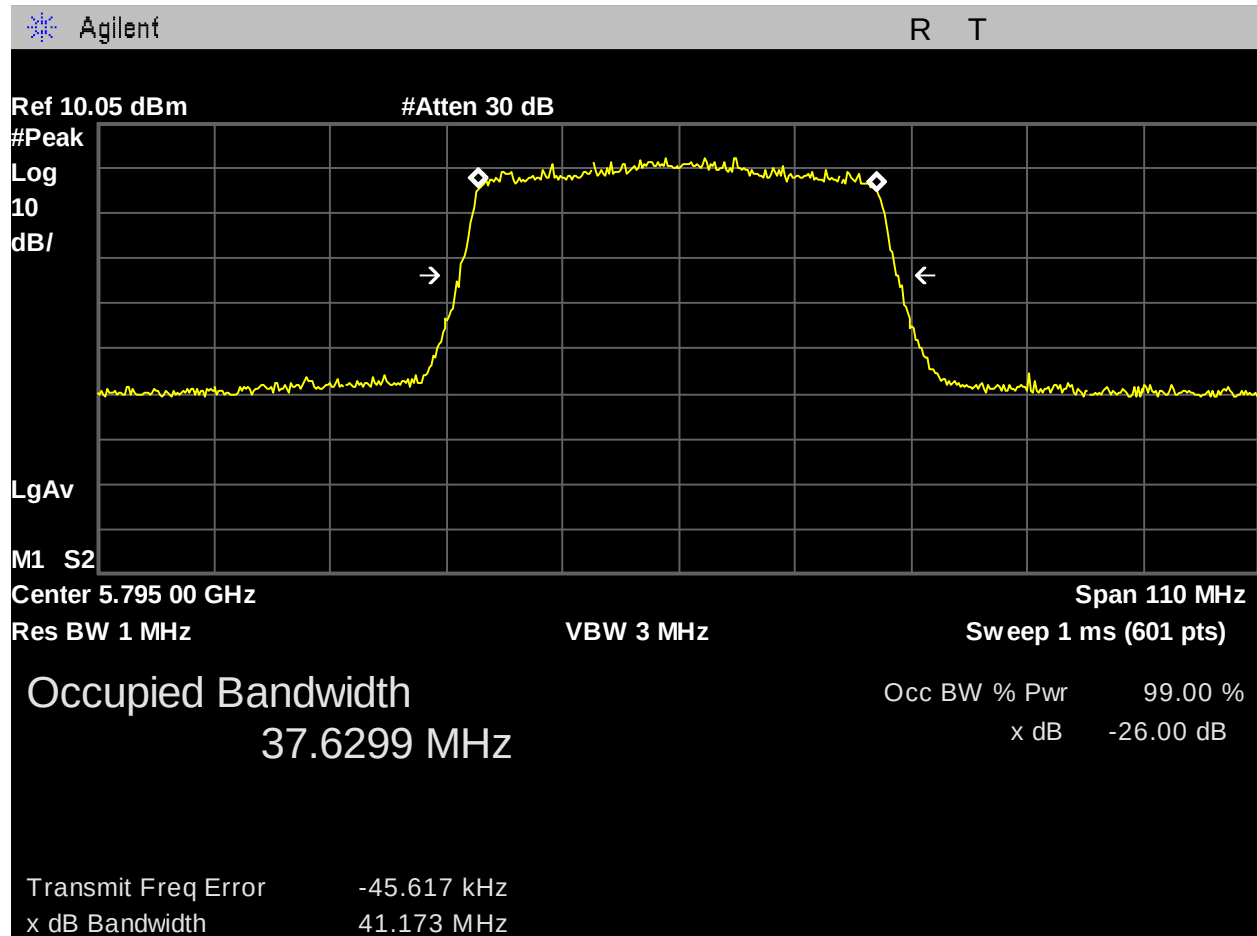


Figure 518: U-NII-3_5795MHz_High Ch_159_40MHz BW_ax-mode_Occupied Bandwidth_99 Percent_Port 1.

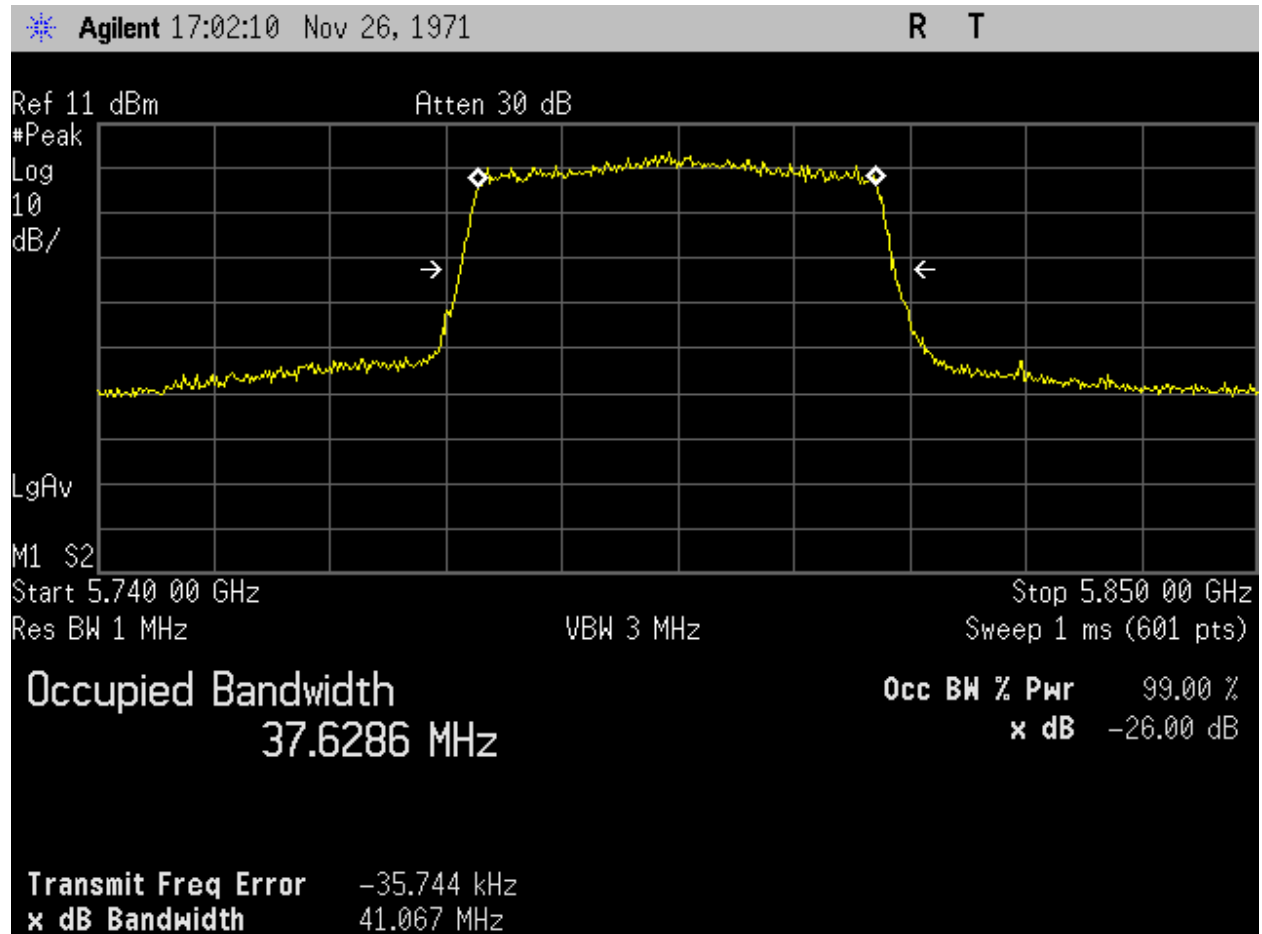


Figure 519: U-NII-3_5795MHz_High Ch_159_40MHz BW_ax-mode_Occupied Bandwidth_99 Percent_Port 2.

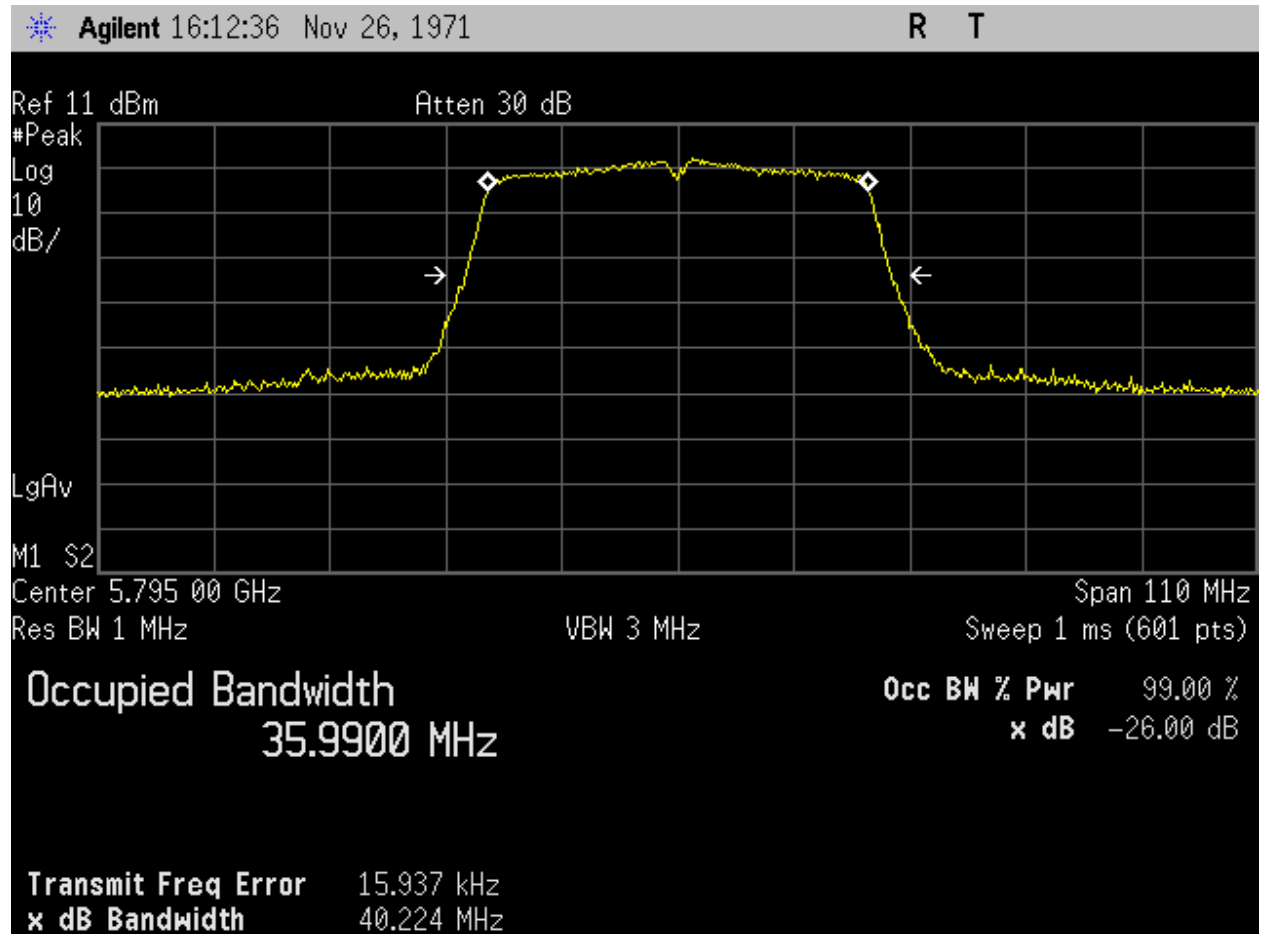


Figure 520: U-NII-3_5795MHz_High Ch_159_40MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 1.

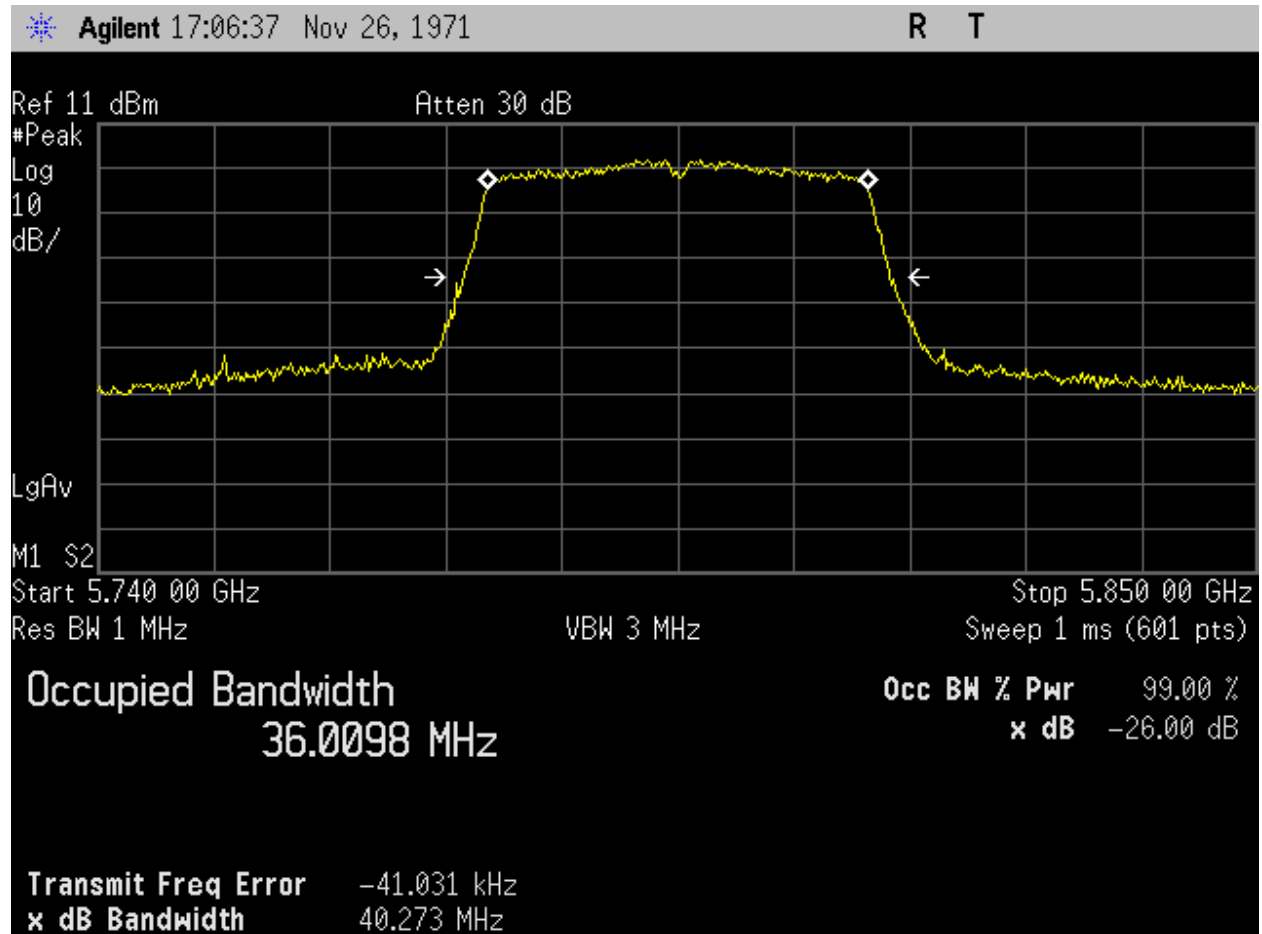


Figure 521: U-NII-3_5795MHz_High Ch_159_40MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 2.

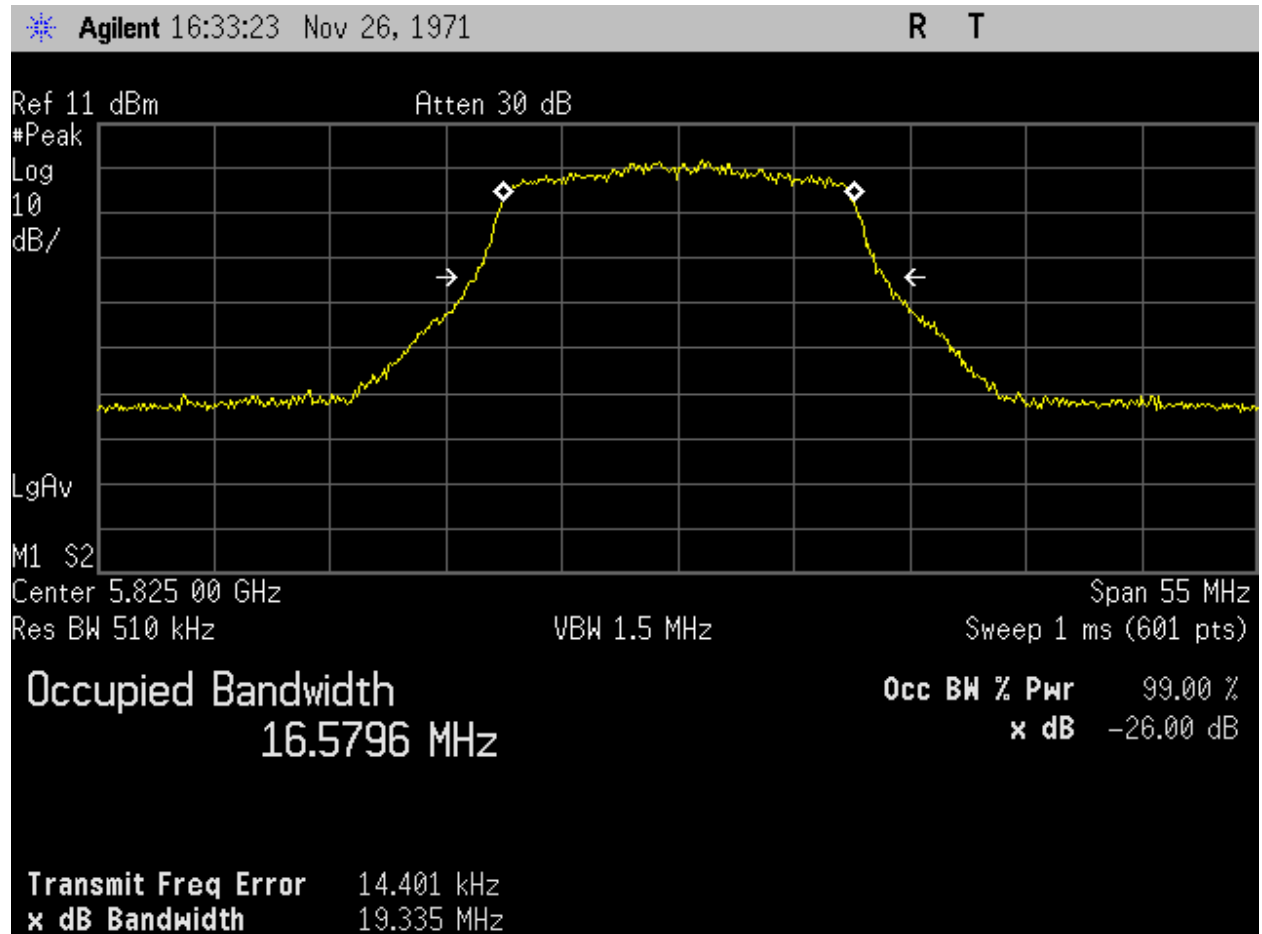


Figure 522: U-NII-3_5825MHz_High Ch_165_20MHz BW_a-mode_Occupied Bandwidth_99 Percent_Port 1.

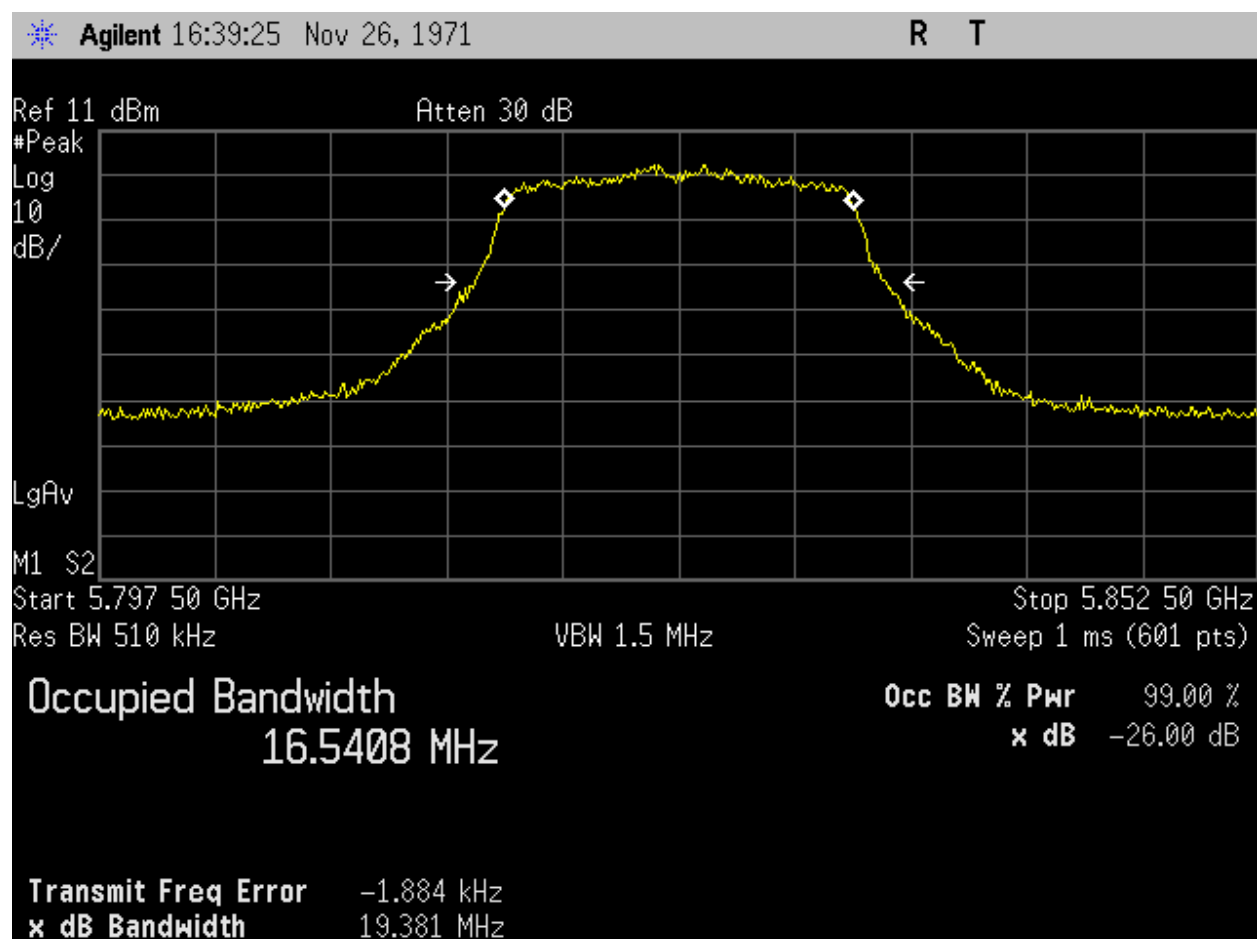


Figure 523: U-NII-3_5825MHz_High Ch_165_20MHz BW_a-mode_Occupied Bandwidth_99 Percent_Port 2.

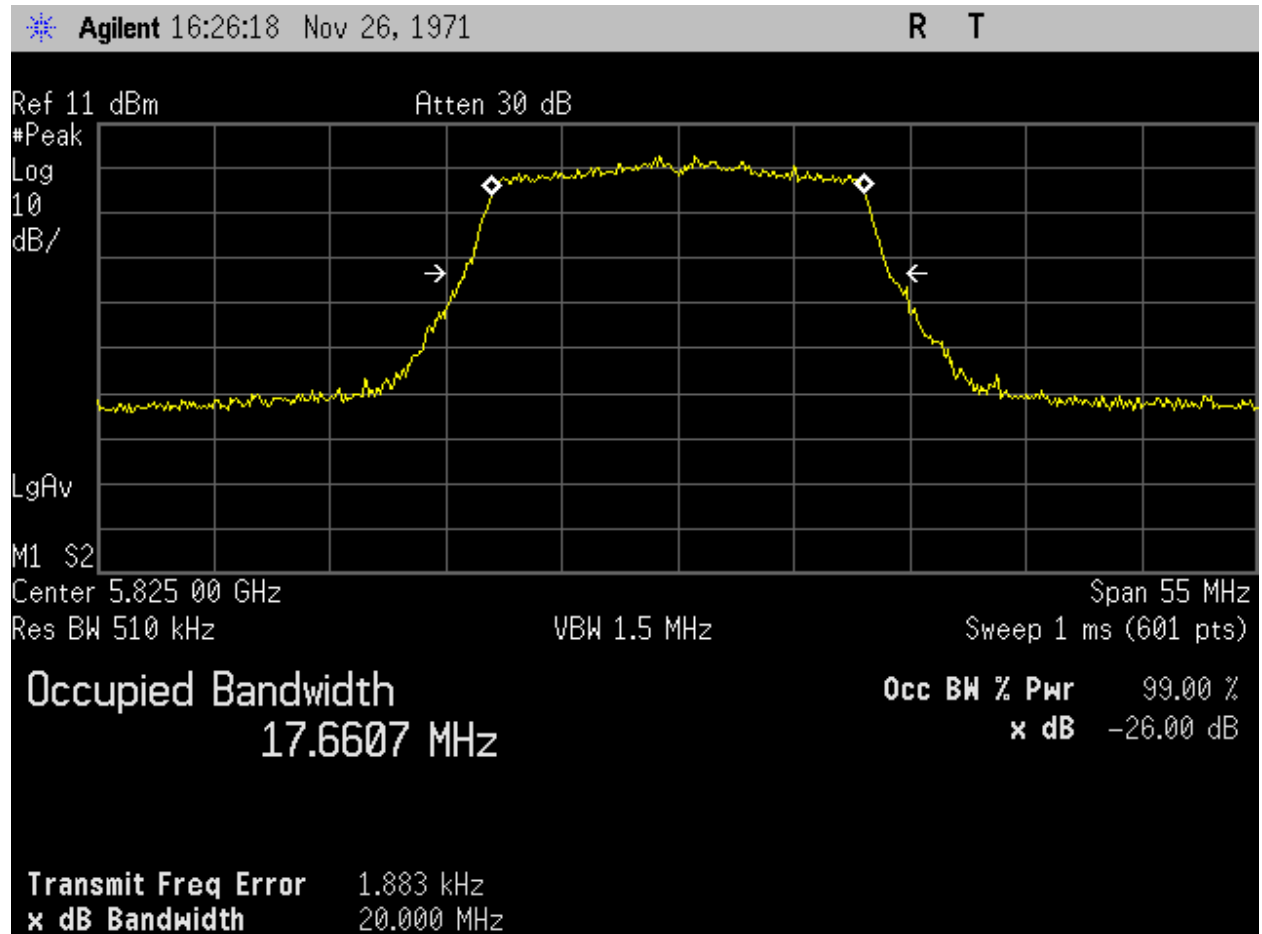


Figure 524: U-NII-3_5825MHz_High Ch_165_20MHz BW_ac-mode_Occupied Bandwidth_99 Percent_Port 1.

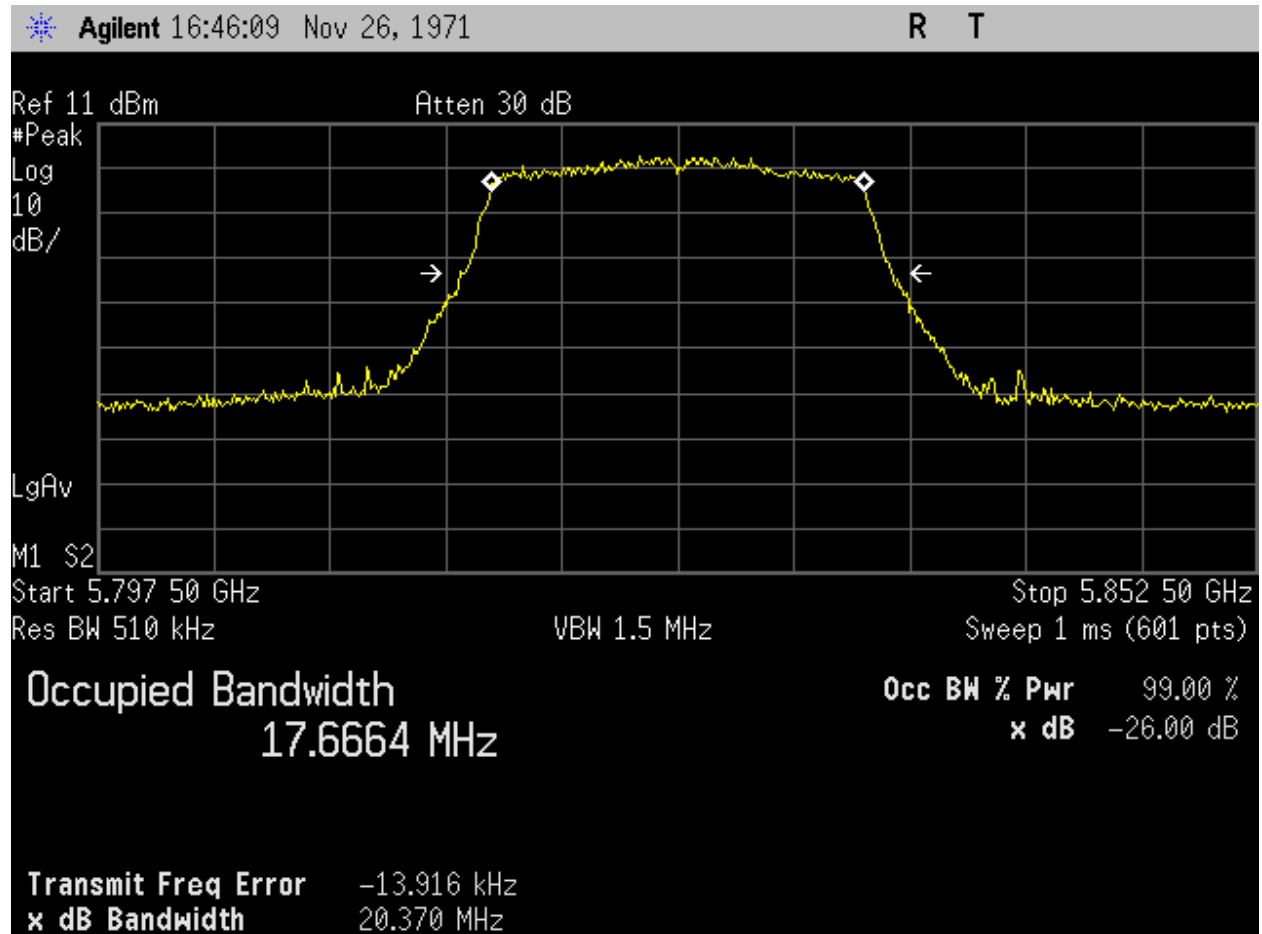


Figure 525: U-NII-3_5825MHz_High Ch_165_20MHz BW_ac-mode_Occupied Bandwidth_99 Percent_Port 2.

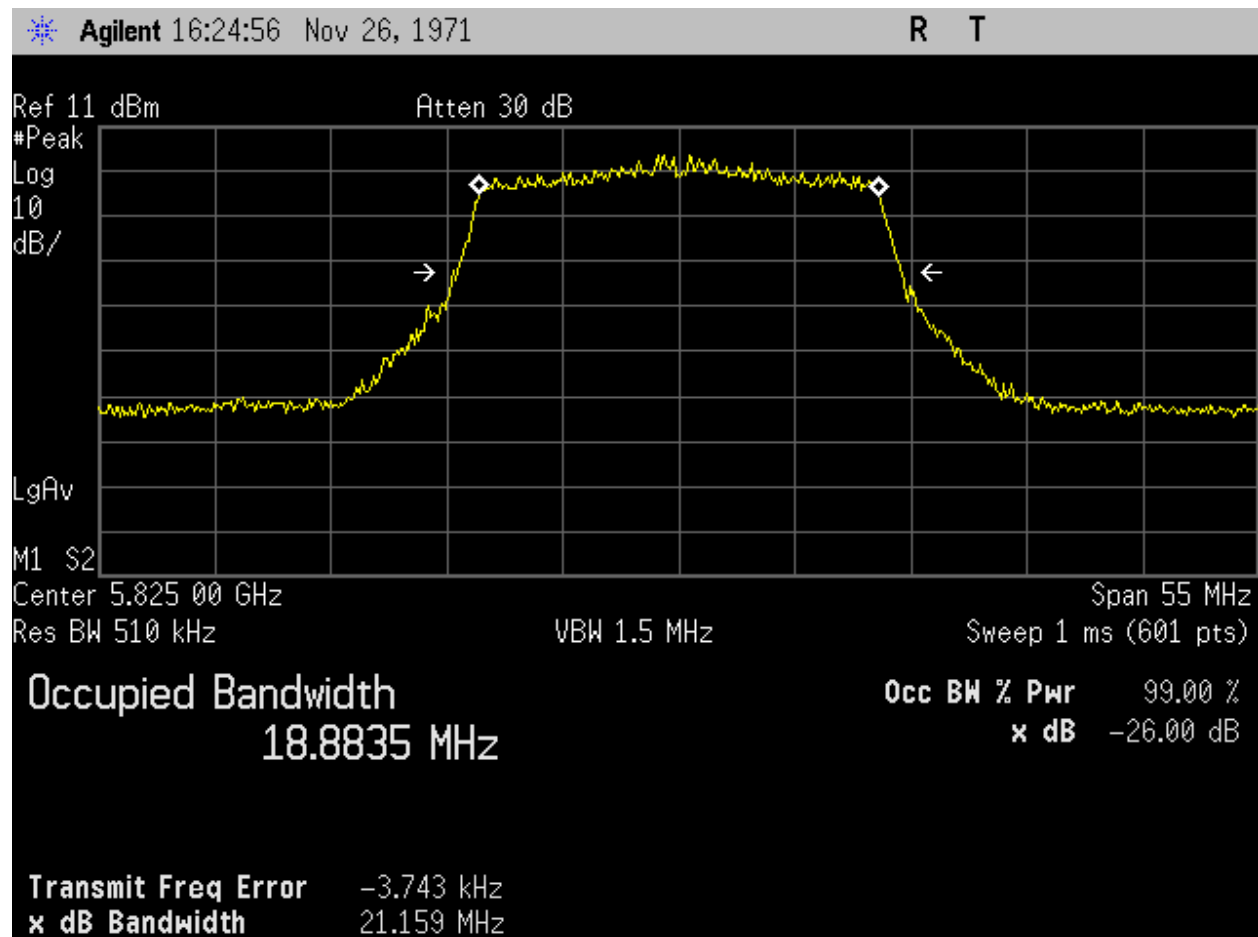


Figure 526: U-NII-3_5825MHz_High Ch_165_20MHz BW_ax-mode_Occupied Bandwidth_99 Percent_Port 1.

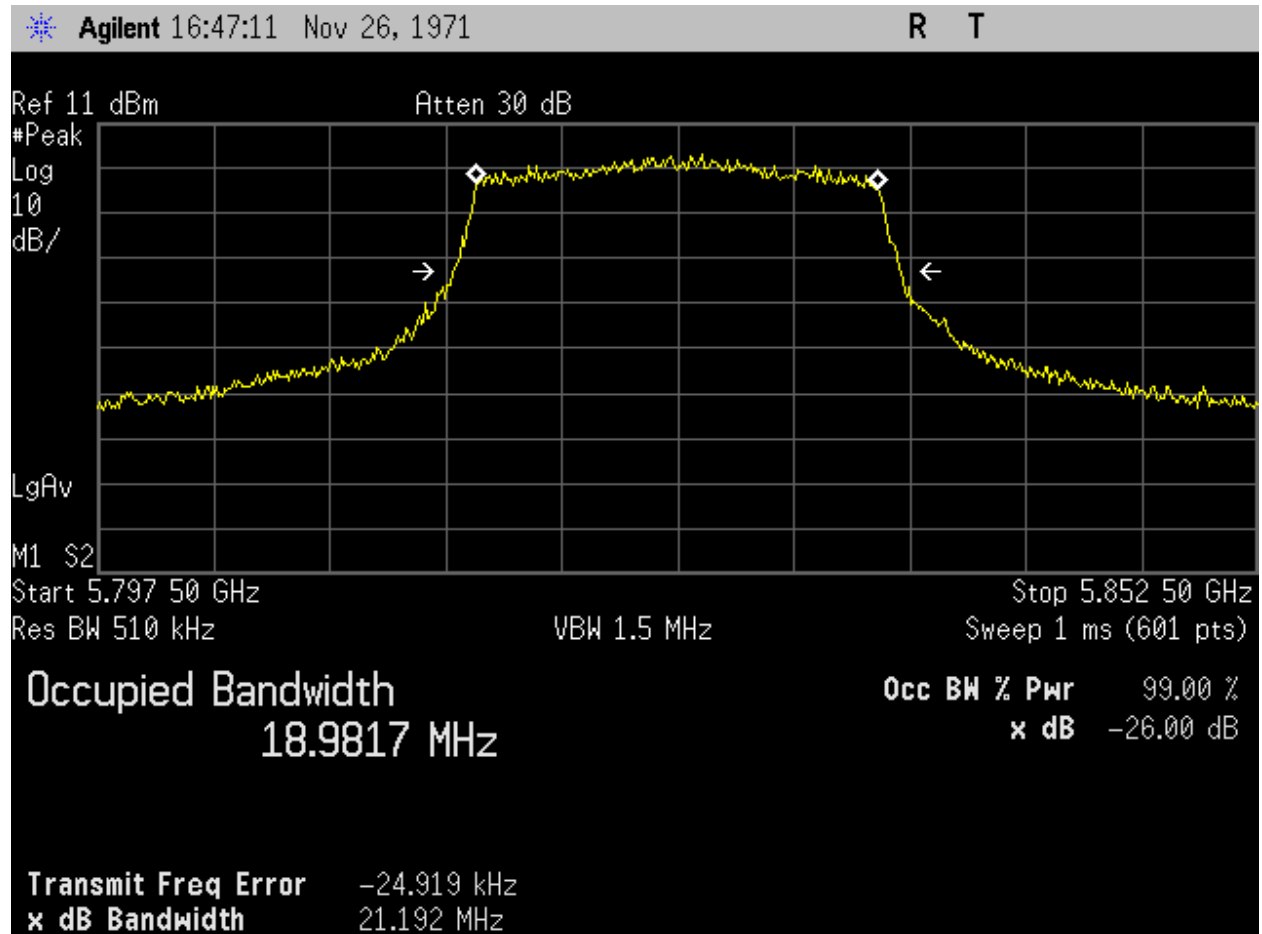


Figure 527: U-NII-3_5825MHz_High Ch_165_20MHz BW_ax-mode_Occupied Bandwidth_99 Percent_Port 2.

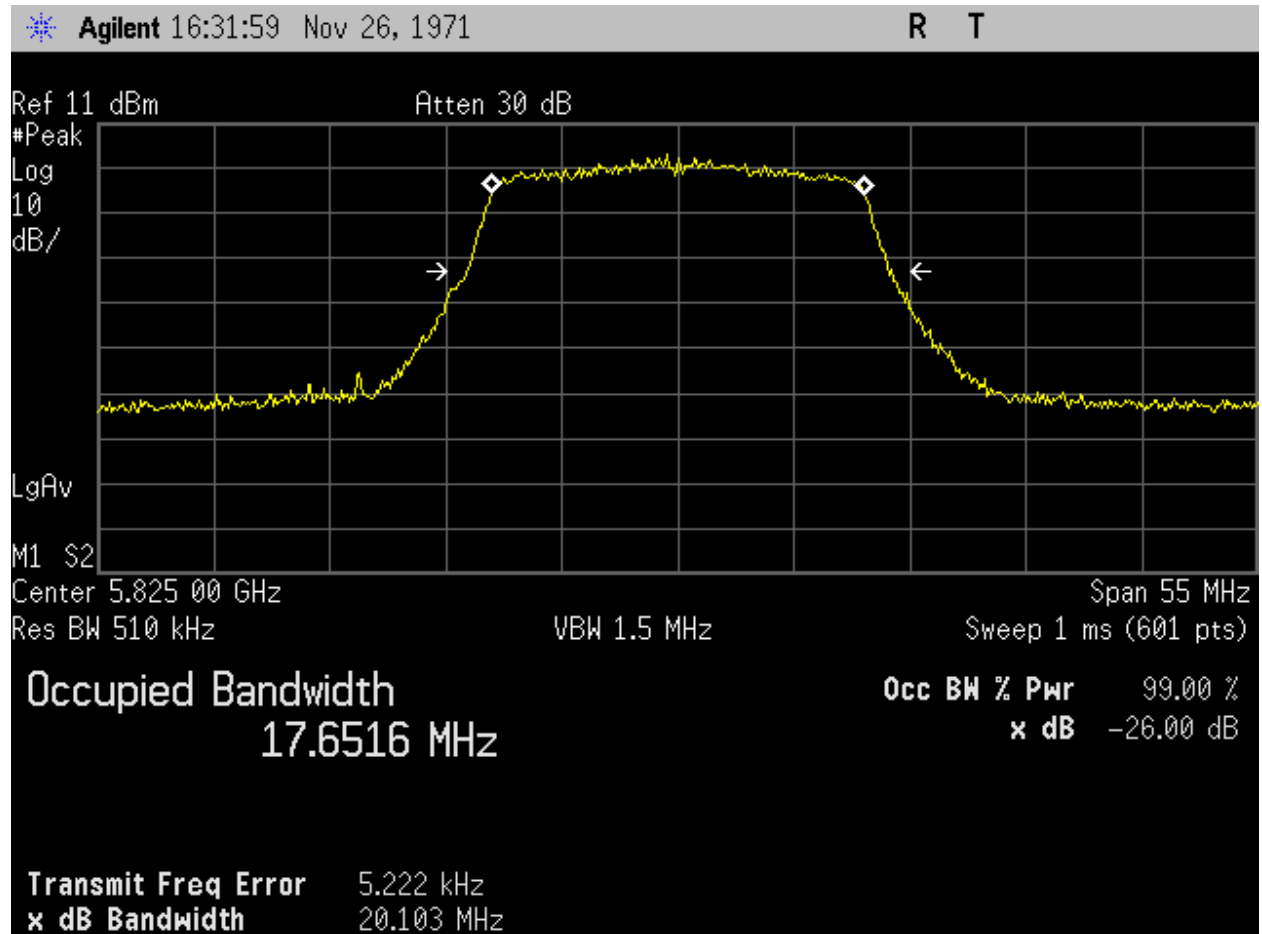


Figure 528: U-NII-3_5825MHz_High Ch_165_20MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 1.

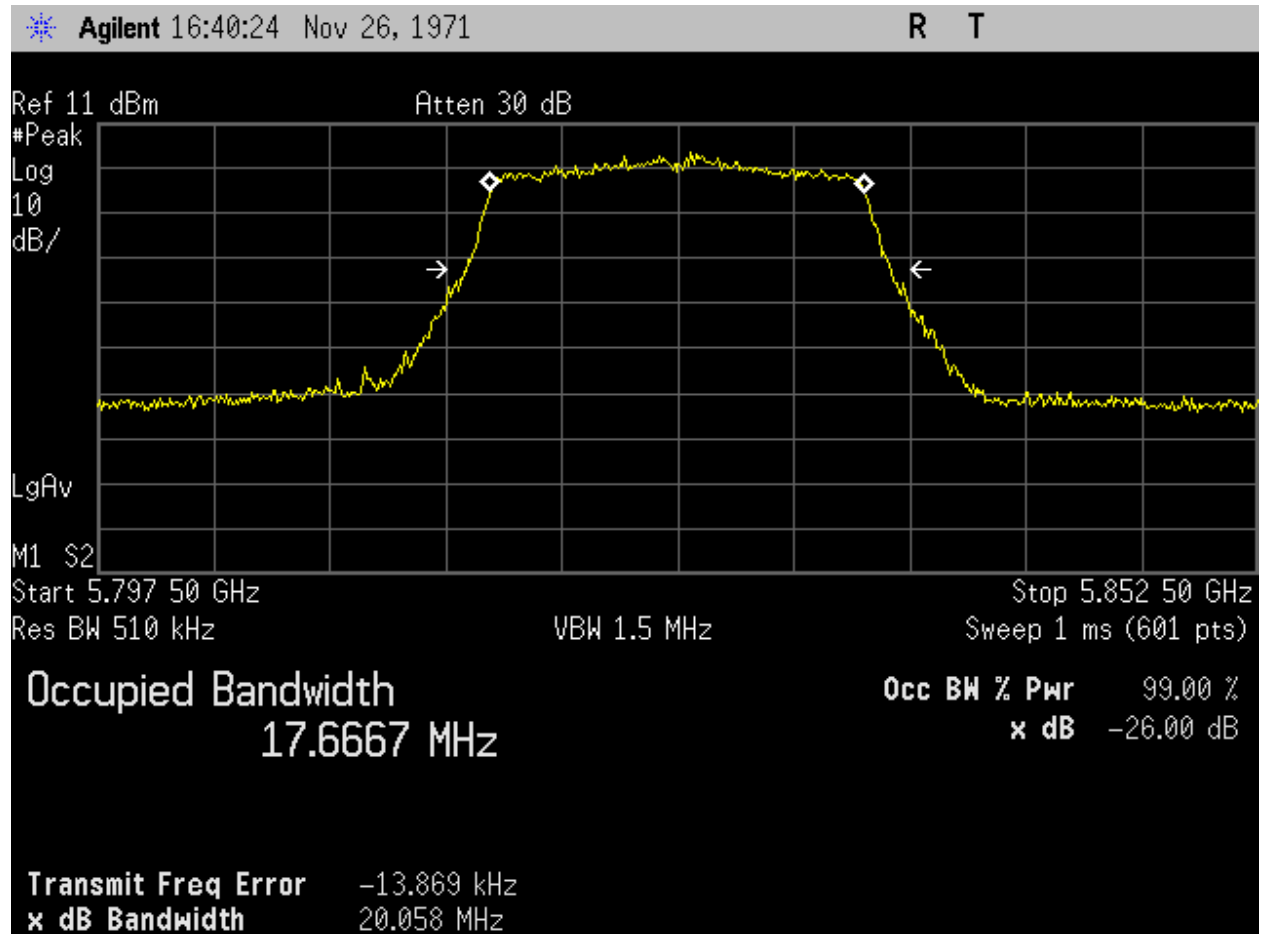


Figure 529: U-NII-3_5825MHz_High Ch_165_20MHz BW_n-mode_Occupied Bandwidth_99 Percent_Port 2.

§ 15.407(f) Maximum Permissible Exposure

No appendix data.