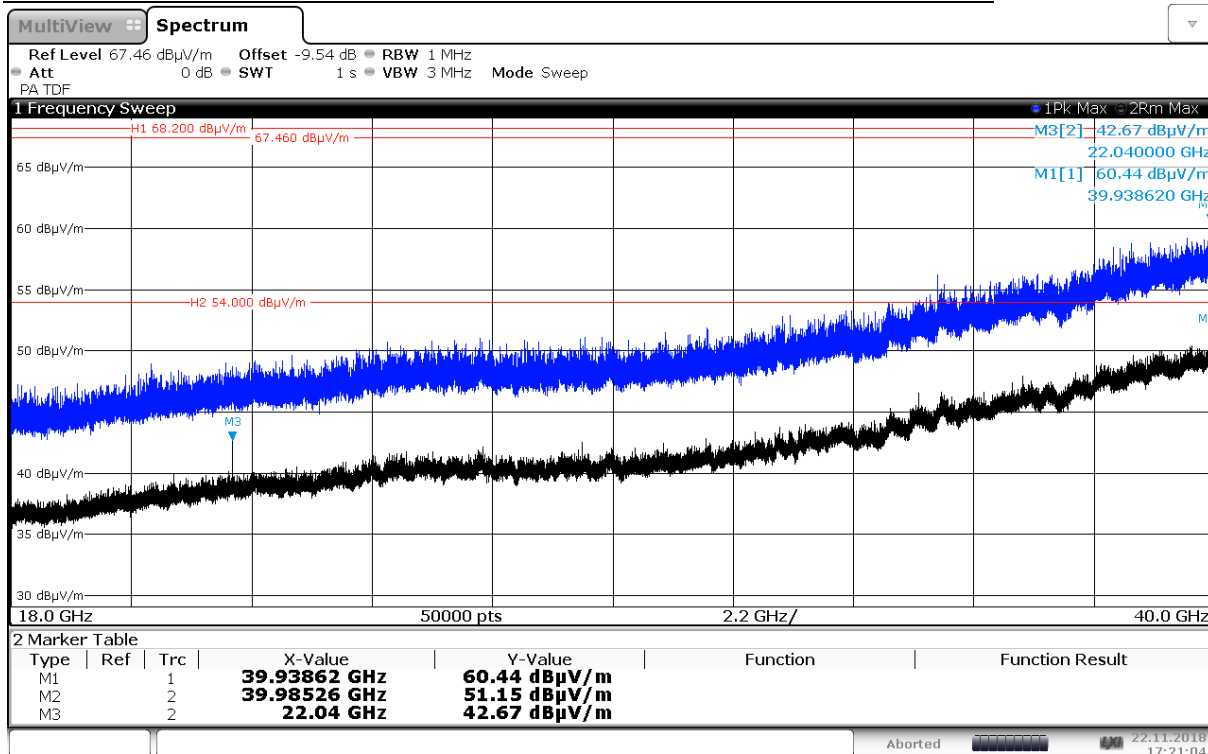
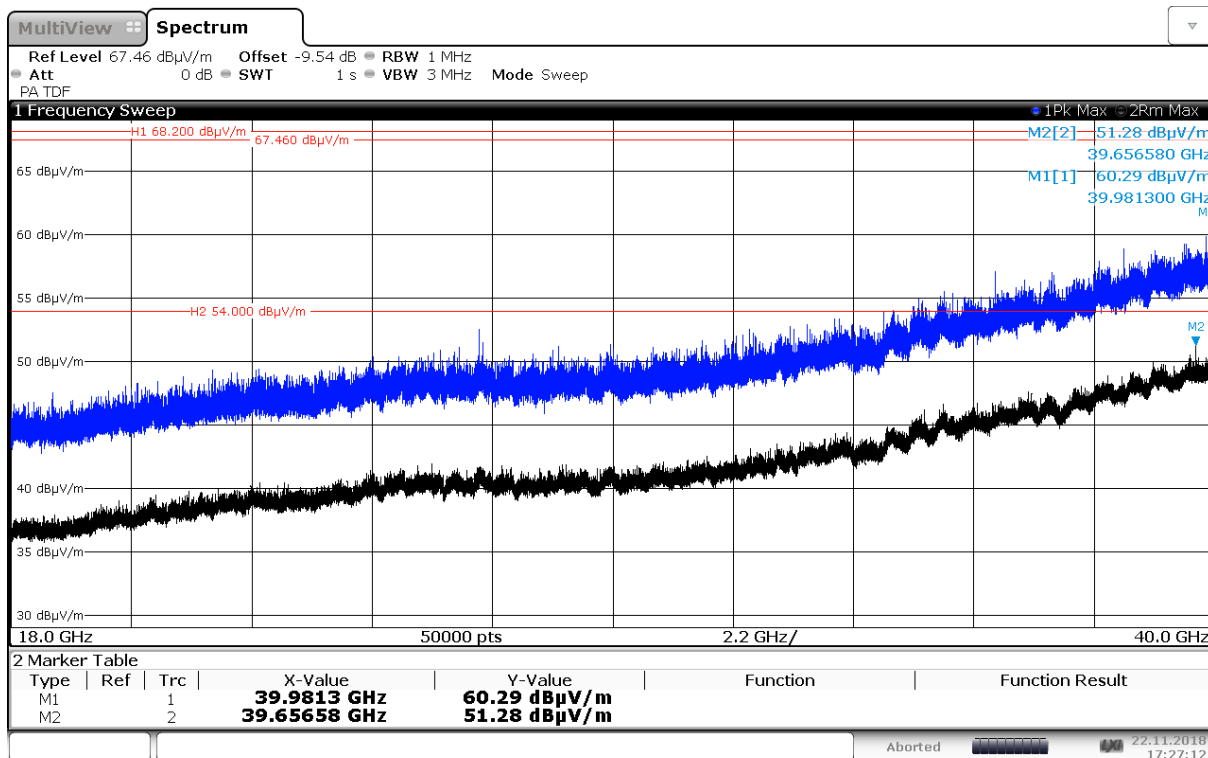




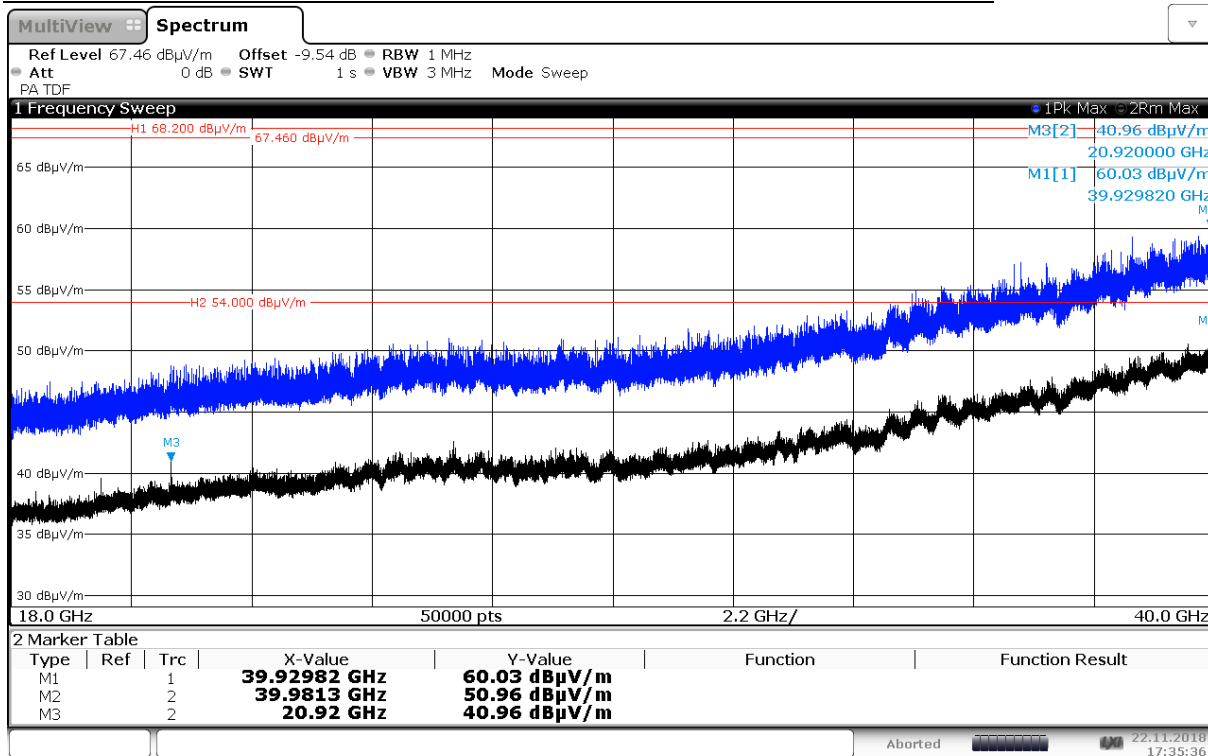
17:21:05 22.11.2018

4.15c_n-mode_MCS3_ch102



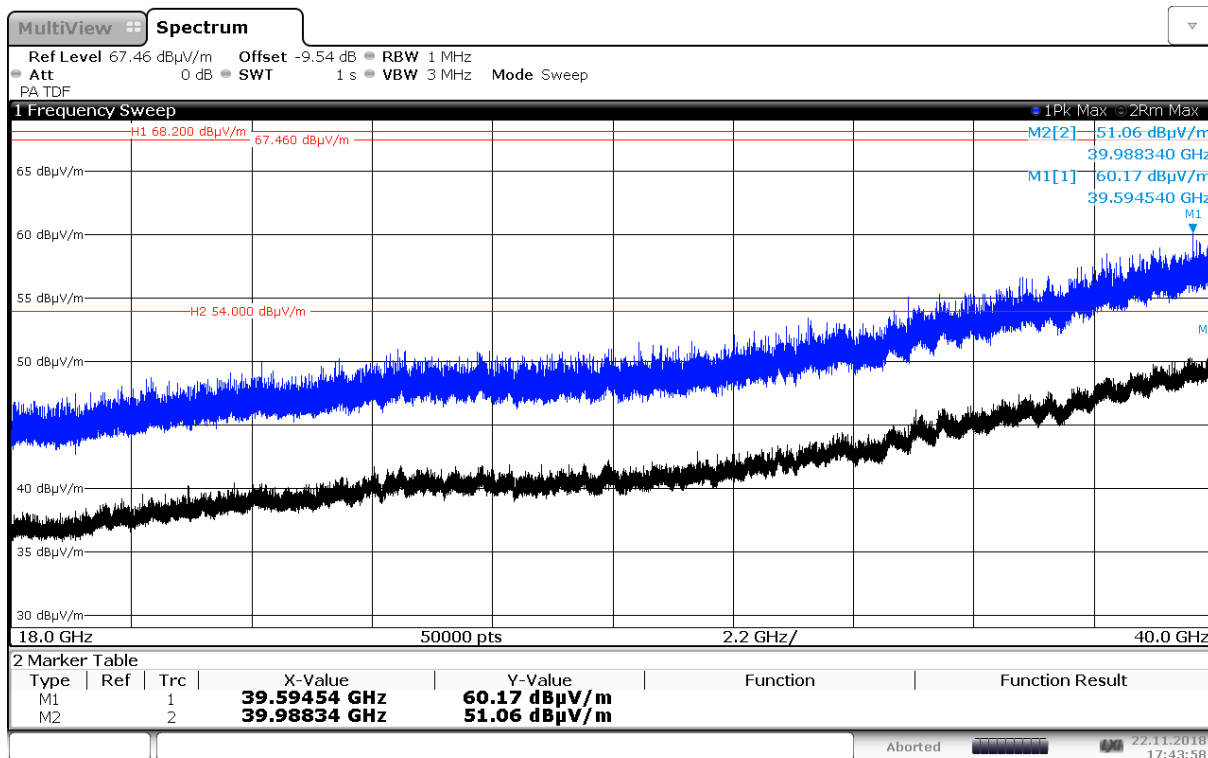
17:27:13 22.11.2018

4.16c_n-mode_MCS3_ch151



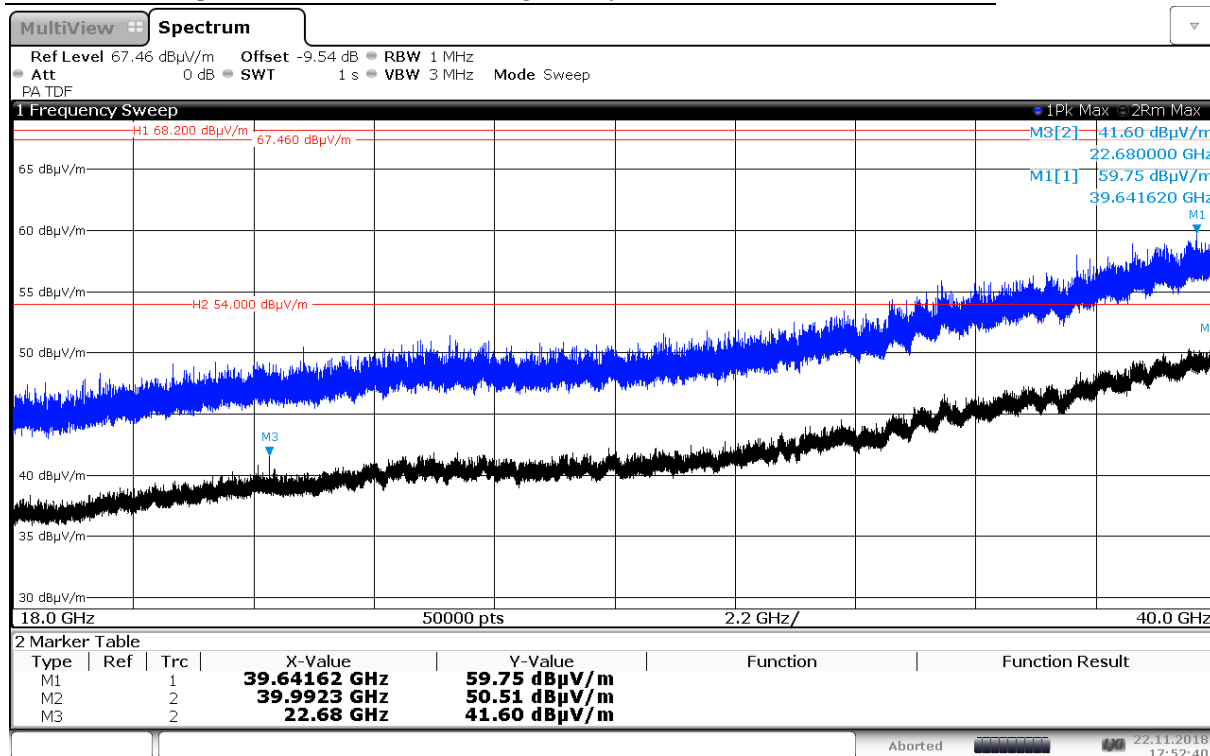
17:35:37 22.11.2018

4.17c_ac-mode_MCS4_ch046



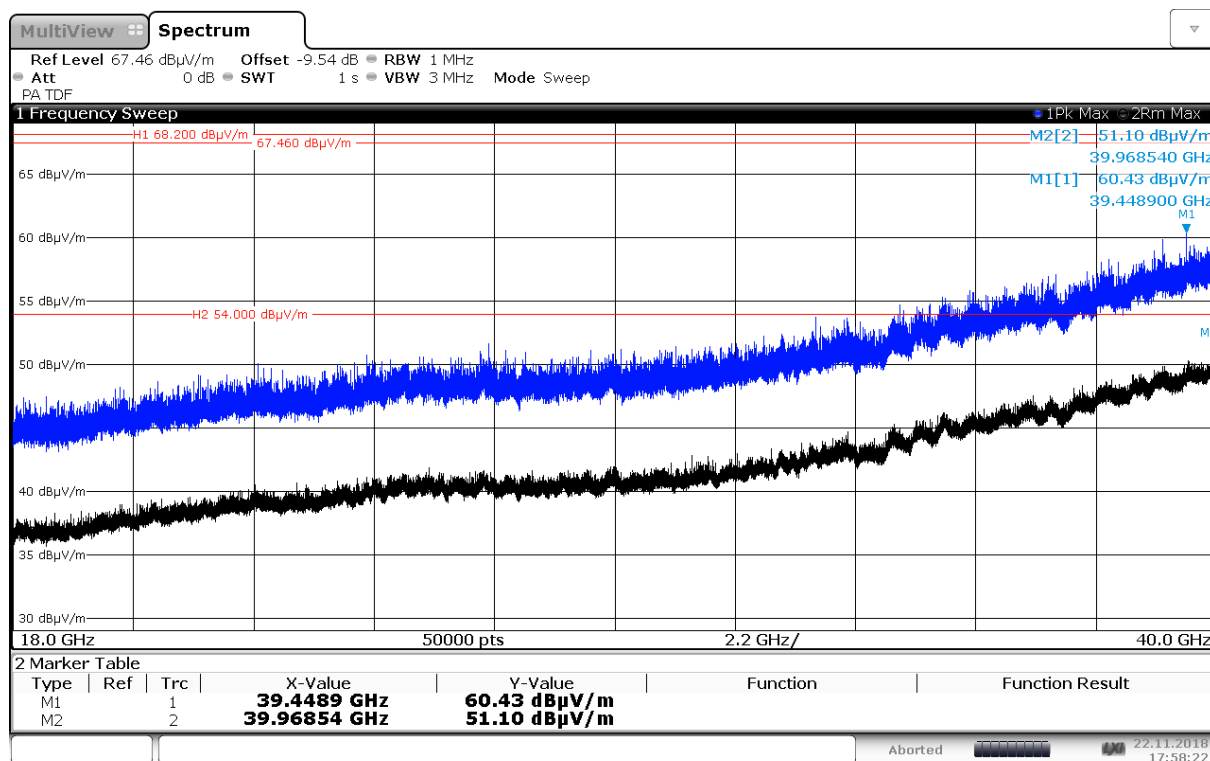
17:43:59 22.11.2018

4.18c_ac-mode_MCS4_ch62



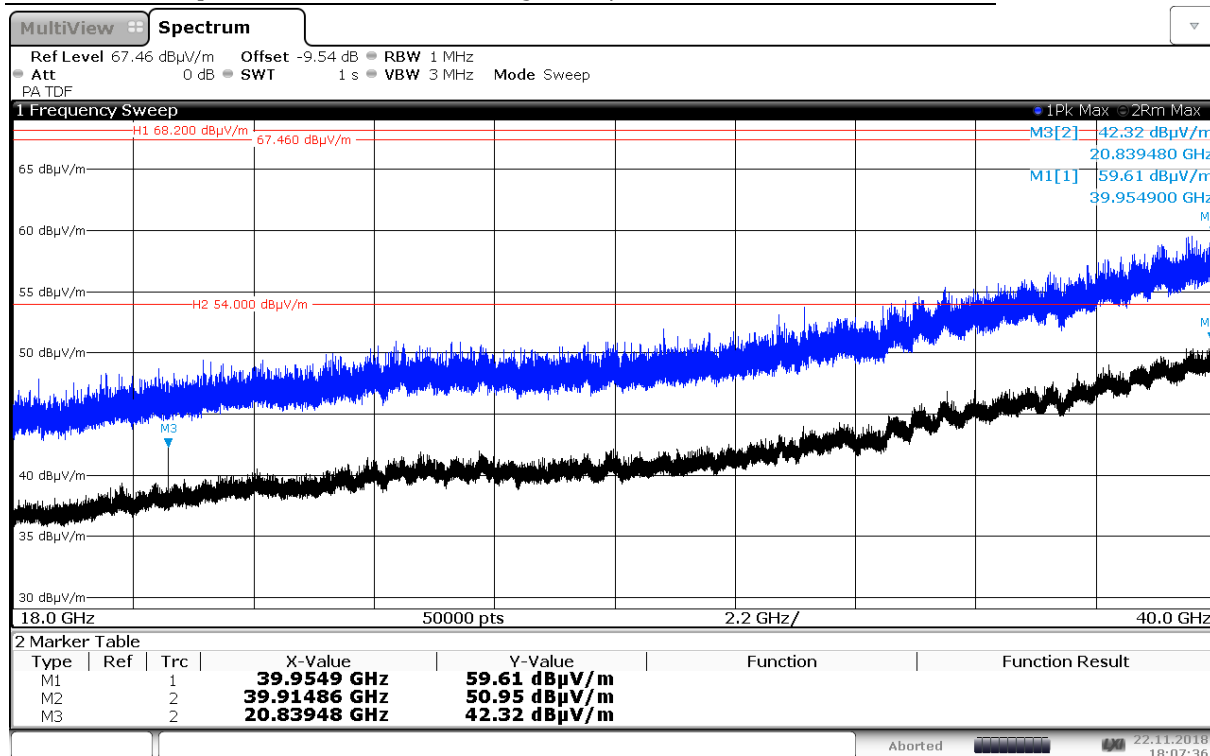
17:52:40 22.11.2018

4.19c_ac-mode_MCS4_ch134



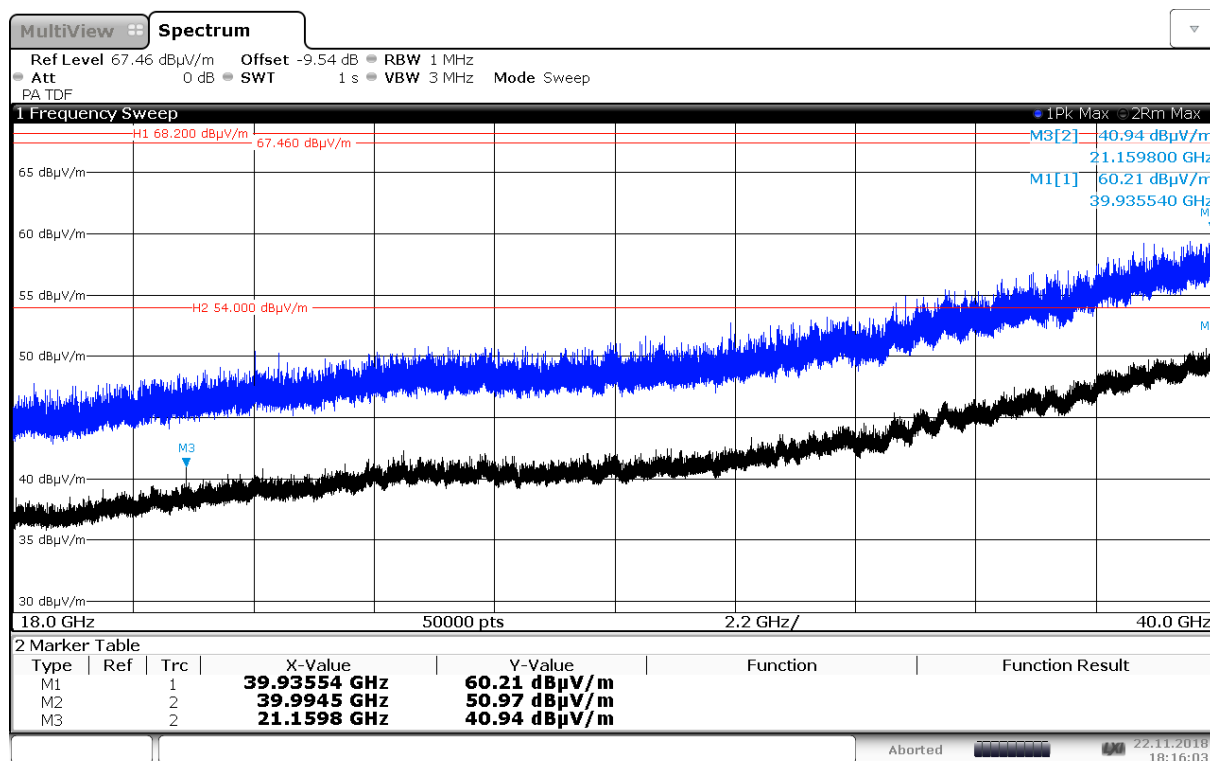
17:58:23 22.11.2018

4.20c_ac-mode_MCS4_ch159



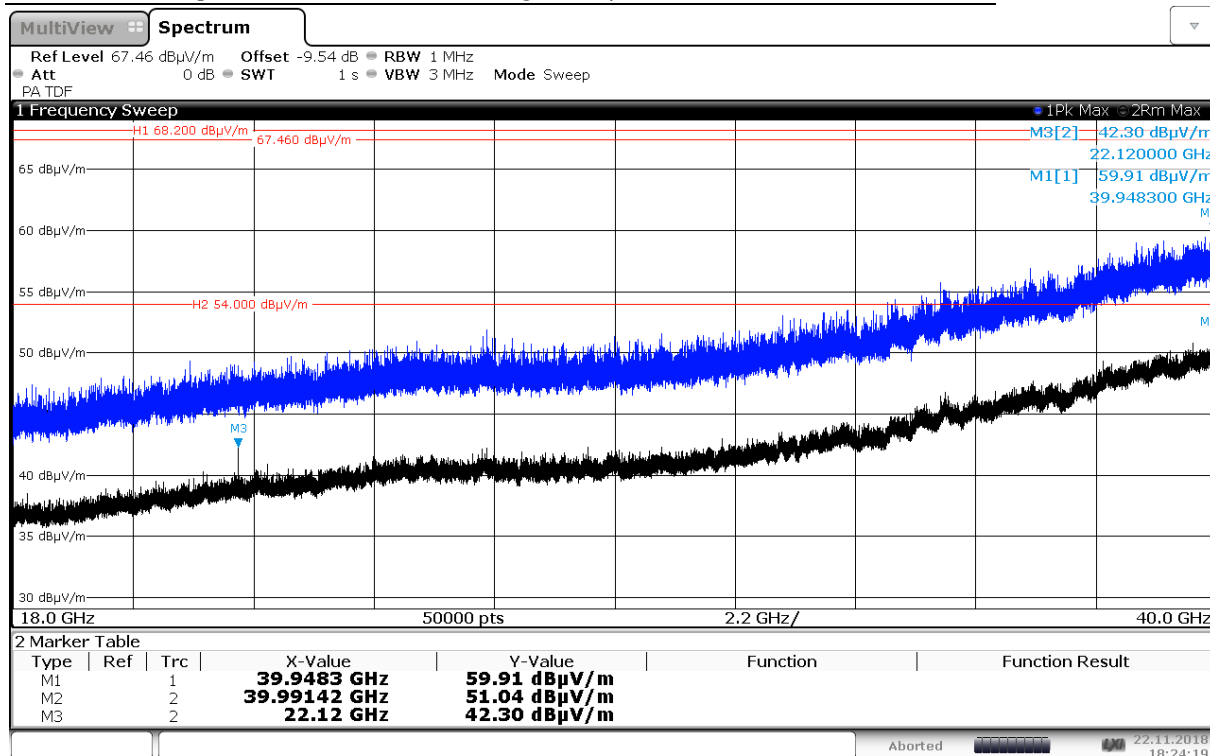
18:07:36 22.11.2018

4.21c_ac-mode_MCS1_ch042



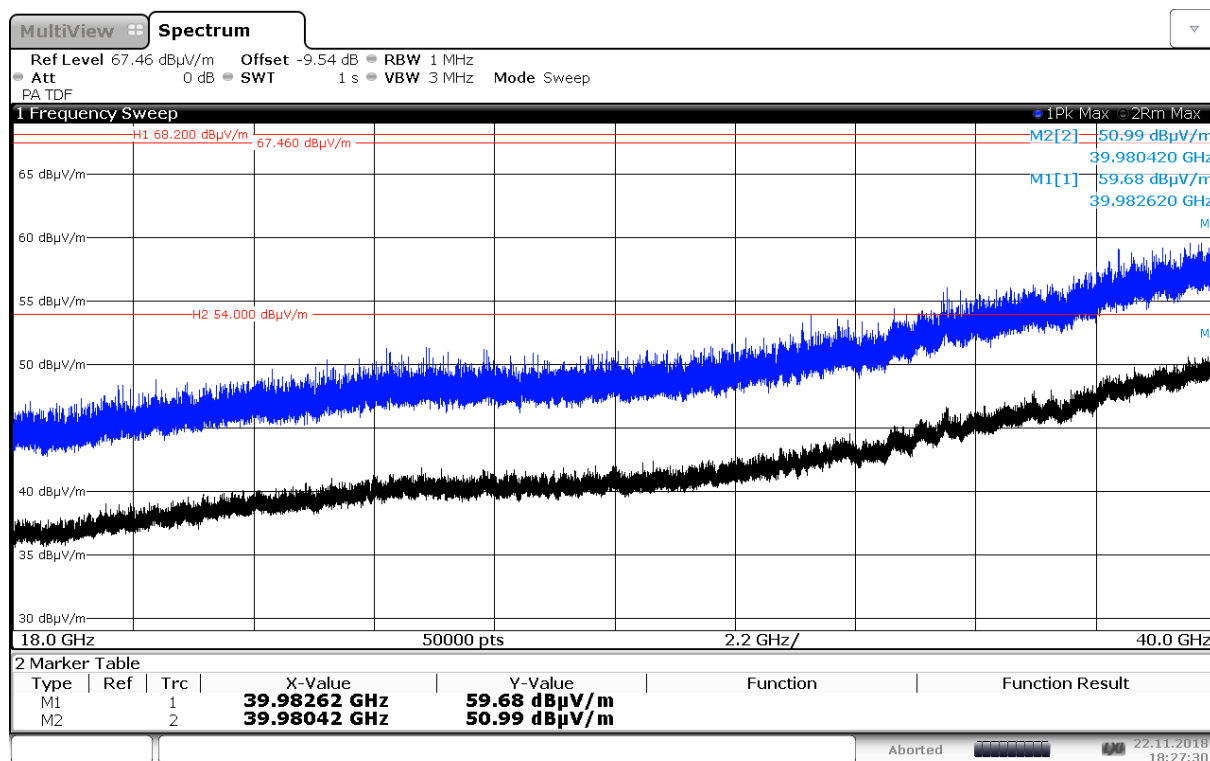
18:16:04 22.11.2018

4.22c_ac-mode_MCS1_ch58



18:24:19 22.11.2018

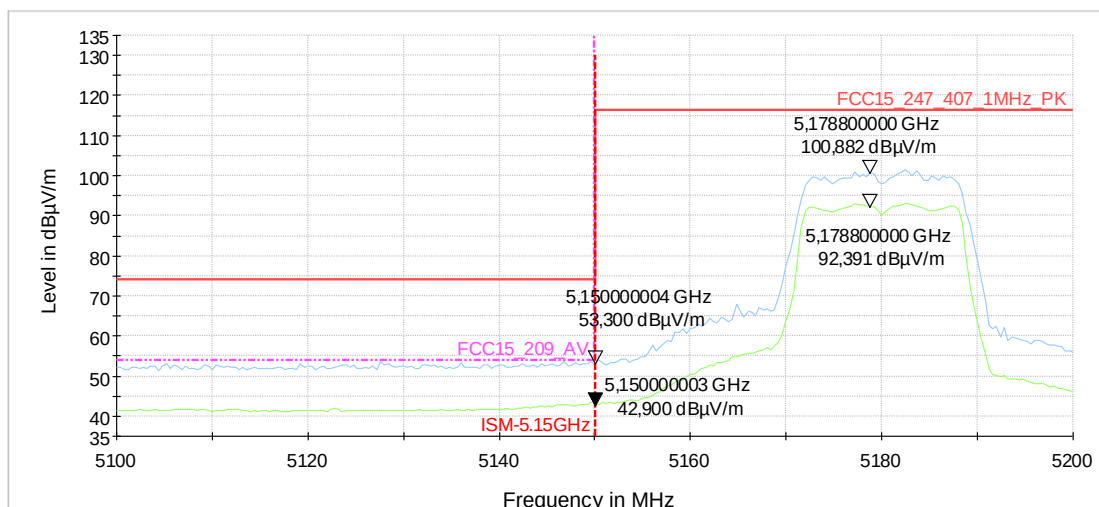
4.23c_ac-mode_MCS1_ch106



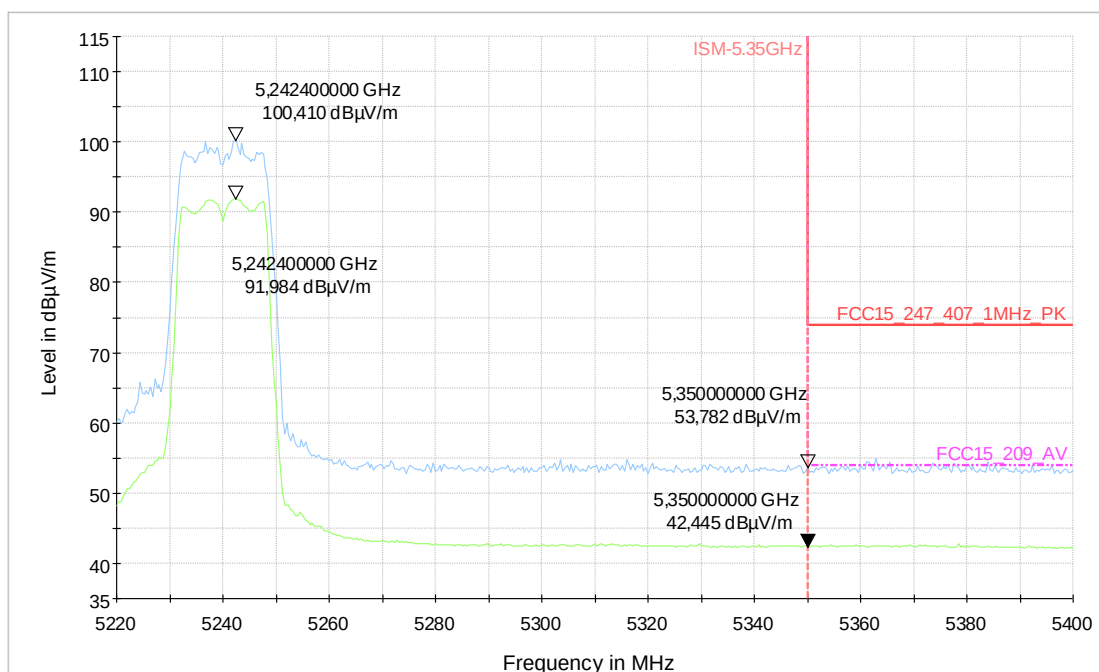
18:27:31 22.11.2018

4.24c_ac-mode_MCS1_ch155

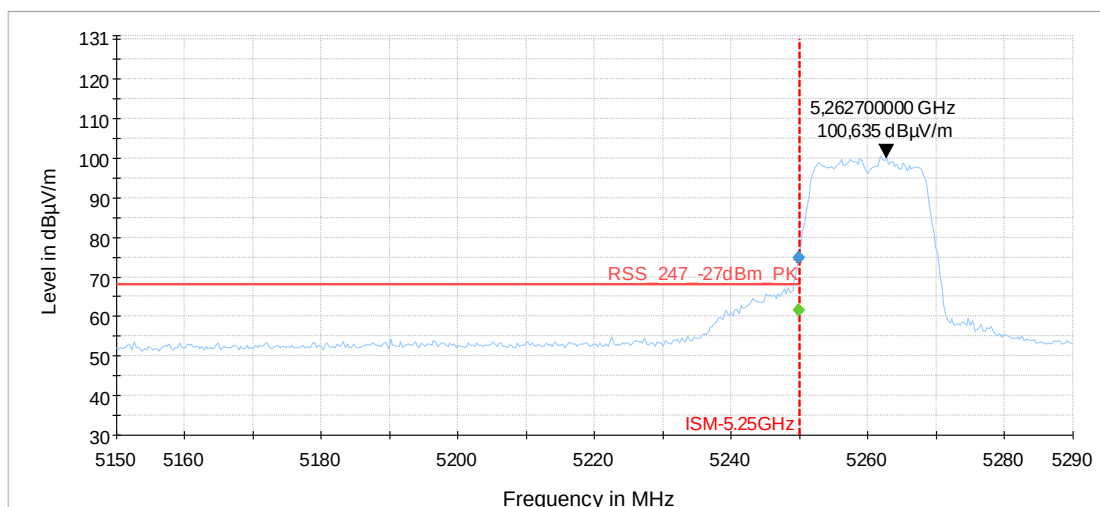
2.6. Band edge compliance



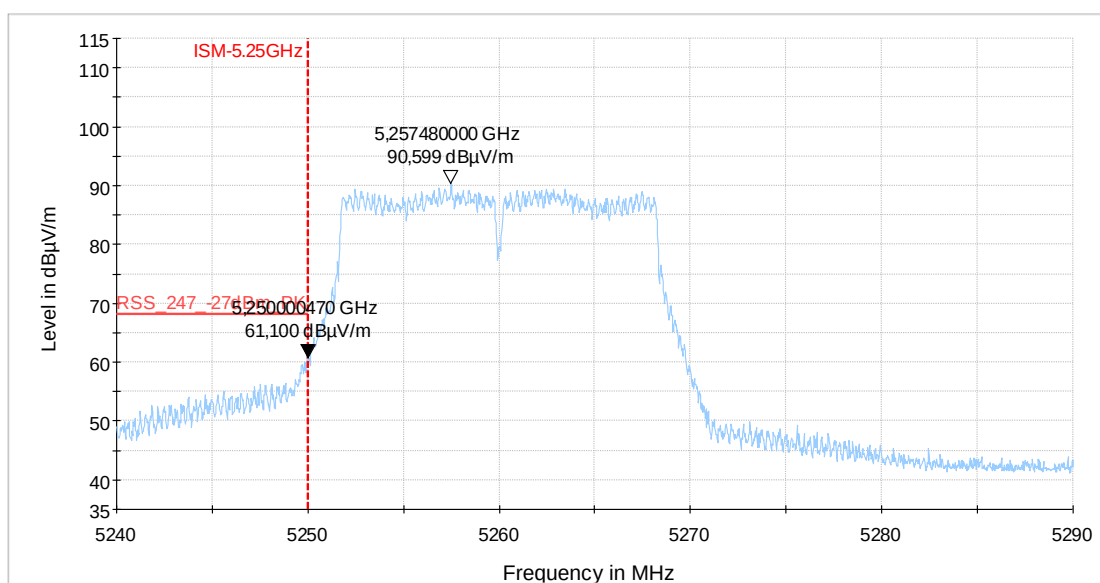
9.01a_a-mode_18MBps_ch036



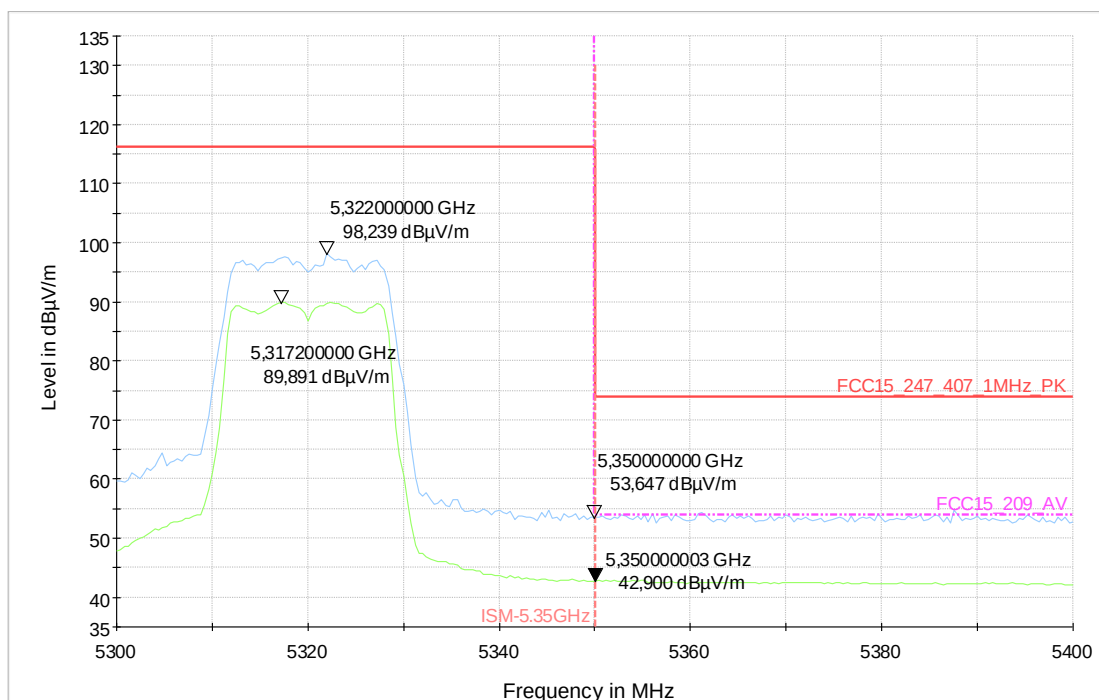
9.01b_a-mode_18MBps_ch048



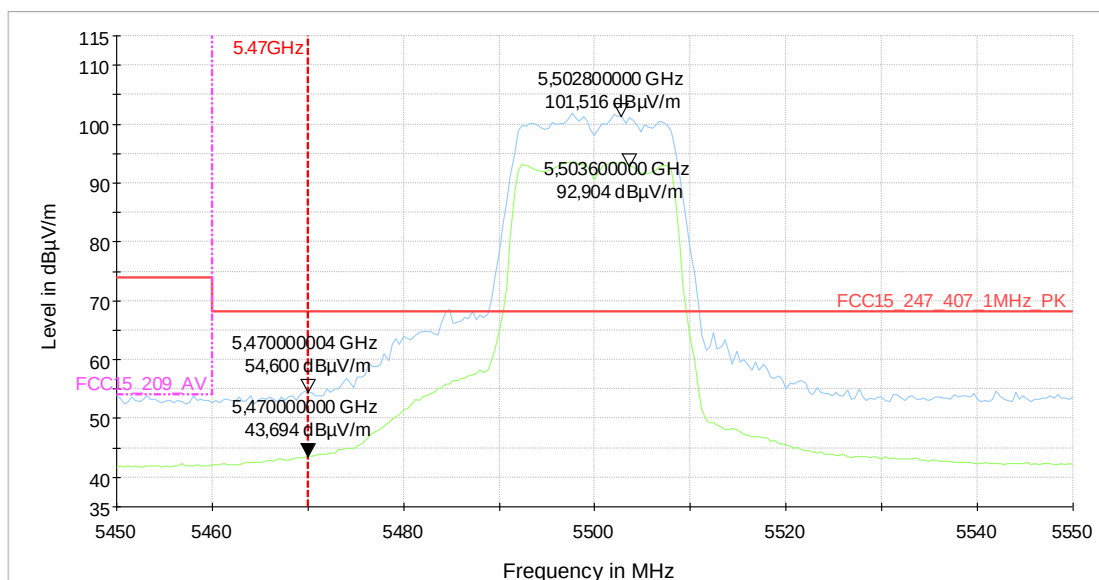
9.02a_a-mode_18Mbps_ch052_STep1



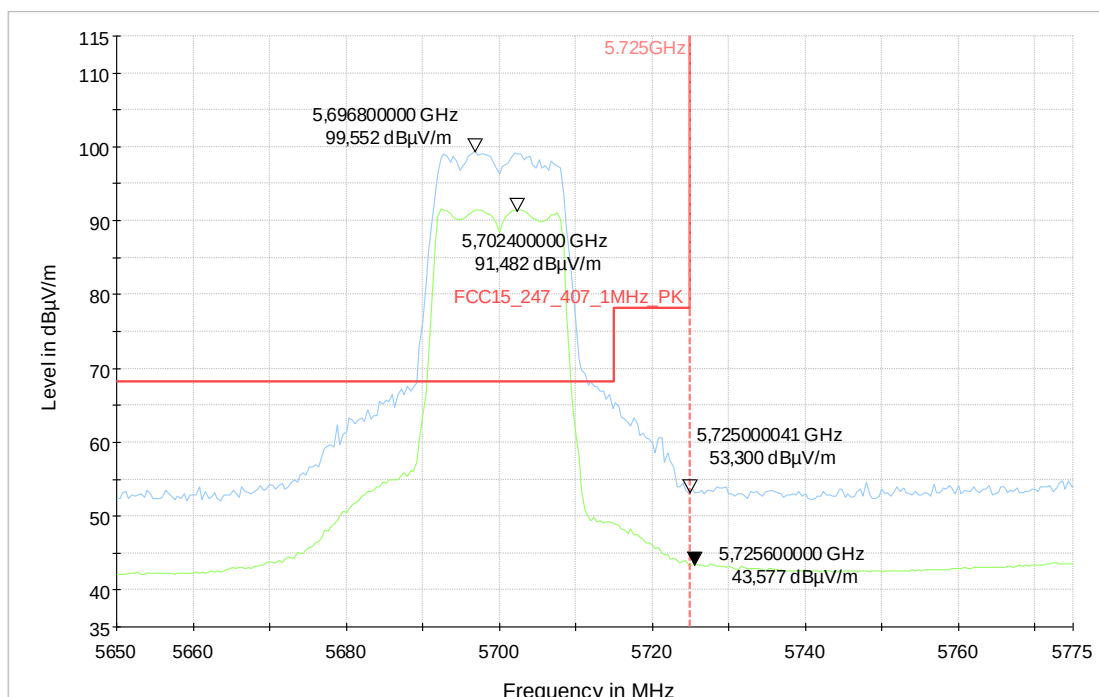
9.02a_a-mode_18Mbps_ch052_STep2



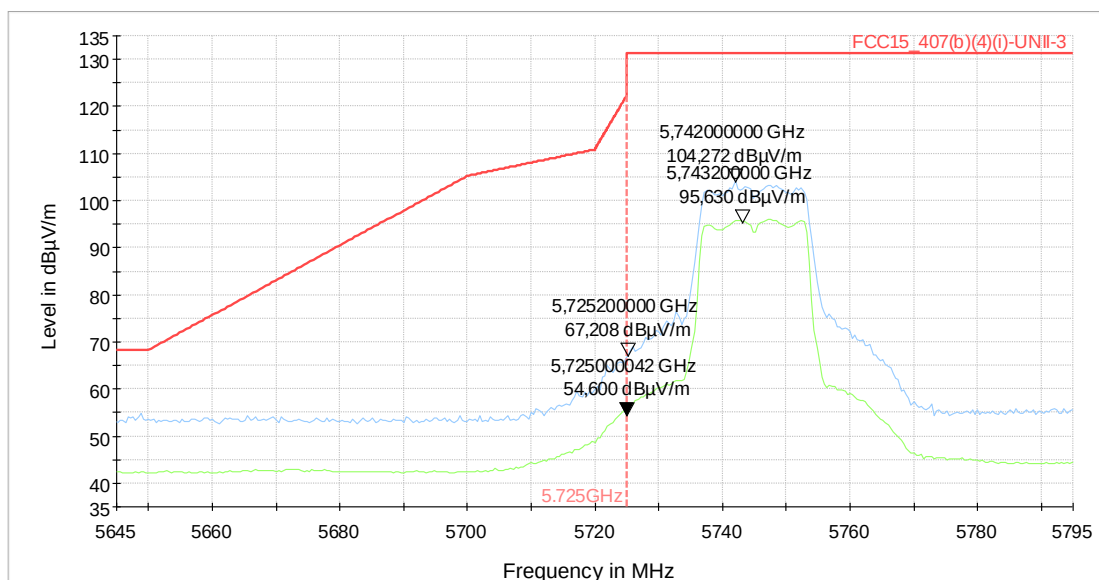
9.02b_a-mode_18Mbps_ch064



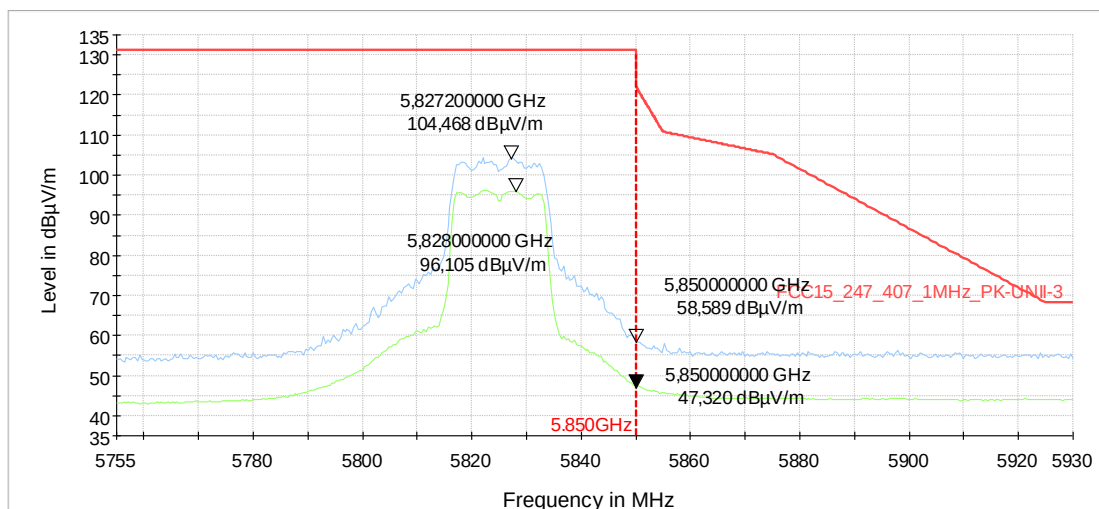
9.03a_a-mode_18Mbps_ch100



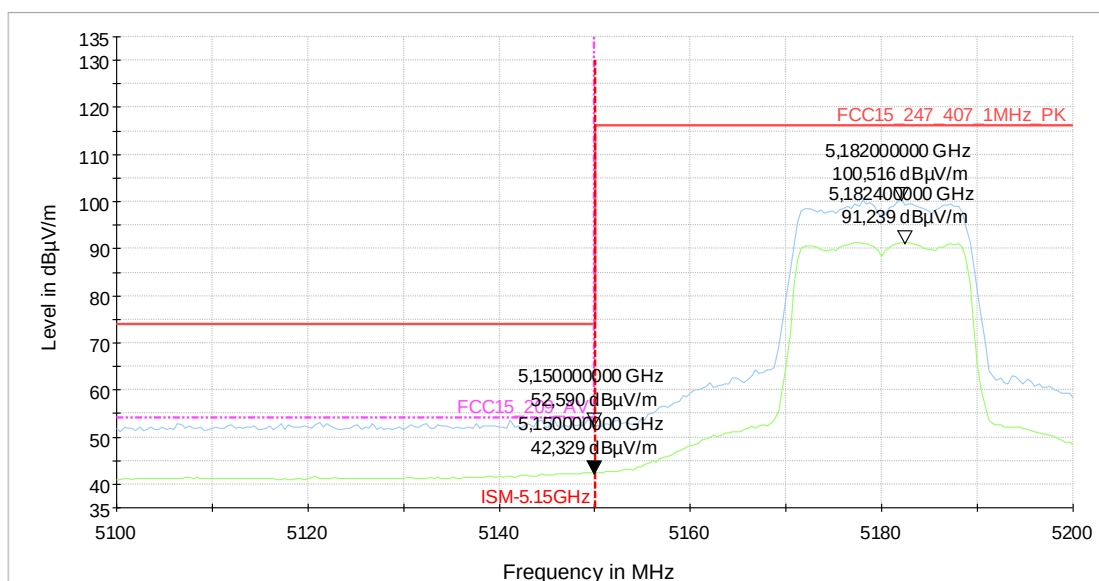
9.03b_a-mode_18Mbps_ch140



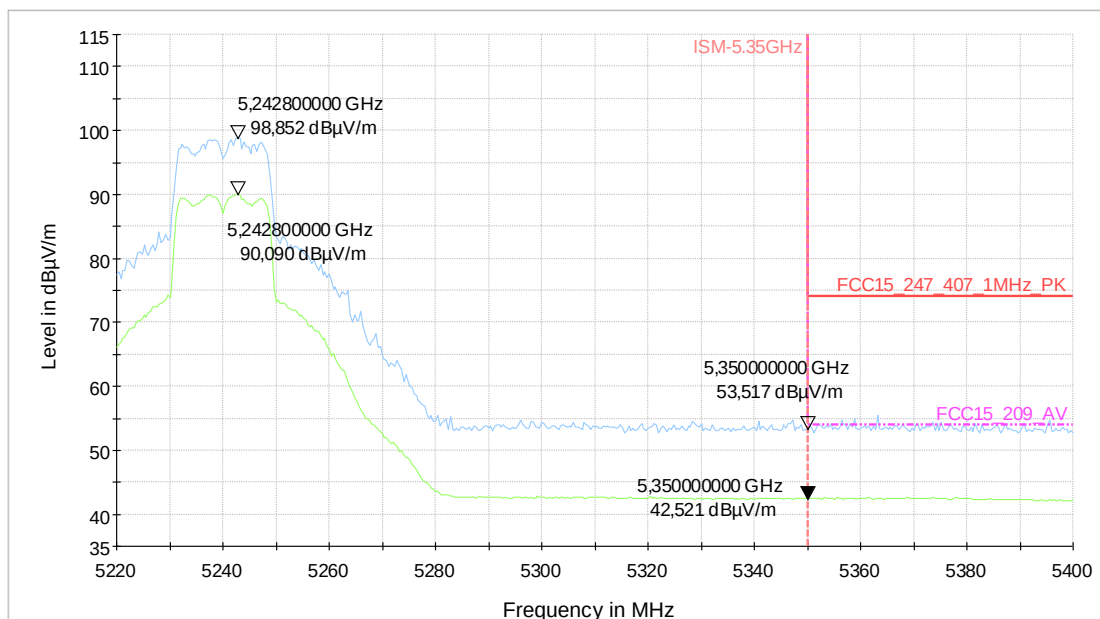
9.04a_a-mode_18Mbps_ch149



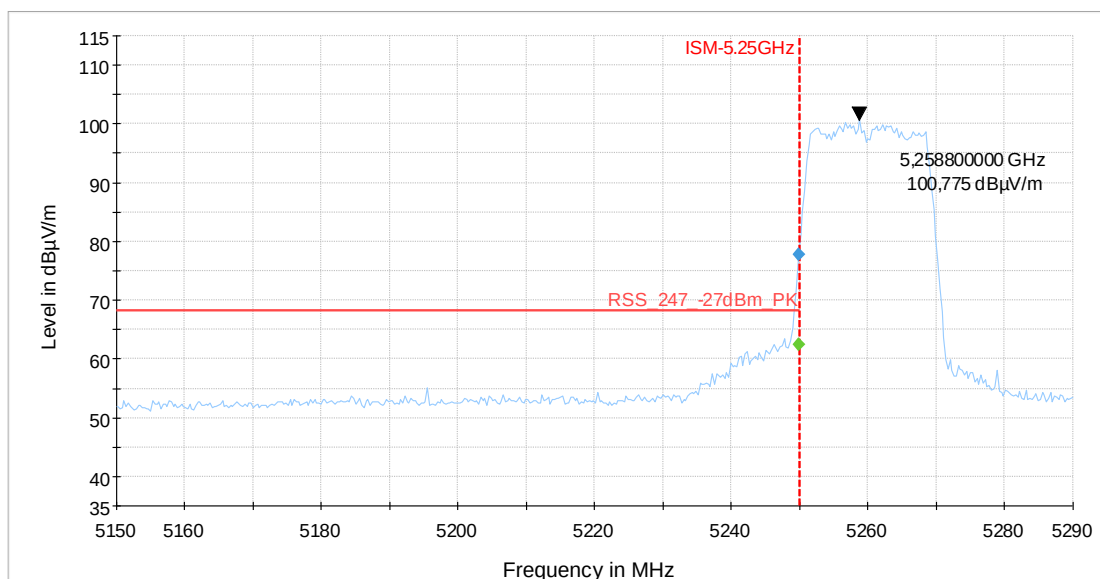
9.04b_a-mode_18Mbps_ch165



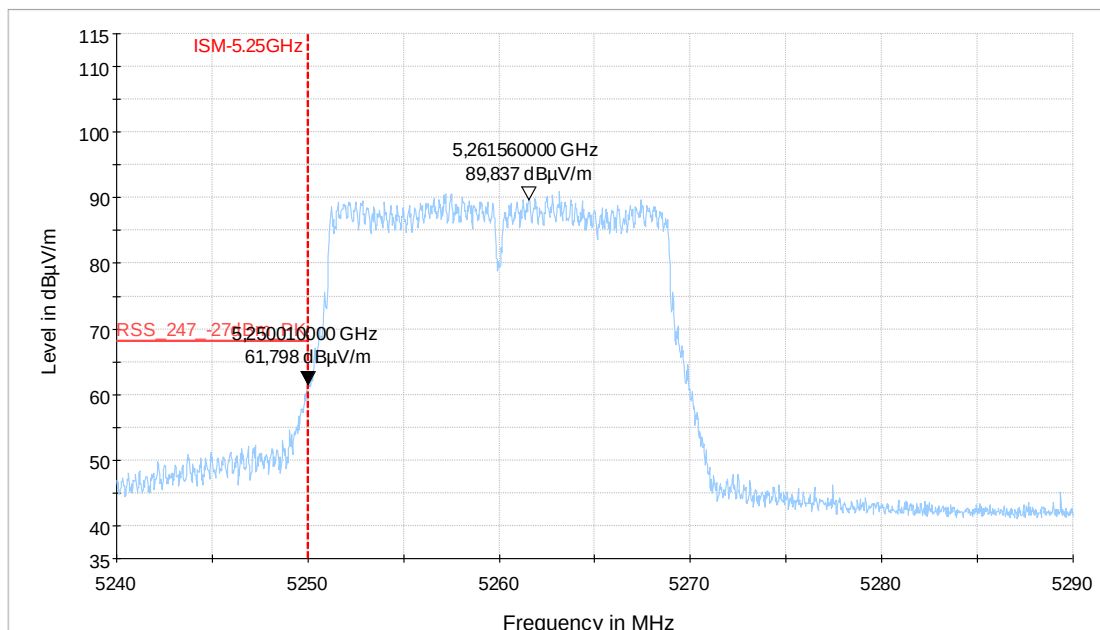
9.05a_n-mode_MCS7_ch036



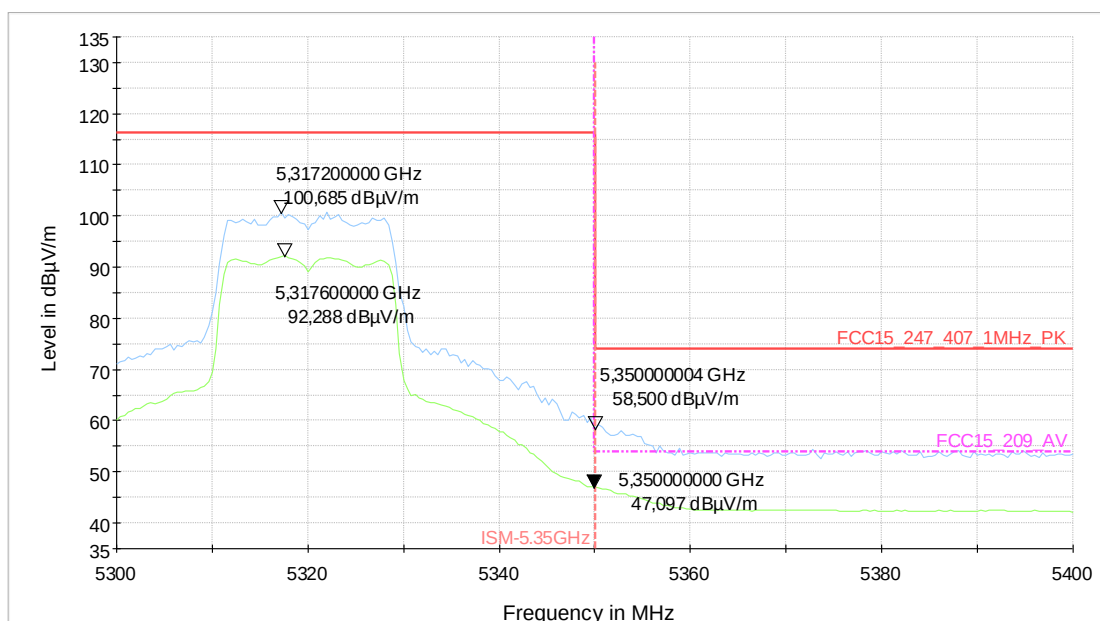
9.05b_n-mode_MCS7_ch048



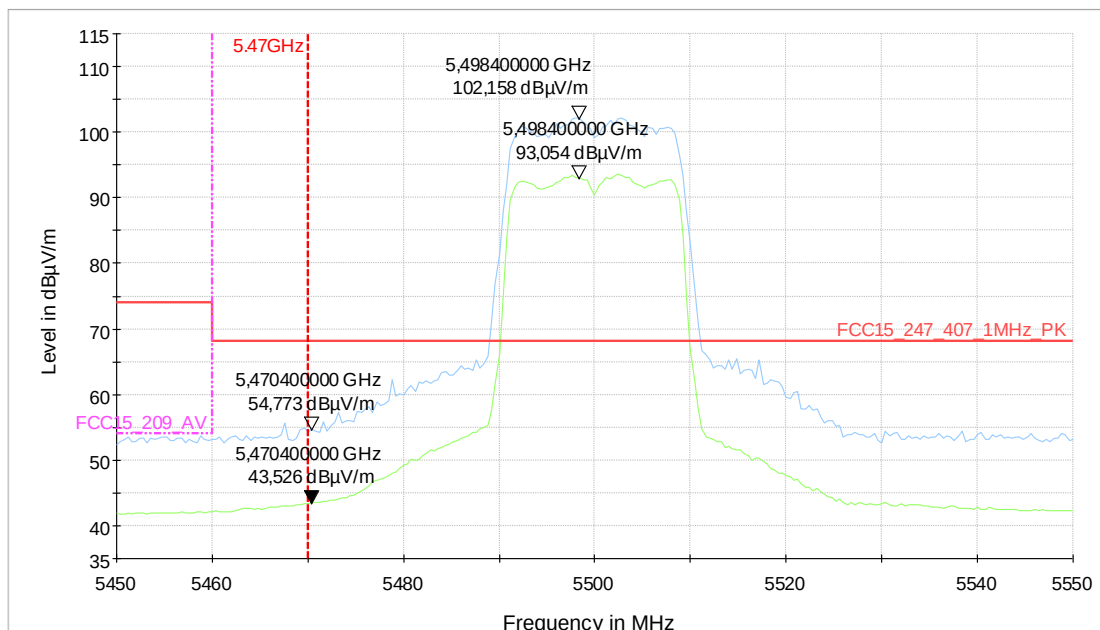
9.06a_n-mode_MCS7_ch52_Step1



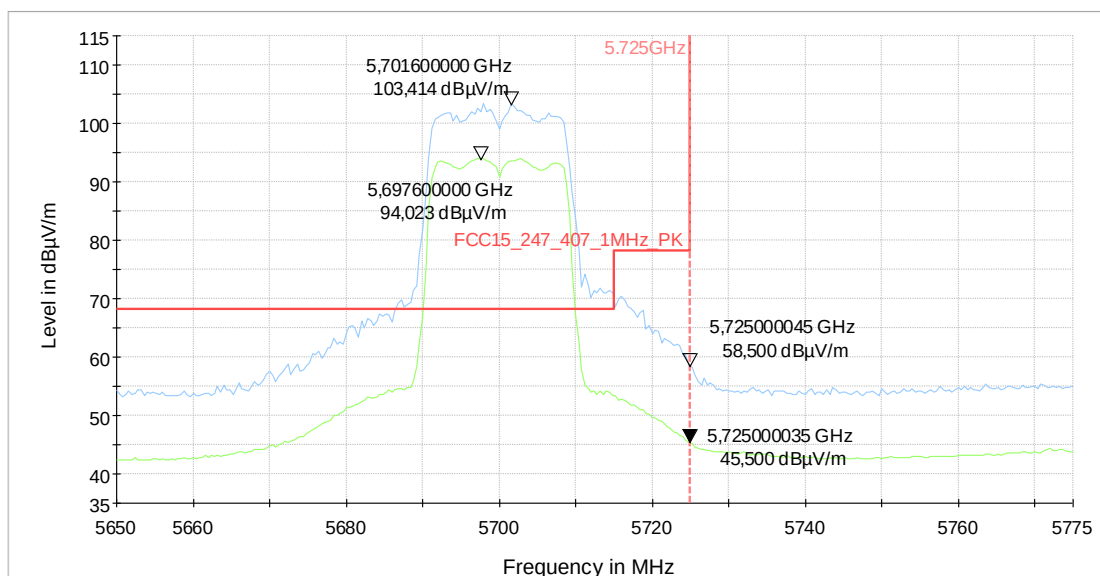
9.06a_n-mode_MCS7_ch52_Step2



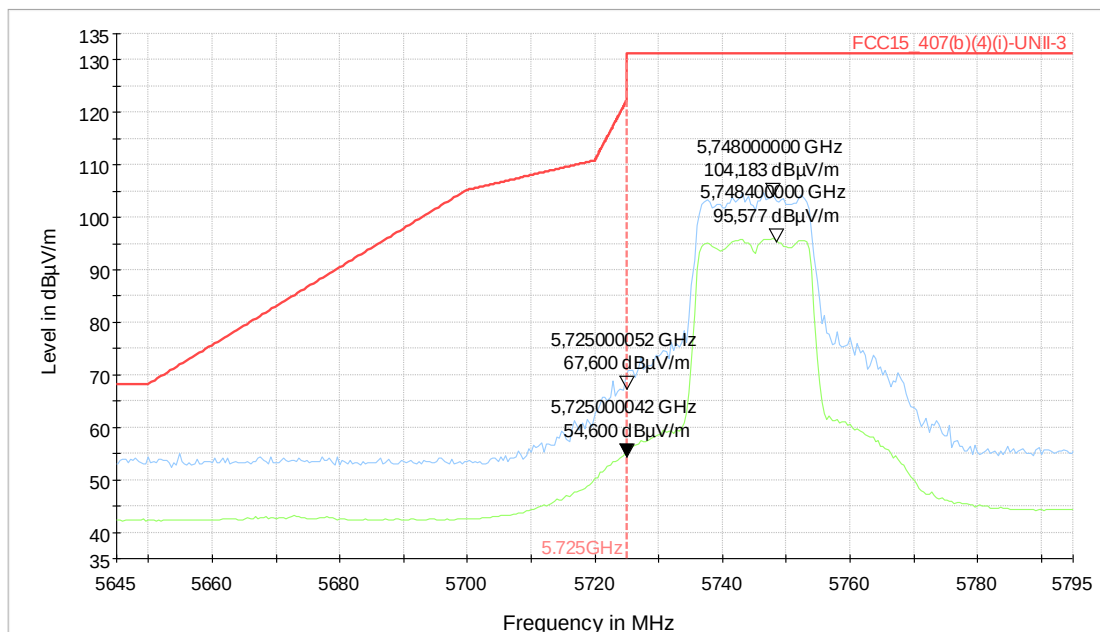
9.06b_n-mode_MCS7_ch064



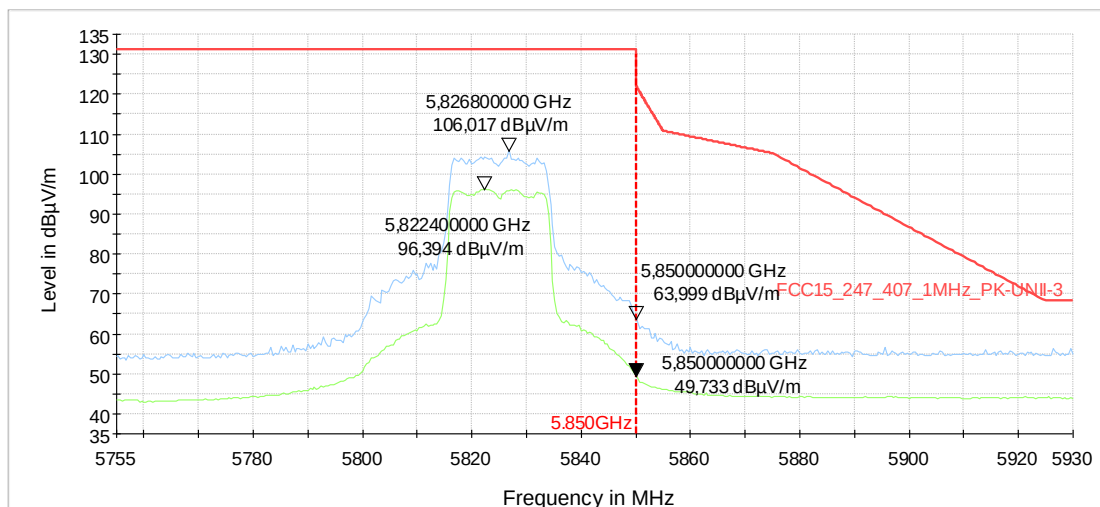
9.07a_n-mode_MCS7_ch100



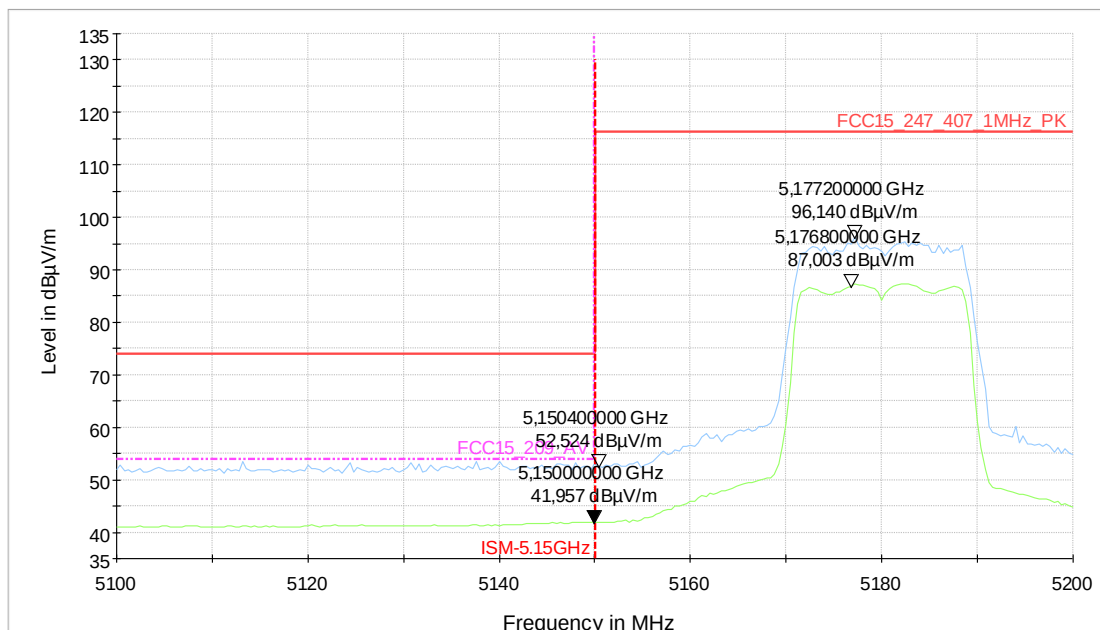
9.07b_n-mode_MCS7_ch140



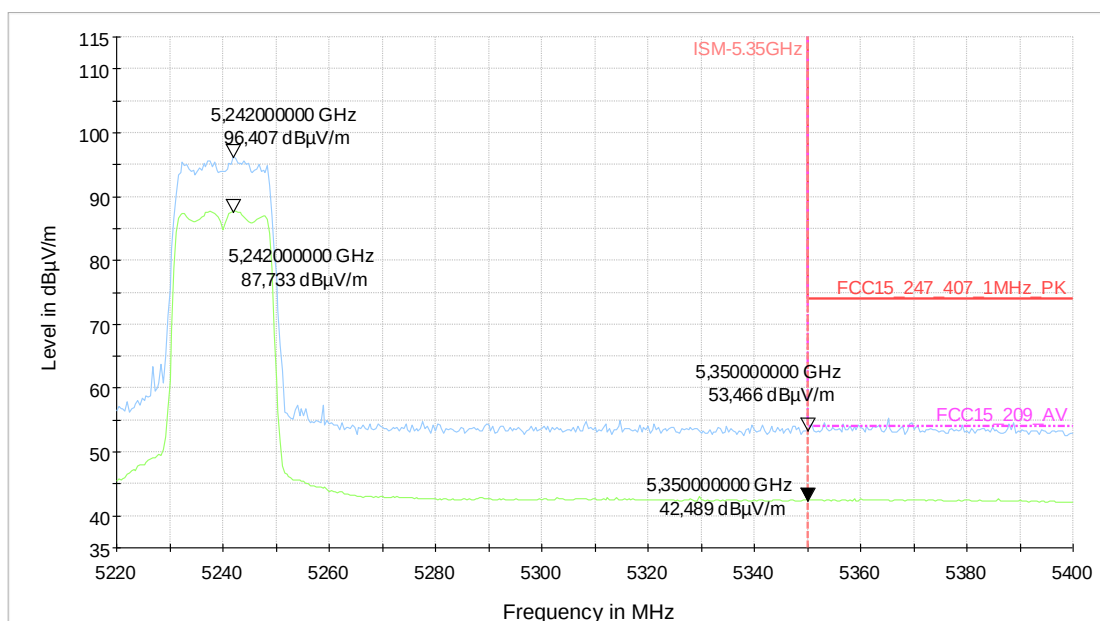
9.08a_n-mode_MCS7_ch149



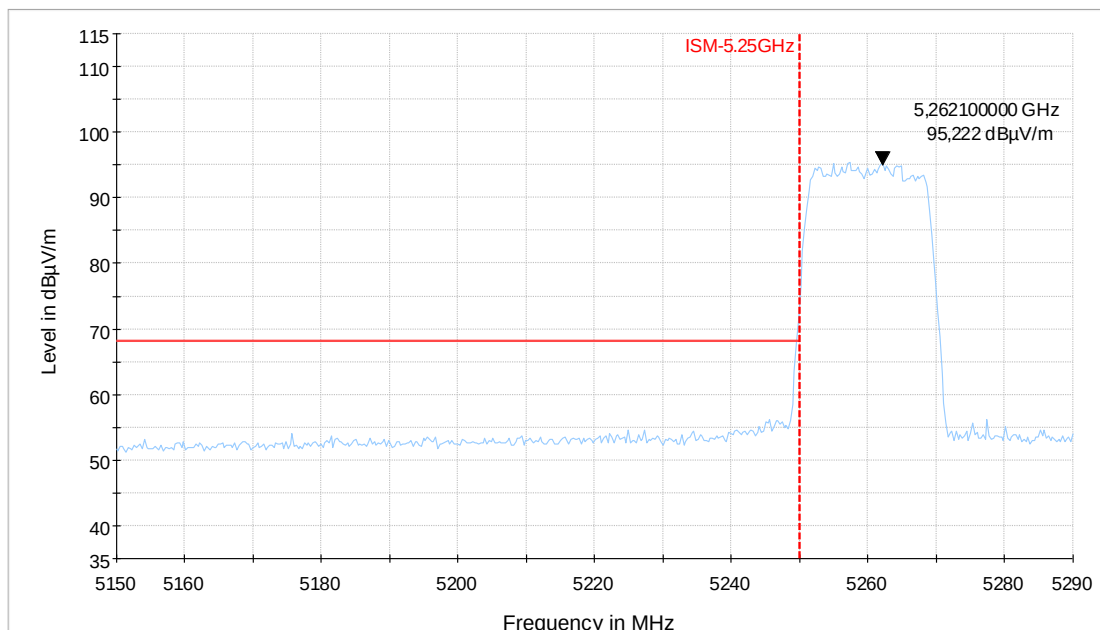
9.08b_n-mode_MCS7_ch165



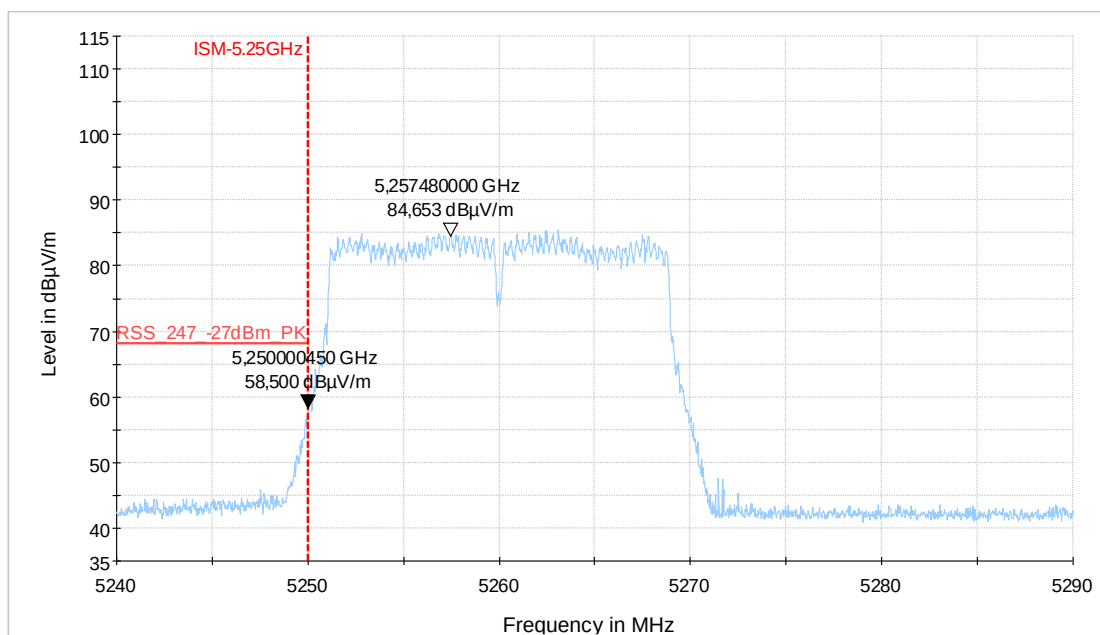
9.09a_ac-mode_MCS1_ch036



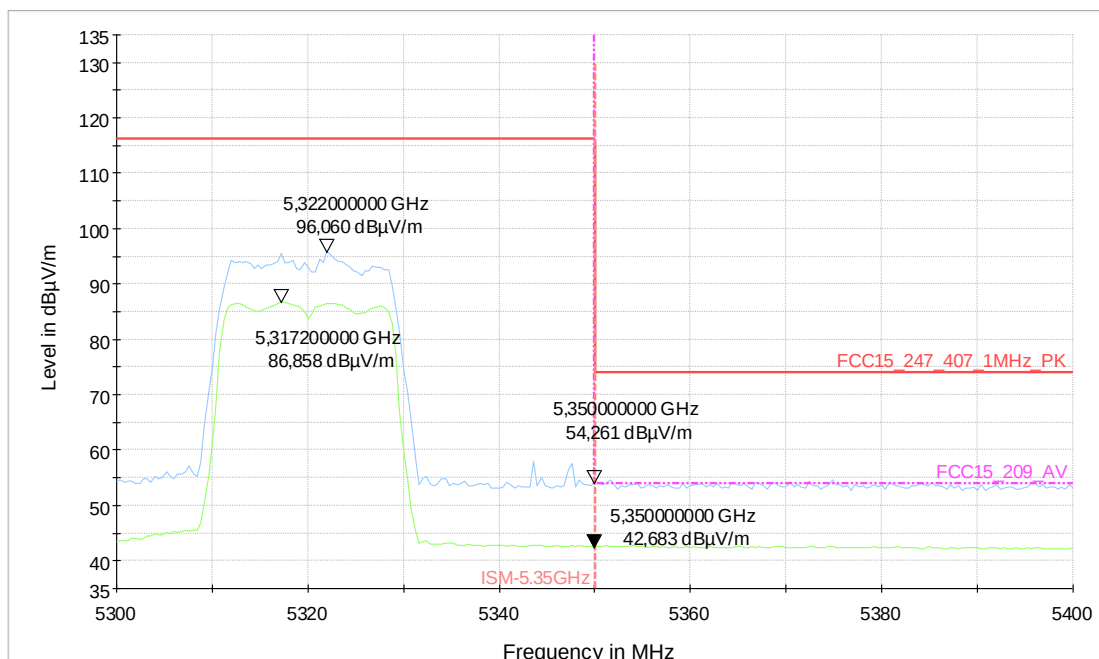
9.09b_ac-mode_MCS1_ch048



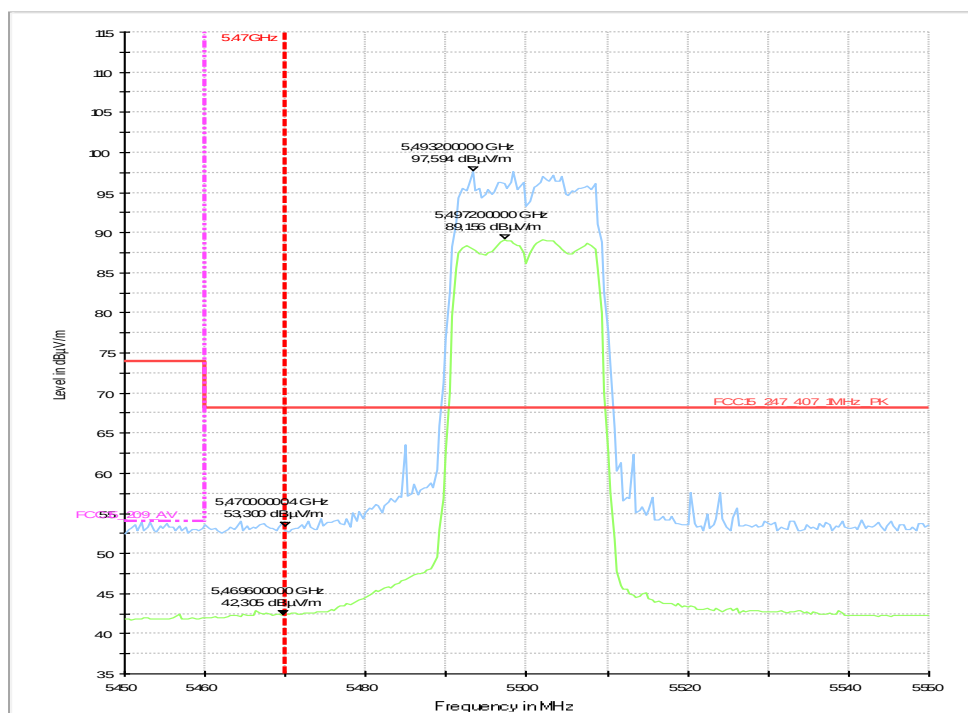
9.10a_ac-mode_MCS1_ch52_Step1



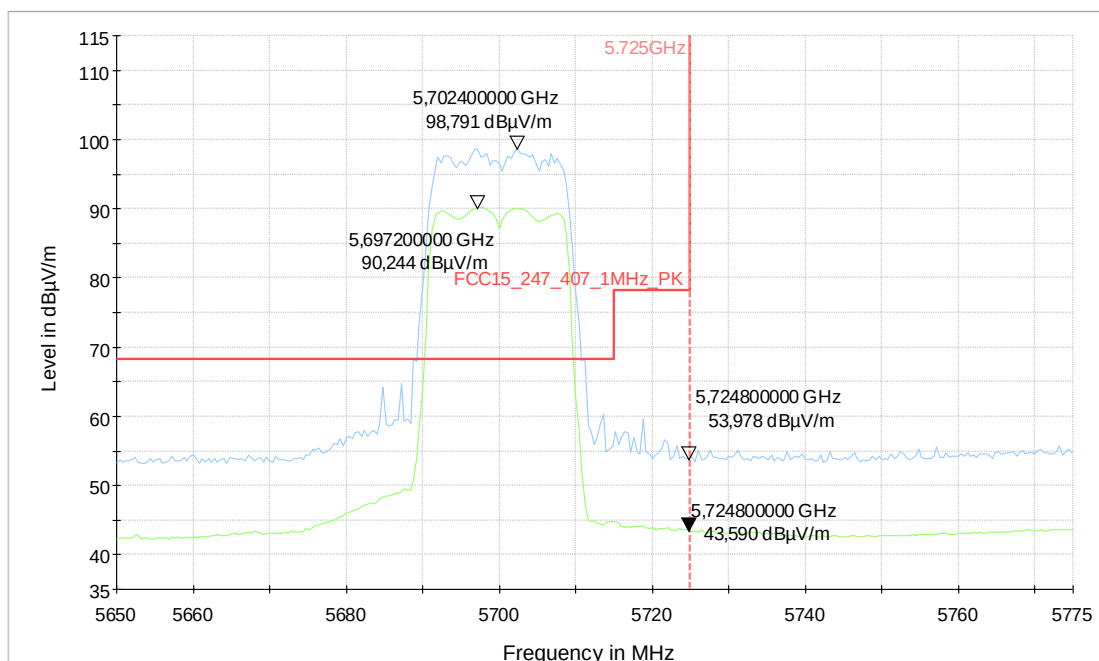
9.10a_ac-mode_MCS1_ch52_Step2



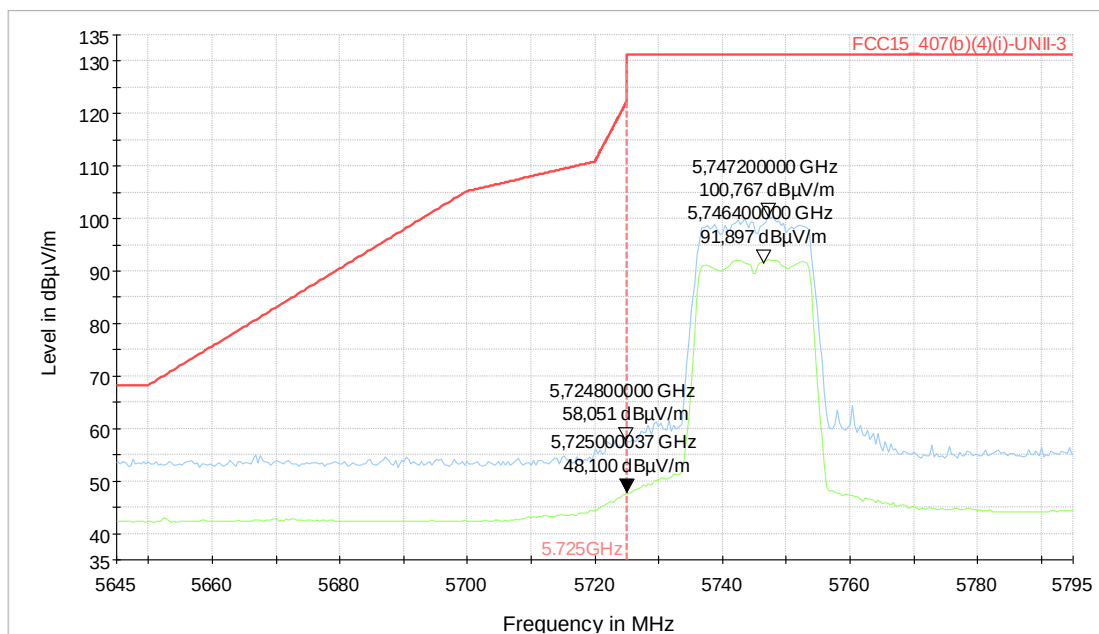
9.10b_ac-mode_MCS1_ch064



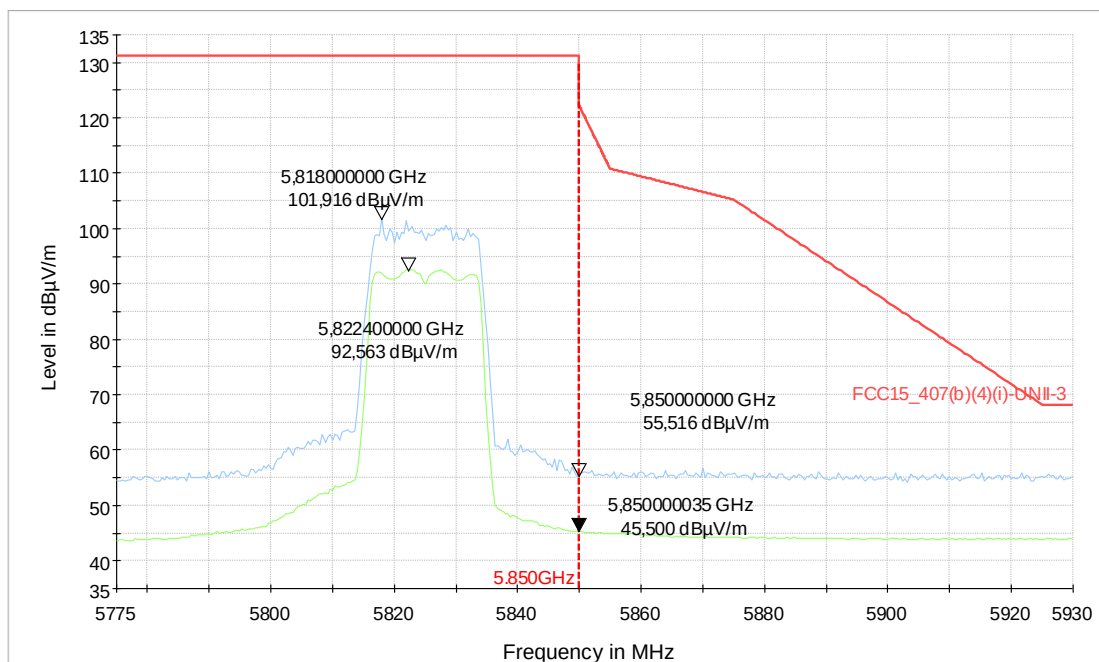
9.11a_ac-mode_MCS1_ch100



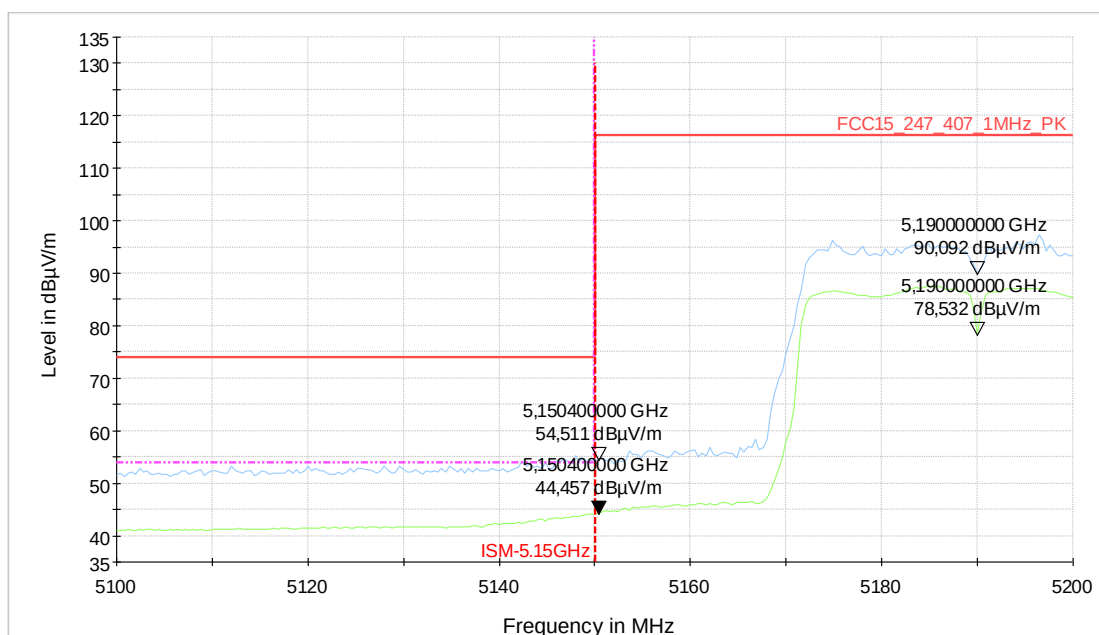
9.11b_ac-mode_MCS1_ch140



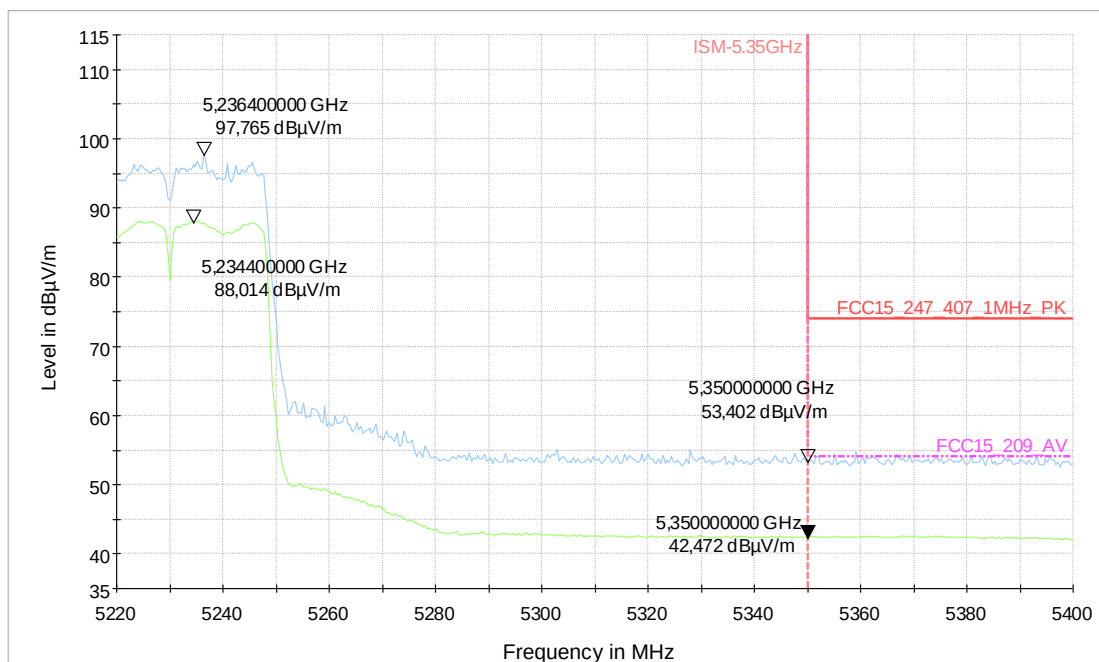
9.12a_ac-mode_MCS1_ch149



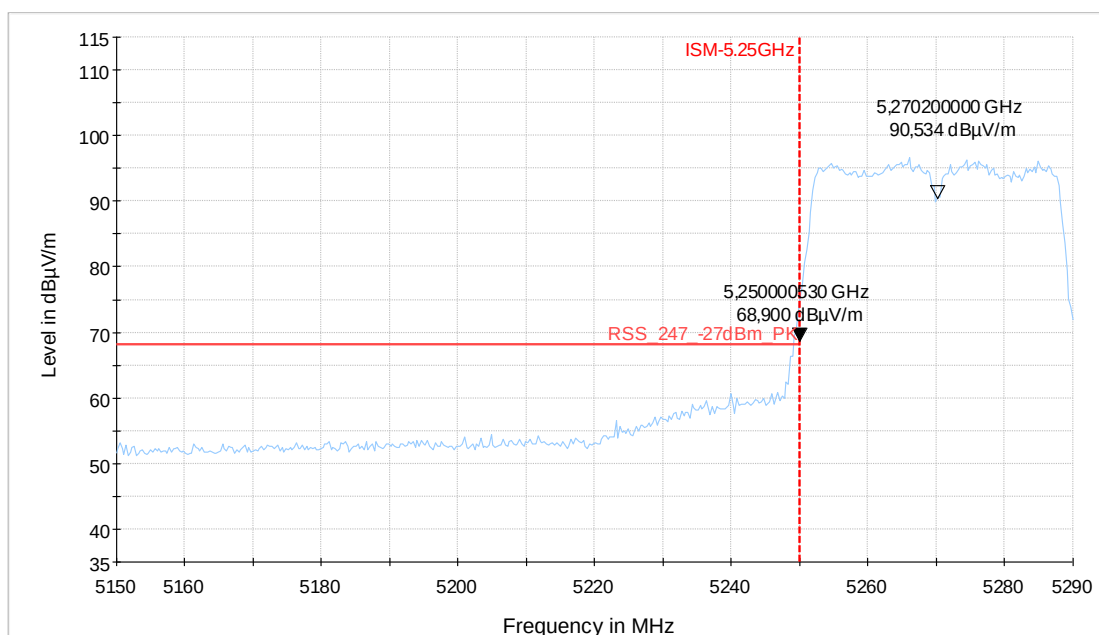
9.12b_ac-mode_MCS1_ch165



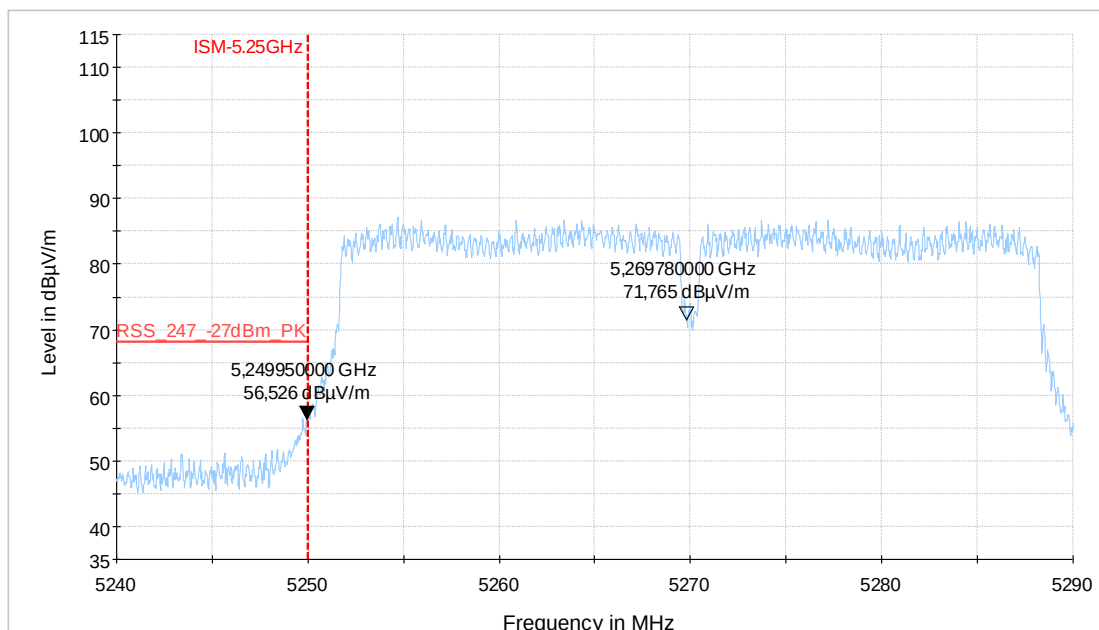
9.13a_n-mode_MCS3_ch038



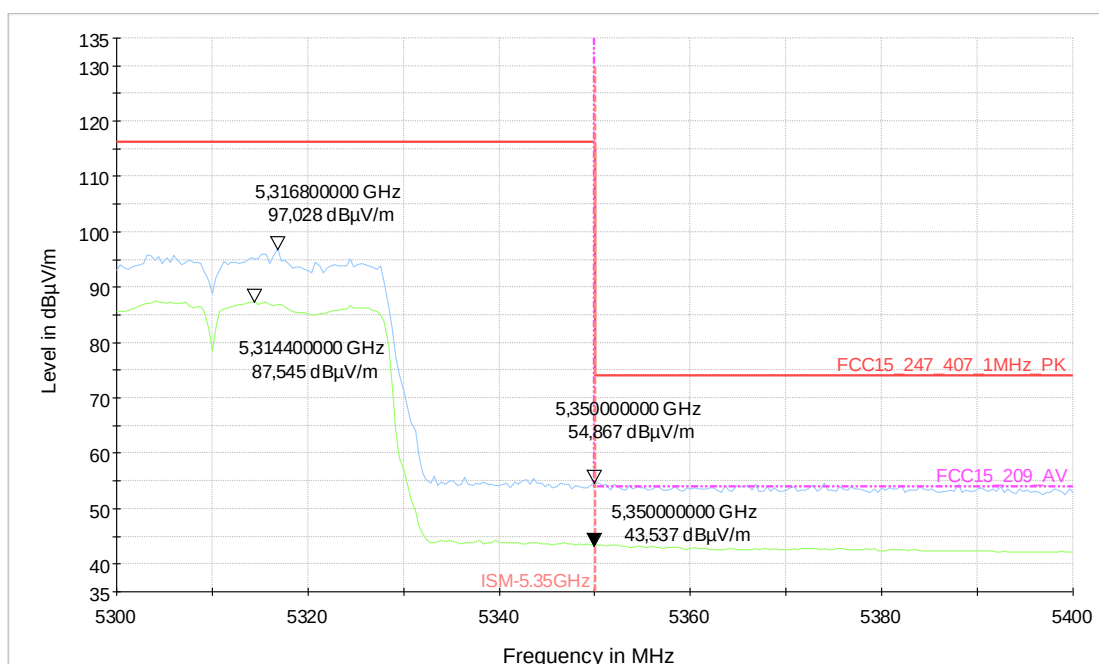
9.13b_n-mode_MCS3_ch046



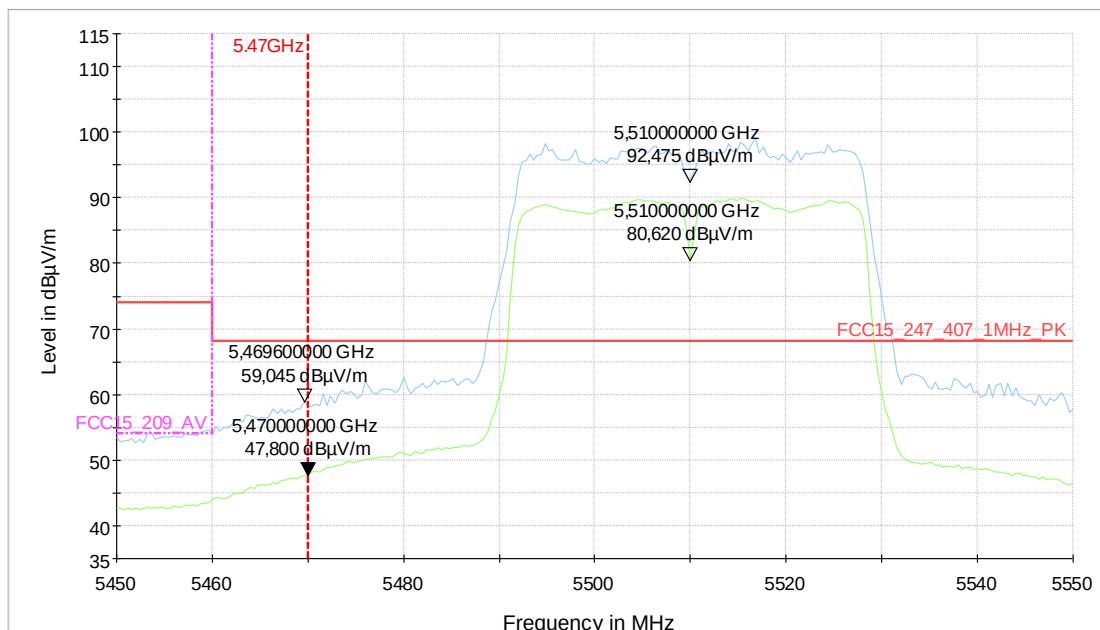
9.14a_n-mode_MCS3_ch54_STep1



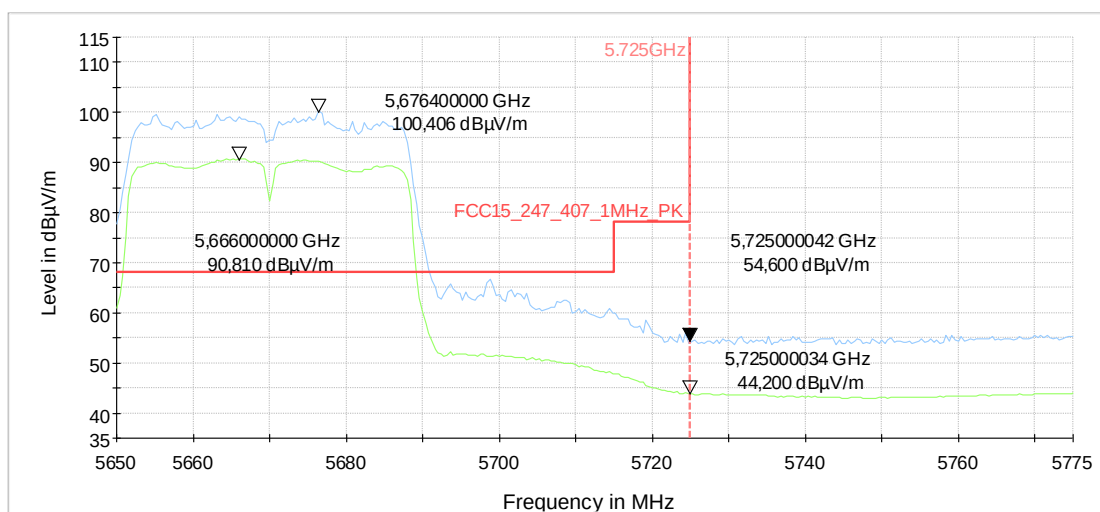
9.14a_n-mode_MCS3_ch54_STep2



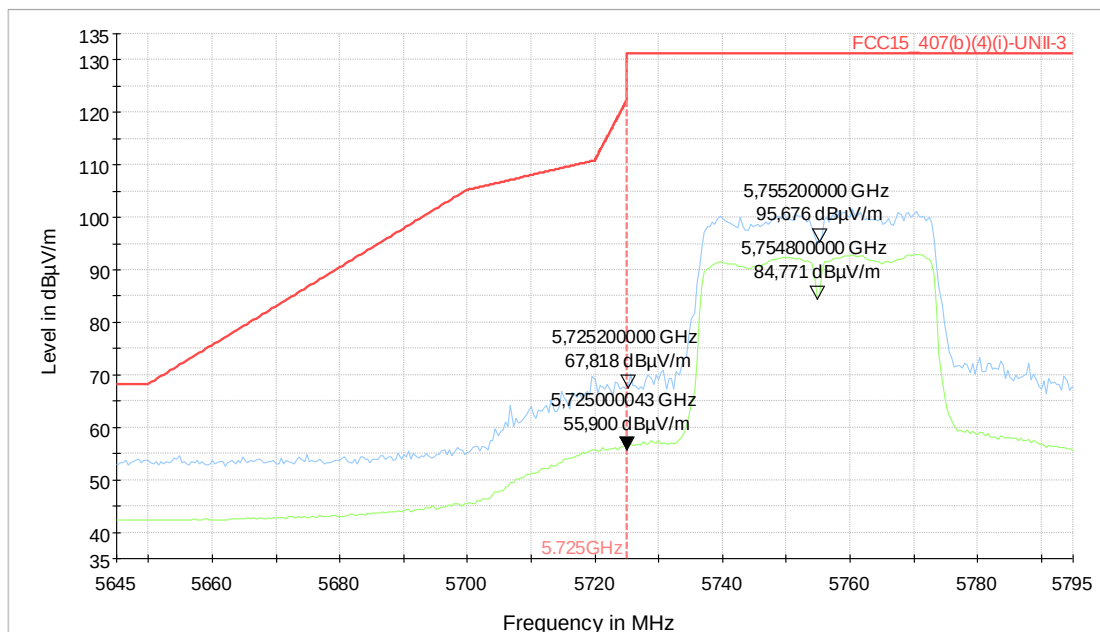
9.14b_n-mode_MCS3_ch062



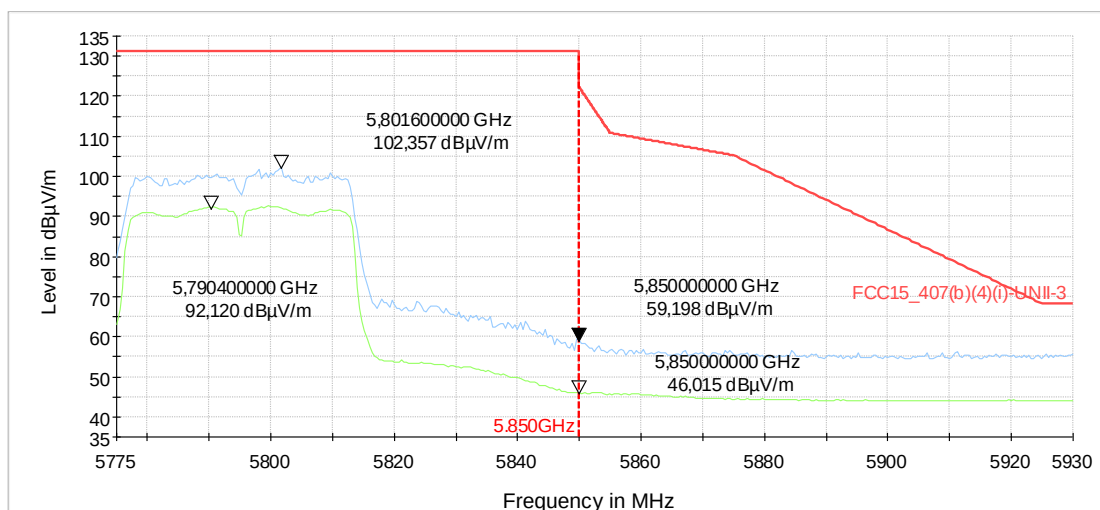
9.15a_n-mode_MCS3_ch102



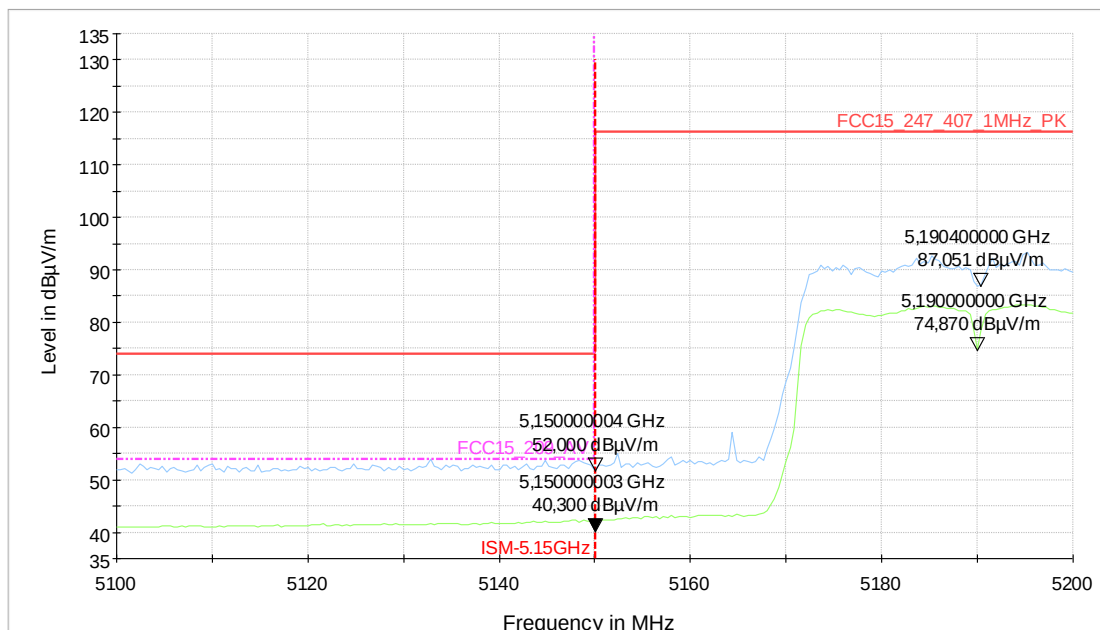
9.15b_n-mode_MCS3_ch134



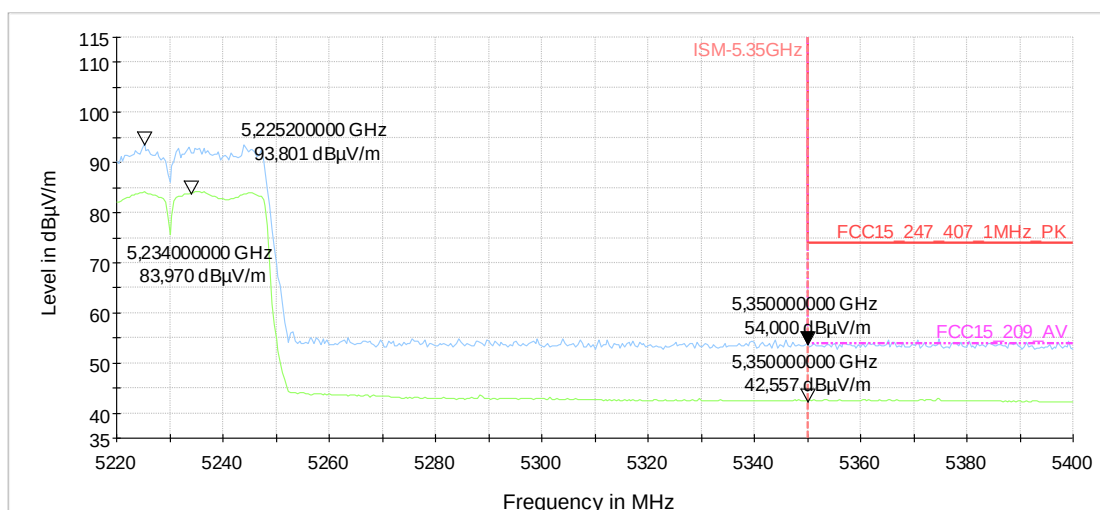
9.16a_n-mode_MCS3_ch151



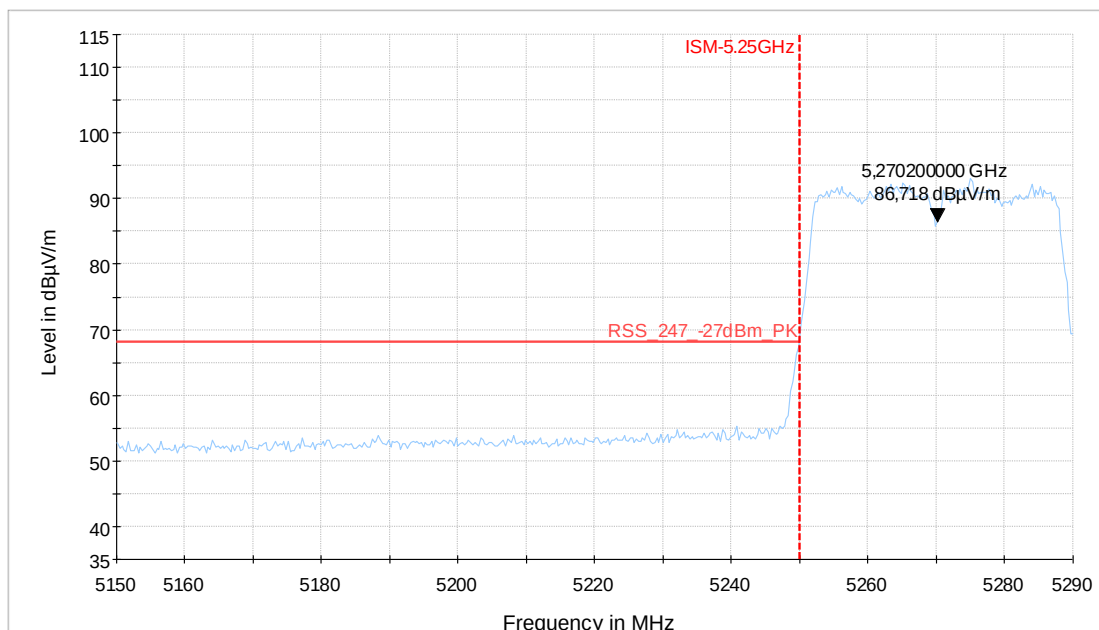
9.16b_n-mode_MCS3_ch159



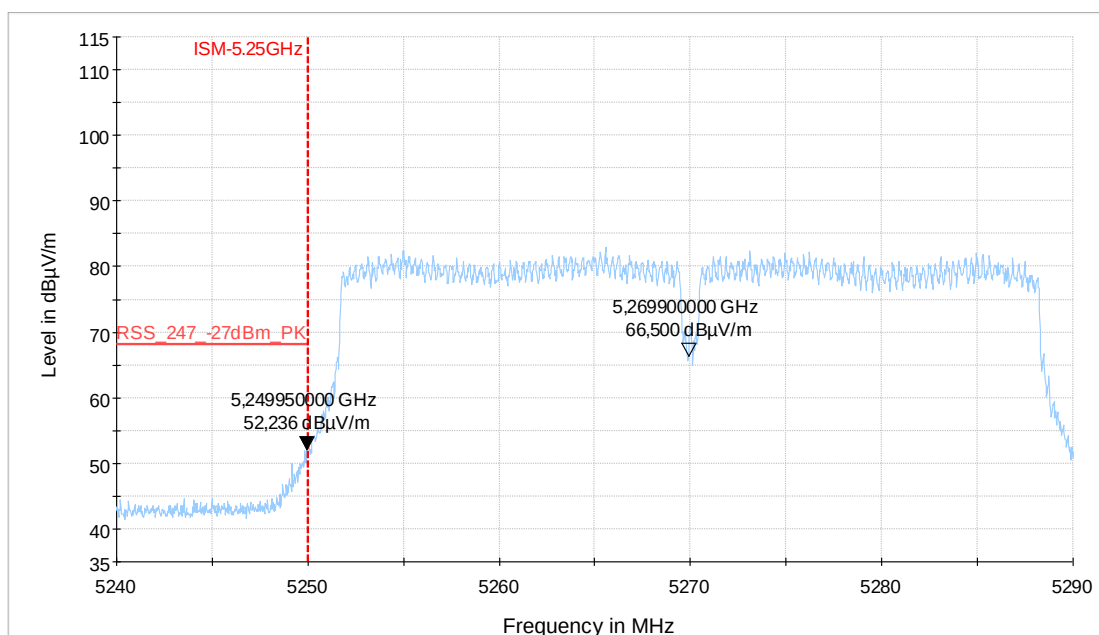
9.17a_ac-mode_MCS4_ch038



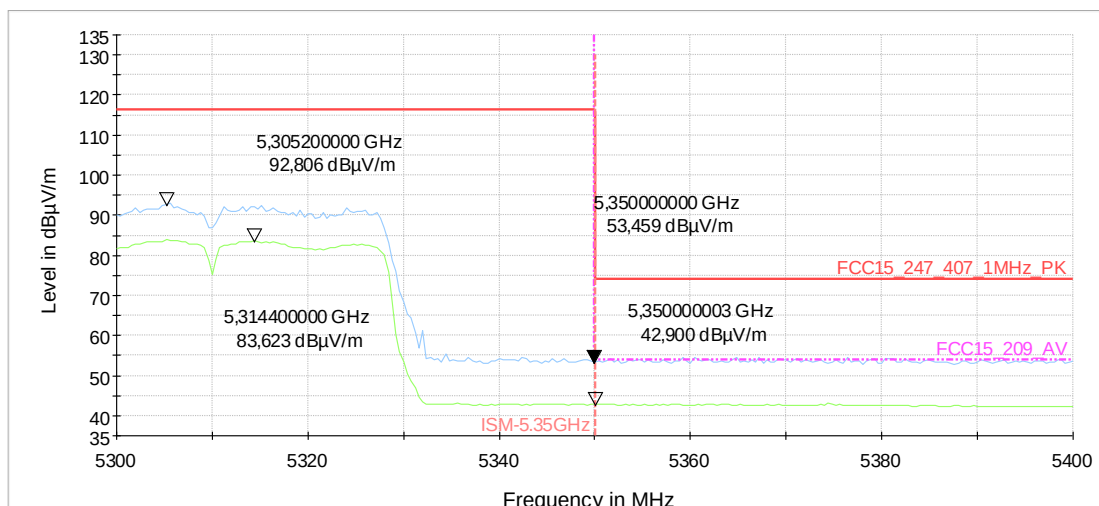
9.17b_ac-mode_MCS4_ch046



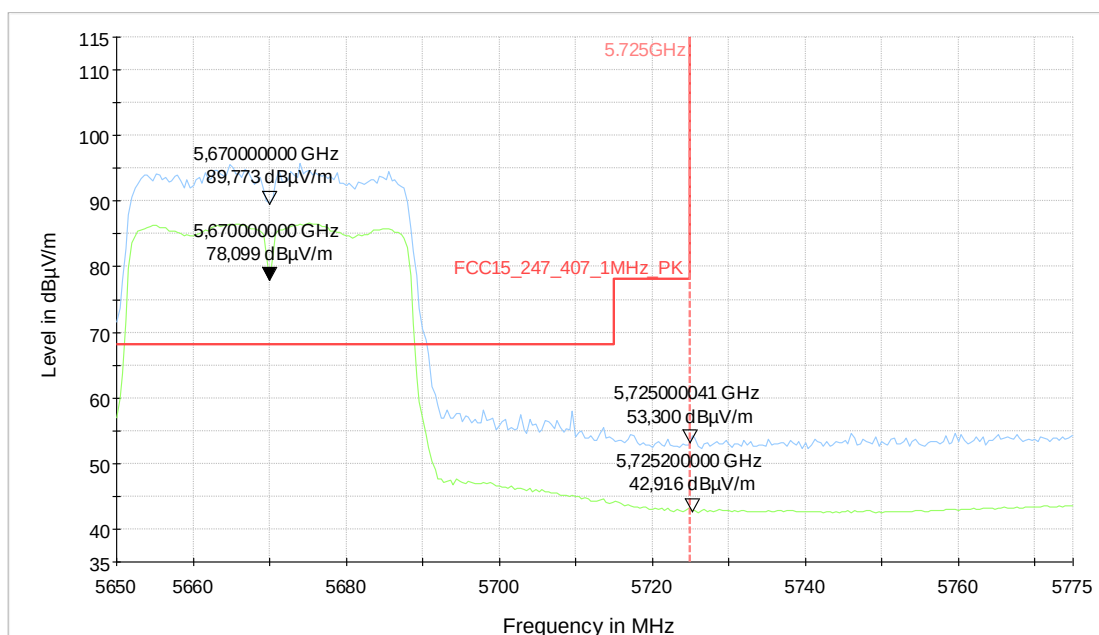
9.18a_ac-mode_MCS4_ch54_Step1



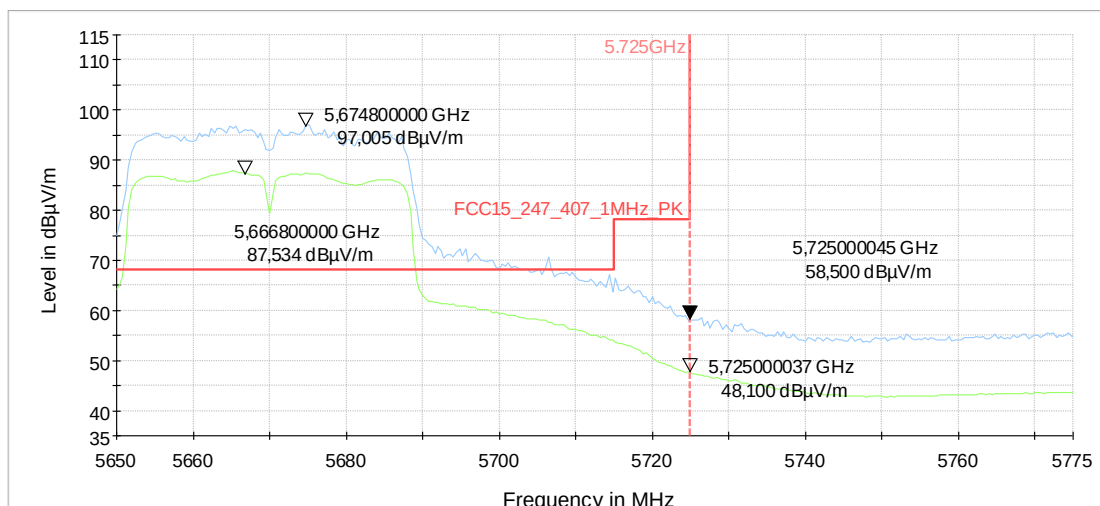
9.18a_ac-mode_MCS4_ch54_Step2



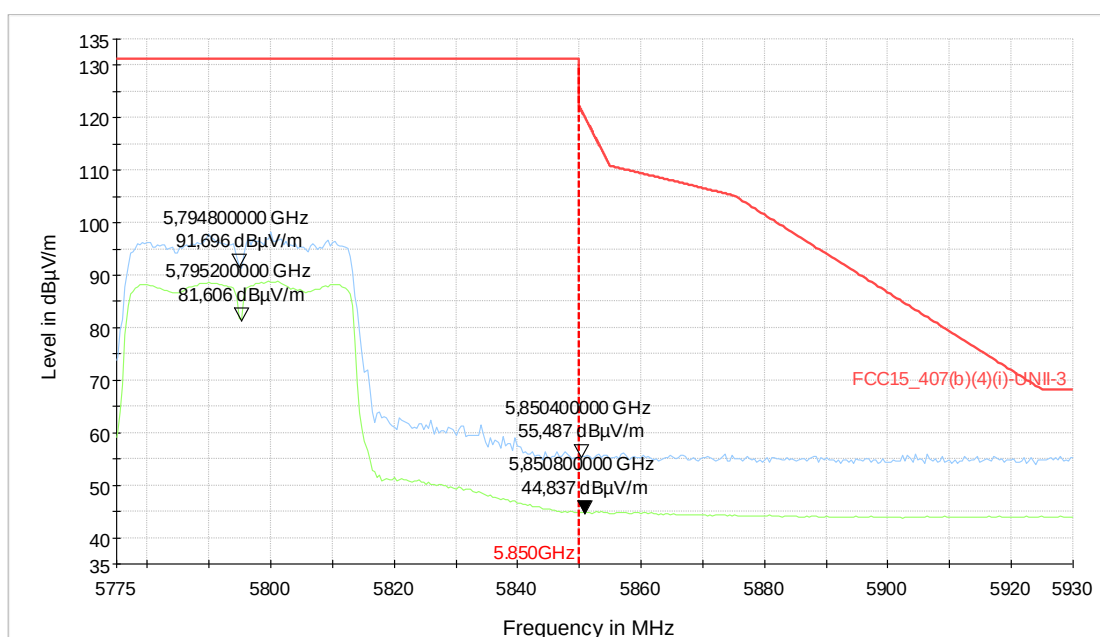
9.18b_ac-mode_MCS4_ch62



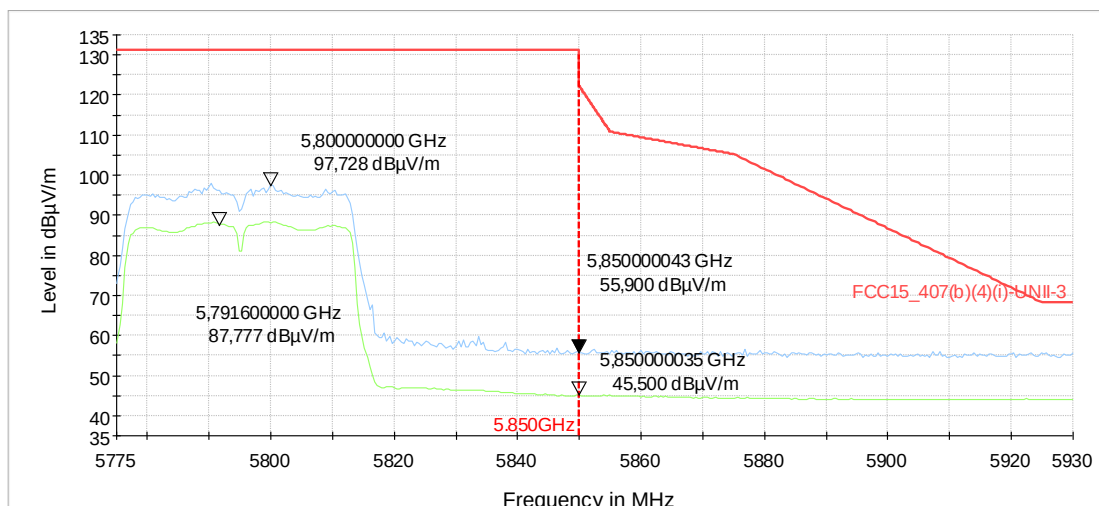
9.19a_ac-mode_MCS4_ch134



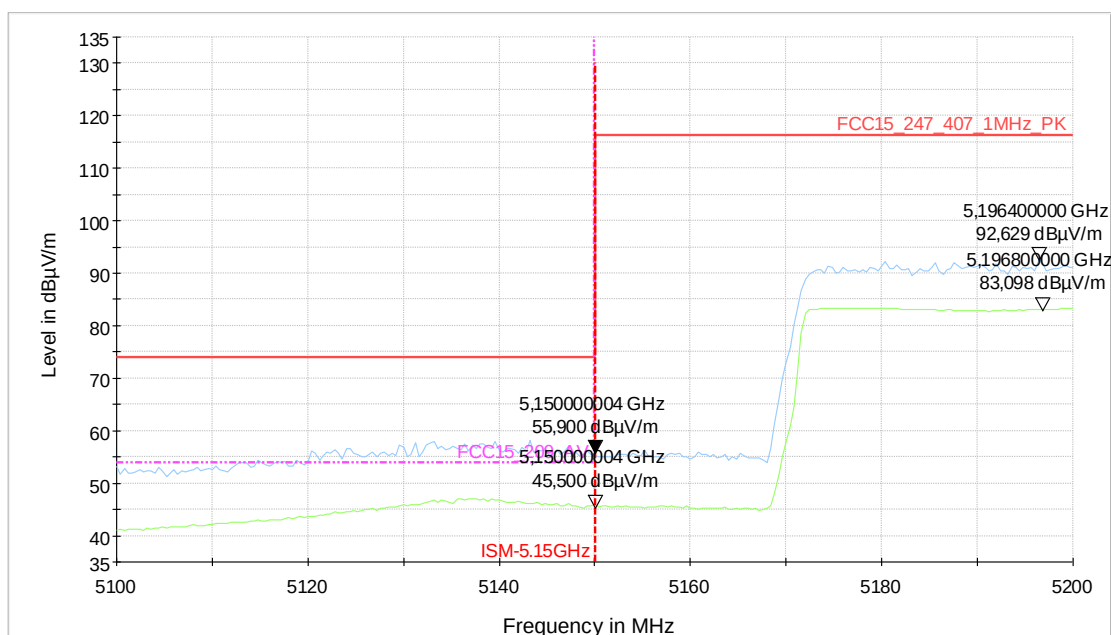
9.19b_ac-mode_MCS4_ch134



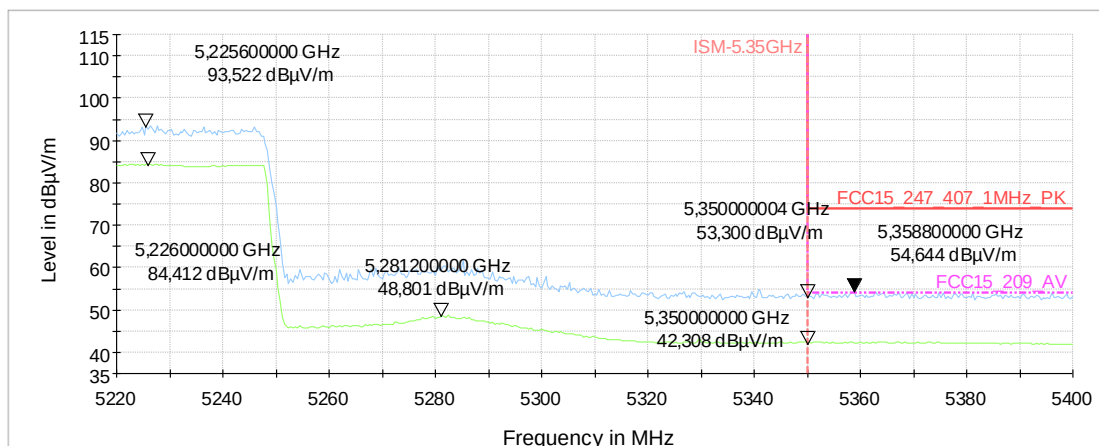
9.20a_ac-mode_MCS4_ch159



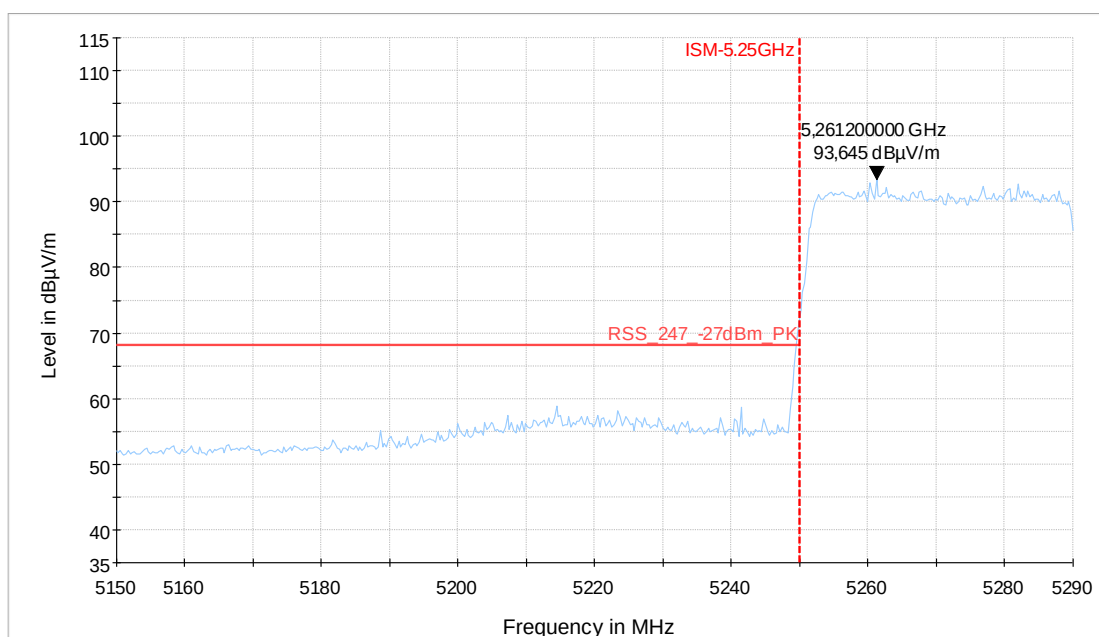
9.20b_ac-mode_MCS4_ch159



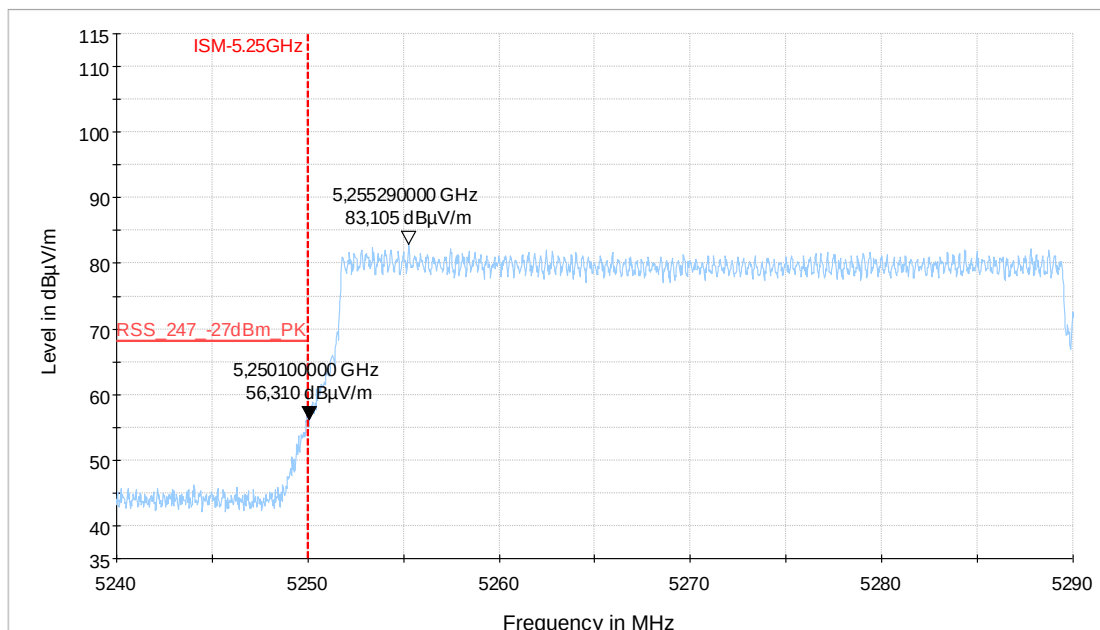
9.21a_ac-mode_MCS1_ch042



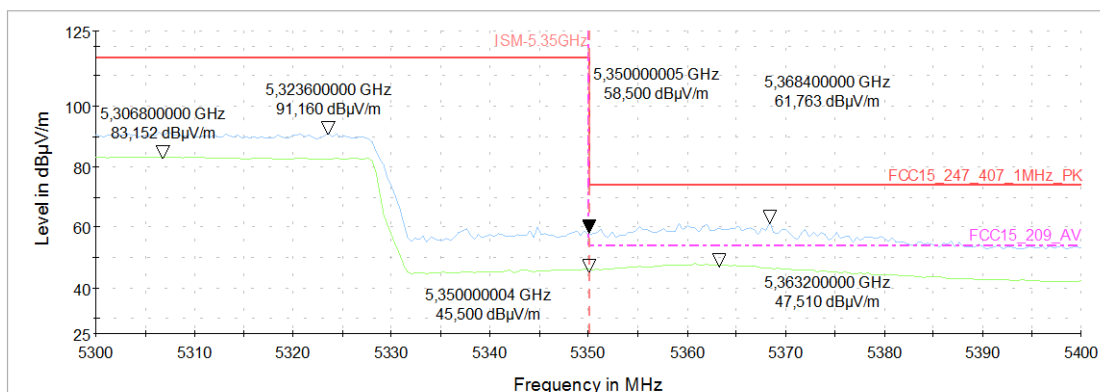
9.21b_ac-mode_MCS1_ch042



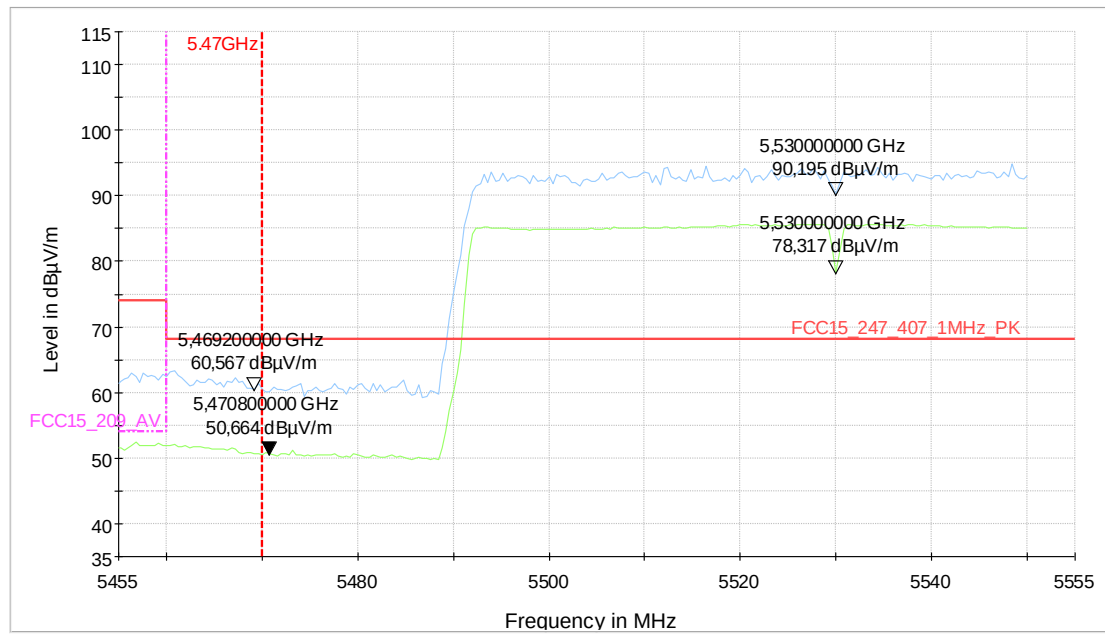
9.22a_ac-mode_MCS1_ch58_Step1



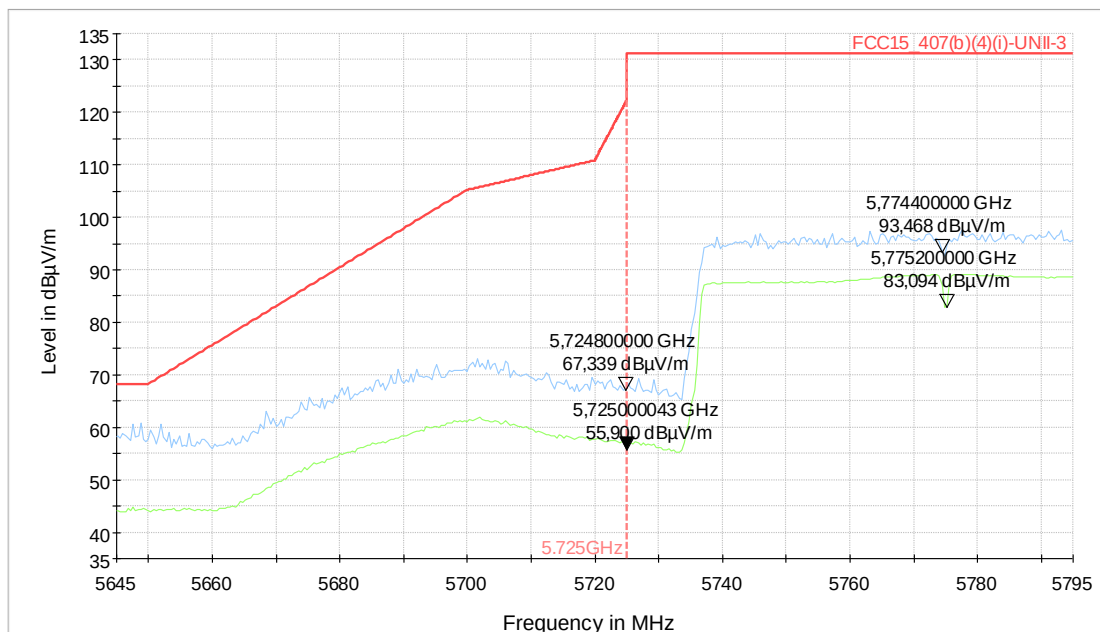
9.22a_ac-mode_MCS1_ch58_Step2



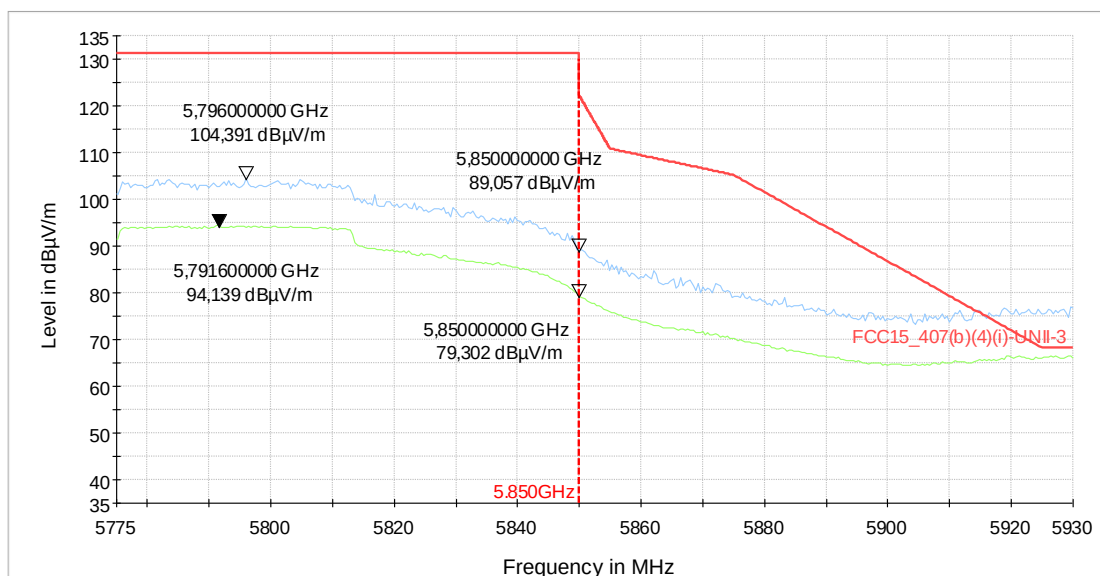
9.22b_ac-mode_MCS1_ch58



9.23a_ac-mode_MCS1_ch106



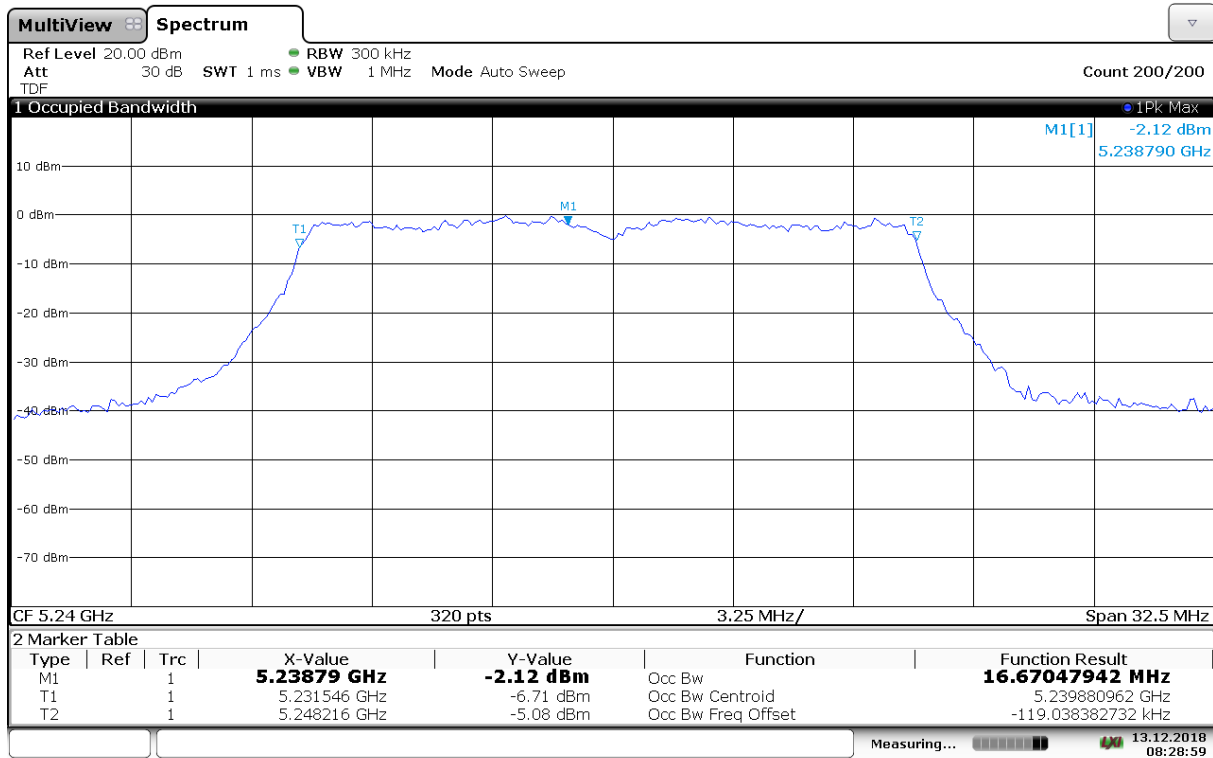
9.24a_ac-mode_MCS1_ch155



9.24b_ac-mode_MCS1_ch155

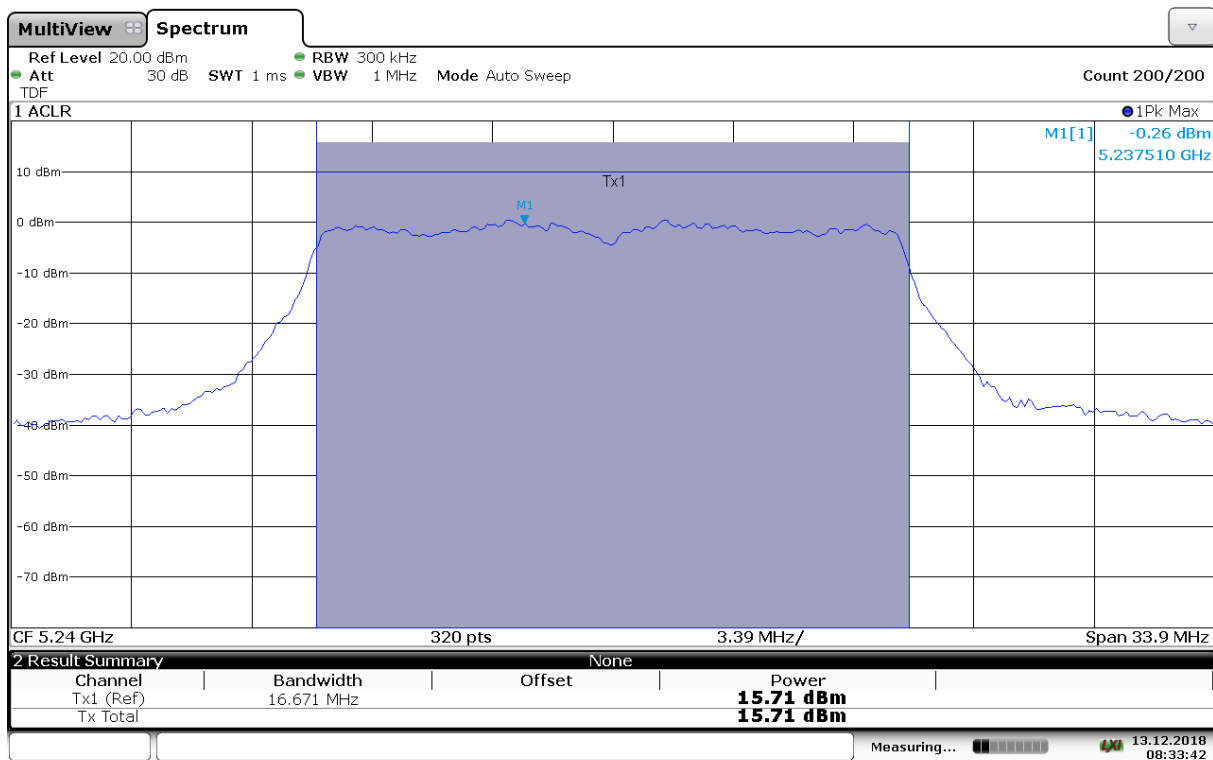
2.6.1. Canada RSS-247 requirement

2.6.1.1. 20 MHz



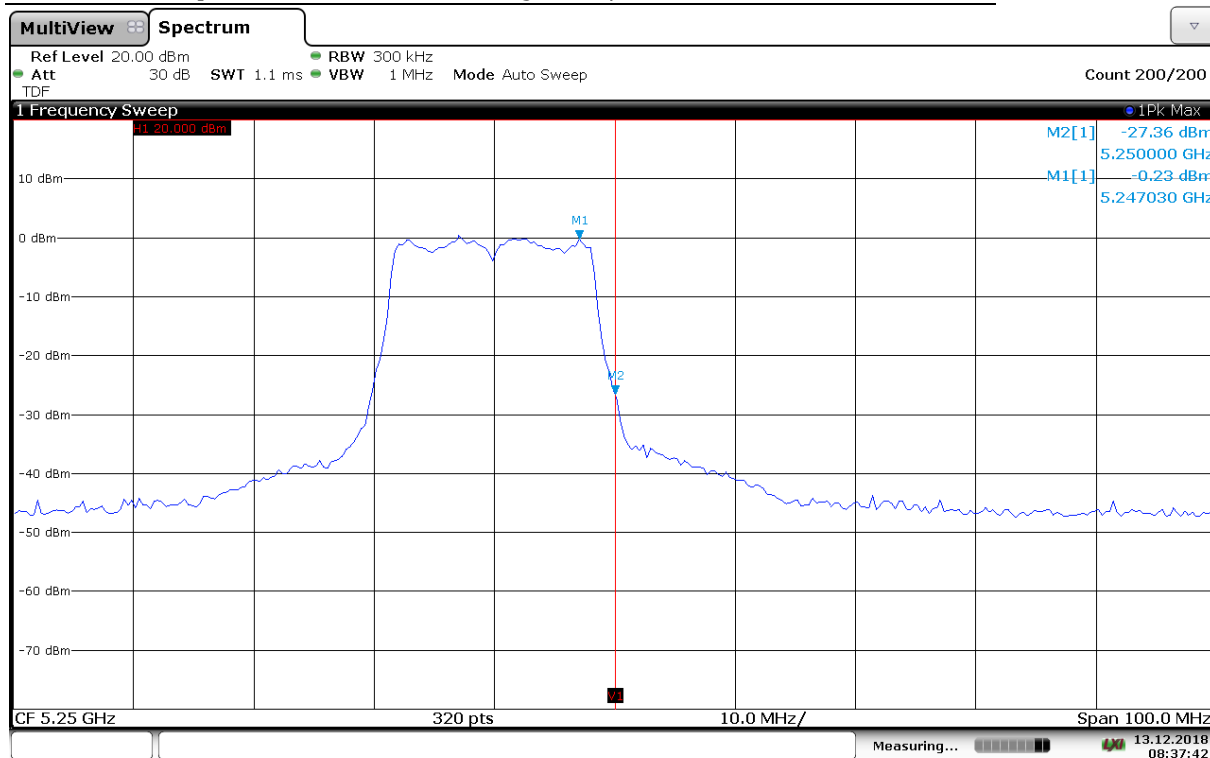
08:29:00 13.12.2018

35.01a_OBW_amode_ch48_20_18Mbit



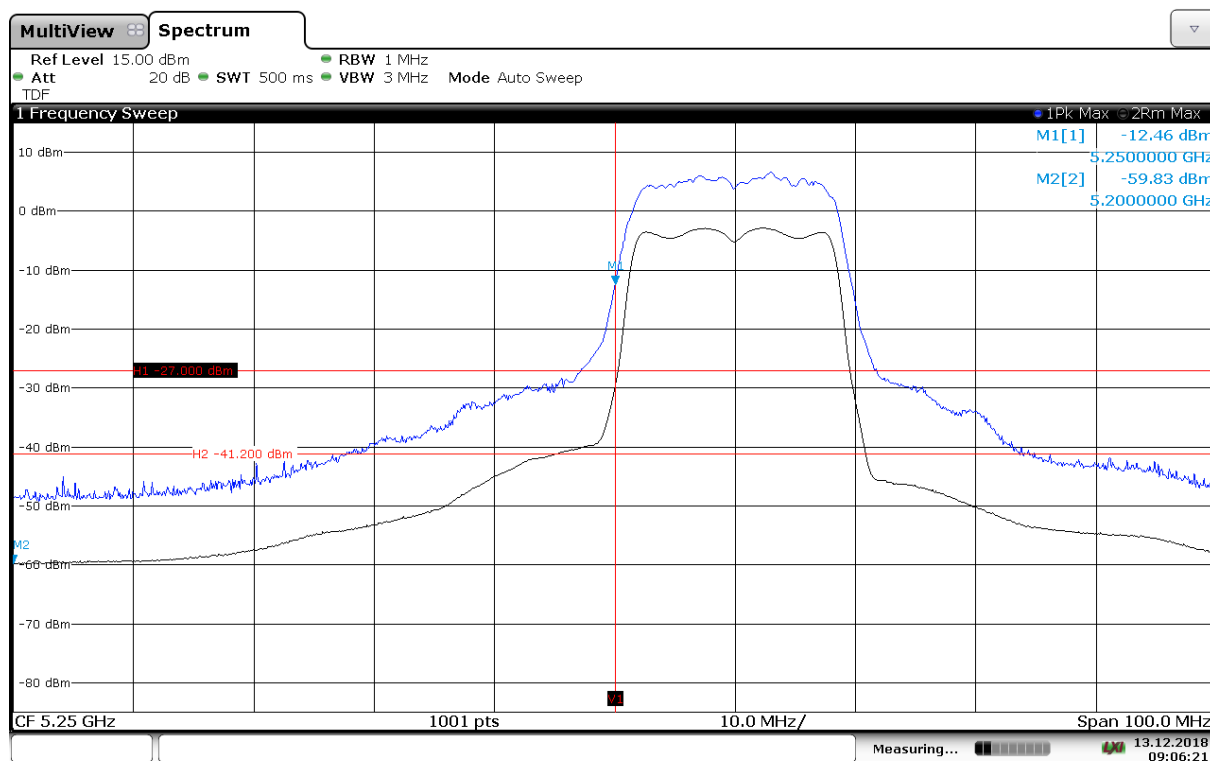
08:33:42 13.12.2018

35.01b_CHPWR_amode_ch48_20_18mbit



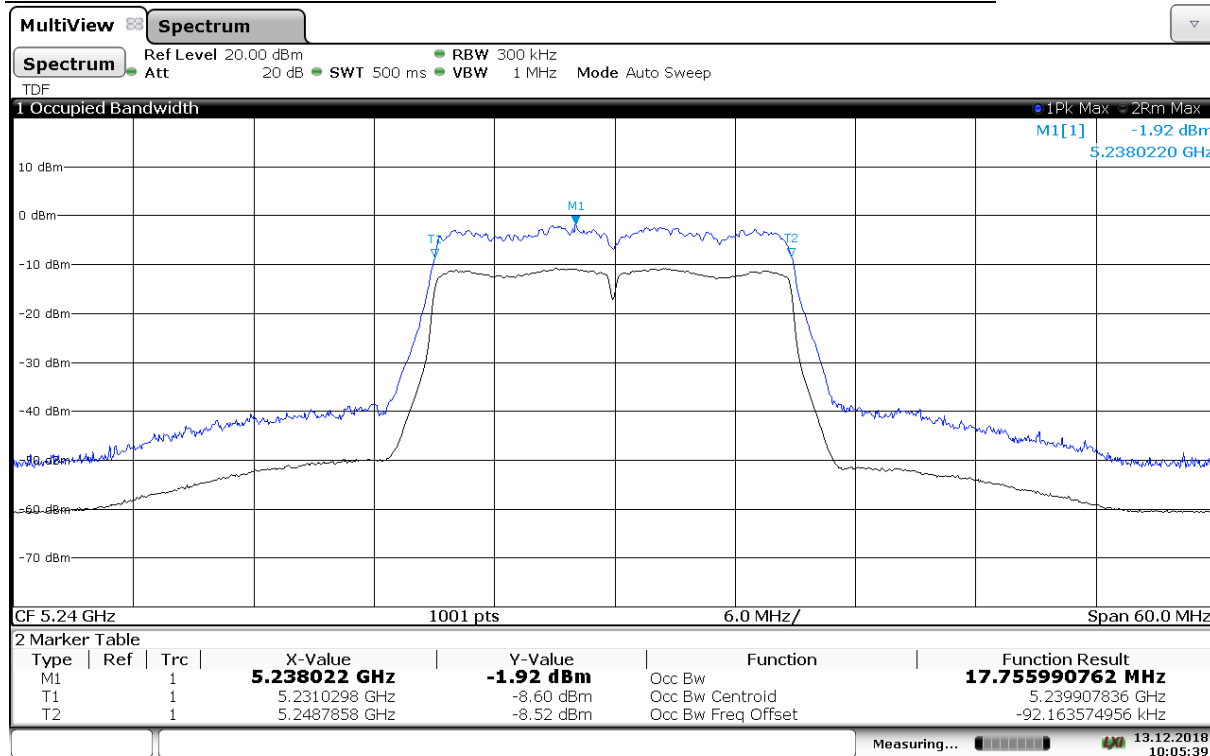
08:37:42 13.12.2018

35.01c_BE_high_amode_ch48_20_18mbit



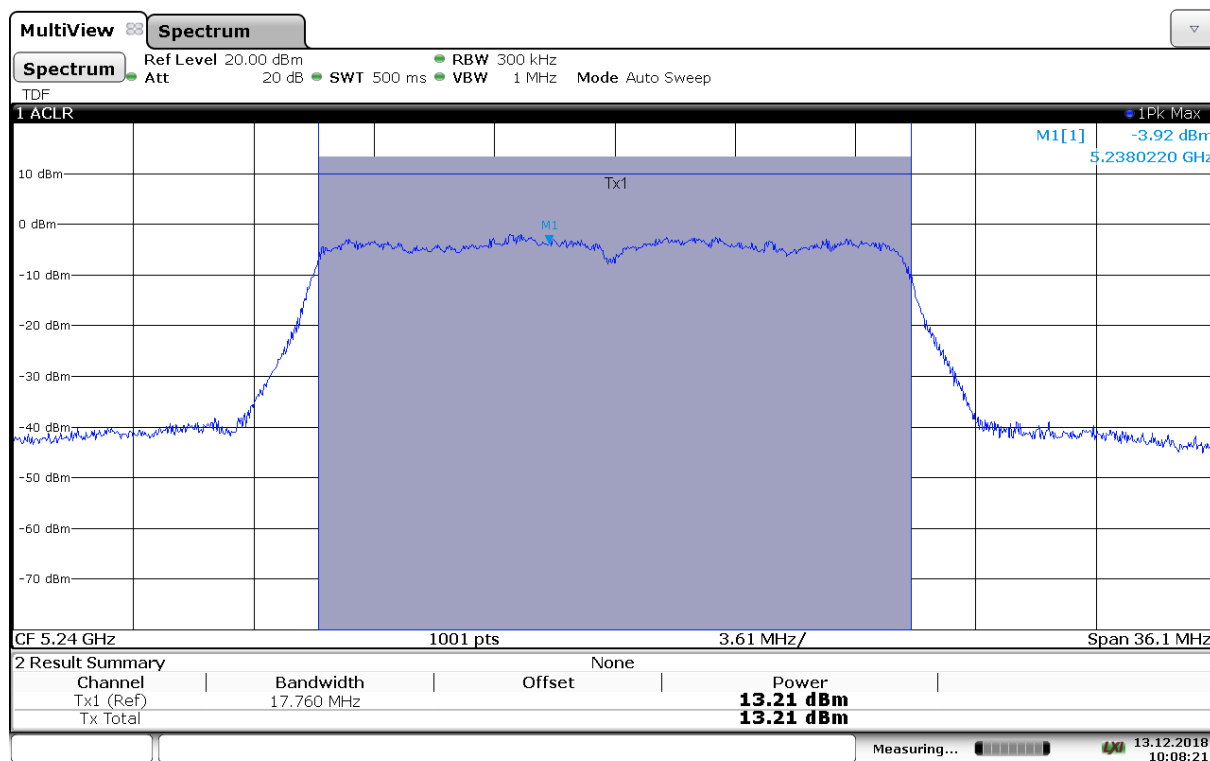
09:06:21 13.12.2018

35.01d_BE_low_amode_ch52_20_18mbit



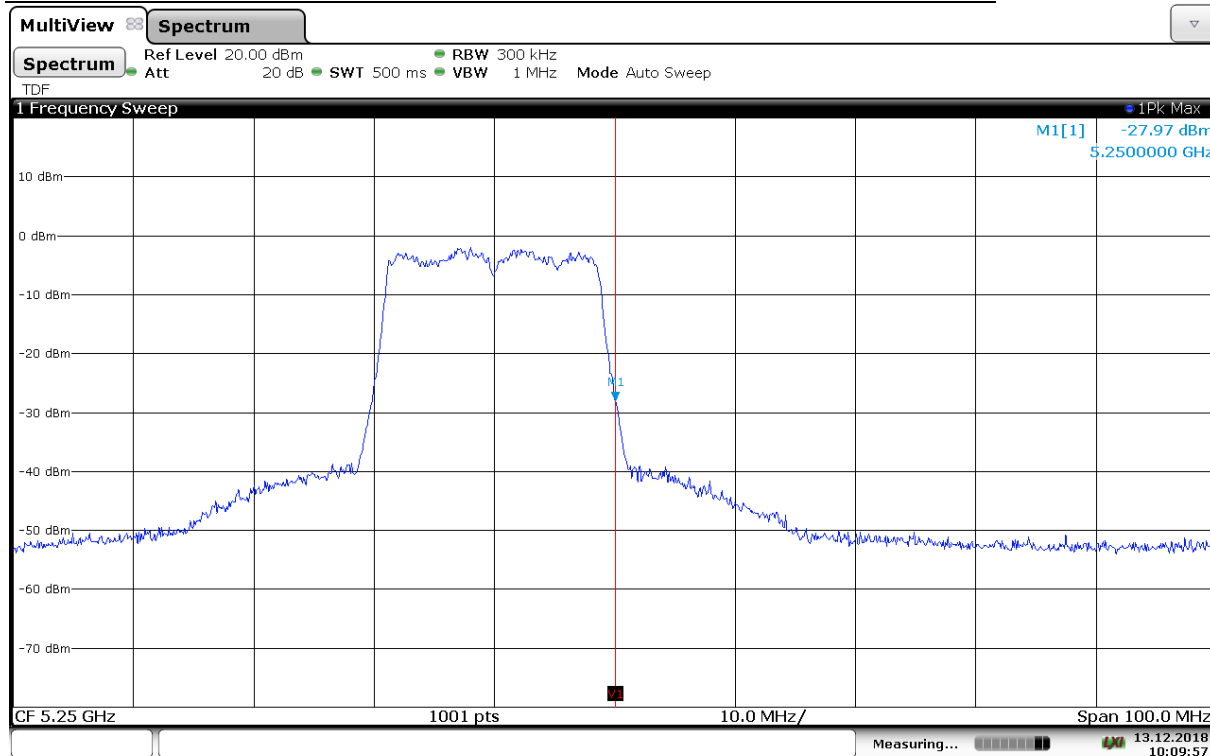
10:05:39 13.12.2018

35.02a_OBW_nmode_ch48_20_MCS7



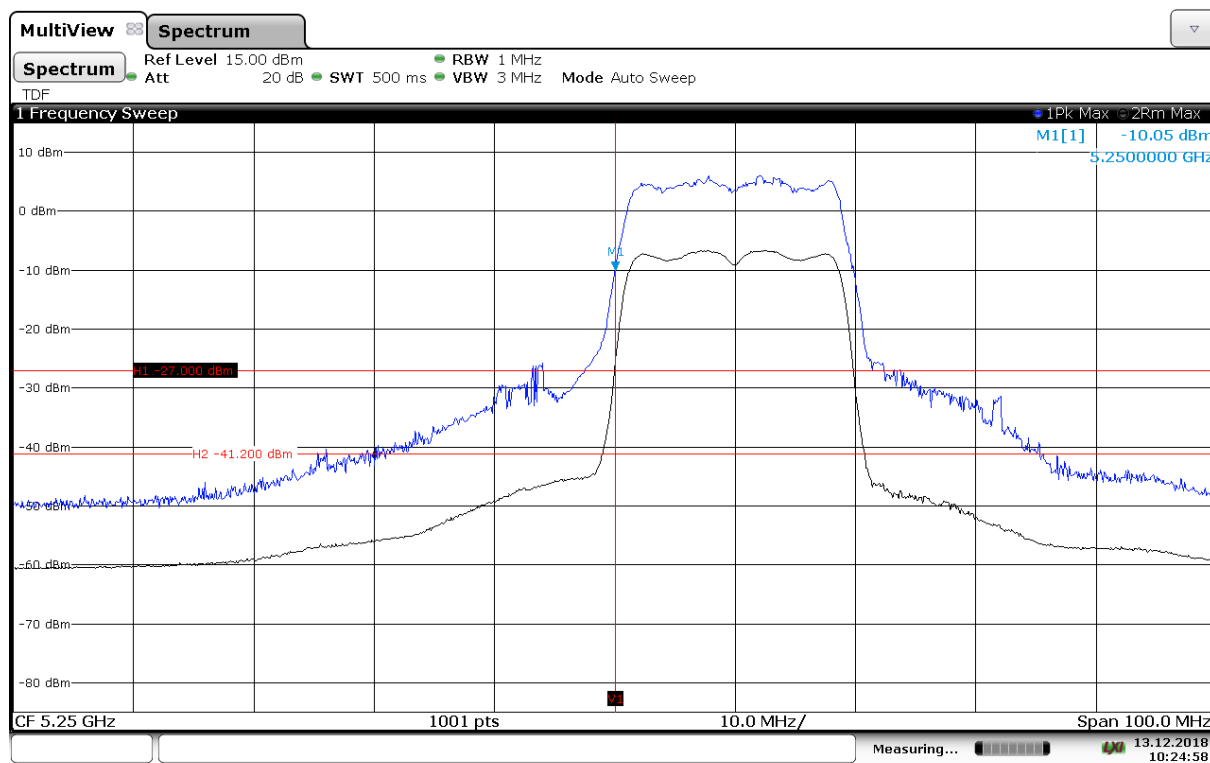
10:08:21 13.12.2018

35.02b_CHPWR_nmode_ch48_20_MCS7



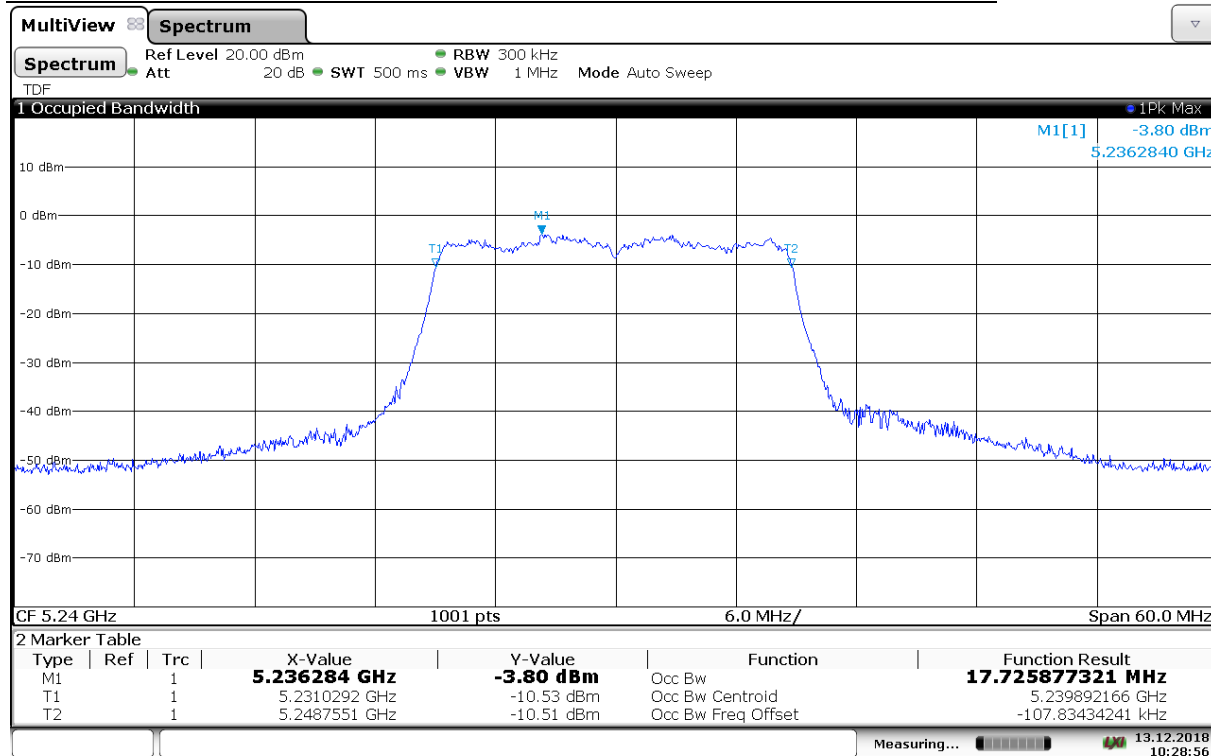
10:09:58 13.12.2018

35.02c_BE_high_nmode_ch48_20_MCS7



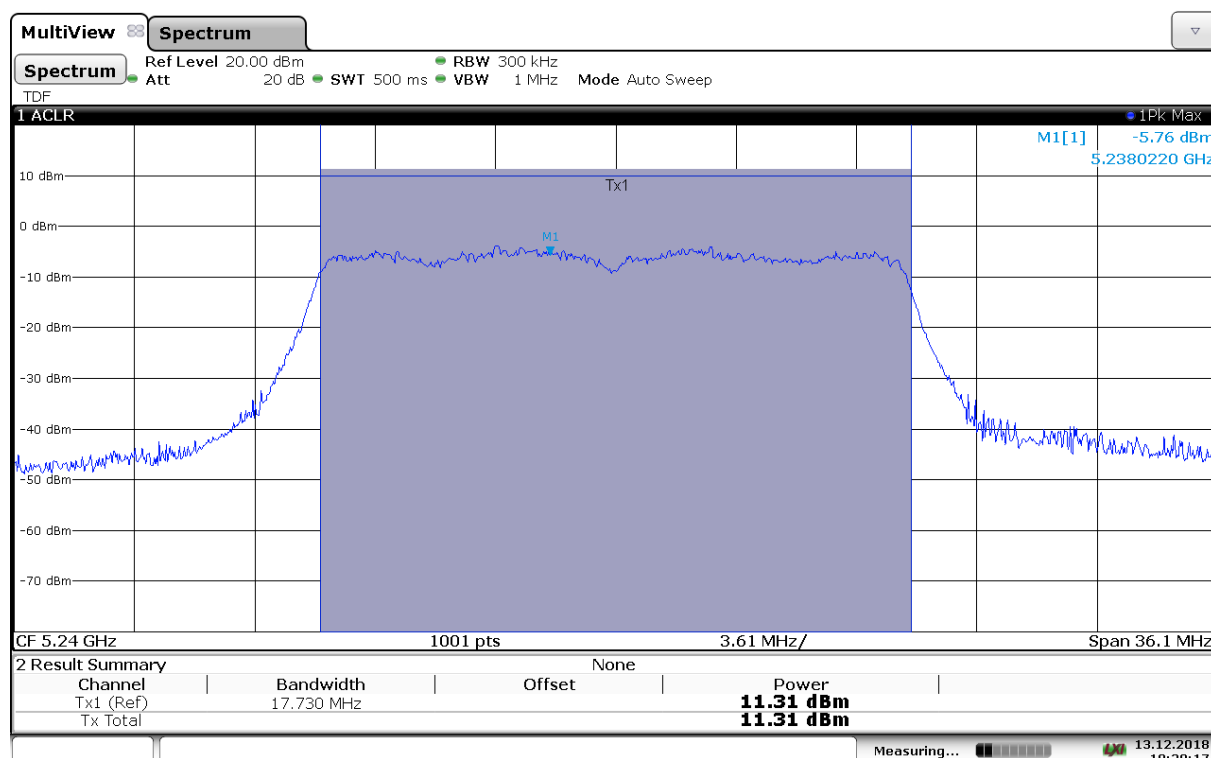
10:24:59 13.12.2018

35.02d_BE_low_nmode_ch52_20_MCS7



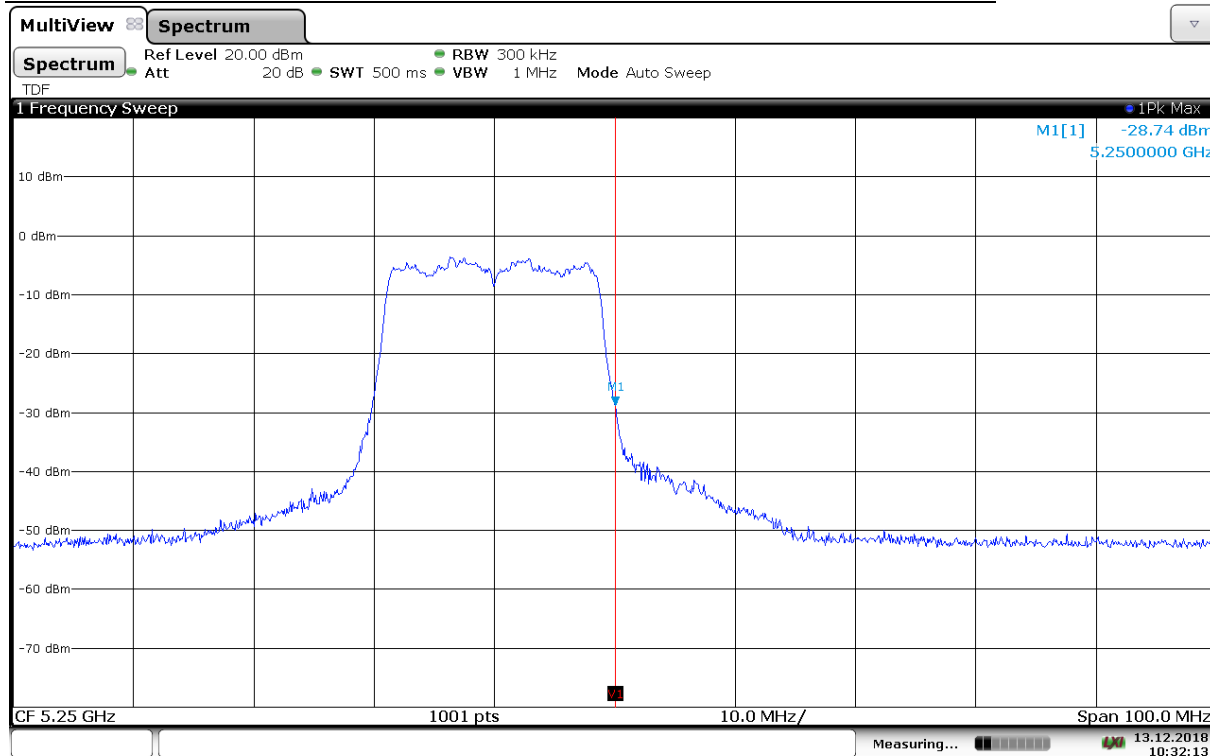
10:28:56 13.12.2018

35.03a_OBW_acmode_ch48_20_MCS1



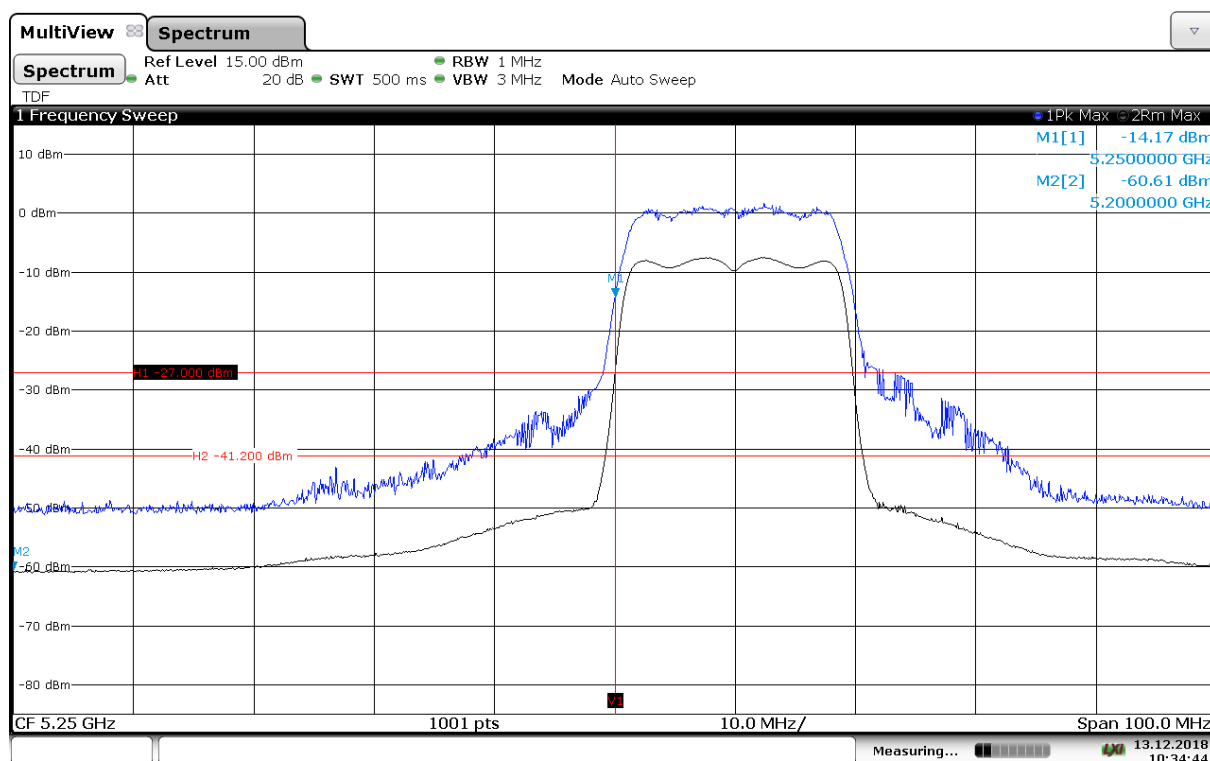
10:30:18 13.12.2018

35.03b_CHPWR_acmode_ch48_20_MCS1



10:32:13 13.12.2018

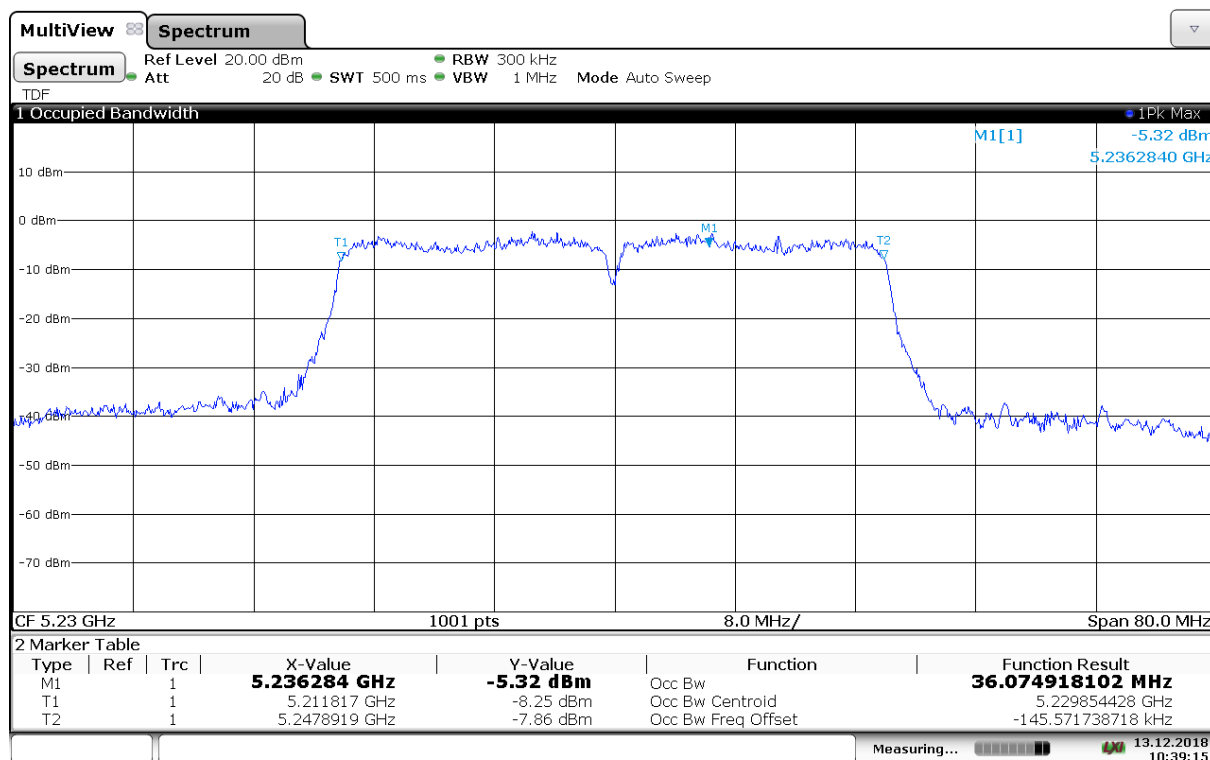
35.03c_BE_high_acmode_ch48_20_MCS1



10:34:44 13.12.2018

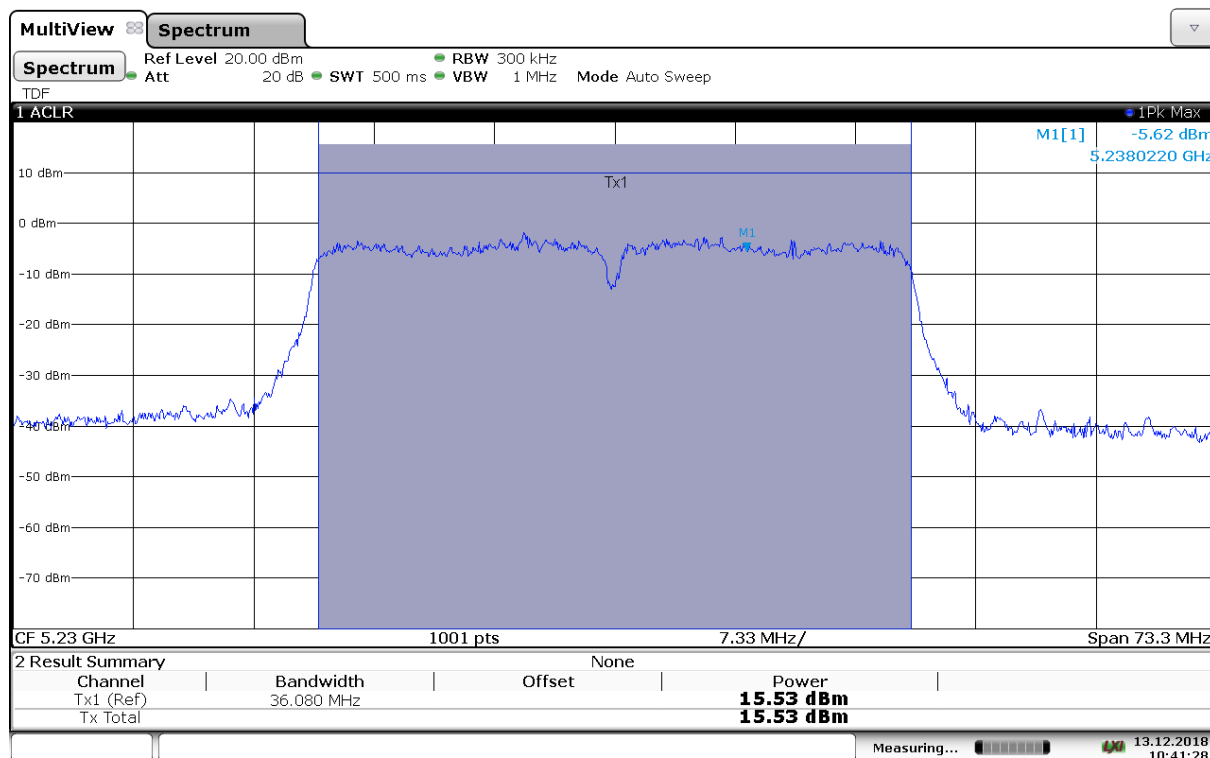
35.03d_BE_low_acmode_ch52_20_MCS1

2.6.1.2. 40MHz



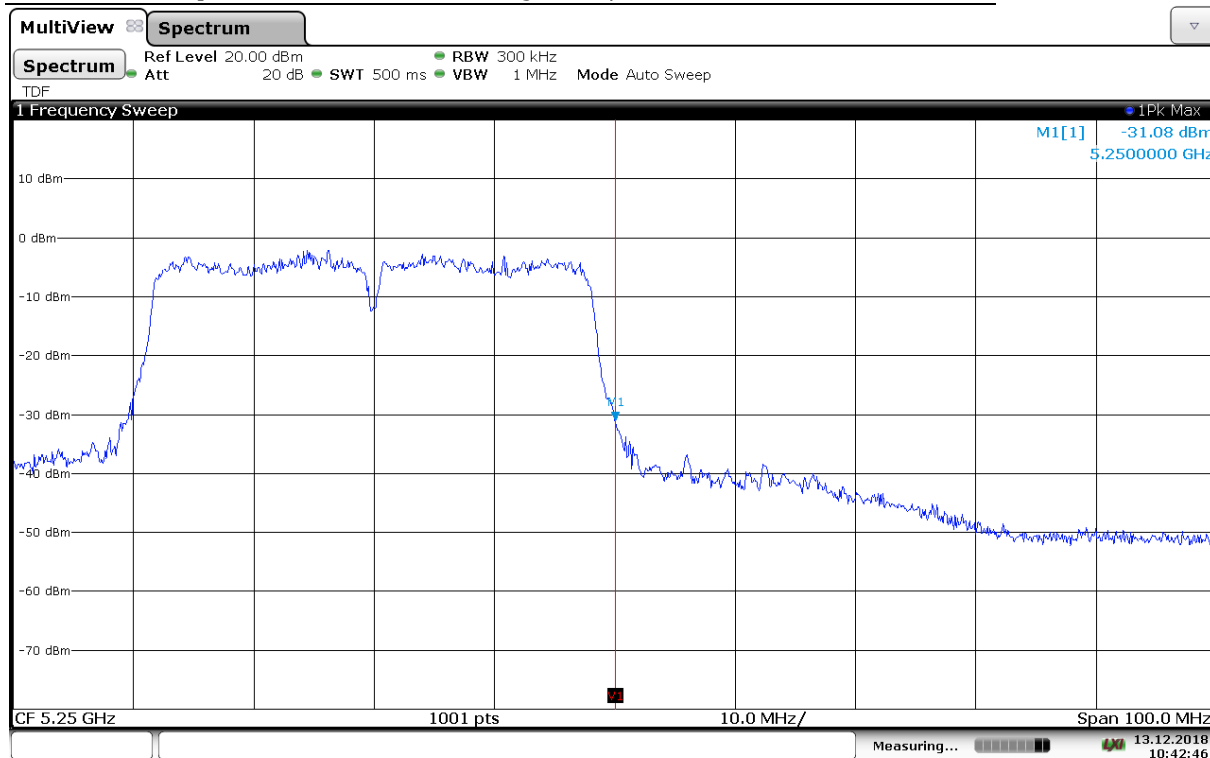
10:39:16 13.12.2018

35.04a_OBW_nmode_ch46_40_MCS3



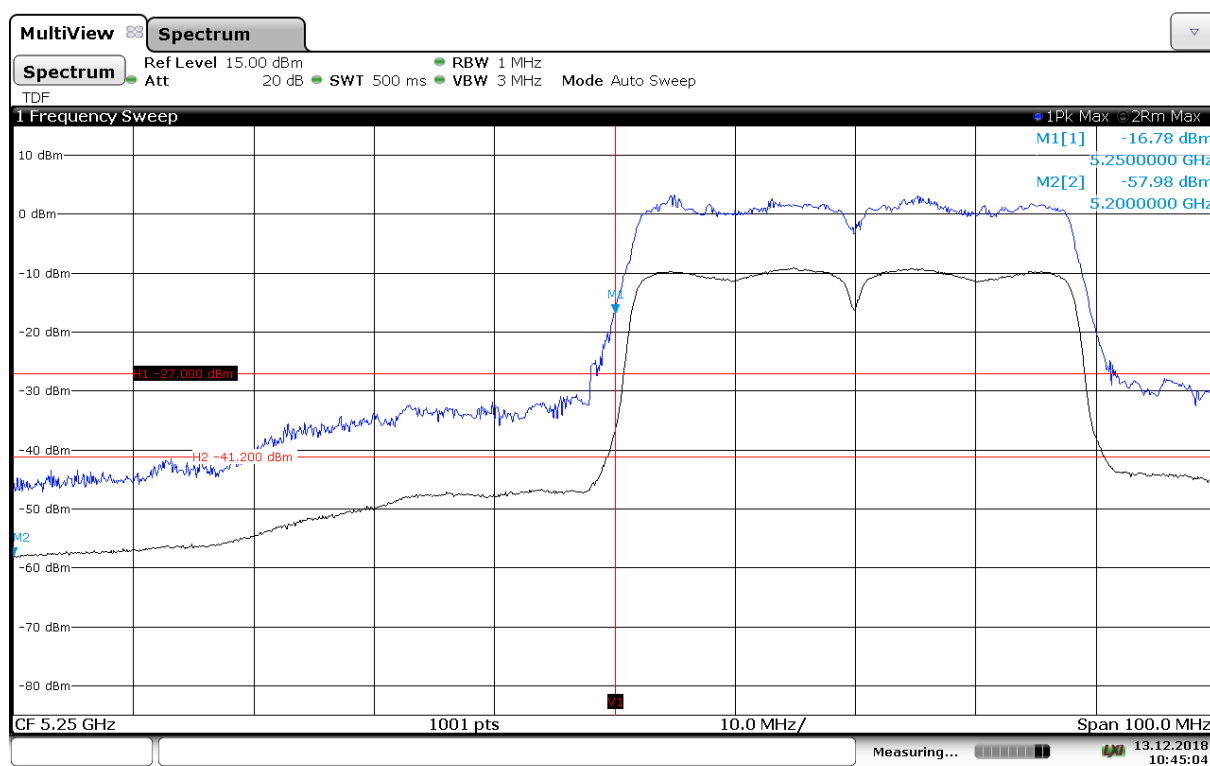
10:41:29 13.12.2018

35.04b_CHPWR_nmode_ch46_40_MCS3



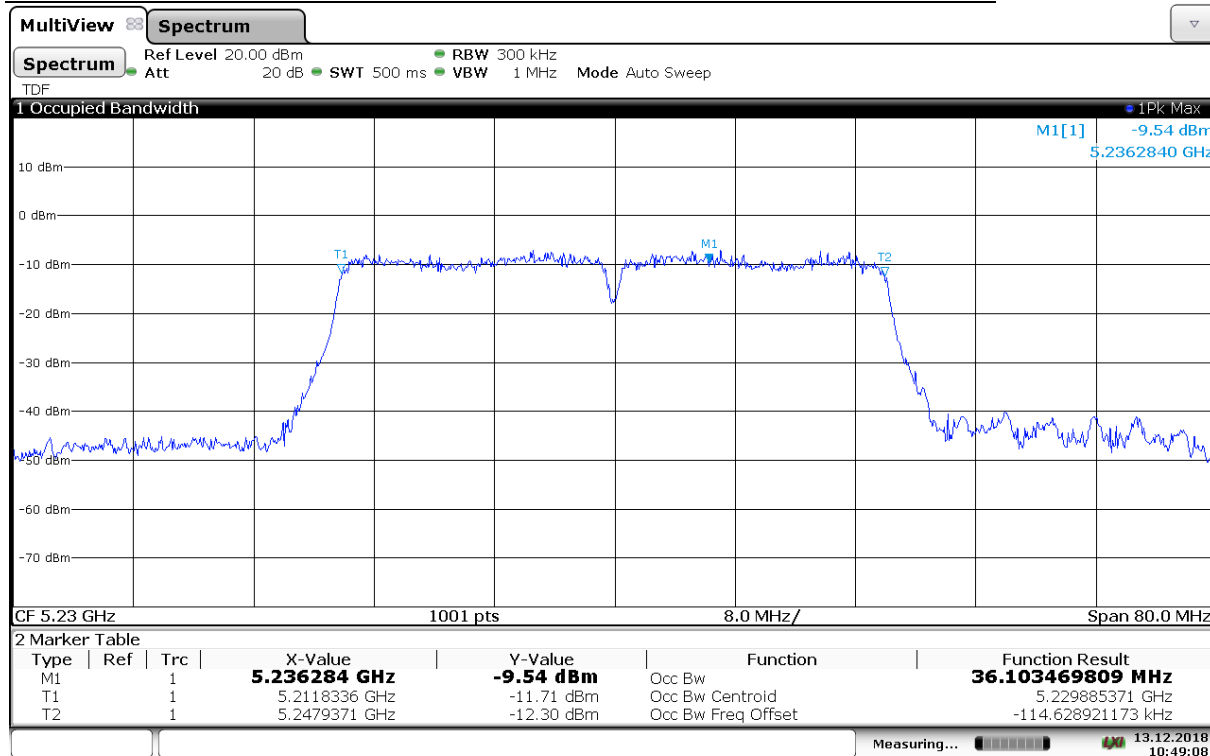
10:42:46 13.12.2018

35.04c_BE_high_nmode_ch46_40_MCS3



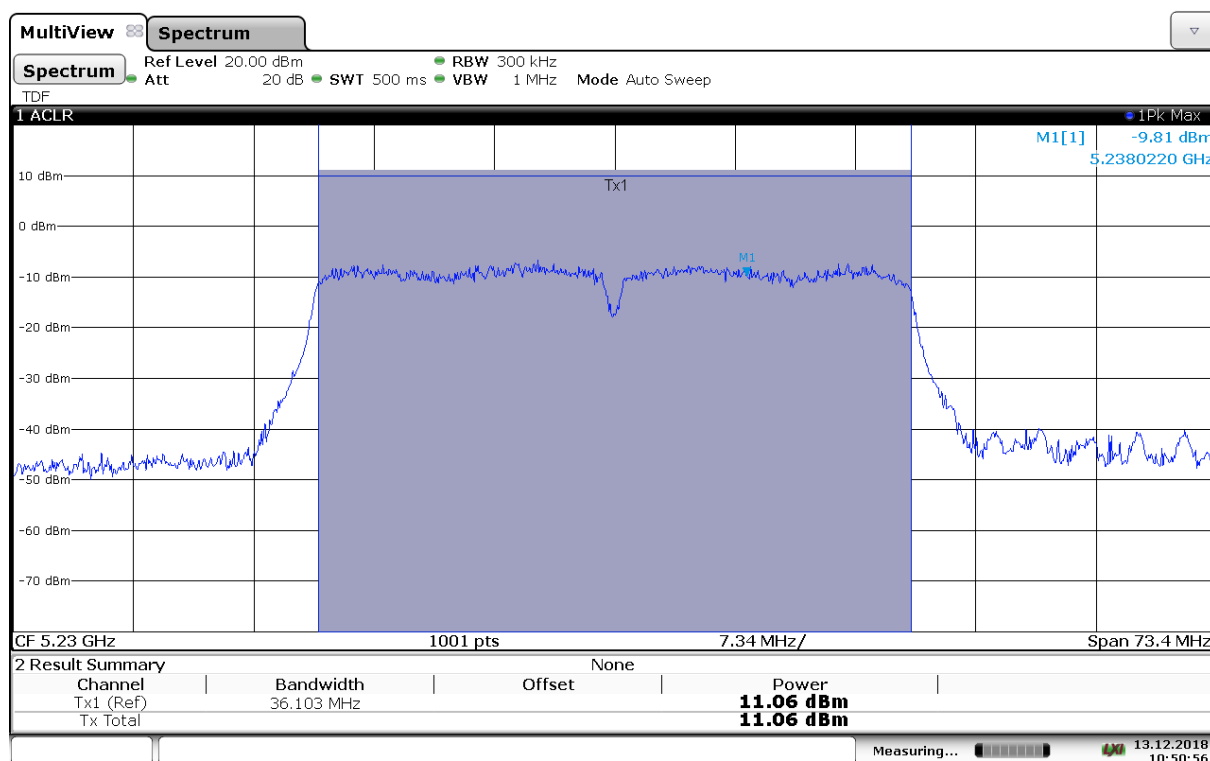
10:45:04 13.12.2018

35.04d_BE_low_nmode_ch54_40_MCS3



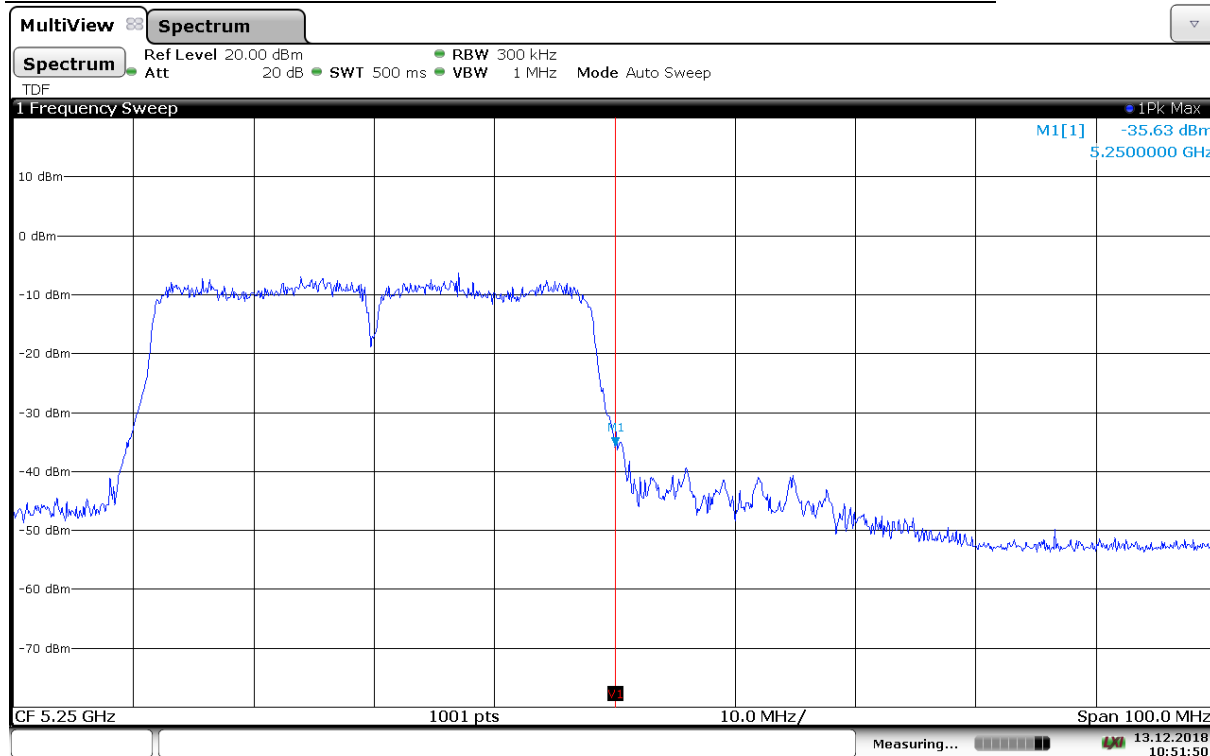
10:49:08 13.12.2018

35.05a_OBW_acmode_ch46_40_MCS4



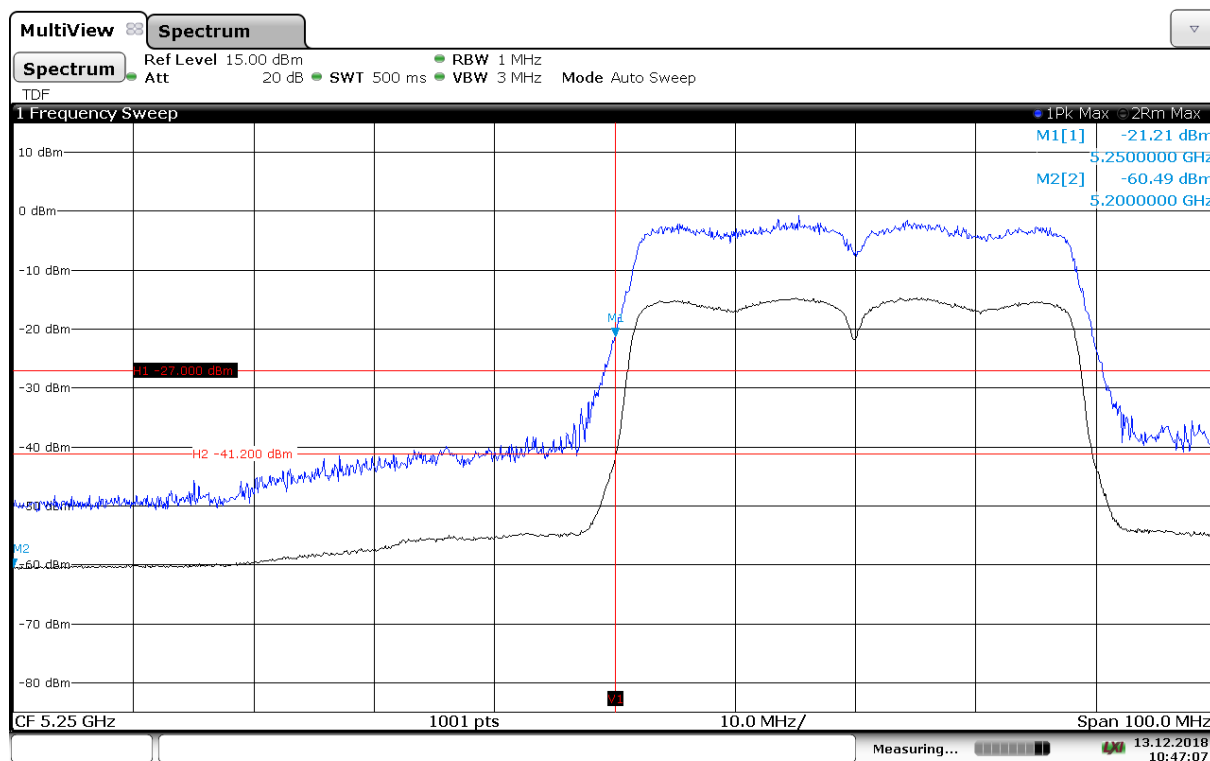
10:50:56 13.12.2018

35.05b_CHPWR_acmode_ch46_40_MCS4



10:51:50 13.12.2018

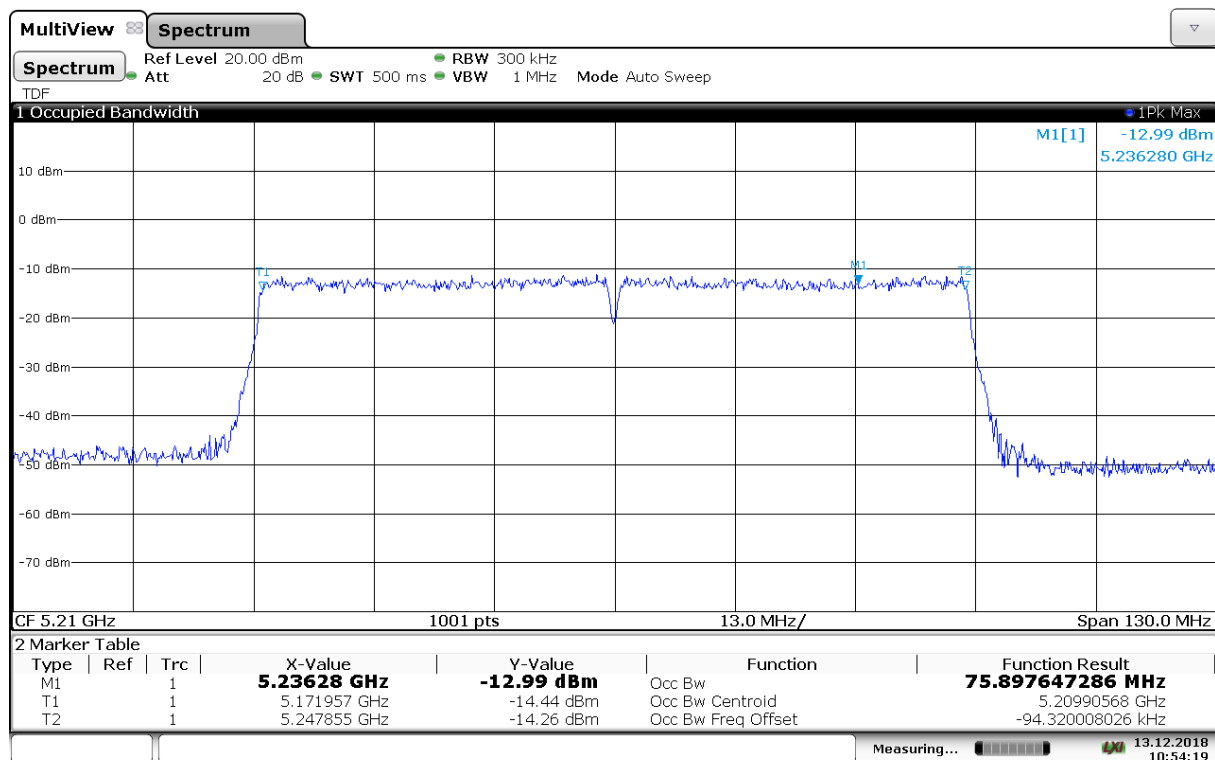
35.05c_BE_high_acmode_ch46_40_MCS4



10:47:08 13.12.2018

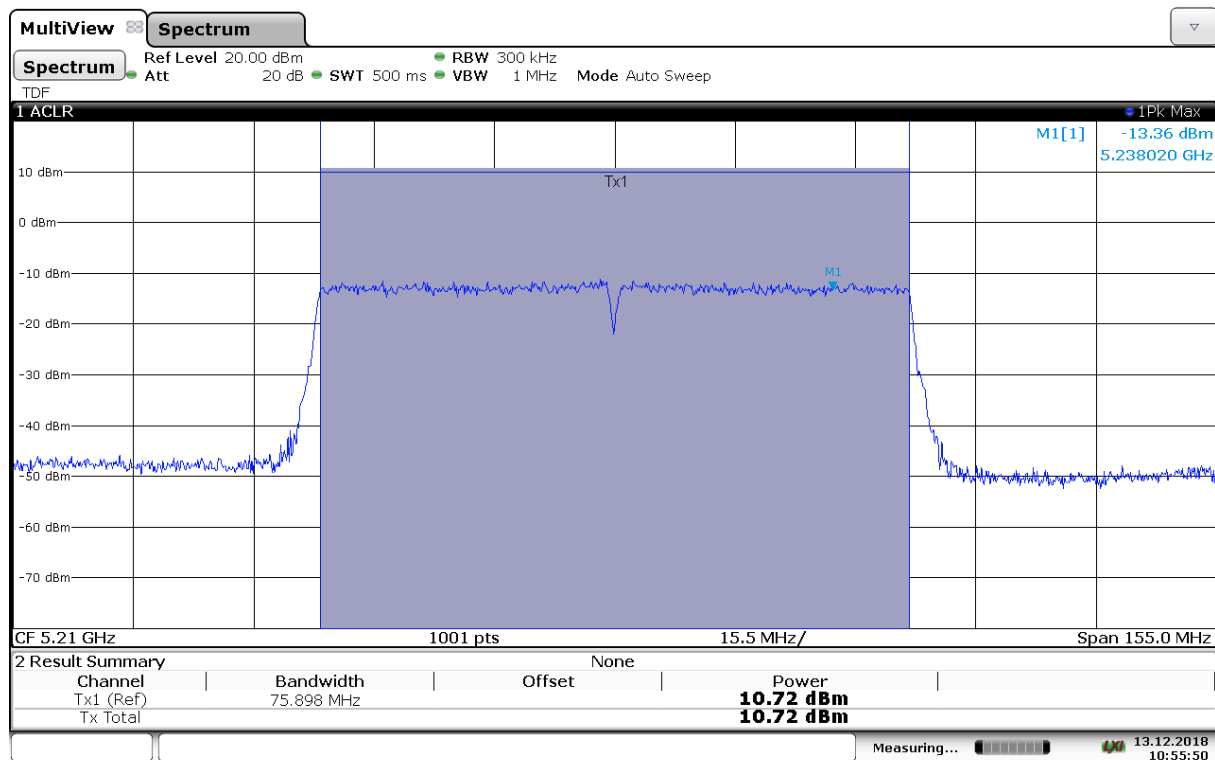
35.05d_BE_low_acmode_ch54_40_MCS4

2.6.1.3. 80MHz



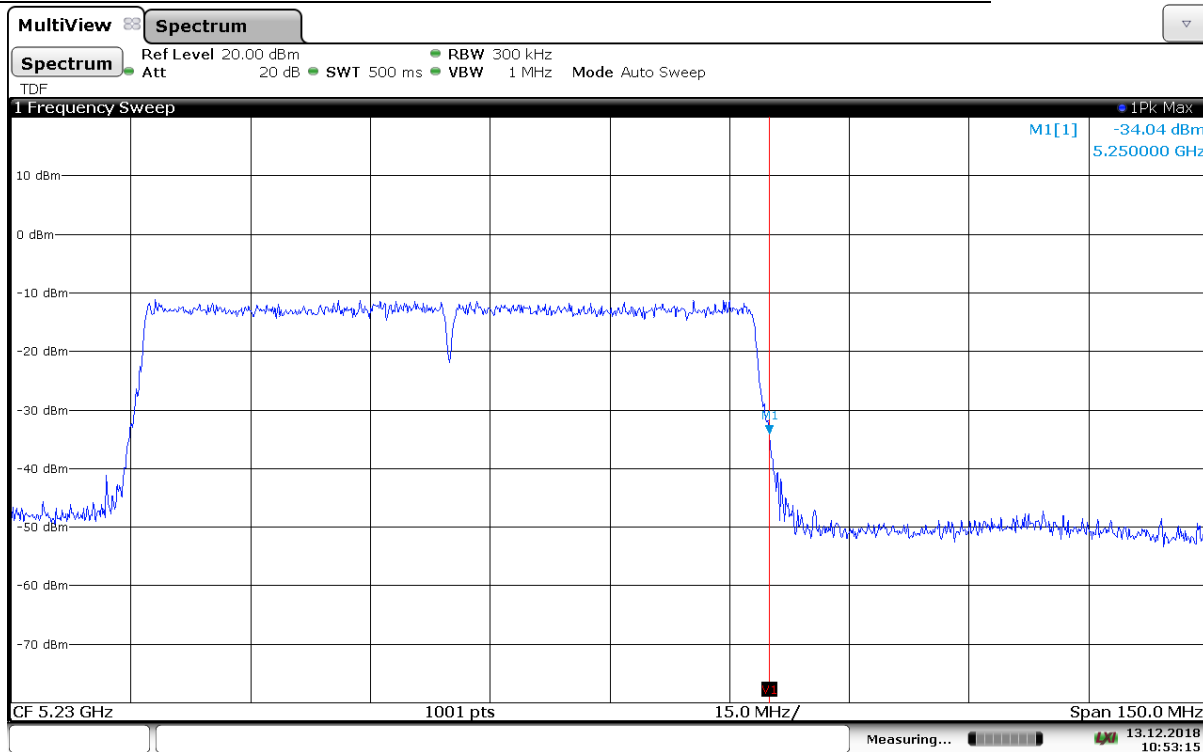
10:54:19 13.12.2018

35.06a_OBW_acmode_ch42_80_MCS1



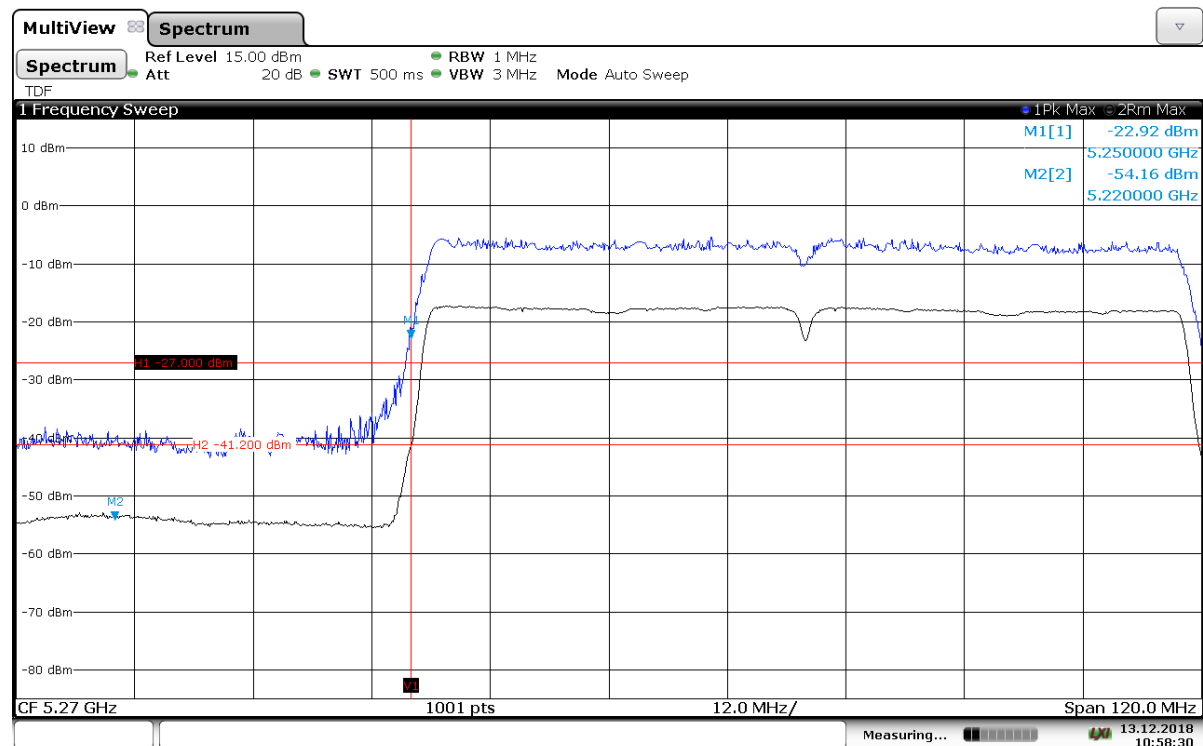
10:55:50 13.12.2018

35.06b_CHPWR_acmode_ch42_80_MCS1



10:53:16 13.12.2018

35.06c_BE_high_acmode_ch42_80_MCS1



10:58:30 13.12.2018

35.06d_BE_low_acmode_ch58_80_MCS1