



Sept 29, 2016

Federal Communications Commission
7435 Oakland Mills Road
Columbia, MD 21046

Subject: DFS Expedited Review for FCC ID: YETP24512NU and YETP24512CU

To whom it may concern:

Please apply the DFS Expedited Review Process for these devices based on the following criteria:

Expedited Review Checklist	Previous Application	Current Application
FCC ID	YETD32-21266NU and YETD32-21266CU	YETP24512NU and YETP24512CU
Test labs used	TUV SD	TUV SUD
Technology	5GHz UNII/Frame based/SISO	5GHz UNII/Frame based/SISO
Differences between the products such as TX power, modulation, receivers, processing circuitry etc.	Supports 1 TX/1RX on each device, 21 dBm maximum output power	Supports 1 TX/1RX on each device, 21 dBm maximum output power
Differences in DFS functioning circuitry, software, etc.	Nextivity proprietary ARES SoC chipset and DFS waveform detection mechanism	Nextivity proprietary ARES SoC chipset and DFS waveform detection mechanism
Antenna information and differences for the minimum gain antennas	Internal Antenna, 2dBi	Internal Antenna, 2dBi
Bandwidth information and differences	Supports 30 and 40 MHz bandwidths	Supports 30 and 40 MHz bandwidths

Sincerely,

Company Officer: C K Li
Title: Sr. Principle Engineer, Regulatory
Email: cli@nextivityinc.com