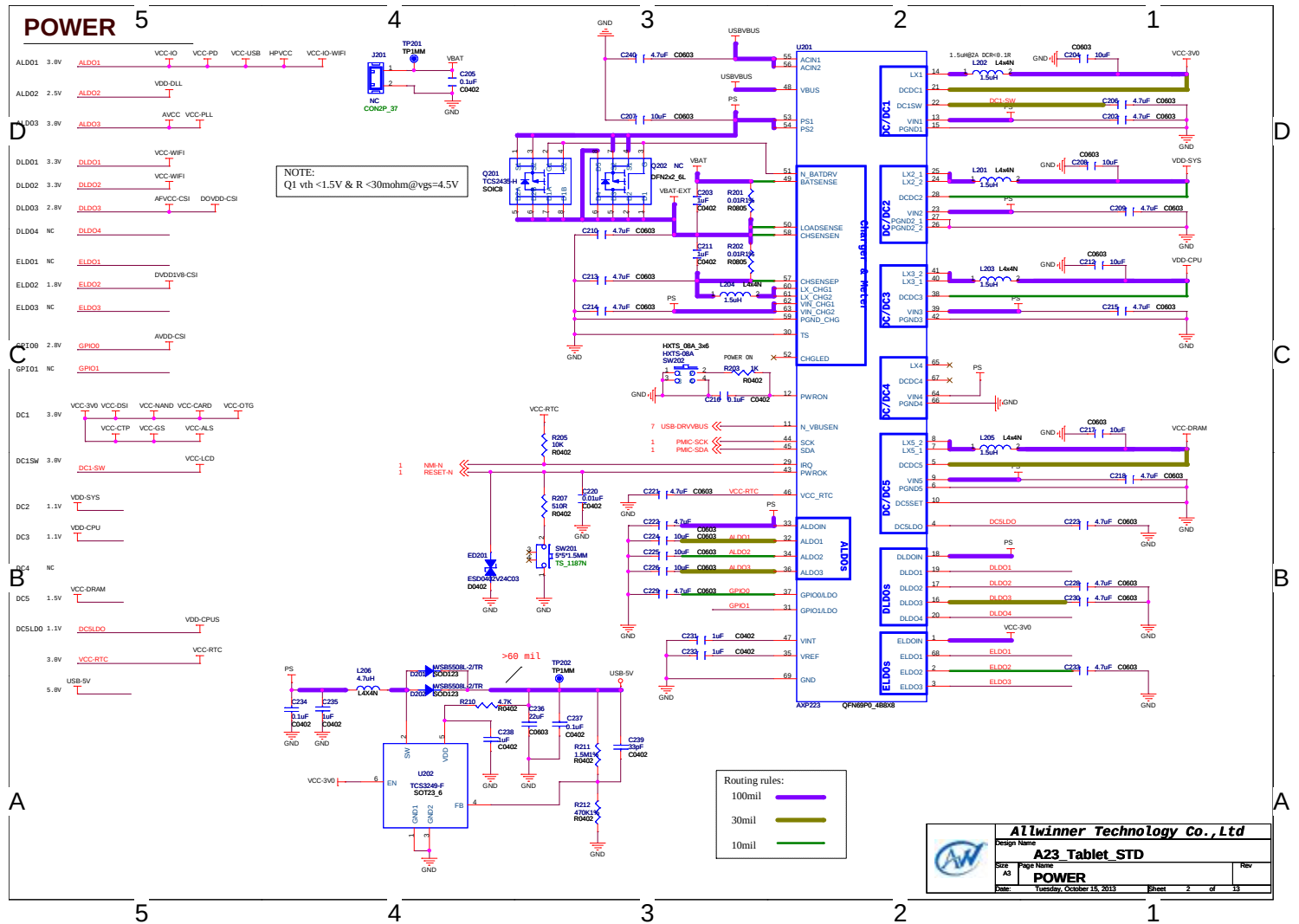
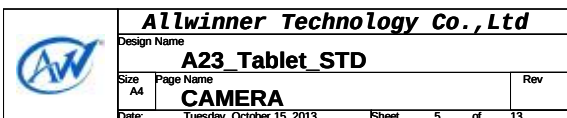
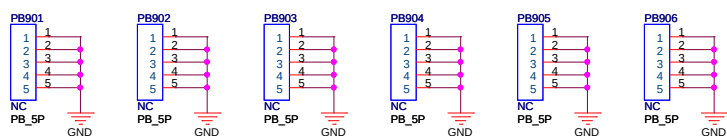
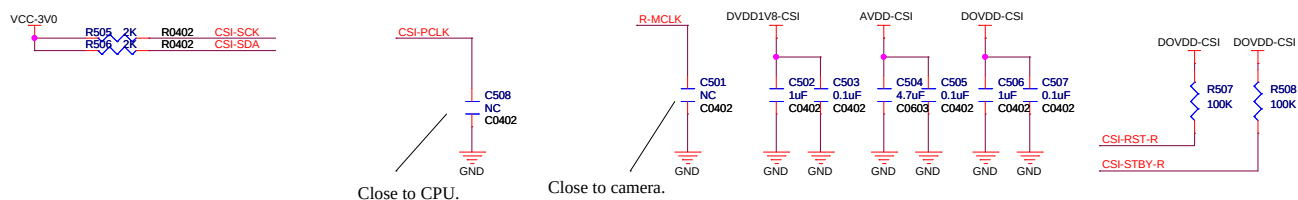




Front & Rear Camera

Close to CPU.

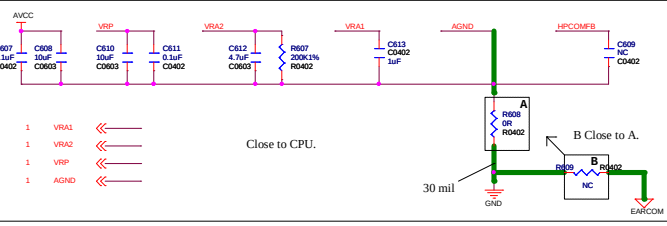
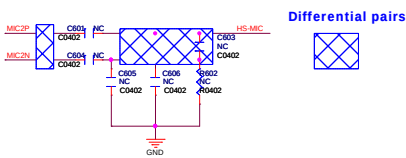
Close to camera.



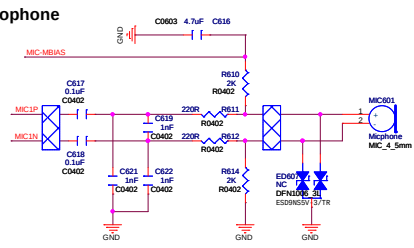
AUDIO(AC couple)

Head Phone

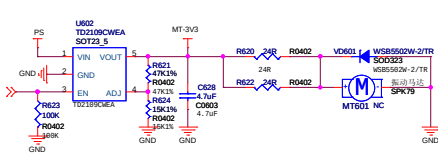
- 1 MIC-MBIAS << MIC-MBIAS
- 1 MIC-MBIAS << MIC-MBIAS
- 1 MIC1P << MIC1N
- 1 MIC2P << MIC2N
- 1 HPR << HPL
- 1 HPR << HPL
- 1 HPCOM << HPCOM
- 1 PA-SHOW << PA-SHOW
- 1 HPCOMFB << HPCOMFB



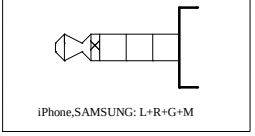
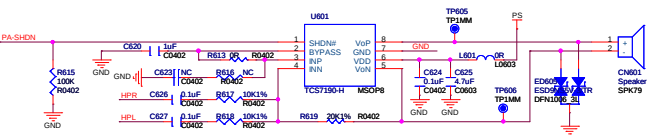
Analog Microphone



MOTOR



Speaker

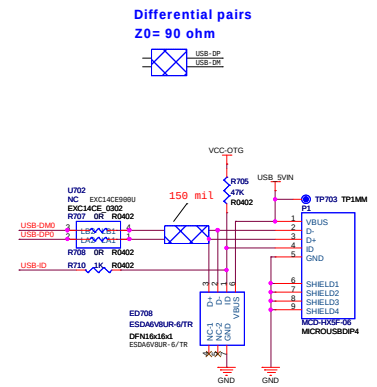
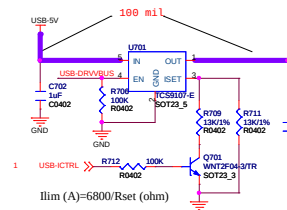


Allwinner Technology Co.,Ltd			
Design Name			
A23 Tablet STD			
Page Name			
AUDIO(AC couple)			
Date			
Tuesday, December 10, 2013			
Sheet			
6 of 13			

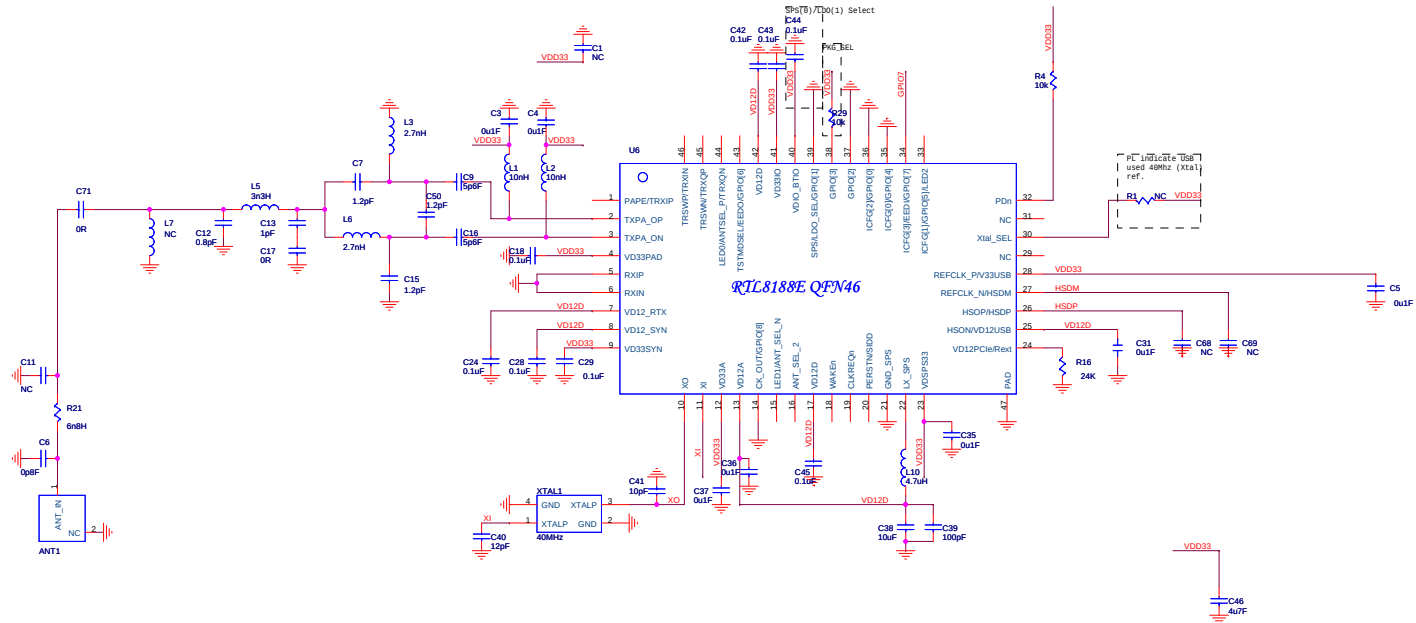
T-CARD



USB



UD88E00-1V0	Initial	20120311
	delete R3, R30, R31, R32, R40	20120318
	add C35, C39	
UD88E00-1V1	update ANV matching value L5, C13	20120326



Title			
UD88E00-1V0			
Size	Document Number		Rev 1V0
	Custom<Doc>		
Date:	Thursday, April 05, 2012	Sheet 1 of 1	

MODEM 2G/GPRS

1.8	BB-PCM-CLK	BB-PCM-CLK
1.8	BB-PCM-SYNC	BB-PCM-SYNC
1.8	BB-PCM-DOOUT	BB-PCM-DOOUT
1.8	BB-PCM-DIN	BB-PCM-DIN
1	AP-UART2-CTS	BB-UART-RTS
1	AP-UART2-RTS	BB-UART-CTS
1	AP-UART2-RX	BB-UART-TX
1	AP-UART2-TX	BB-UART-RX
1	BB-WAKE-AP	BB-WAKE-AP
1	BB-PWREN	BB-PWREN
1	AP-WAKE-BB	AP-WAKE-BB
1	BB-RST	BB-RST
1	PHOUTP	MC-P
1	PHOUTN	MC-N
1	PHINP	EAB-P
1	PHINN	EAB-N

Allwinner Technology Co., Ltd			
Design Name			
A23 Tablet STD			
Size	Page Name		Rev
A3	MODEM		
Date	Tuesday, October 15, 2013		Sheet 9 of 13

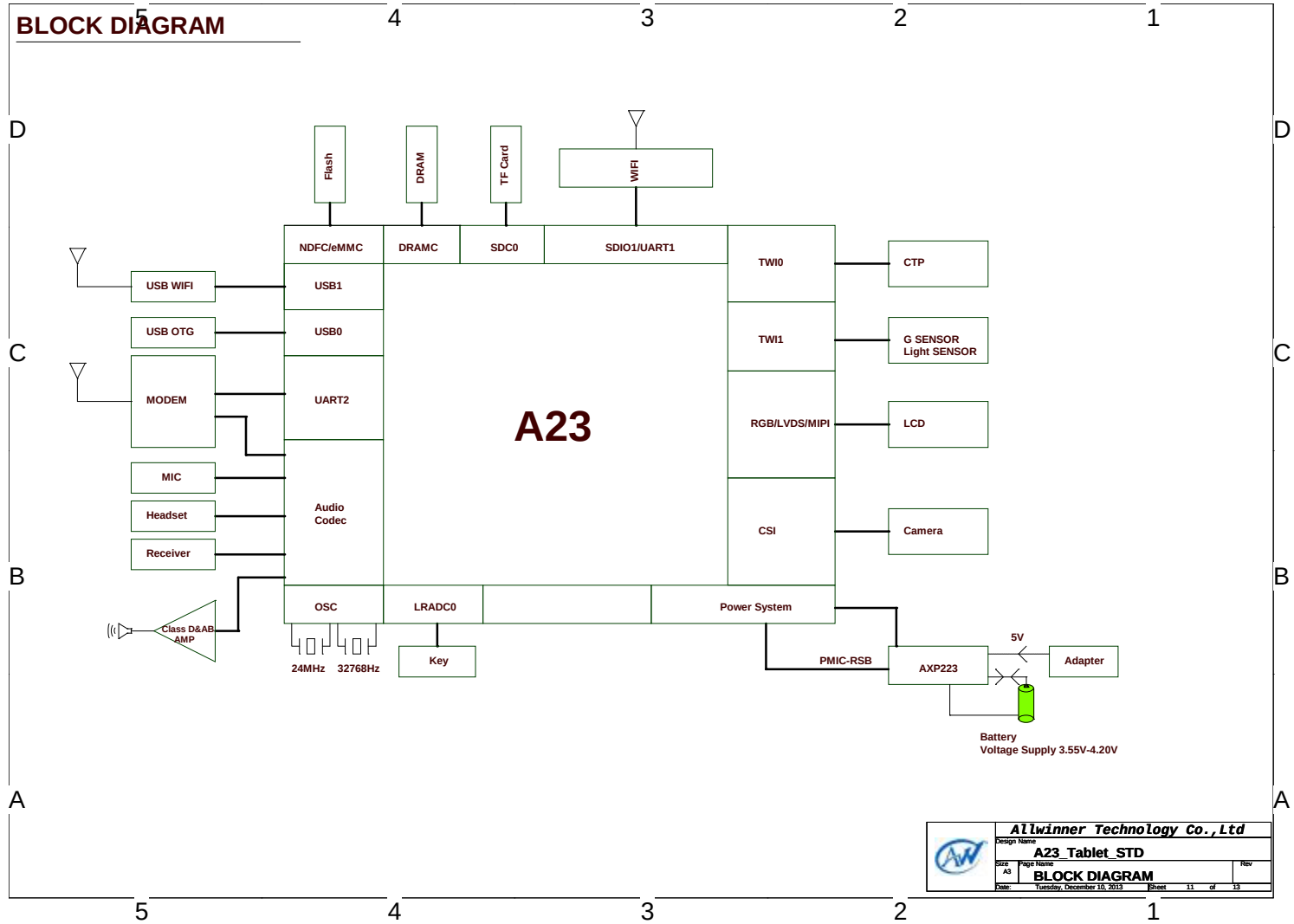
VERSION HISTORY

Index:

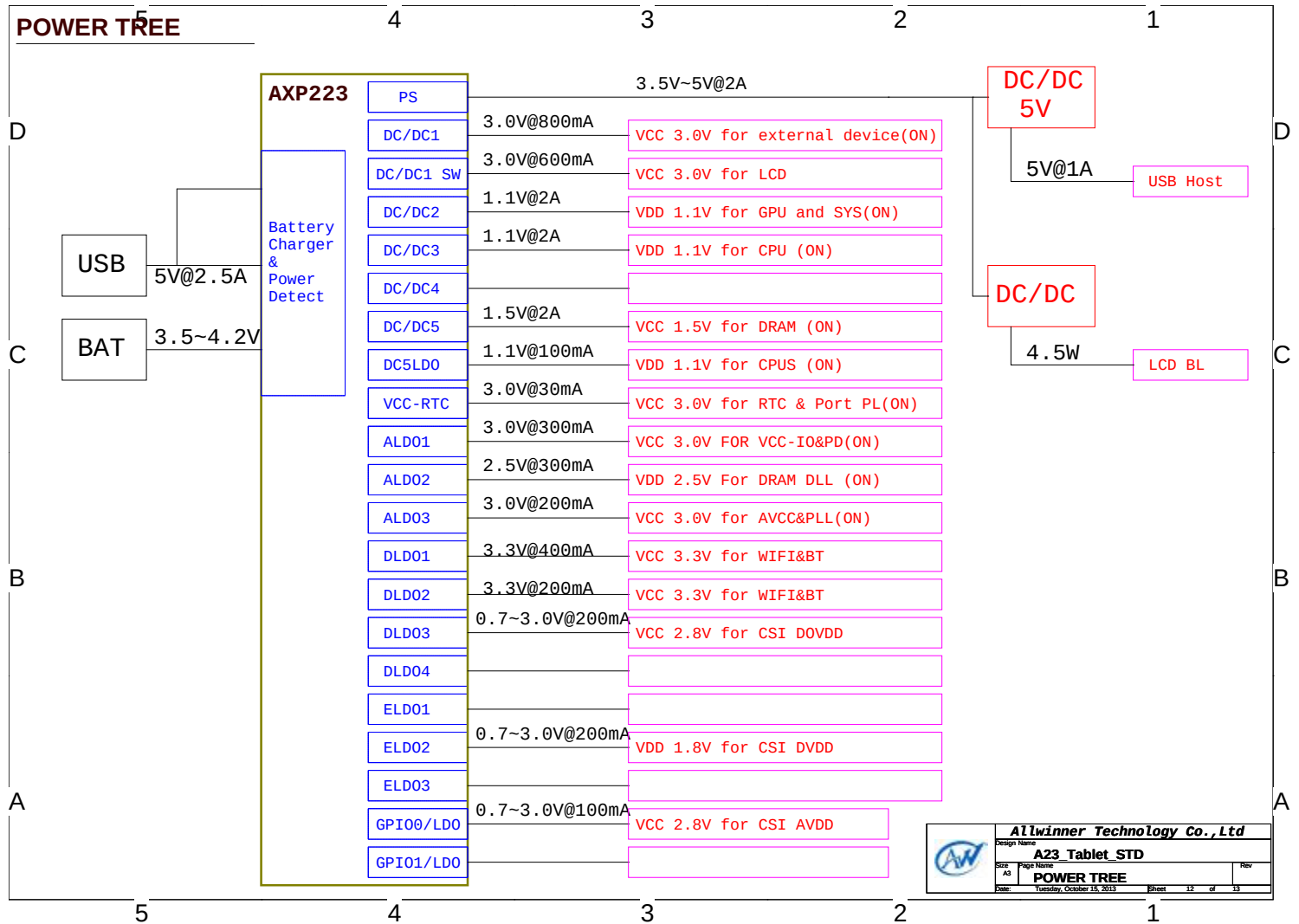
P01: VERSION HISTORY
P02: BLOCK DIAGRAM
P03: POWER TREE
P04: GPIO ASSIGNMENT
P05: DRAM DDR3 8X2
P06: CPU
P07: POWER
P08: NAND
P09: LCD TP
P10: CAMERA
P11: AUDIO(DC direct)
P12: T-CARD/USB/KEY/SENSOR
P13: MODEM 2G
P14: SDIO WIFI&BT
P15: USB WIFI
OPTION:AUDIO(AC couple)
OPTION:DRAM DDR3 16X1

Revision		Description	Date
V1.0	Release		2013-09-05

		Allwinner Technology Co., Ltd	
Design Name		A23 Tablet STD	
Size	Page Number	Rev	
A3	VERSION HISTORY		
Date	Tuesday, October 15, 2013	Sheet	10 of 13



Allwinner Technology Co., Ltd			
Design Name			
A23 Tablet STD			
Size	Page Name	Rev	
A3	BLOCK DIAGRAM		
Date:	Tuesday, December 16, 2013	Sheet	11 of 13



GPIO ASSIGNMENT

PIN	Name	Function
PB0	AP-UART2-TX	MODEM
PB1	AP-UART2-RX	
PB2	AP-UART2-RTS	
PB3	AP-UART2-CTS	T CARD
PB4	T-CARD-DET	
PB5	TP-INT	TP
PB6	GS-INT	G SENSOR
PB7	LS-INT	ALS

PIN	Name	Function
PG0	WL-SDIO-CLK	WLAN
PG1	WL-SDIO-CMD	
PG2	WL-SDIO-D0	
PG3	WL-SDIO-D1	
PG4	WL-SDIO-D2	
PG5	WL-SDIO-D3	BT
PG6	AP-UART1-TX	
PG7	AP-UART1-RX	
PG8	AP-UART1-RTS	
PG9	AP-UART1-CTS	
PG10		
PG11		
PG12		
PG13		

PIN	Name	Function
PH0	LCD-PWM	LCM
PH1	TP-RST	TP
PH2	TP-SCK	
PH3	TP-SDA	SENSOR
PH4	SENSOR-SCK	
PH5	SENSOR-SDA	LCM
PH6	LCD-BL-EN	
PH7	LCD-RST	USB
PH8	USB-ID	
PH9	PA-SHDN	AUDIO PA

PIN	Name	Function
PL0	PMIC-SCK	PMIC
PL1	PMIC-SDA	
PL2	BB-WAKE-AP	MODEM
PL3	BB-PWREN	
PL4	AP-WAKE-BB	
PL5	BB-RST	WLAN/BT
PL6	WL-DIS	
PL7	WL-WAKE-AP	
PL8	BT-DIS	
PL9	BT-WAKE-AP	
PL10	AP-WAKE-BT	
PL11	WL-BT-ENABLE	

			
Allwinner Technology Co., Ltd			
Design Name			
A23 Tablet STD			
Size	Page Name	Rev	
A3	GPIO ASSIGNMENT		
Date	Tuesday, October 15, 2013	Sheet	13 of 13