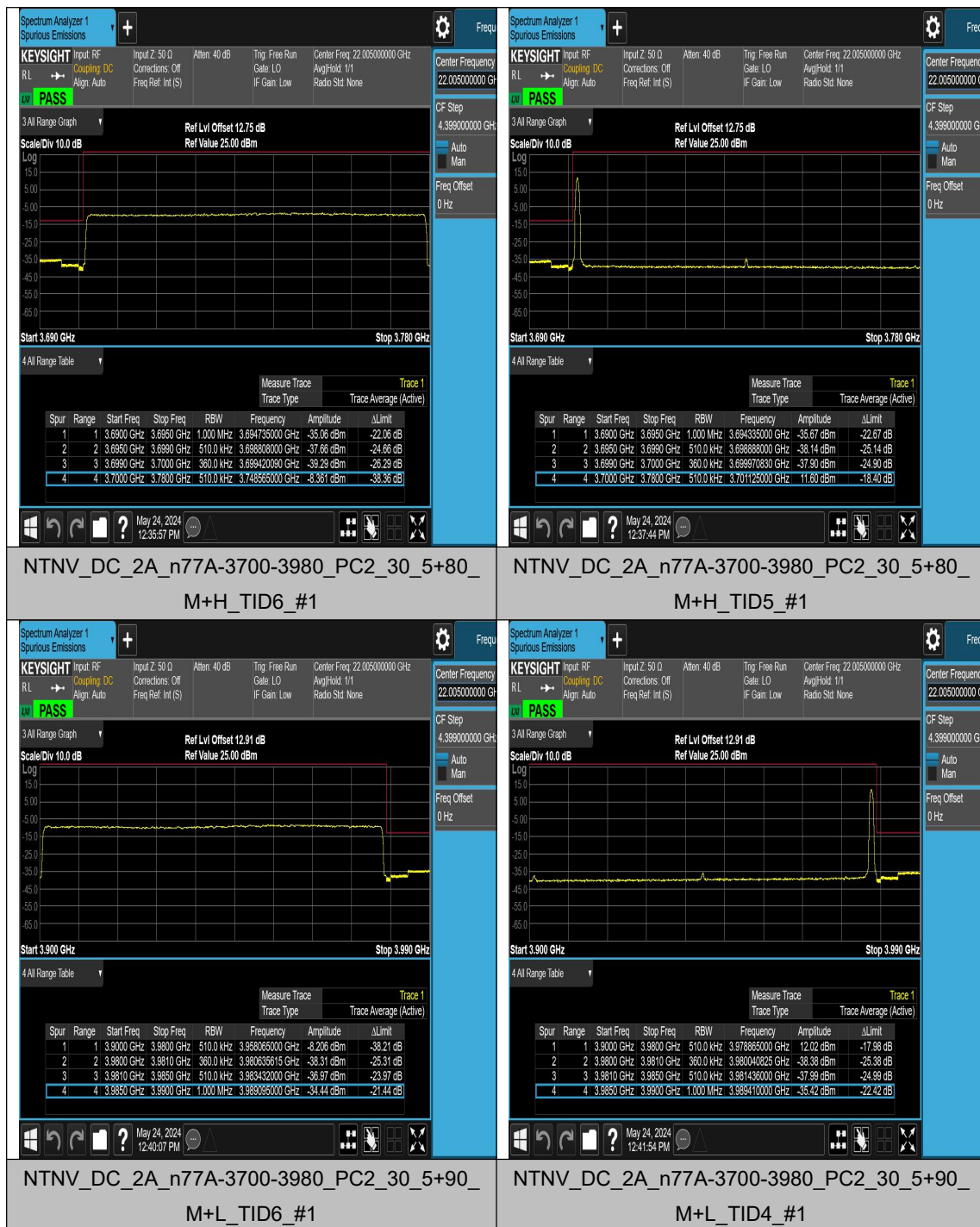
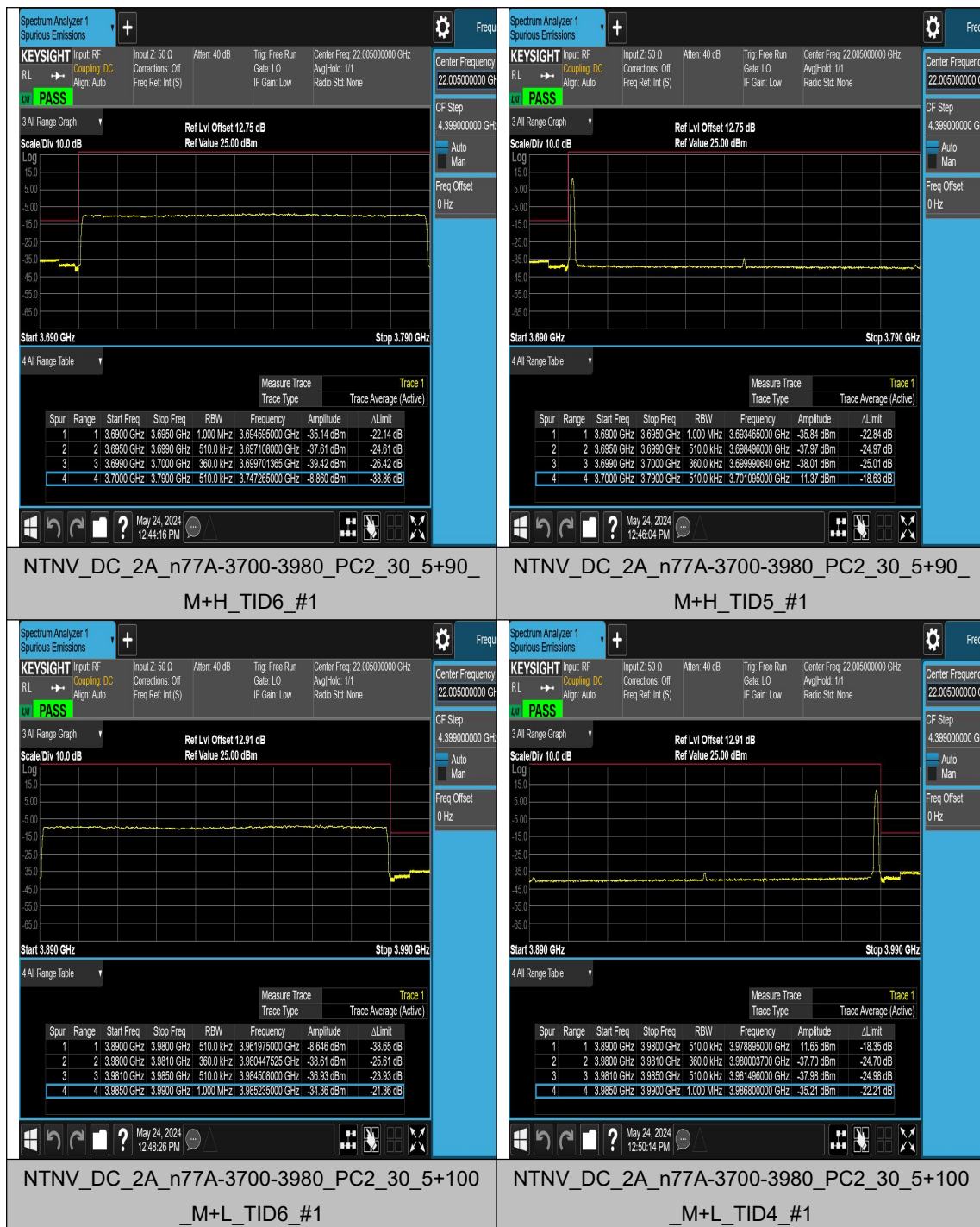
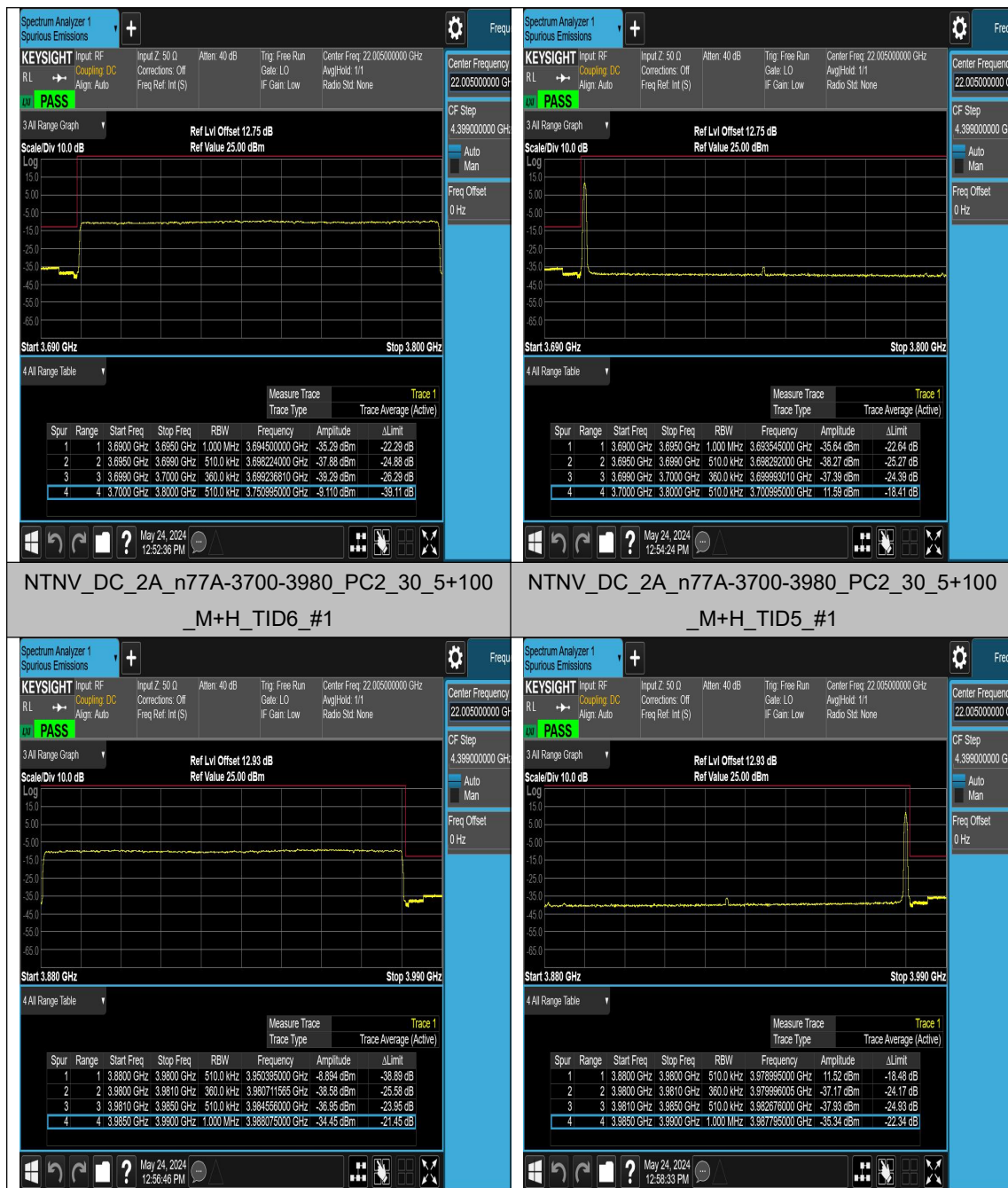








Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Limit	Verdict
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A- 3700-3980	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS

DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS

DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS

DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS

DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS

DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS

DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS

DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS

DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS

DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS

DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS

DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS

DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS

DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+50	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS

DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS

DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS

DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS

DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS

DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS

DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS

DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS

3700-3980									
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	0.15	30	-33	PASS

DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	26000	40000	-13	PASS

3700-3980		0							
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Outer_Full	30	1000	-23	PASS

DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS

3700-3980		0							
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Outer_Full	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Outer_Full	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Outer_Full	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Outer_Full	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Outer_Full	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Outer_Full	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	1000	6000	-13	PASS

DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	26000	40000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	0.009	0.15	-43	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	0.15	30	-33	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-23	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	1000	6000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	6000	26000	-13	PASS
DC_2A_n77A-3700-3980	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	26000	40000	-13	PASS

Test Graphs

