

Appendix for 2A_N41A

Product Name: Smart Phone

Model No: K50

Appendix A: Average Power Output

Test Result

Band	SC S	Bandwidth h	Modulation	Channel	RB Config	Power (dBm)	Power Class	Verdict
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Outer_Full	24.54	PC2	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Inner_Full	25.58	PC2	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	22.31	PC2	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	22.19	PC2	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Outer_Full	23.63	PC2	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Inner_Full	24.59	PC2	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	22.33	PC2	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	22.39	PC2	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Outer_Full	24.41	PC2	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Inner_Full	25.33	PC2	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	21.83	PC2	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	22.22	PC2	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Outer_Full	23.41	PC2	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Inner_Full	24.34	PC2	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	21.92	PC2	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	22.27	PC2	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Outer_Full	24.74	PC2	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Inner_Full	25.81	PC2	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	22.20	PC2	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	22.49	PC2	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Outer_Full	23.82	PC2	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Inner_Full	24.81	PC2	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	22.37	PC2	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	22.56	PC2	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Outer_Full	24.63	PC2	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Inner_Full	25.52	PC2	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	22.05	PC2	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	21.79	PC2	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Outer_Full	23.50	PC2	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Inner_Full	24.68	PC2	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	22.33	PC2	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	21.77	PC2	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Outer_Full	24.39	PC2	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Inner_Full	25.31	PC2	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	21.70	PC2	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	22.24	PC2	PASS

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DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Outer_Full	23.37	PC2	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Inner_Full	24.38	PC2	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	21.67	PC2	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	22.11	PC2	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Outer_Full	24.79	PC2	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Inner_Full	25.68	PC2	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	22.24	PC2	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	22.39	PC2	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Outer_Full	23.66	PC2	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Inner_Full	24.82	PC2	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	22.29	PC2	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	22.29	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	24.43	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Inner_Full	25.48	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	22.23	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	21.75	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Outer_Full	23.40	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Inner_Full	24.63	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	22.20	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	21.56	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	24.36	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Inner_Full	25.34	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	21.61	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	22.32	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Outer_Full	23.36	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Inner_Full	24.42	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	21.76	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	22.14	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	24.67	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Inner_Full	25.80	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	22.36	PC2	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	22.29	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Outer_Full	23.74	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Inner_Full	24.74	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	22.32	PC2	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	22.30	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Outer_Full	24.48	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Inner_Full	25.40	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	22.23	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	21.93	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Outer_Full	23.38	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Inner_Full	24.32	PC2	PASS

DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	22.09	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	22.09	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Outer_Full	24.42	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Inner_Full	25.43	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	21.72	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	22.09	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Outer_Full	23.41	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Inner_Full	24.41	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	21.64	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	22.15	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Outer_Full	24.75	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Inner_Full	25.83	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	22.09	PC2	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	22.30	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Outer_Full	23.73	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Inner_Full	24.84	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	22.23	PC2	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	22.25	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	24.42	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Inner_Full	25.29	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	21.99	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	22.08	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Outer_Full	23.37	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Inner_Full	24.32	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	22.15	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	22.21	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	24.45	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Inner_Full	25.45	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	21.97	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	21.78	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Outer_Full	23.39	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Inner_Full	24.49	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	21.85	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	21.92	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	24.72	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Inner_Full	25.77	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	21.83	PC2	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	22.40	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Outer_Full	23.69	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Inner_Full	24.83	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	21.91	PC2	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	22.33	PC2	PASS

DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	24.29	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Inner_Full	25.36	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	21.88	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	21.54	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Outer_Full	23.37	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Inner_Full	24.30	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	22.03	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	21.67	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	24.27	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Inner_Full	25.37	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	21.96	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	21.60	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Outer_Full	23.27	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Inner_Full	24.38	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	21.87	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	21.72	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	24.70	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Inner_Full	25.60	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	21.98	PC2	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	22.17	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Outer_Full	23.52	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Inner_Full	24.51	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	22.02	PC2	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	22.34	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Outer_Full	24.42	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Inner_Full	25.44	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	22.42	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	22.00	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Outer_Full	23.44	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Inner_Full	24.41	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	22.40	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	21.88	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Outer_Full	24.45	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Inner_Full	25.53	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	21.78	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	22.17	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Outer_Full	23.41	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Inner_Full	24.49	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	21.88	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	22.07	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Outer_Full	24.64	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Inner_Full	25.80	PC2	PASS

DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	22.09	PC2	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	22.44	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Outer_Full	23.62	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Inner_Full	24.62	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	22.10	PC2	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	22.28	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+L	Outer_Full	24.49	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+L	Inner_Full	25.49	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+L	Edge_1RB_Left	22.14	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+L	Edge_1RB_Right	21.67	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+L	Outer_Full	23.41	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+L	Inner_Full	24.43	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+L	Edge_1RB_Left	22.07	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+L	Edge_1RB_Right	21.57	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+M	Outer_Full	24.48	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+M	Inner_Full	25.53	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+M	Edge_1RB_Left	21.63	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+M	Edge_1RB_Right	22.09	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+M	Outer_Full	23.43	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+M	Inner_Full	24.45	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+M	Edge_1RB_Left	21.77	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+M	Edge_1RB_Right	22.30	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+H	Outer_Full	24.67	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+H	Inner_Full	25.69	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+H	Edge_1RB_Left	22.32	PC2	PASS
DC_4A_n41A	30	5+90	DFT-QPSK	M+H	Edge_1RB_Right	22.30	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+H	Outer_Full	23.62	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+H	Inner_Full	24.57	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+H	Edge_1RB_Left	22.39	PC2	PASS
DC_4A_n41A	30	5+90	DFT-16QAM	M+H	Edge_1RB_Right	22.21	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+L	Outer_Full	24.43	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+L	Inner_Full	25.46	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Left	21.95	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+L	Edge_1RB_Right	21.92	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+L	Outer_Full	23.37	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+L	Inner_Full	24.41	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+L	Edge_1RB_Left	21.93	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+L	Edge_1RB_Right	22.03	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+M	Outer_Full	24.44	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+M	Inner_Full	25.49	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Left	21.65	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+M	Edge_1RB_Right	21.95	PC2	PASS

DC_4A_n41A	30	5+100	DFT-16QAM	M+M	Outer_Full	23.44	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+M	Inner_Full	24.45	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Left	21.51	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+M	Edge_1RB_Right	21.99	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+H	Outer_Full	23.94	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+H	Inner_Full	25.86	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Left	21.77	PC2	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+H	Edge_1RB_Right	22.19	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+H	Outer_Full	23.60	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+H	Inner_Full	24.53	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Left	21.87	PC2	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+H	Edge_1RB_Right	22.03	PC2	PASS

Appendix B: Peak-to-Average Ratio for NSA

Peak-to-Average Ratio(CCDF)

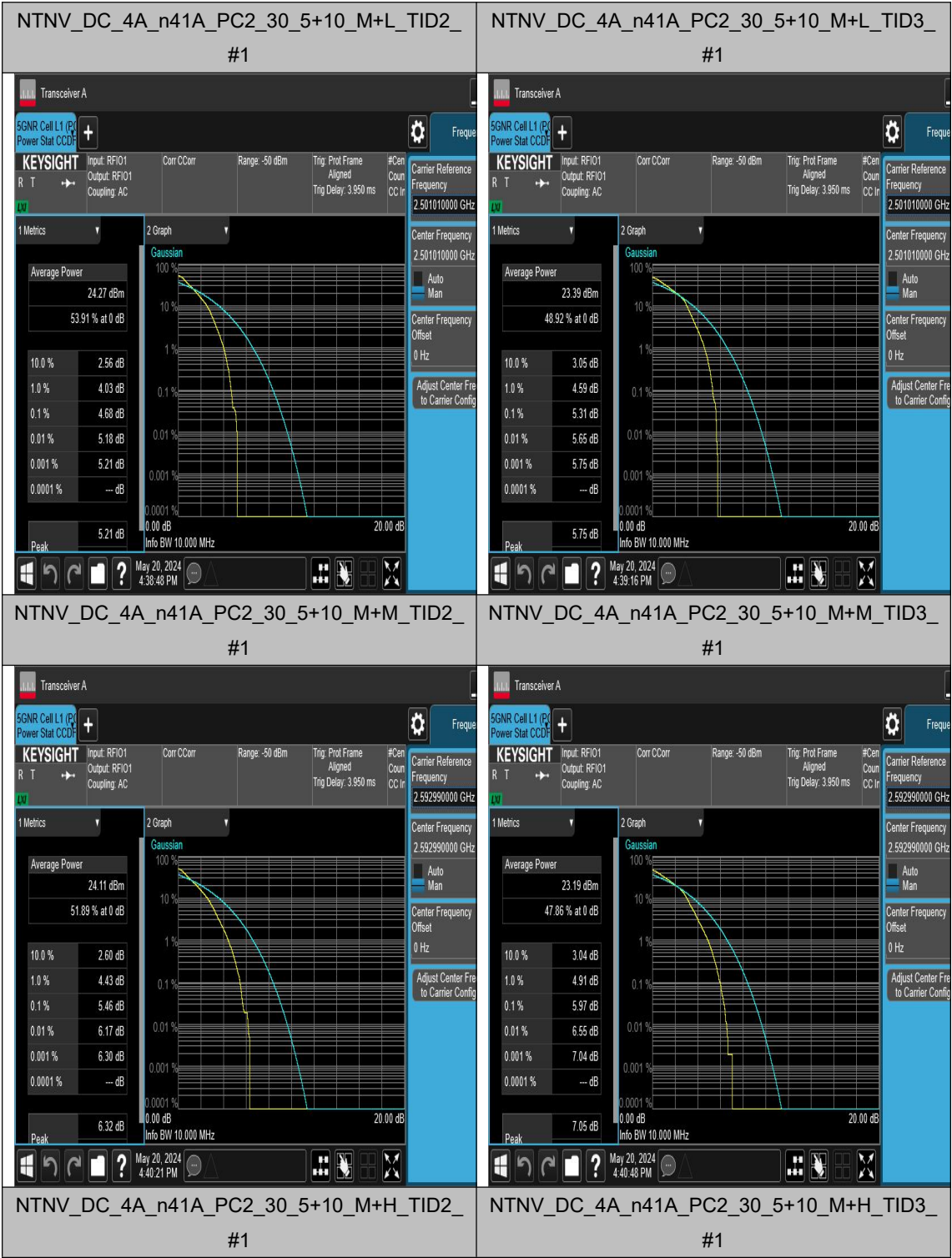
Test Result

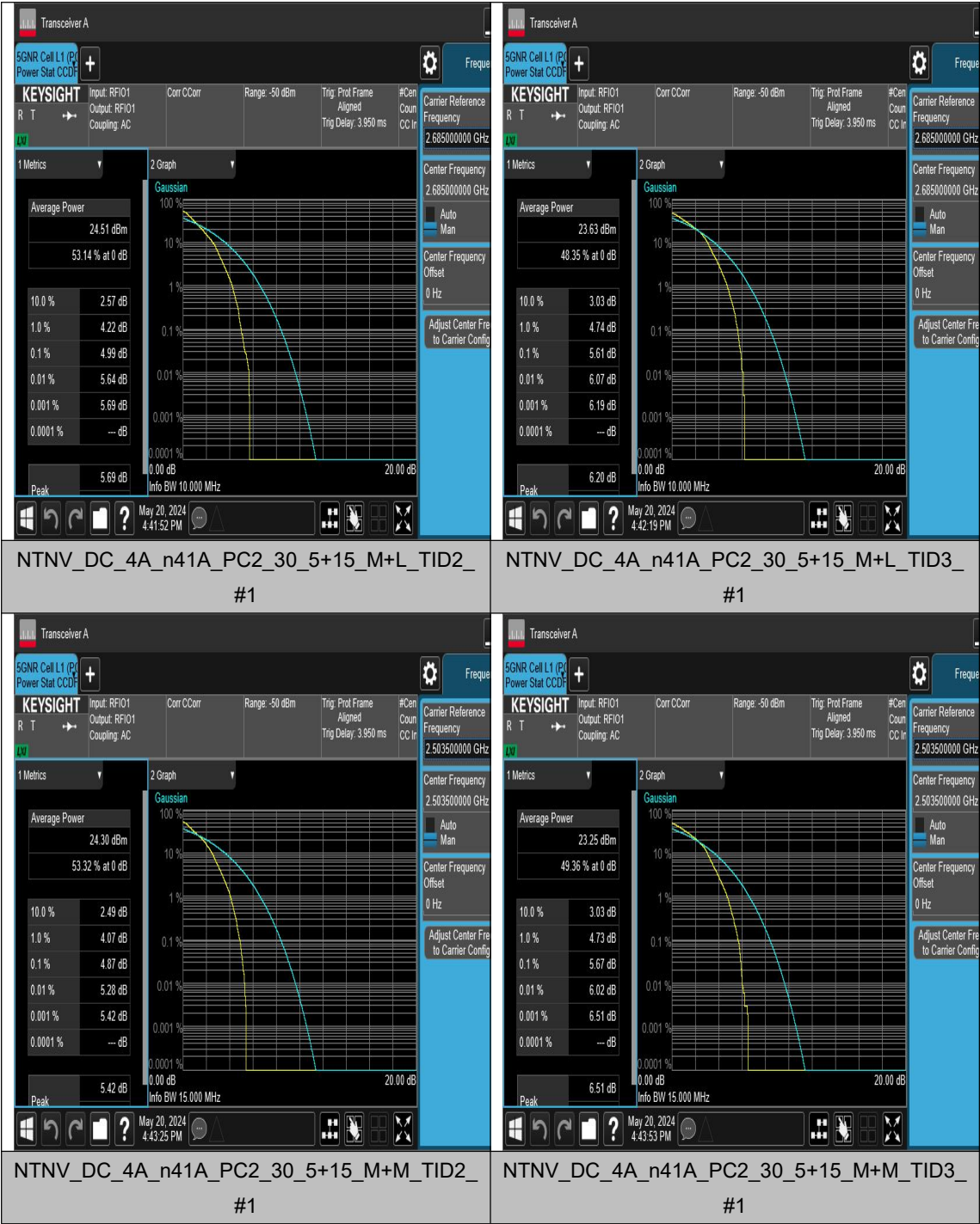
Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result	Limit	Verdict
DC_4A_n4 1A	30	5+10	DFT-QPSK	M+L	Outer_Full	4.68	≤13	PASS
DC_4A_n4 1A	30	5+10	DFT-16QAM	M+L	Outer_Full	5.31	≤13	PASS
DC_4A_n4 1A	30	5+10	DFT-QPSK	M+M	Outer_Full	5.46	≤13	PASS
DC_4A_n4 1A	30	5+10	DFT-16QAM	M+M	Outer_Full	5.97	≤13	PASS
DC_4A_n4 1A	30	5+10	DFT-QPSK	M+H	Outer_Full	4.99	≤13	PASS
DC_4A_n4 1A	30	5+10	DFT-16QAM	M+H	Outer_Full	5.61	≤13	PASS
DC_4A_n4 1A	30	5+15	DFT-QPSK	M+L	Outer_Full	4.87	≤13	PASS
DC_4A_n4 1A	30	5+15	DFT-16QAM	M+L	Outer_Full	5.67	≤13	PASS
DC_4A_n4 1A	30	5+15	DFT-QPSK	M+M	Outer_Full	5.49	≤13	PASS
DC_4A_n4 1A	30	5+15	DFT-16QAM	M+M	Outer_Full	6.21	≤13	PASS
DC_4A_n4 1A	30	5+15	DFT-QPSK	M+H	Outer_Full	5.09	≤13	PASS
DC_4A_n4 1A	30	5+15	DFT-16QAM	M+H	Outer_Full	5.92	≤13	PASS
DC_4A_n4 1A	30	5+20	DFT-QPSK	M+L	Outer_Full	5.25	≤13	PASS
DC_4A_n4 1A	30	5+20	DFT-16QAM	M+L	Outer_Full	6.01	≤13	PASS
DC_4A_n4 1A	30	5+20	DFT-QPSK	M+M	Outer_Full	5.64	≤13	PASS
DC_4A_n4 1A	30	5+20	DFT-16QAM	M+M	Outer_Full	6.34	≤13	PASS
DC_4A_n4 1A	30	5+20	DFT-QPSK	M+H	Outer_Full	5.46	≤13	PASS

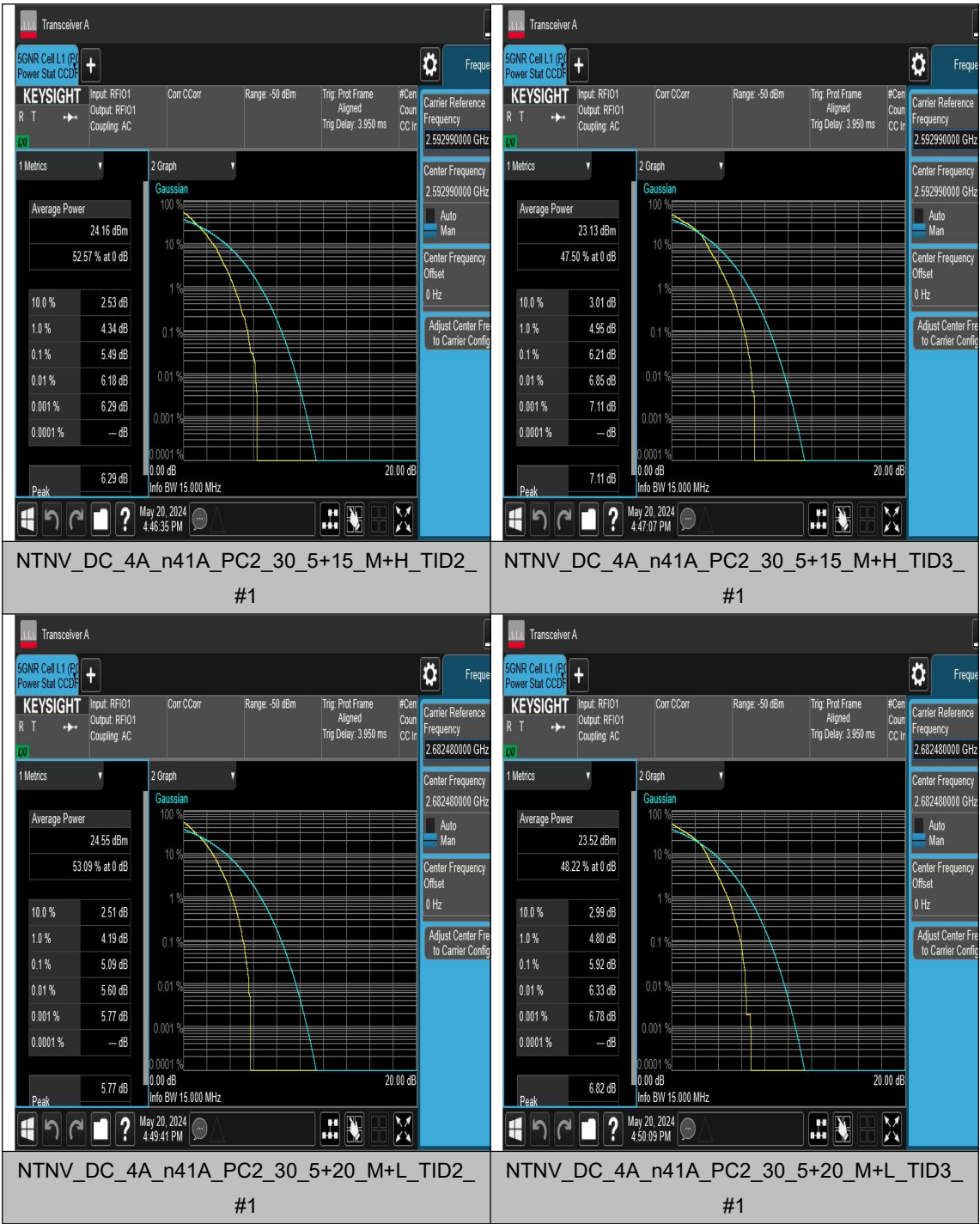
DC_4A_n4 1A	30	5+20	DFT-16QAM	M+H	Outer_Full	6.01	≤13	PASS
DC_4A_n4 1A	30	5+30	DFT-QPSK	M+L	Outer_Full	5.39	≤13	PASS
DC_4A_n4 1A	30	5+30	DFT-16QAM	M+L	Outer_Full	6.21	≤13	PASS
DC_4A_n4 1A	30	5+30	DFT-QPSK	M+M	Outer_Full	5.51	≤13	PASS
DC_4A_n4 1A	30	5+30	DFT-16QAM	M+M	Outer_Full	6.13	≤13	PASS
DC_4A_n4 1A	30	5+30	DFT-QPSK	M+H	Outer_Full	5.17	≤13	PASS
DC_4A_n4 1A	30	5+30	DFT-16QAM	M+H	Outer_Full	5.80	≤13	PASS
DC_4A_n4 1A	30	5+40	DFT-QPSK	M+L	Outer_Full	5.36	≤13	PASS
DC_4A_n4 1A	30	5+40	DFT-16QAM	M+L	Outer_Full	6.00	≤13	PASS
DC_4A_n4 1A	30	5+40	DFT-QPSK	M+M	Outer_Full	5.57	≤13	PASS
DC_4A_n4 1A	30	5+40	DFT-16QAM	M+M	Outer_Full	6.21	≤13	PASS
DC_4A_n4 1A	30	5+40	DFT-QPSK	M+H	Outer_Full	5.31	≤13	PASS
DC_4A_n4 1A	30	5+40	DFT-16QAM	M+H	Outer_Full	5.97	≤13	PASS
DC_4A_n4 1A	30	5+60	DFT-QPSK	M+L	Outer_Full	5.41	≤13	PASS
DC_4A_n4 1A	30	5+60	DFT-16QAM	M+L	Outer_Full	6.23	≤13	PASS
DC_4A_n4 1A	30	5+60	DFT-QPSK	M+M	Outer_Full	5.48	≤13	PASS
DC_4A_n4 1A	30	5+60	DFT-16QAM	M+M	Outer_Full	6.23	≤13	PASS
DC_4A_n4 1A	30	5+60	DFT-QPSK	M+H	Outer_Full	5.26	≤13	PASS
DC_4A_n4 1A	30	5+60	DFT-16QAM	M+H	Outer_Full	6.01	≤13	PASS
DC_4A_n4 1A	30	5+80	DFT-QPSK	M+L	Outer_Full	5.65	≤13	PASS
DC_4A_n4 1A	30	5+80	DFT-16QAM	M+L	Outer_Full	6.22	≤13	PASS
DC_4A_n4	30	5+80	DFT-QPSK	M+M	Outer_Full	5.63	≤13	PASS

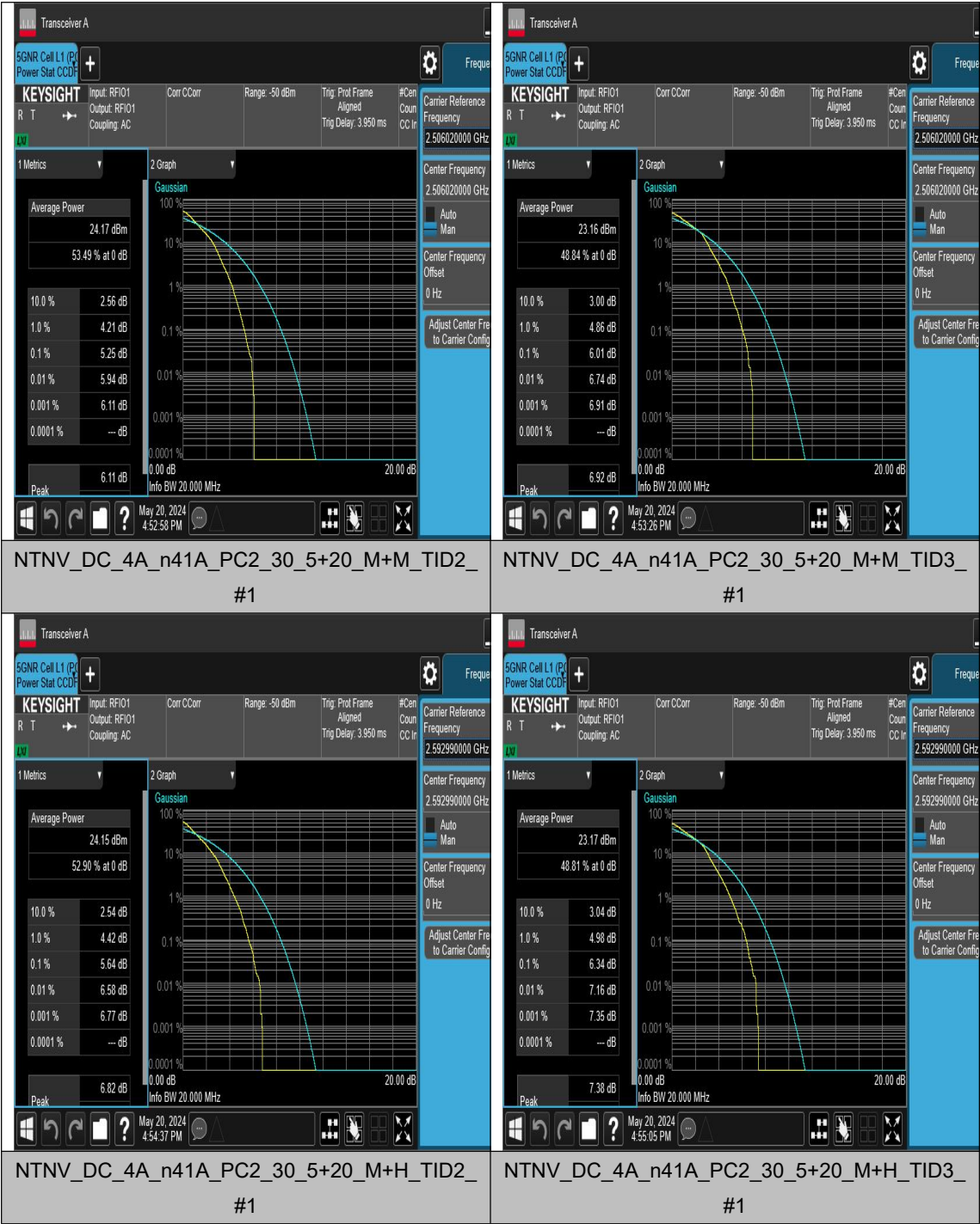
1A								
DC_4A_n4 1A	30	5+80	DFT-16QAM	M+M	Outer_Full	6.19	≤13	PASS
DC_4A_n4 1A	30	5+80	DFT-QPSK	M+H	Outer_Full	5.54	≤13	PASS
DC_4A_n4 1A	30	5+80	DFT-16QAM	M+H	Outer_Full	6.15	≤13	PASS
DC_4A_n4 1A	30	5+100	DFT-QPSK	M+L	Outer_Full	5.60	≤13	PASS
DC_4A_n4 1A	30	5+100	DFT-16QAM	M+L	Outer_Full	6.33	≤13	PASS
DC_4A_n4 1A	30	5+100	DFT-QPSK	M+M	Outer_Full	5.64	≤13	PASS
DC_4A_n4 1A	30	5+100	DFT-16QAM	M+M	Outer_Full	6.33	≤13	PASS
DC_4A_n4 1A	30	5+100	DFT-QPSK	M+H	Outer_Full	5.62	≤13	PASS
DC_4A_n4 1A	30	5+100	DFT-16QAM	M+H	Outer_Full	6.39	≤13	PASS

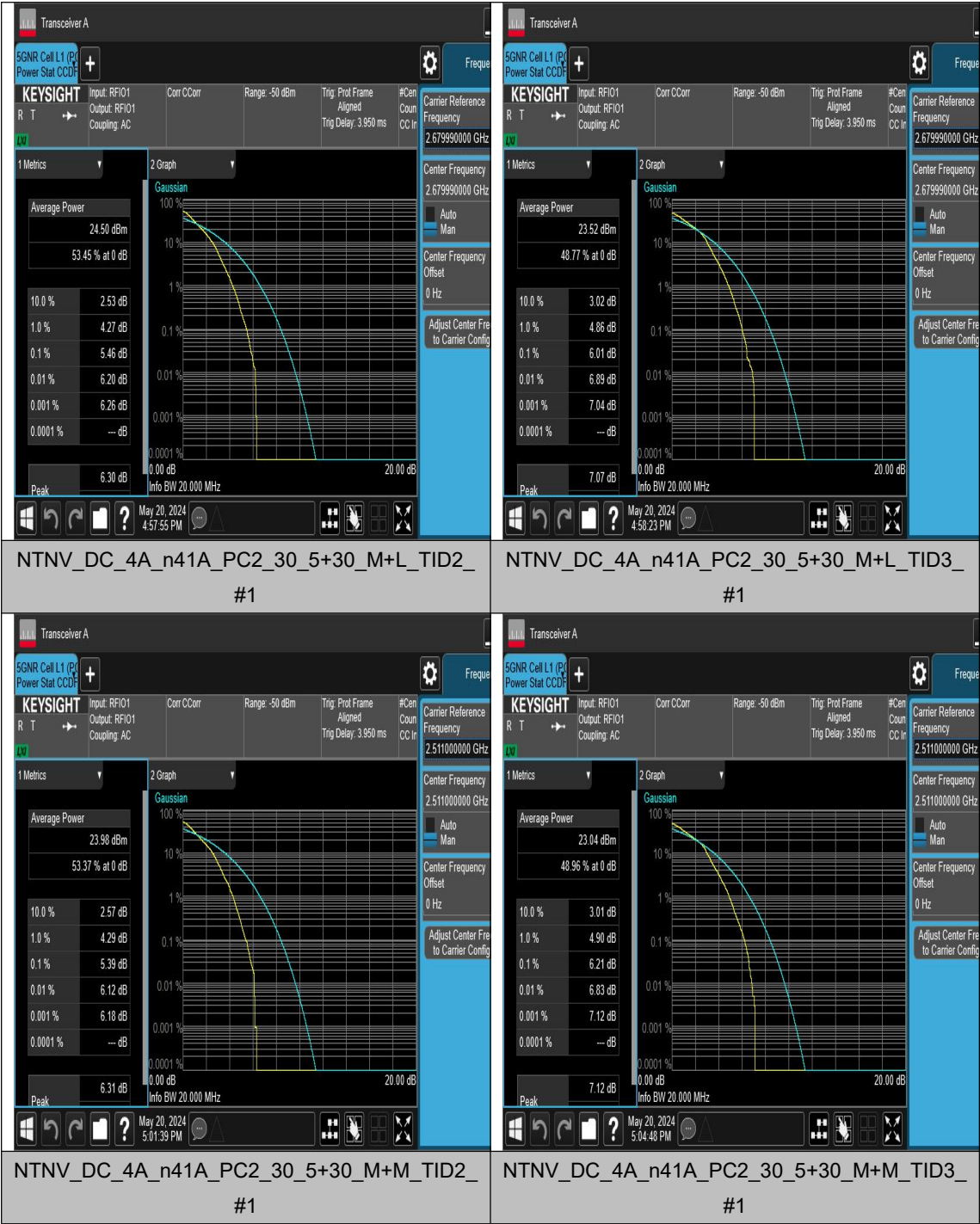
Test Graphs

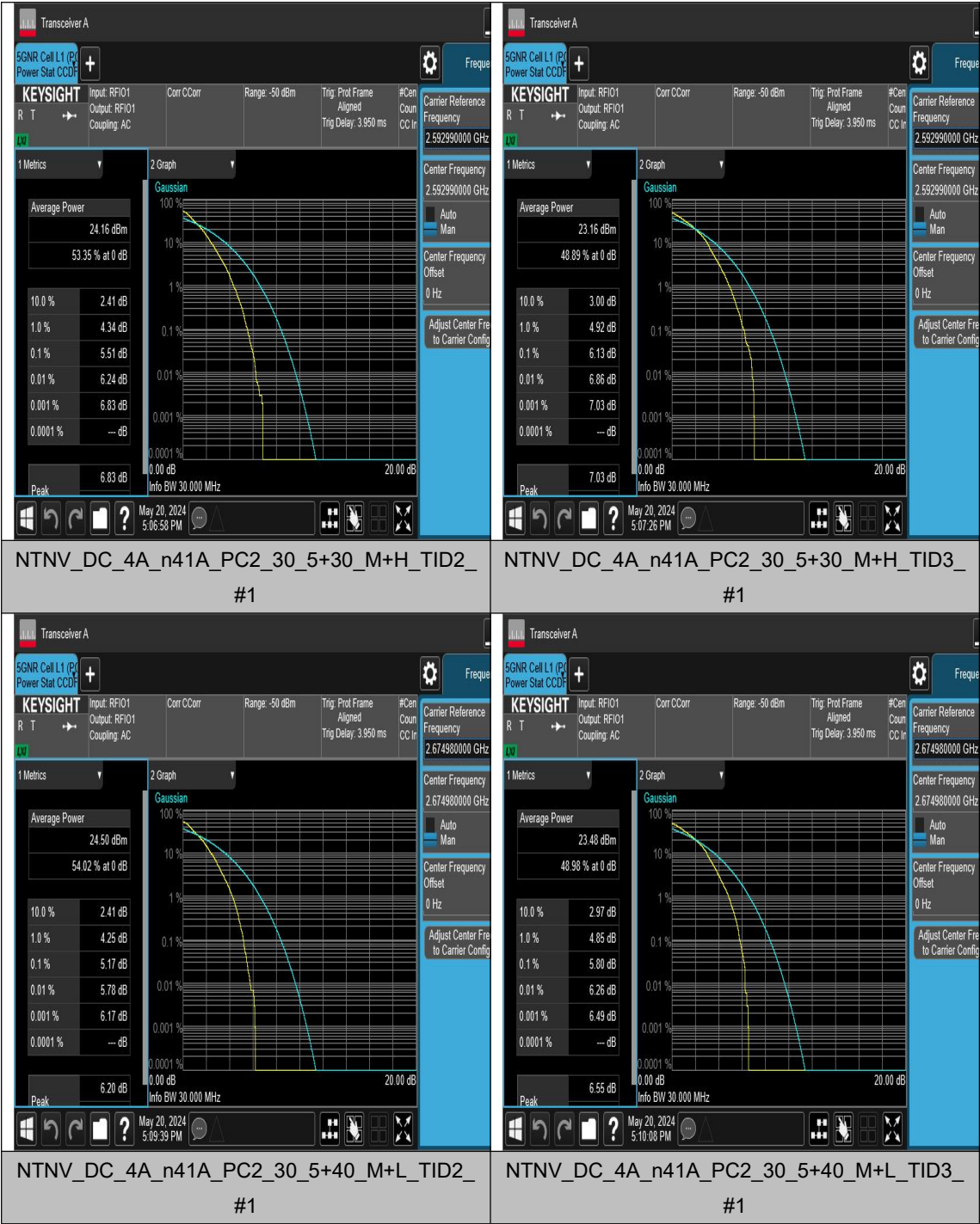


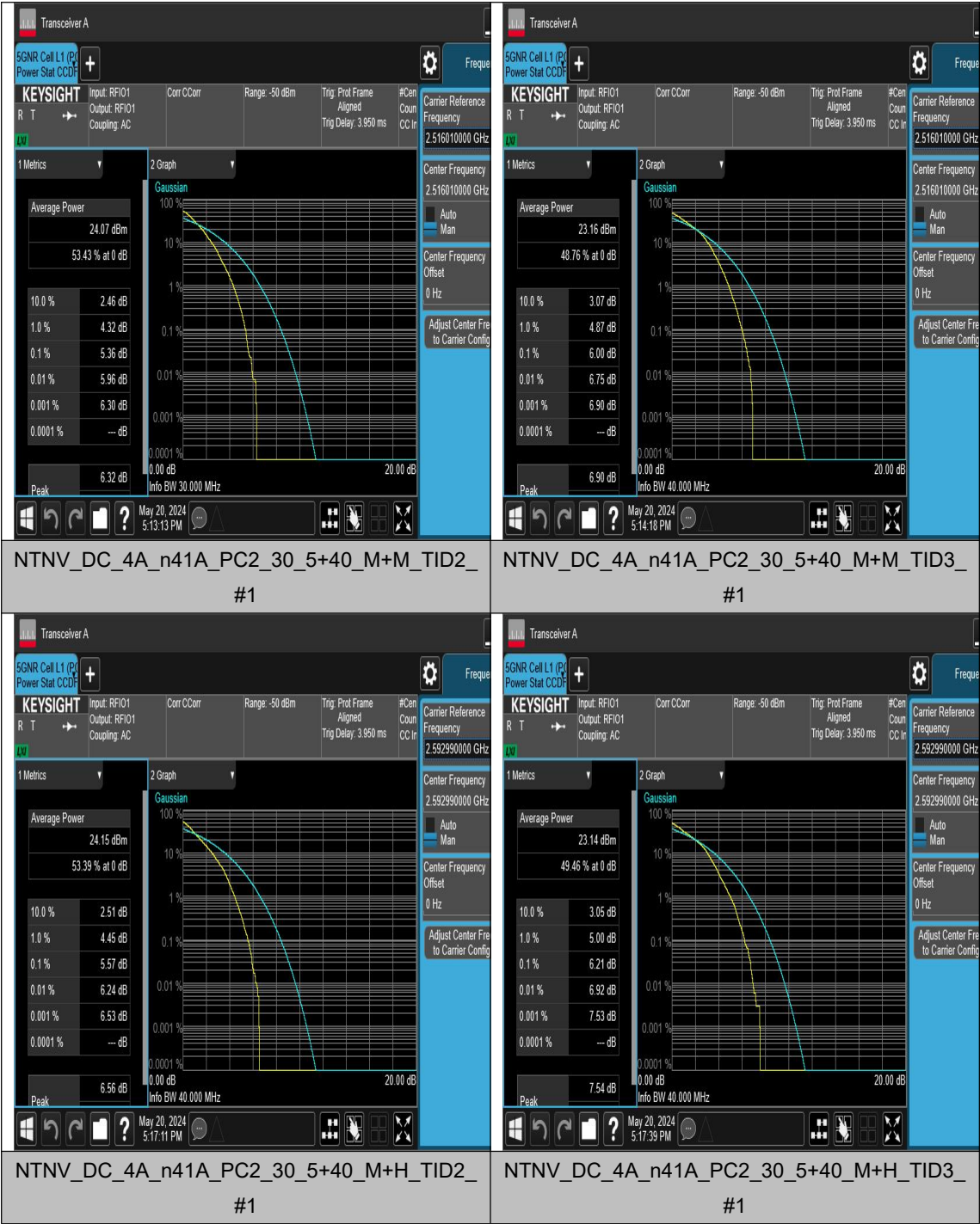


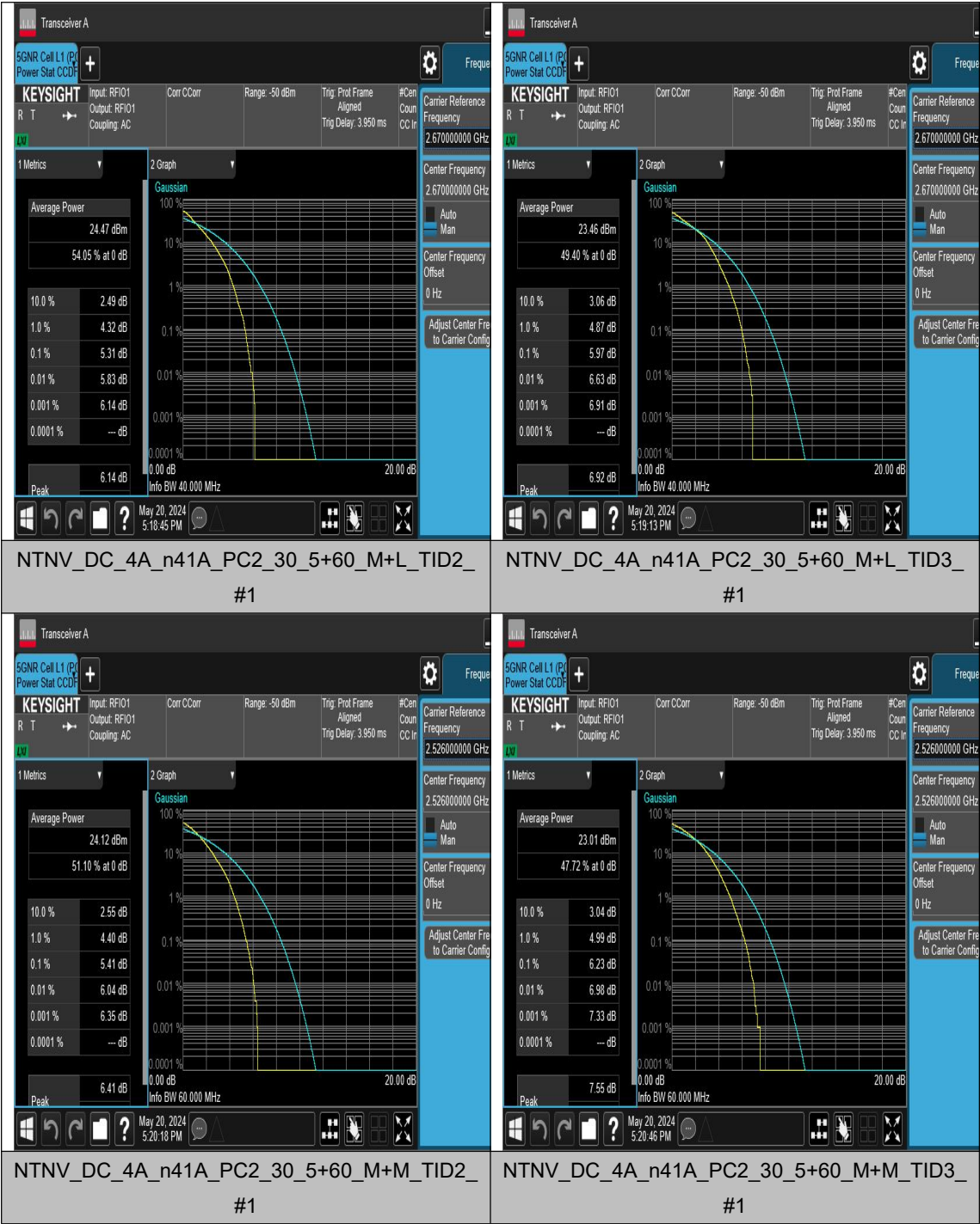


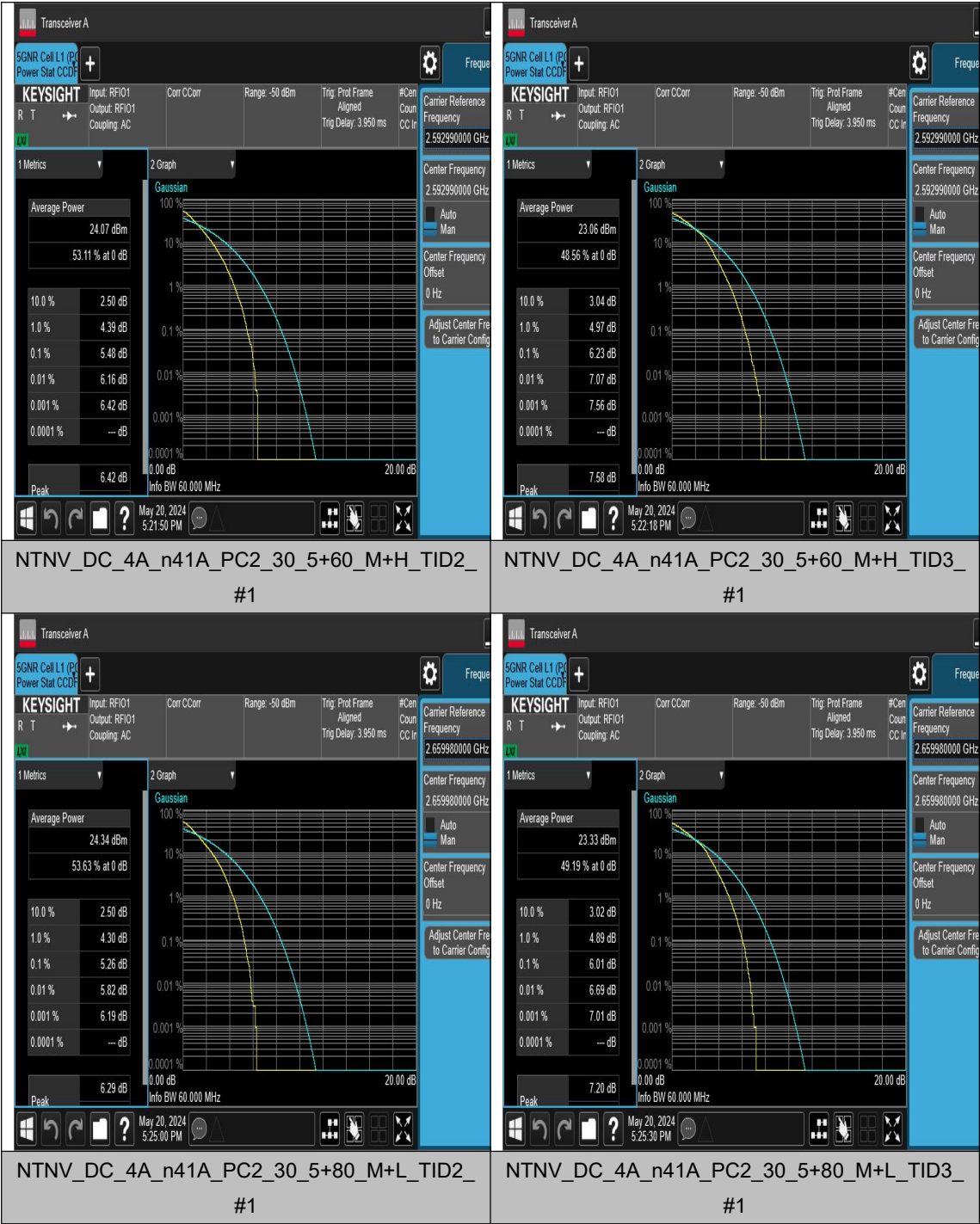


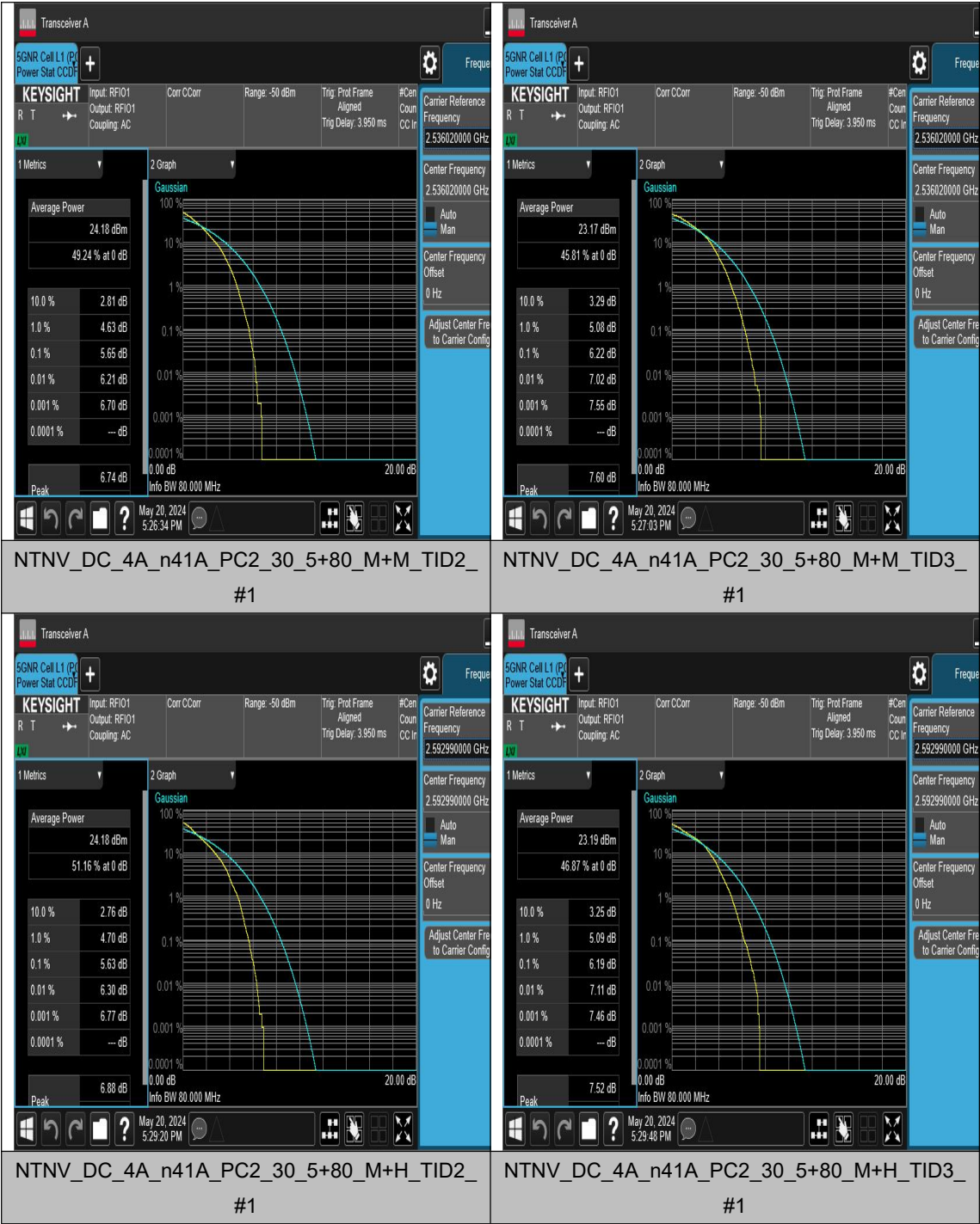


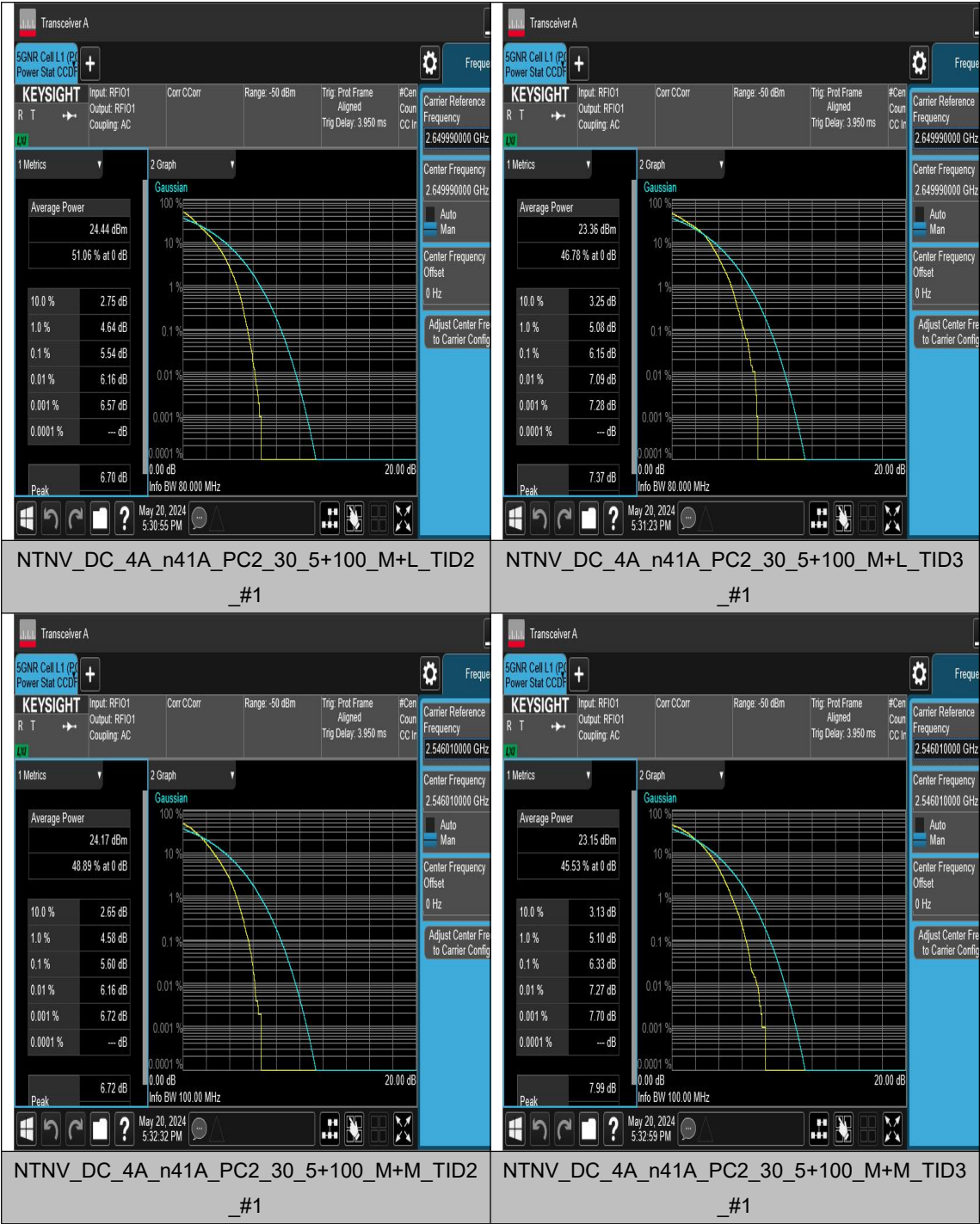


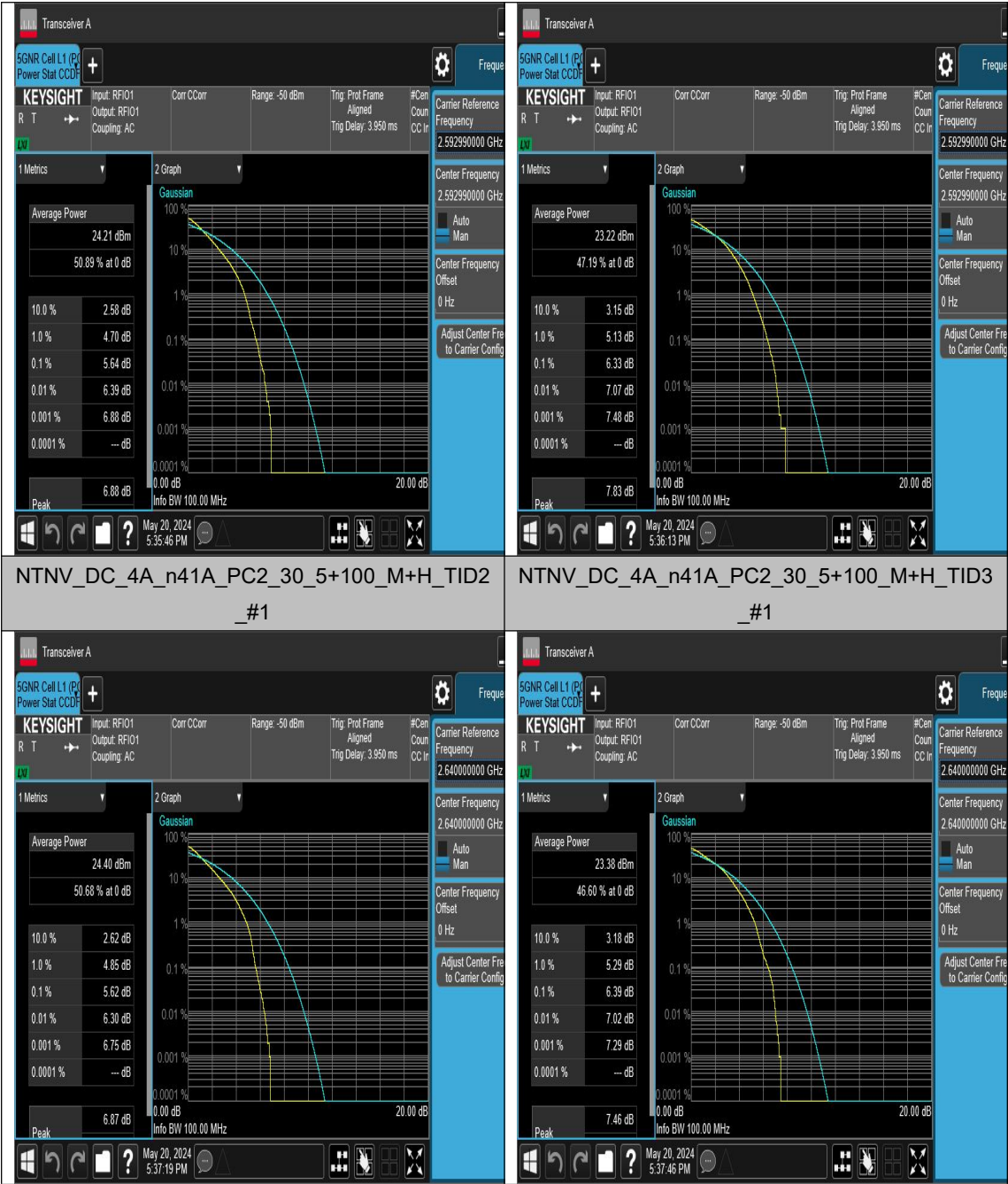












Appendix C: 26dB Bandwidth and Occupied Bandwidth for

NSA

Test Result

Band	SCS	Bandwidth	Modulation	Channel	RB Config	Result (99%)	Result (26dB)	Verdict
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Outer_Full	8.6123	9.340	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Outer_Full	8.5901	9.309	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Outer_Full	8.5858	9.289	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Outer_Full	8.6097	9.347	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Outer_Full	8.6051	9.373	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Outer_Full	8.6049	9.232	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Outer_Full	12.897	13.72	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Outer_Full	12.866	13.79	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Outer_Full	12.889	13.72	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Outer_Full	12.869	13.79	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Outer_Full	12.884	13.74	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Outer_Full	12.844	13.68	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	17.892	18.97	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Outer_Full	17.861	18.83	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	17.889	18.85	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Outer_Full	17.894	18.86	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	17.867	18.85	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Outer_Full	17.875	18.81	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Outer_Full	26.802	28.21	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Outer_Full	26.810	28.21	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Outer_Full	26.787	28.22	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Outer_Full	26.798	28.13	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Outer_Full	26.815	28.33	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Outer_Full	26.797	28.17	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	35.782	37.50	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Outer_Full	35.798	37.32	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	35.664	37.35	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Outer_Full	35.730	37.51	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	35.718	37.47	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Outer_Full	35.756	37.33	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	57.889	59.94	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Outer_Full	57.738	59.86	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	57.872	59.82	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Outer_Full	57.699	59.90	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	57.834	59.89	PASS

DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Outer_Full	57.677	59.87	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Outer_Full	76.996	79.72	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Outer_Full	76.968	79.70	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Outer_Full	77.017	79.71	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Outer_Full	77.007	79.74	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Outer_Full	76.960	79.82	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Outer_Full	76.921	79.64	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+L	Outer_Full	96.353	99.65	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+L	Outer_Full	96.207	99.63	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+M	Outer_Full	96.404	99.63	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+M	Outer_Full	96.351	99.58	PASS
DC_4A_n41A	30	5+100	DFT-QPSK	M+H	Outer_Full	96.309	99.65	PASS
DC_4A_n41A	30	5+100	DFT-16QAM	M+H	Outer_Full	96.161	99.62	PASS

Test Graphs

