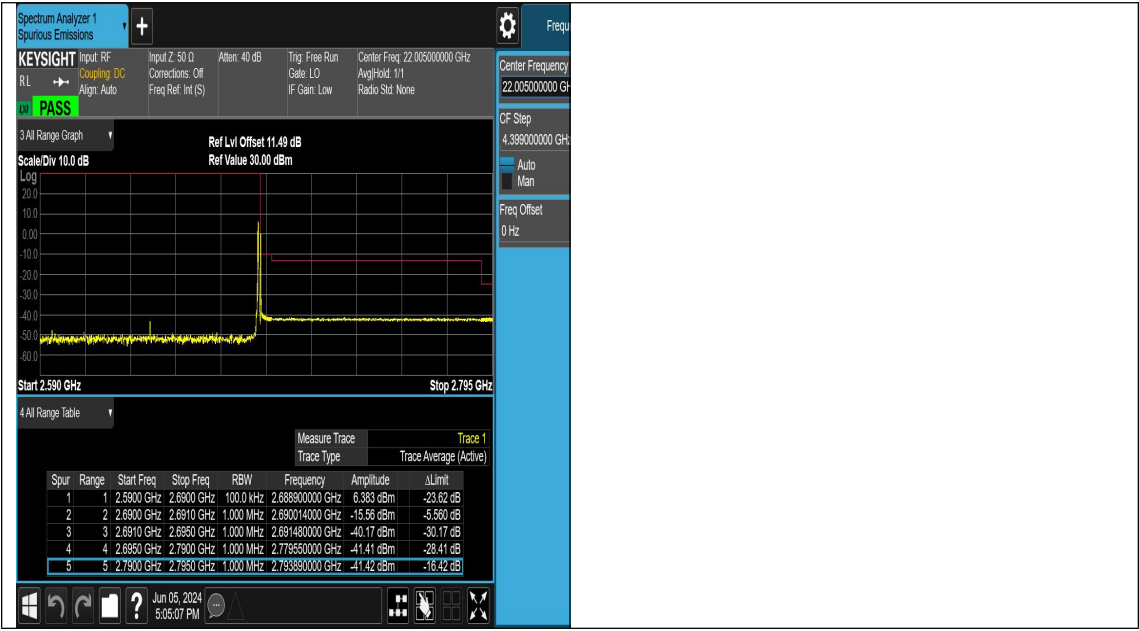




Appendix E: Conducted Spurious Emission for NSA

Test Result

Band	SC S	Band width	Modulation	Chan nel	RB Config	StartFreq	StopFreq	Limit	Verdict
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+M	Edge_1RB_Right	18000	26500	-25	PASS

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DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-QPSK	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-25	PASS

DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+M	Edge_1RB_Right	18000	26500	-25	PASS

DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-QPSK	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+15	DFT-16QAM	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-25	PASS

DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-QPSK	M+H	Edge_1RB_Right	18000	26500	-25	PASS

DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+20	DFT-16QAM	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-25	PASS

DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-QPSK	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+30	DFT-16QAM	M+H	Edge_1RB_Right	18000	26500	-25	PASS

DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-25	PASS

DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-QPSK	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+40	DFT-16QAM	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+L	Edge_1RB_Right	18000	26500	-25	PASS

DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-25	PASS

DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-QPSK	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+60	DFT-16QAM	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+L	Edge_1RB_Right	18000	26500	-25	PASS

DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-QPSK	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-25	PASS

DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+80	DFT-16QAM	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+L	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+L	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+L	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+L	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+L	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+L	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+L	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+L	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+L	Edge_1RB_Left	3000	18000	-25	PASS

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DC_4A_n41A	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+L	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+M	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+M	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+M	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+M	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+M	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+M	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+M	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+M	Outer_Full	18000	26500	-25	PASS

DC_4A_n41A	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+M	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+H	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+H	Outer_Full	1000	3000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+H	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+H	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-QPSK	M+H	Edge_1RB_Right	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+H	Outer_Full	30	1000	-35	PASS
DC_4A_n41A	30	5+10	DFT-16QAM	M+H	Outer_Full	1000	3000	-25	PASS

		0							
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+H	Outer_Full	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+H	Outer_Full	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	1000	3000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Left	18000	26500	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	30	1000	-35	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	1000	3000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	3000	18000	-25	PASS
DC_4A_n41A	30	5+10 0	DFT-16QAM	M+H	Edge_1RB_Right	18000	26500	-25	PASS

Test Graphs





