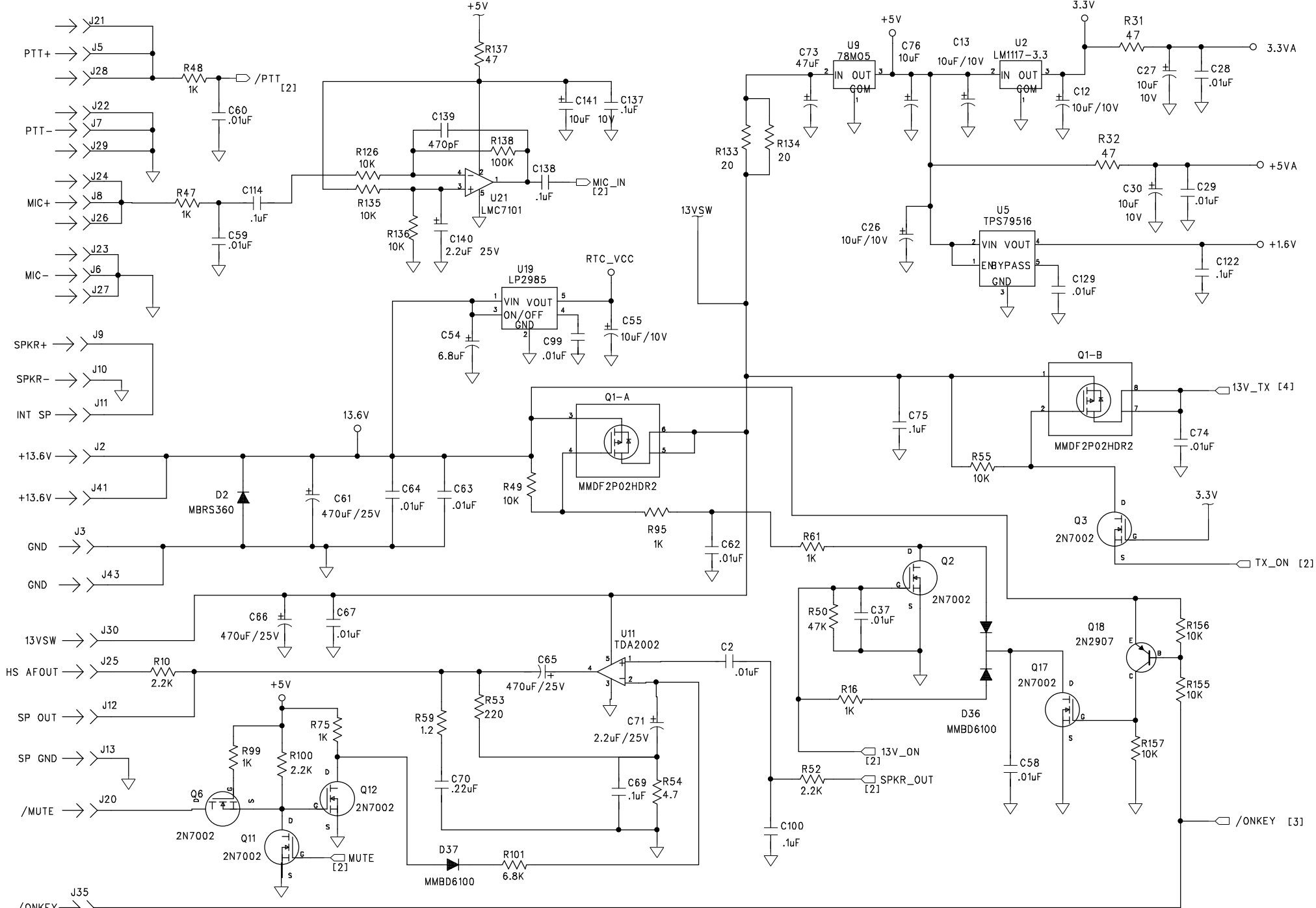




HIGHEST REFERENCE DESIGNATOR USED:

C165 D37 J51 L7 M2 Q16 R178
S16 T1 U24 Y1

DESIGNATORS NOT USED:

C6,C22-C24,C38,C42,C44,C46,C49,C92,C120-124
D4,D8-D16,D35 J34 S14 U13,U14,U18,U20
R3,R22,R88,R102,R112,R115,R117,R122-R124

REVISION HISTORY

REV P8 MIC BOOST,PASSIVE SERIAL PORT 2/15/2008
ADD BALANCED OUTPUT 3/19/2008

REV P9 REMOVED LEVEL SHIFTERS, T/R SWITCH
CHG. RS-232 CONNECTIONS 8/14/2009

REV P10 CHANGE BACKLIGHT, ADD CONNECTIONS:
COMPUTER BD - RX BD, CHG. U10 2/9/10

NOTES: (UNLESS OTHERWISE NOTED)

1. CAPACITORS ARE IN MICROFARADS μ F
2. RESISTORS ARE IN OHMS 1/BW, 5%

THIS PRODUCT CONTAINS CIRCUITRY OF
A PROPRIETARY NATURE, ISSUED ON THE
CONDITION THAT IT IS NOT TO BE COPIED
MODIFIED OR DISCLOSED IN ANY WAY
WITHOUT PRIOR WRITTEN CONSENT OF
WEST PAC CORP AND SEA COM CORP.

ASY-157S-01 P10
SEA157S MAIN PCB

M.D.Peterson 3/30/07

Sheet 1 of 5

Fig 7.2.1

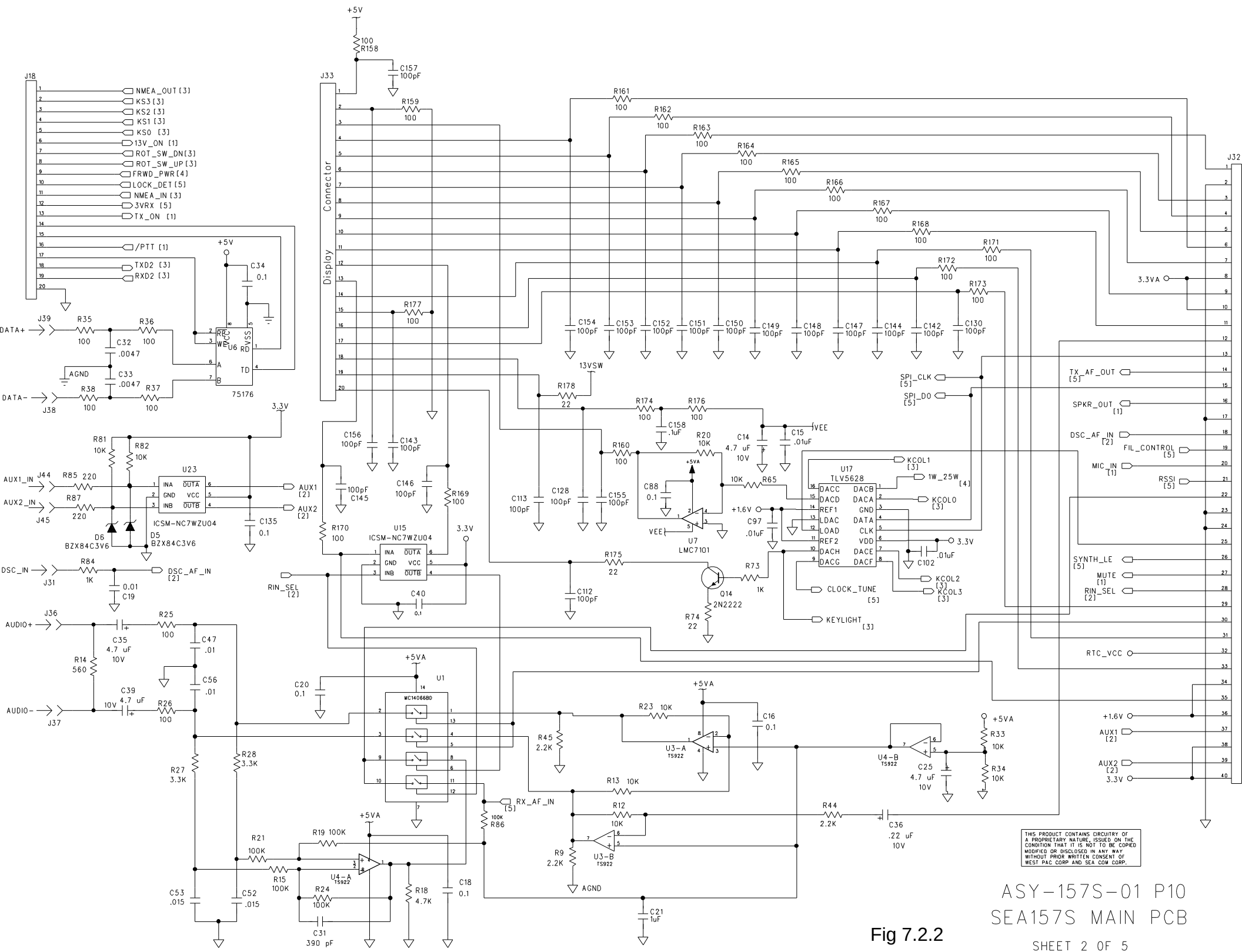


Fig 7.2.2

ASY-157S-01 P10
SEA157S MAIN PCB

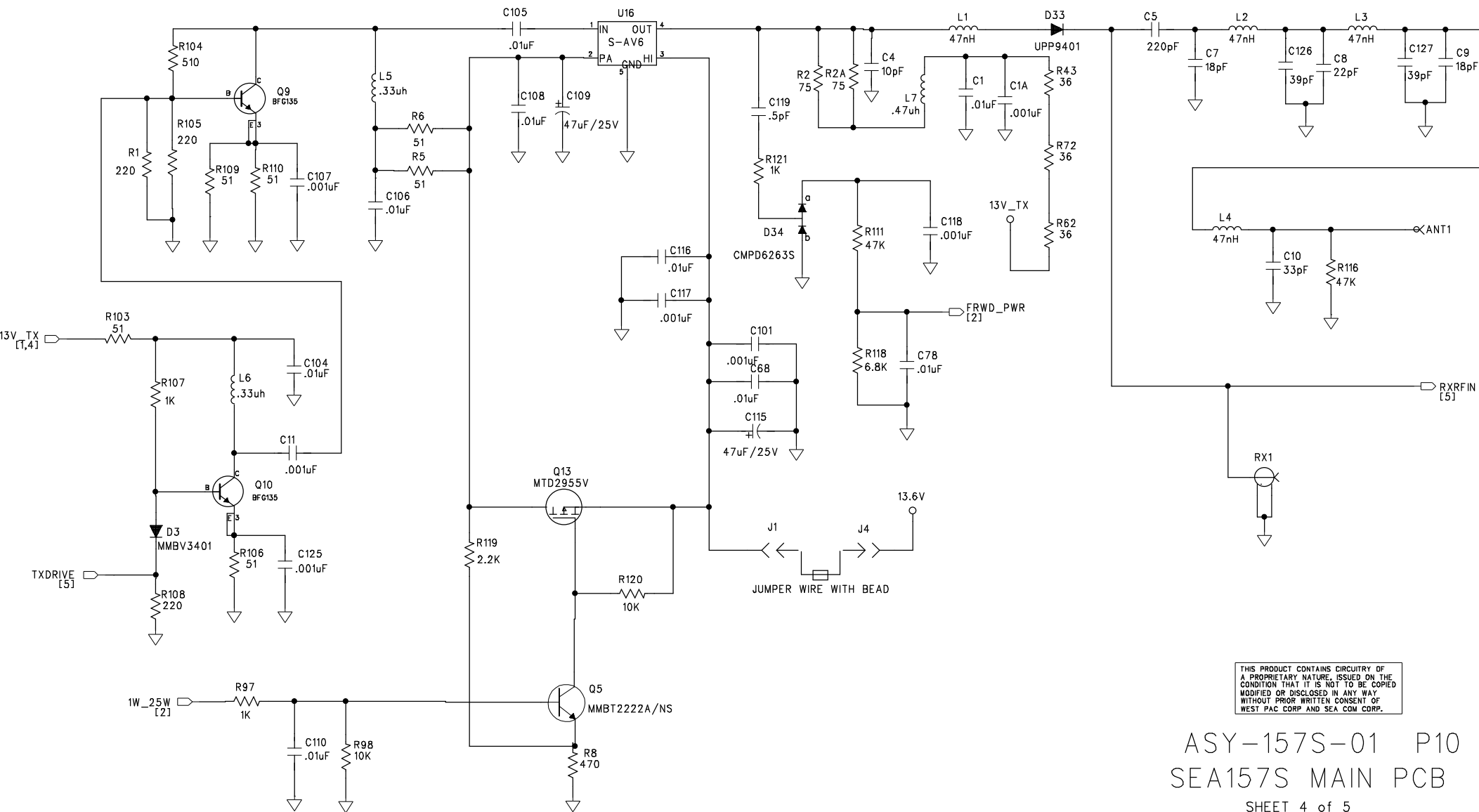
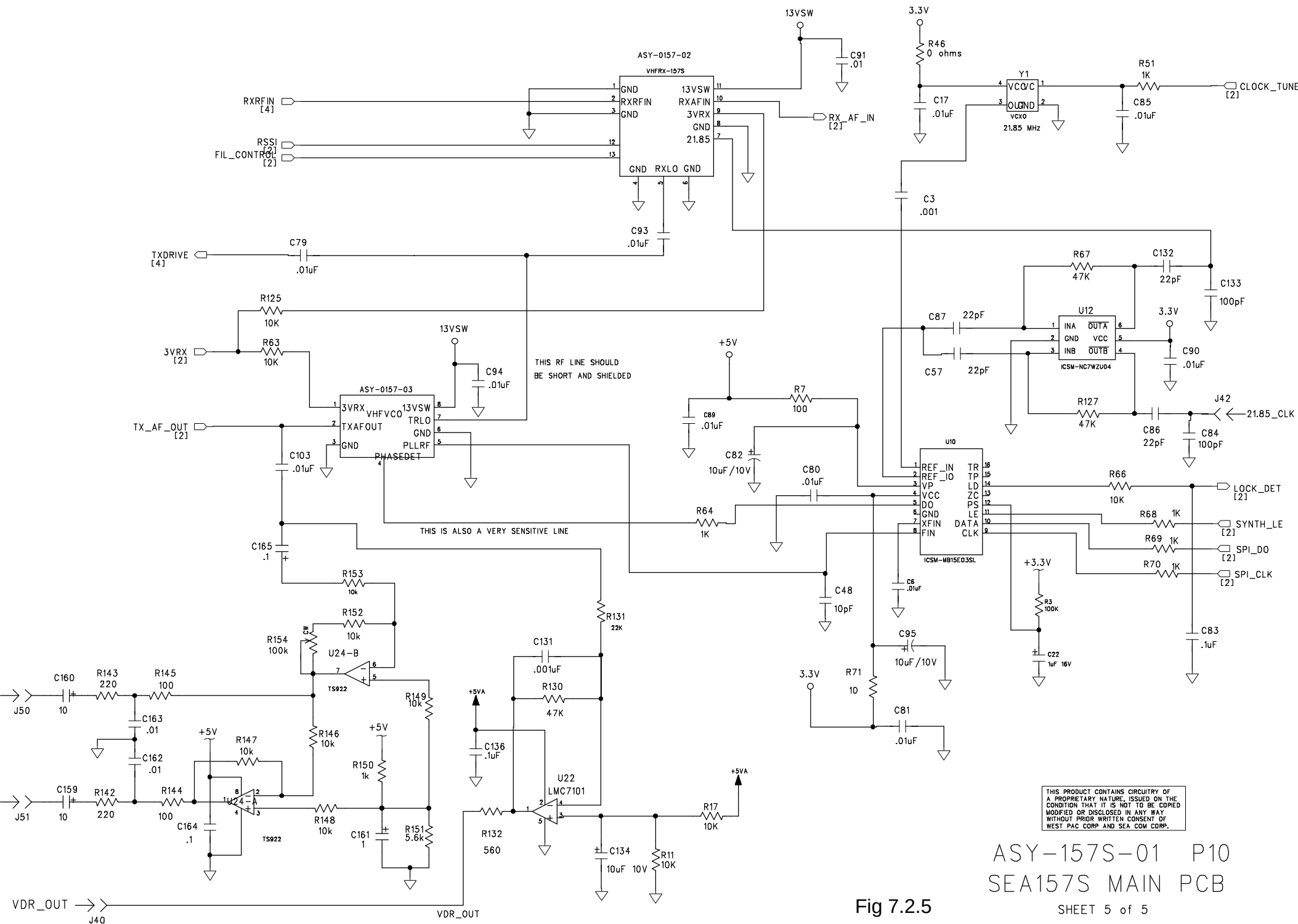


Fig 7.2.4



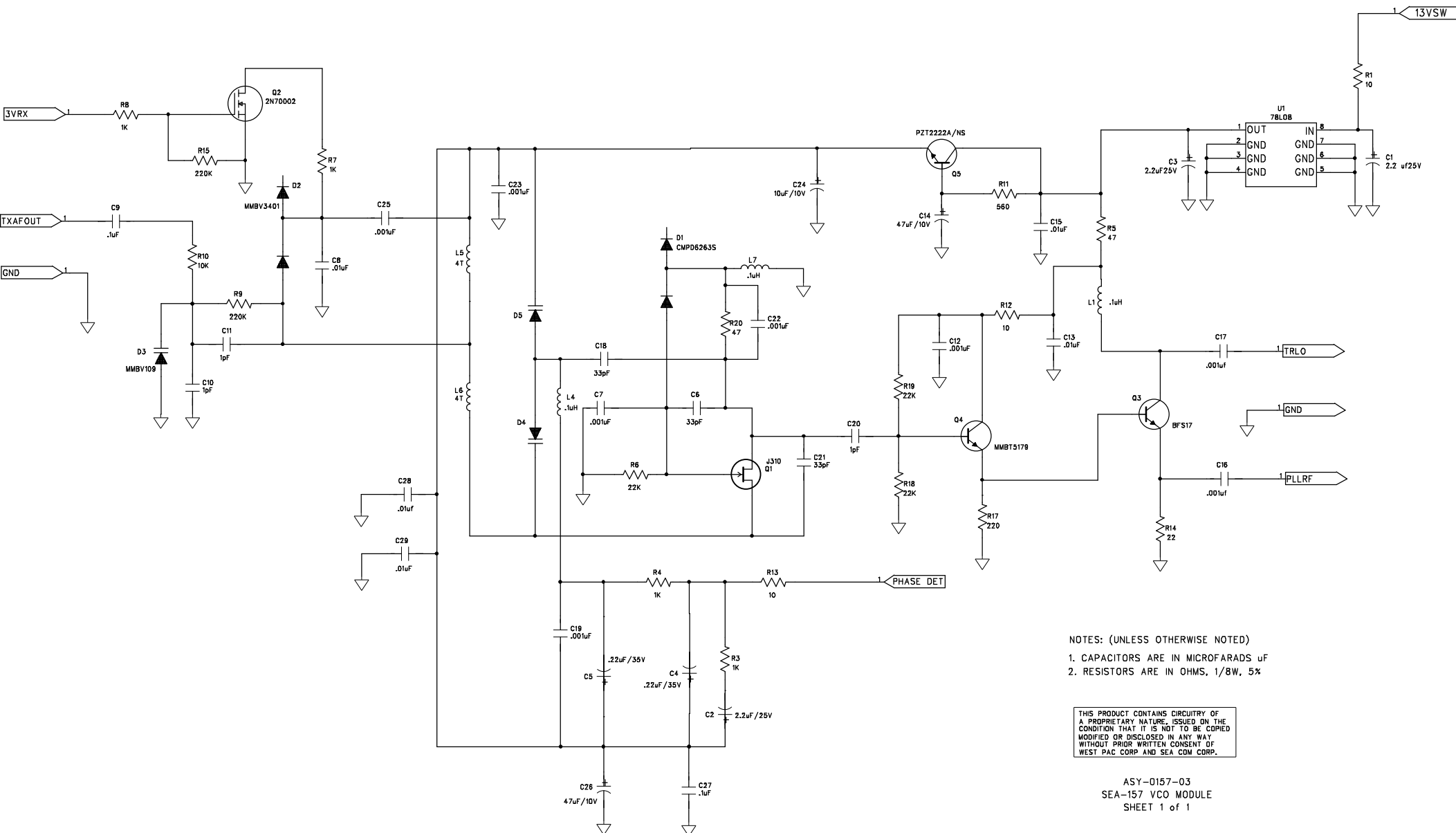
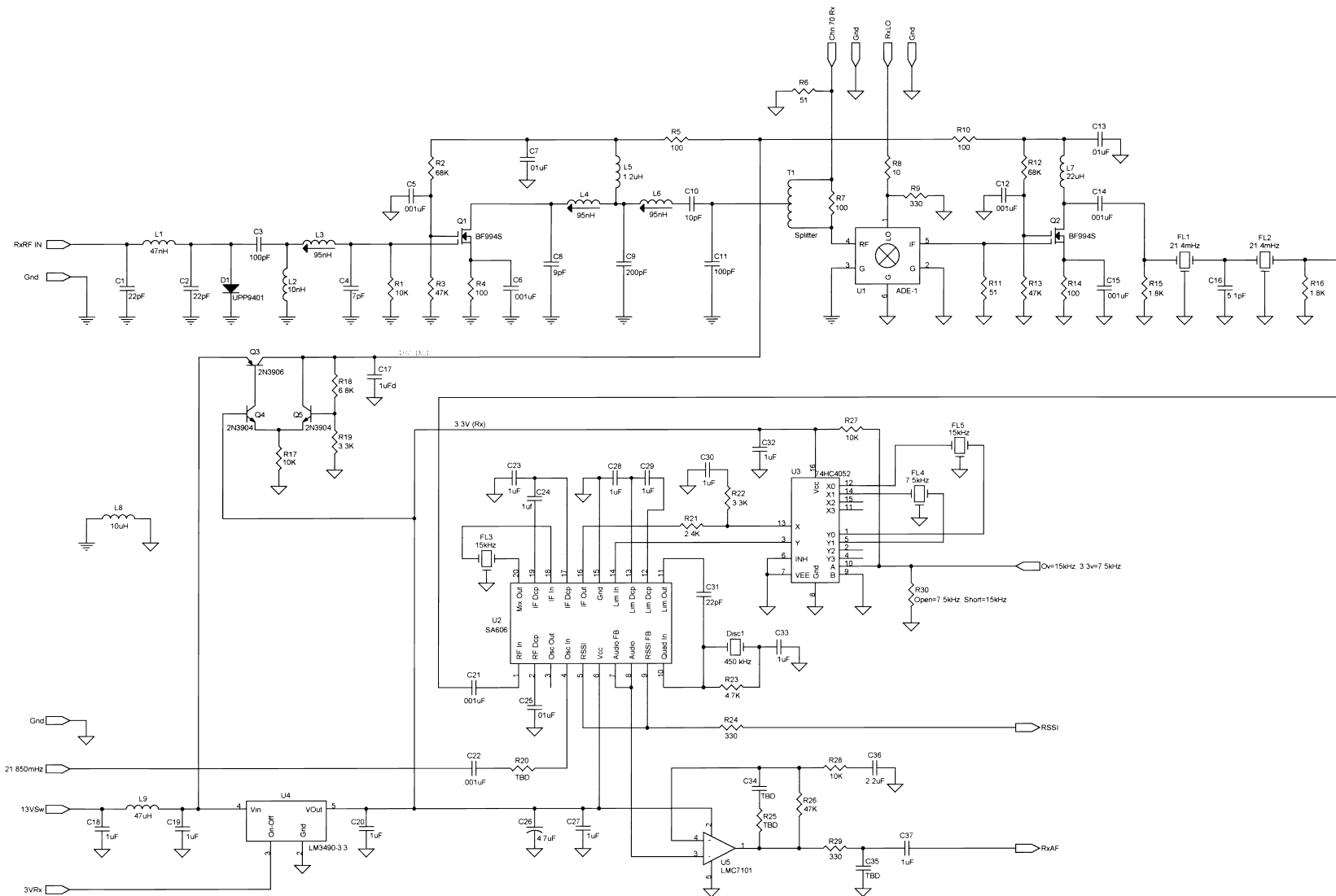


Fig 7.3



ASY -0157-03
157s Receiver Module
Sheet 1 of 1

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SEA COM Corp. and WEST PAC Corp.

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Fig 7.4

Sea Com Corp.-West Pac Corp.			
157s Receiver Schematic			
Size	Document Number	Rev	
C	157s Receiver Schematic DSN	-	
Date	Wednesday, September 01, 2010	Sheet	1 of 1

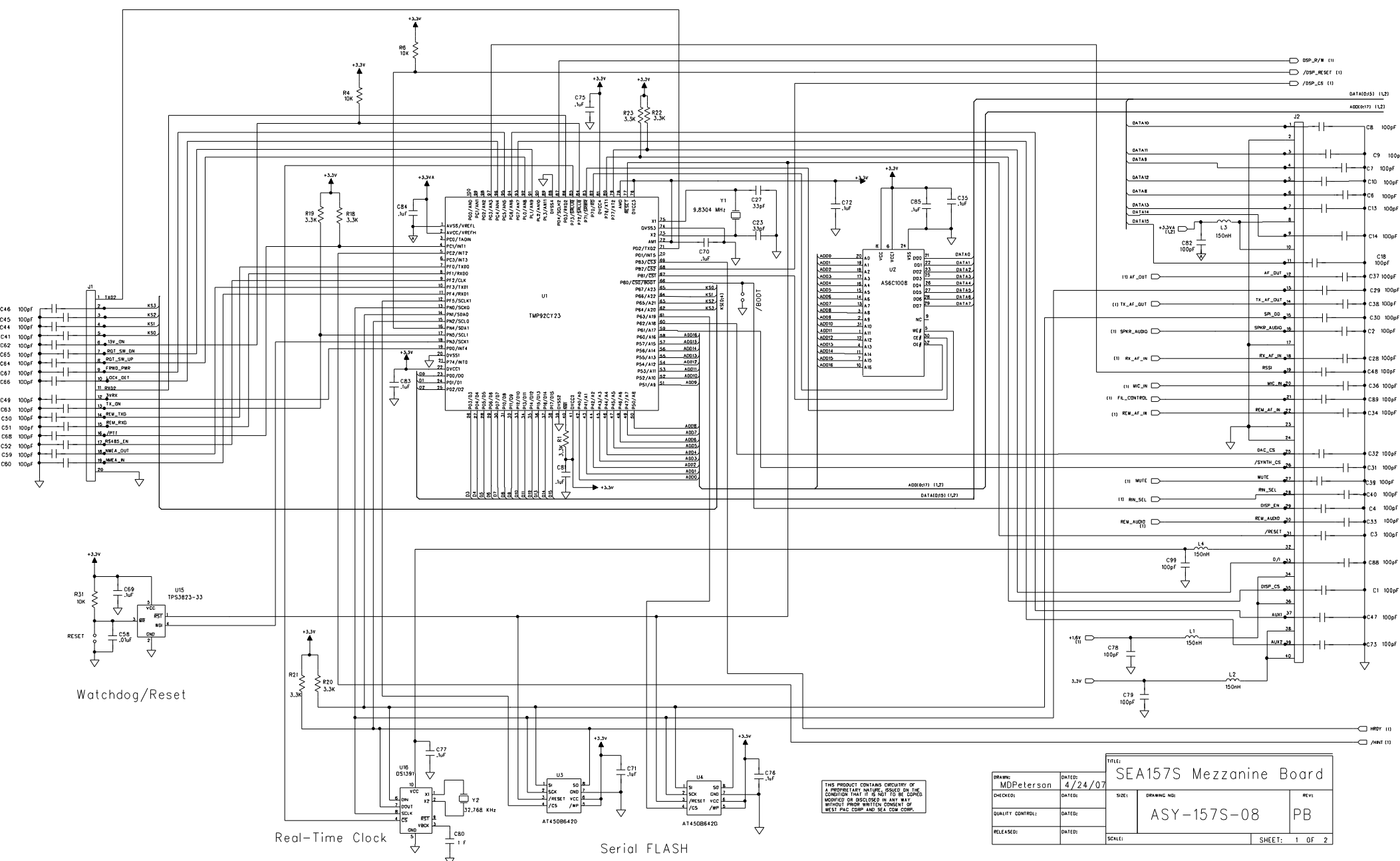


Fig 7.5.1

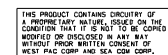
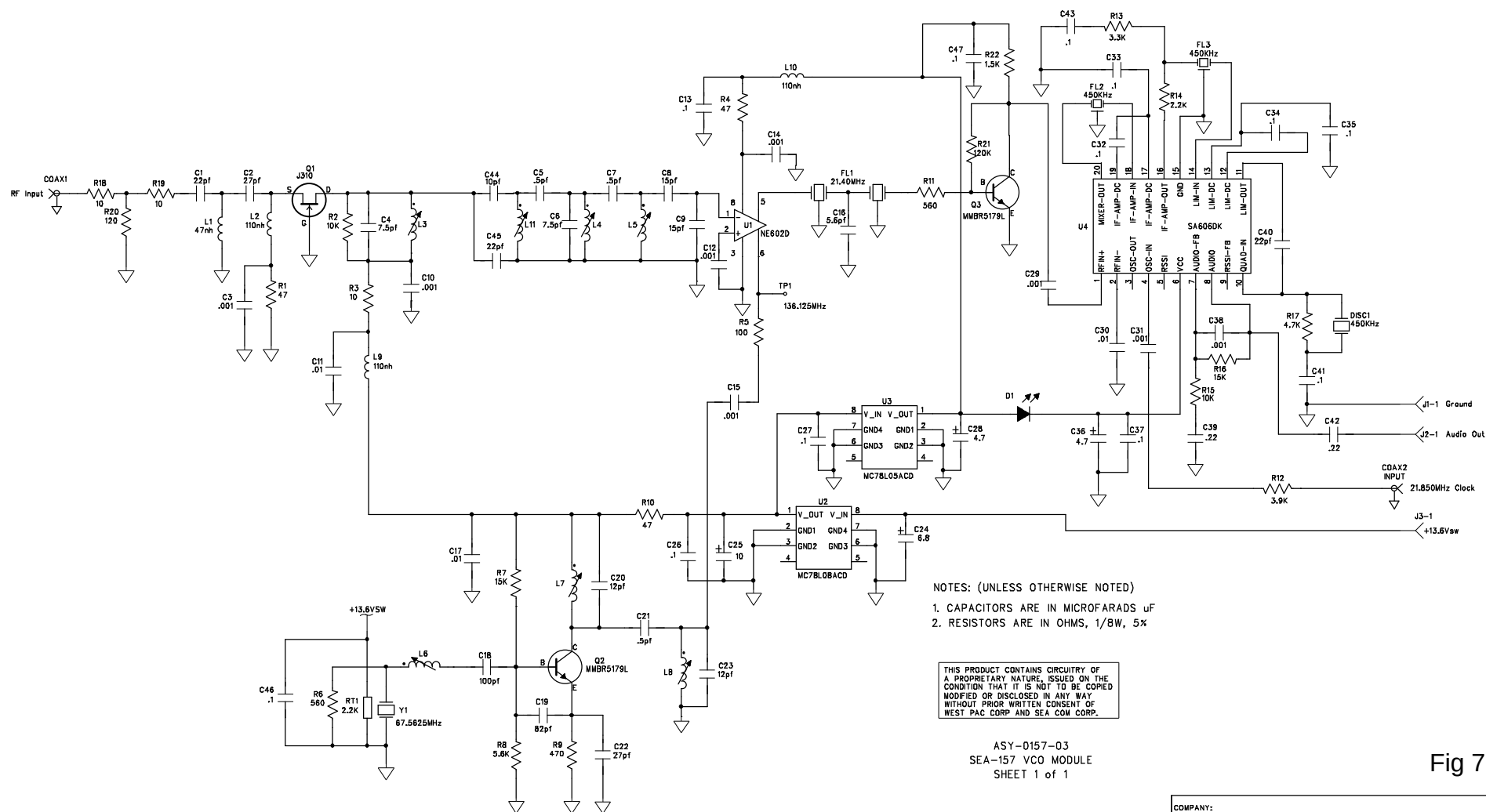


Fig 7.5.2

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:
P2	Add C43 .1uf; change C16 to 5.6pf.	GLK	1-29-6



NOTES: (UNLESS OTHERWISE NOTED)
1. CAPACITORS ARE IN MICROFARADS μF
2. RESISTORS ARE IN OHMS, 1/8W, 5%

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ASY-0157-03
SEA-157 VCO MODULE
SHEET 1 of 1

Fig 7.6

		COMPANY:			
		TITLE:			
DRAWN: GARY KISON		DATED: 11/22/06			
CHECKED:		DATED:		157S CHANNEL 70 RECEIVER	
QUALITY CONTROL:		DATED:			
RELEASED:		DATED:			
		SCALE:			
		SIZE:		DRAWING NO:	
		C		ASY-0157-05	
				REV:	
				P2	
				SHEET: OF	