RMS on 50 Ohms). The presence of the external reference above a certain level will lock the Ref Gen clock frequency to that reference.

The "REF MON" SMA Female connector provides a 10MHz low impedance reference signal output at 0dBm (nominal). The output is daisy chained through each Ref Gen + Aux module that requires the clock reference. When daisy chained the Ref Gen + Aux modules will buffer the 10 MHz reference prior to forwarding onto the next Ref Gen + Aux module.

Where more than one sub rack frame is required a Ref Gen + Aux (without the cell modem option) has to be included in each additional sub rack frame. The master Ref Gen + Aux module should have the cell modem option fitted if required. Only one cell modem is required in the master sub rack frame to communicate the alarms from the Master and slave sub rack frames.

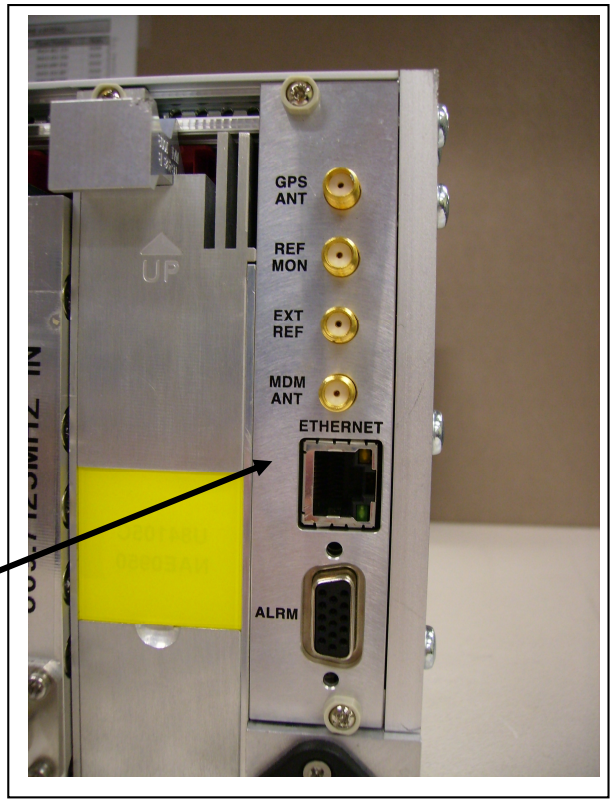

Figure 7 – Ref Gen + Aux Module

3.3.5 DSP – Digital Signal Processor Module

A DSP module is partitioned into Side "A" and Side "B". Channel and multi-band independent a single DSP board Side "A" will service up to 4 bi-direction carriers (uplink and downlink) in two bands. With both sides populated a DSP module is capable of processing up to 8 bi-directional channels in 3 bands.

The DSP module fits into slot 10. This is the only slot allocated to the DSP module in the motherboard architecture. The DSP module does not require a tool to remove it once the screw fasteners have been fully released, there are two ejector lever handles which assist in its removal from the sub rack frame.