

FCC PART 15.247

TEST REPORT

For

Shenzhen Firstview Electronic Co. Ltd.

3-4/F, Block B, Huafeng 1st Technology Zone Baoan Main Road, Baoan District, Shenzhen , China

FCC ID: YW59001H

Report Type: Original Report	Product Type: 9inch tablet PC
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Report Number: RDG160621801-00B	
Report Date: 2016-07-13	
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Note: This test report is prepared for the customer shown above and for the device described herein. It may not be duplicated or used in part without prior written consent from Bay Area Compliance Laboratories Corp. (Dongguan).

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GENERAL INFORMATION

Product Description for Equipment under Test (EUT)

The *Shenzhen Firstview Electronic Co. Ltd.*'s product, model number: *9001h(FCC ID: YW59001H)* (the "EUT") in this report was a *9inch tablet PC*, which was measured approximately: 24.0 cm (L) x 14.3 cm (W) x 1.3 cm (H), rated input voltage: DC3.7V rechargeable Li-ion battery or DC5V charging from adapter.

Adapter Information :

MODEL : HNB050150U

INPUT :AC 100-240V 50/60Hz 0.5A MAX

OUTPUT : DC5V, 1.5A

Note: The series product, model 9001h and M926 are electrically identical, the difference them is the model name, we selected 9001h for fully testing, the details was explained in the declaration letter.

All measurement and test data in this report was gathered from production sample serial number: 1606218001 (Assigned byBACL, Dongguan). The EUT was received on 2016-06-22.

Objective

This report is prepared on behalf of *Shenzhen Firstview Electronic Co. Ltd.* in accordance with Part 2, Subpart J, Part 15, Subparts A, B and C of the Federal Communications Commission's rules

The tests were performed in order to determine the Bluetooth BDR and EDR mode of EUT compliance with FCC Part 15, Subpart C, and section 15.203, 15.205, 15.207, 15.209 and 15.247 rules.

Related Submittal(s)/Grant(s)

FCC Part 15B JBP submissions with FCC ID: YW59001H.

FCC Part 15C DTS submissions with FCC ID: YW59001H.

Test Methodology

All measurements contained in this report were conducted with ANSI C63.10-2013, American National Standard of Procedures for Compliance Testing of Unlicensed Wireless Devices.

All emissions measurement was performed and Bay Area Compliance Laboratories Corp. (Dongguan).

Test Facility

The Test site used by Bay Area Compliance Laboratories Corp. (Dongguan) to collect test data is located on the No.69 Pulongcun, Puxinhu Industrial Zone, Tangxia, Dongguan, Guangdong, China

Test site at Bay Area Compliance Laboratories Corp. (Dongguan) has been fully described in reports submitted to the Federal Communications Commission (FCC). The details of these reports have been found to be in compliance with the requirements of Section 2.948 of the FCC Rules on February 06, 2015.

The Federal Communications Commission has the reports on file and is listed under FCC Registration No.: 273710. The test site has been approved by the FCC for public use and is listed in the FCC Public Access Link (PAL) database.

SYSTEM TEST CONFIGURATION

Description of Test Configuration

The system was configured for testing in engineering mode.

Equipment Modifications

No modification was made to the EUT.

EUT Exercise Software

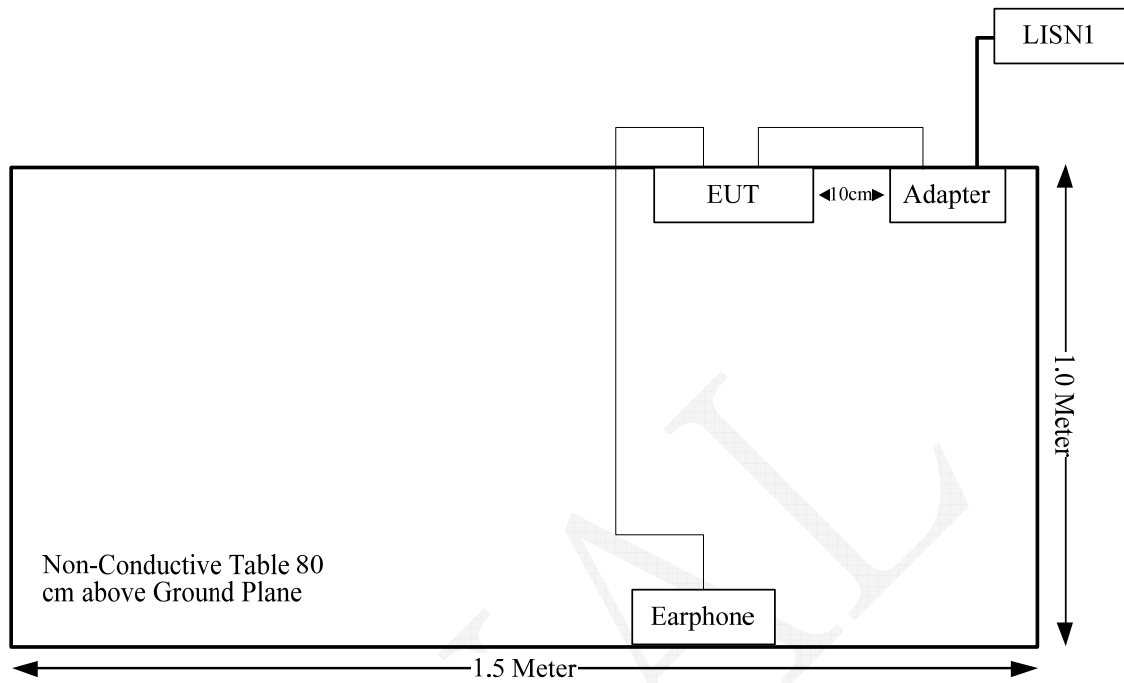
The software Realtek MP test was built in the device, the worst condition (maximum power) was setting by the software as following table:

Test Software Version	Realtek MP Test		
Test Frequency	2402MHz	2441MHz	2480MHz
BDR (GFSK)	7	7	7
EDR ($\pi/4$ -DQPSK)	7	7	7
EDR (8DPSK)	7	7	7

External Cable

Cable Description	Shielding Type	Ferrite Core	Length (m)	From Port	To
USB Cable	Yes	No	1.0	USB Port of Laptop	EUT
Earphone	No	No	1.2	Earphone	EUT

Block Diagram of Test Setup



SUMMARY OF TEST RESULTS

FCC Rules	Description of Test	Result
FCC §15.247 (i) & §1.1310 & §2.1093	RF Exposure	Compliance
§15.203	Antenna Requirement	Compliance
§15.207 (a)	Conducted Emissions	Compliance
§15.205, §15.209, §15.247(d)	Spurious Emissions	Compliance
§15.247 (a)(1)	20 dB Bandwidth	Compliance
§15.247(a)(1)	Channel Separation Test	Compliance
§15.247(a)(1)(iii)	Time of Occupancy (Dwell Time)	Compliance
§15.247(a)(1)(iii)	Quantity of hopping channel Test	Compliance
§15.247(b)(1)	Peak Output Power Measurement	Compliance
§15.247(d)	Band Edges	Compliance

FCC §15.247 (i) & §1.1310 & §2.1093- RF EXPOSURE

Applicable Standard

According to §15.247(i) and §1.1310, systems operating under the provisions of this section shall be operated in a manner that ensure that the public is not exposed to radio frequency energy level in excess of the Commission's guideline.

According to KDB447498 D01 General RF Exposure Guidance v06

The 1-g and 10-g SAR test exclusion thresholds for 100 MHz to 6 GHz at test separation distances ≤ 50 mm are determined by:

$[(\text{max. power of channel, including tune-up tolerance, mW})/(\text{min. test separation distance, mm})] \cdot [\sqrt{f(\text{GHz})}]$
 ≤ 3.0 for 1-g SAR and ≤ 7.5 for 10-g extremity SAR, where

- $f(\text{GHz})$ is the RF channel transmit frequency in GHz
- Power and distance are rounded to the nearest mW and mm before calculation
- The result is rounded to one decimal place for comparison
- 3.0 and 7.5 are referred to as the numeric thresholds in the step 2 below

The test exclusions are applicable only when the minimum test separation distance is ≤ 50 mm and for transmission frequencies between 100 MHz and 6 GHz. When the minimum test separation distance is < 5 mm, a distance of 5 mm according to 5) in section 4.1 is applied to determine SAR test exclusion.

Measurement Result

The maximum power including tolerance is 6.8dBm(4.79mW)

$[(\text{max. power of channel, mW})/(\text{min. test separation distance, mm})][\sqrt{f(\text{GHz})}]$
 $= 4.79/5 \cdot (\sqrt{2.480}) = 1.5 < 3.0$

So the stand-alone SAR evaluation is not necessary.

FCC §15.203 - ANTENNA REQUIREMENT

Applicable Standard

According to FCC § 15.203, an intentional radiator shall be designed to ensure that no antenna other than that furnished by the responsible party shall be used with the device. The use of a permanently attached antenna or of an antenna that uses a unique coupling to the intentional radiator shall be considered sufficient to comply with the provisions of this Section. The manufacturer may design the unit so that a broken antenna can be replaced by the user, but the use of a standard antenna jack or electrical connector is prohibited.

Antenna Connector Construction

The EUT has one internal antenna arrangement, and the antenna gain is 2 dBi, fulfill the requirement of this section. Please refer to the EUT photos.

Result: Compliance.

FCC §15.207 (a) – AC LINE CONDUCTED EMISSIONS

Applicable Standard

FCC§15.207

Measurement Uncertainty

Compliance or non-compliance with a disturbance limit shall be determined in the following manner:

If U_{lab} is less than or equal to U_{cisp} of Table 1, then:

- compliance is deemed to occur if no measured disturbance level exceeds the disturbance limit;
- non-compliance is deemed to occur if any measured disturbance level exceeds the disturbance limit.

If U_{lab} is greater than U_{cisp} of Table 1, then:

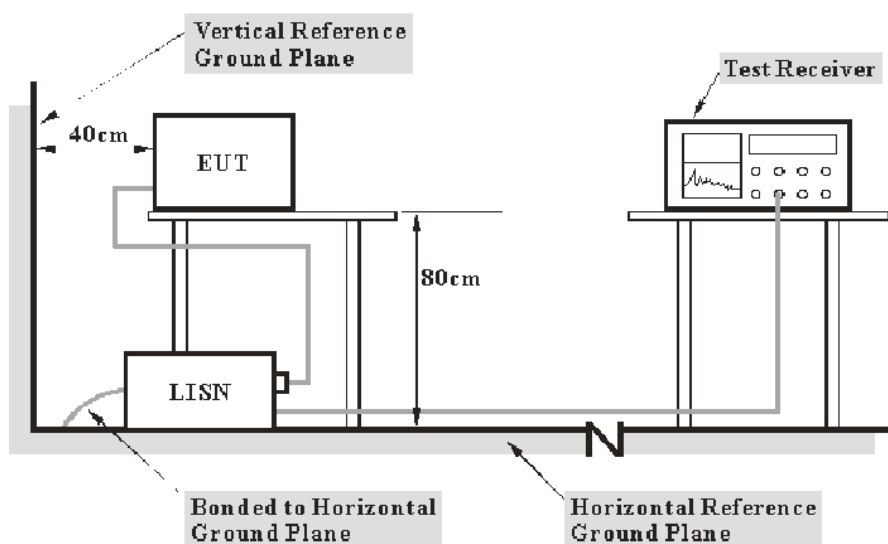
- compliance is deemed to occur if no measured disturbance level, increased by $(U_{lab} - U_{cisp})$, exceeds the disturbance limit;
- non-compliance is deemed to occur if any measured disturbance level, increased by $(U_{lab} - U_{cisp})$, exceeds the disturbance limit.

Based on CISPR 16-4-2: 2011, measurement uncertainty of conducted disturbance at mains port using AMN at Bay Area Compliance Laboratories Corp. (Dongguan) is 3.12 dB (150 kHz to 30 MHz).

Table 1 – Values of U_{cisp}

Measurement	U_{cisp}
Conducted disturbance at mains port using AMN (150 kHz to 30 MHz)	3.4 dB

EUT Setup



Note: 1. Support units were connected to second LISN.
 2. Both of LISNs (AMN) 80 cm from EUT and at the least 80 cm from other units and other metal planes support units.

The setup of EUT is according with per ANSI C63.10-2013 measurement procedure. The specification used was with the FCC Part 15.207 limits.

The spacing between the peripherals was 10 cm.

The adapter was connected to a 120 VAC/60 Hz power source.

EMI Test Receiver Setup

The EMI test receiver was set to investigate the spectrum from 150 kHz to 30 MHz.

During the conducted emission test, the EMI test receiver was set with the following configurations:

Frequency Range	IF B/W
150 kHz – 30 MHz	9 kHz

Test Procedure

During the conducted emission test, the adapter was connected to the outlet of the first LISN.

Maximizing procedure was performed on the six (6) highest emissions of the EUT.

All data was recorded in the Quasi-peak and average detection mode.

Corrected Amplitude & Margin Calculation

The basic equation is as follows:

$$V_C = V_R + A_C + VDF$$

Herein,

V_C : corrected voltage amplitude

V_R : reading voltage amplitude

A_C : attenuation caused by cable loss

VDF: voltage division factor of AMN or ISN

The “**Margin**” column of the following data tables indicates the degree of compliance within the applicable limit. For example, a margin of 7dB means the emission is 7dB below the maximum limit. The equation for margin calculation is as follows:

$$\text{Margin} = \text{Limit} - \text{Corrected Amplitude}$$

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	EMI Test Receiver	ESCS 30	830245/006	2015-10-20	2016-10-20
R&S	L.I.S.N	ESH2-Z5	892107/021	2016-06-09	2017-06-09
R&S	Two-line V-network	ENV 216	3560.6550.12	2015-11-26	2016-11-25
N/A	Coaxial Cable	1.8m	N/A	2016-05-06	2017-05-06
R&S	Test Software	EMC32	Version8.53.0	N/A	N/A

* **Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Results Summary

According to the recorded data in following table, the EUT complied with the FCC Part 15.207, with the worst margin reading of:

11.3 dB at 3.997889 MHz in the Line conducted mode

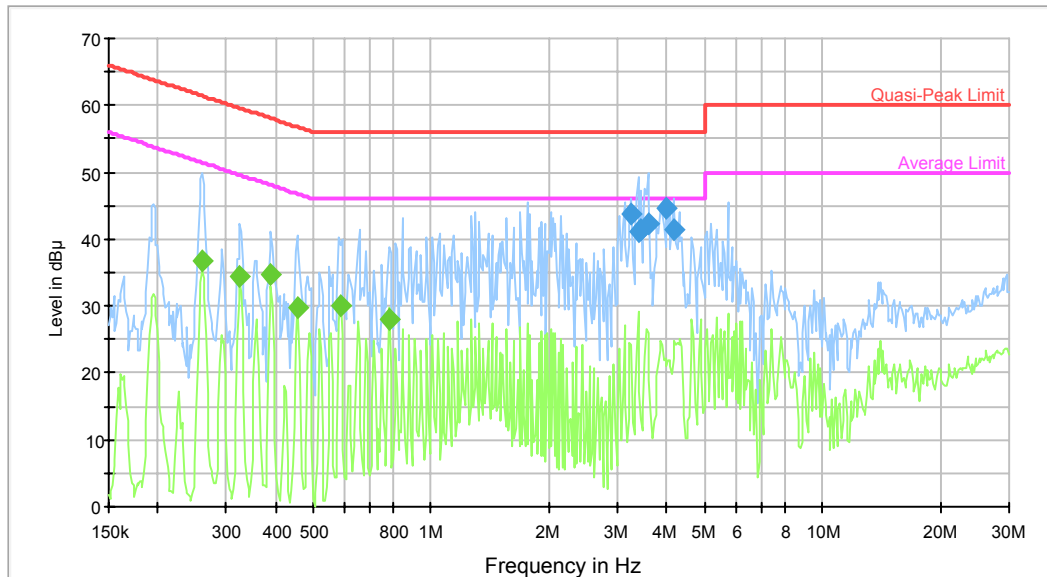
Test Data**Environmental Conditions**

Temperature:	29.4 °C
Relative Humidity:	57 %
ATM Pressure:	100.4 kPa

The testing was performed by Dean Liu on 2016-06-22.

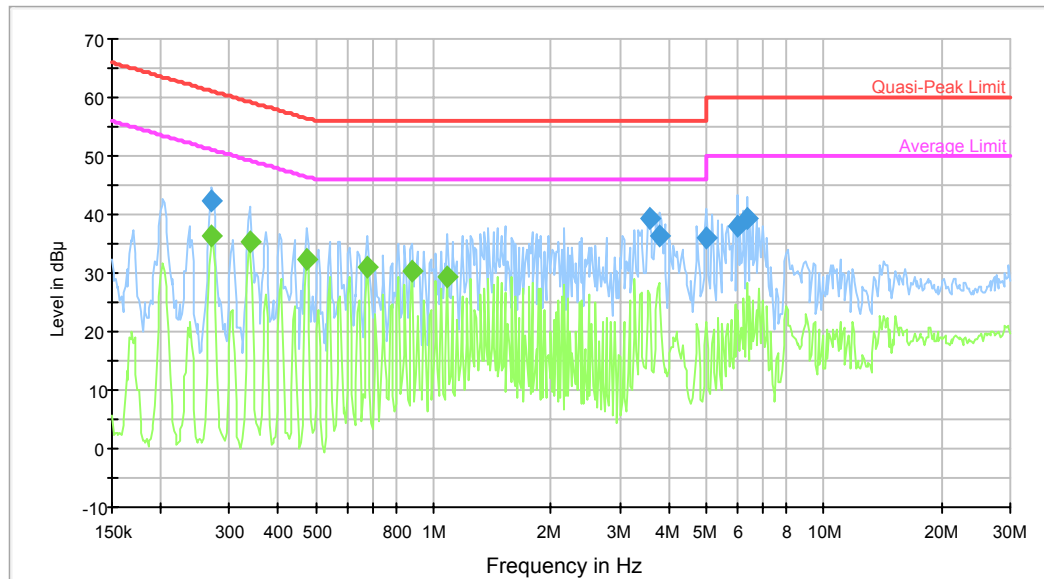
Test Mode: Transmitting

AC120 V, 60 Hz, Line:



Frequency (MHz)	QuasiPeak (dBμV)	Bandwidth (kHz)	Line	Corr. (dB)	Margin (dB)	Limit (dBμV)	Comment
3.249802	43.7	9.000	L1	10.6	12.3	56.0	Compliance
3.381891	41.0	9.000	L1	10.6	15.0	56.0	Compliance
3.436218	41.4	9.000	L1	10.6	14.6	56.0	Compliance
3.575883	42.4	9.000	L1	10.6	13.6	56.0	Compliance
3.997889	44.7	9.000	L1	10.7	11.3	56.0	Compliance
4.160384	41.5	9.000	L1	10.7	14.5	56.0	Compliance

Frequency (MHz)	Average (dBμV)	Bandwidth (kHz)	Line	Corr. (dB)	Margin (dB)	Limit (dBμV)	Comment
0.259937	36.9	9.000	L1	10.2	14.5	51.4	Compliance
0.324910	34.3	9.000	L1	10.3	15.3	49.6	Compliance
0.390261	34.6	9.000	L1	10.2	13.5	48.1	Compliance
0.454052	29.7	9.000	L1	10.1	17.1	46.8	Compliance
0.585926	30.1	9.000	L1	10.2	15.9	46.0	Compliance
0.780588	28.0	9.000	L1	10.4	18.0	46.0	Compliance

AC120 V, 60 Hz, Neutral:

Frequency (MHz)	QuasiPeak (dBμV)	Bandwidth (kHz)	Line	Corr. (dB)	Margin (dB)	Limit (dBμV)	Comment
0.270502	42.5	9.000	N	10.2	18.6	61.1	Compliance
3.575883	39.3	9.000	N	10.6	16.7	56.0	Compliance
3.811251	36.2	9.000	N	10.6	19.8	56.0	Compliance
4.997188	36.0	9.000	N	10.7	20.0	56.0	Compliance
6.002303	38.1	9.000	N	10.7	21.9	60.0	Compliance
6.346607	39.2	9.000	N	10.7	20.8	60.0	Compliance

Frequency (MHz)	Average (dBμV)	Bandwidth (kHz)	Line	Corr. (dB)	Margin (dB)	Limit (dBμV)	Comment
0.270502	36.3	9.000	N	10.2	14.8	51.1	Compliance
0.338116	35.4	9.000	N	10.3	13.8	49.2	Compliance
0.472507	32.2	9.000	N	10.1	14.3	46.5	Compliance
0.676289	30.9	9.000	N	10.4	15.1	46.0	Compliance
0.879690	30.3	9.000	N	10.4	15.7	46.0	Compliance
1.082190	29.5	9.000	N	10.4	16.5	46.0	Compliance

FCC §15.209, §15.205 & §15.247(d) - SPURIOUS EMISSIONS

Applicable Standard

FCC §15.247 (d); §15.209; §15.205;

Measurement Uncertainty

Compliance or non-compliance with a disturbance limit shall be determined in the following manner:

If U_{lab} is less than or equal to U_{cisp} of Table 1, then:

- compliance is deemed to occur if no measured disturbance level exceeds the disturbance limit;
- non-compliance is deemed to occur if any measured disturbance level exceeds the disturbance limit.

If U_{lab} is greater than U_{cisp} of Table 1, then:

- compliance is deemed to occur if no measured disturbance level, increased by $(U_{lab} - U_{cisp})$, exceeds the disturbance limit;
- non-compliance is deemed to occur if any measured disturbance level, increased by $(U_{lab} - U_{cisp})$, exceeds the disturbance limit.

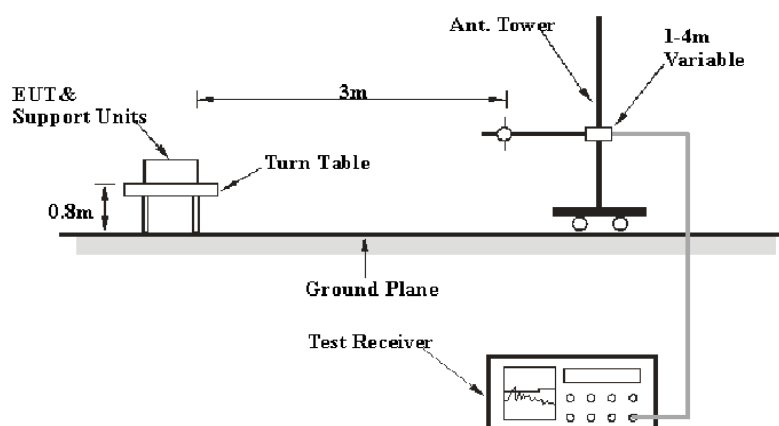
Based on CISPR 16-4-2: 2011, measurement uncertainty of radiated emission at a distance of 3m at Bay Area Compliance Laboratories Corp. (Dongguan) is: 30M~200MHz: 4.58 dB for Horizontal, 4.59 dB for Vertical; 200M~1GHz: 4.83 dB for Horizontal, 5.85 dB for Vertical; 1G~6GHz: 4.45 dB, 6G~18GHz: 5.23 dB

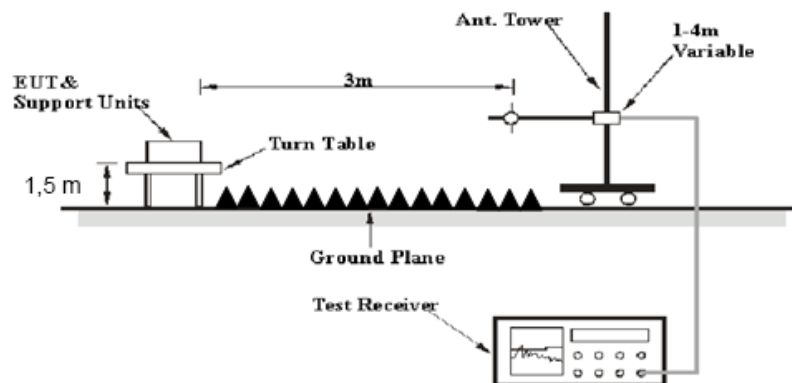
Table 1 – Values of U_{cisp}

Measurement	U_{cisp}
Radiated disturbance (electric field strength at an OATS or in a SAC) (30 MHz to 1000 MHz)	6.3 dB
Radiated disturbance (electric field strength in a FAR) (1 GHz to 6 GHz)	5.2 dB
Radiated disturbance (electric field strength in a FAR) (6 GHz to 18 GHz)	5.5 dB

EUT Setup

Below 1GHz:



Above 1GHz:

The radiated emission tests were performed in the 3 meters test site, using the setup accordance with the ANSI C63.10-2013. The specification used was the FCC 15.209, and FCC 15.247 limits.

The external I/O cables were draped along the test table and formed a bundle 30 to 40 cm long in the middle.

The spacing between the peripherals was 10 cm.

EMI Test Receiver & Spectrum Analyzer Setup

The system was investigated from 30 MHz to 25 GHz.

During the radiated emission test, the EMI test receiver & Spectrum Analyzer Setup were set with the following configurations:

Frequency Range	RBW	Video B/W	IF B/W	Detector
30 MHz – 1000 MHz	120 kHz	300 kHz	120 kHz	QP
Above 1 GHz	1MHz	3 MHz	/	PK
	1MHz	10 Hz	/	AV

Test Procedure

Maximizing procedure was performed on the highest emissions to ensure that the EUT complied with all installation combinations.

Data was recorded in Quasi-peak detection mode for frequency range of 30 MHz - 1 GHz, peak and average detection modes for frequencies above 1 GHz.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	EMI Test Receiver	ESCI	100224	2015-08-03	2016-08-02
Sunol Sciences	Antenna	JB3	A060611-3	2014-11-06	2017-11-05
HP	Amplifier	8447E	2434A02181	2015-09-01	2016-09-01
Agilent	Spectrum Analyzer	E4440A	SG43360054	2015-11-23	2016-11-22
ETS-Lindgren	Horn Antenna	3115	9808-5557	2015-09-06	2018-09-06
Mini-Circuit	Amplifier	ZVA-213-S+	054201245	2016-02-19	2017-02-19
R&S	Spectrum Analyzer	FSP 38	100478	2015-11-23	2016-11-22
Ducommun Technologies	Horn Antenna	ARH-4223-02	1007726-01 1304	2014-06-16	2017-06-15
Quinstar	Amplifier	QLW-18405536-JO	15964001001	2015-09-06	2016-09-06
N/A	Coaxial Cable	14m	N/A	2016-05-06	2017-05-06
N/A	Coaxial Cable	8m	N/A	2016-05-06	2017-05-06
N/A	Coaxial Cable	0.1m	N/A	2016-05-06	2017-05-06
E-Microwave	DC Blocking	EMDCB-00036	0E01201047	2016-05-06	2017-05-06

* **Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Corrected Amplitude & Margin Calculation

The Corrected Amplitude is calculated by adding the Antenna Factor and Cable Loss, and subtracting the Amplifier Gain from the Meter Reading. The basic equation is as follows:

$$\text{Corrected Amplitude} = \text{Meter Reading} + \text{Antenna Factor} + \text{Cable Loss} - \text{Amplifier Gain}$$

The “**Margin**” column of the following data tables indicates the degree of compliance with the applicable limit. For example, a margin of 7dB means the emission is 7dB below the limit. The equation for margin calculation is as follows:

$$\text{Margin} = \text{Limit} - \text{Corrected Amplitude}$$

Test Results Summary

According to the recorded data in following table, the EUT complied with the FCC Title 47, Part 15, Subpart C, and section 15.205, 15.209 and 15.247, with the worst margin reading of:

5.5 dB at 42.6100 MHz in the Vertical polarization

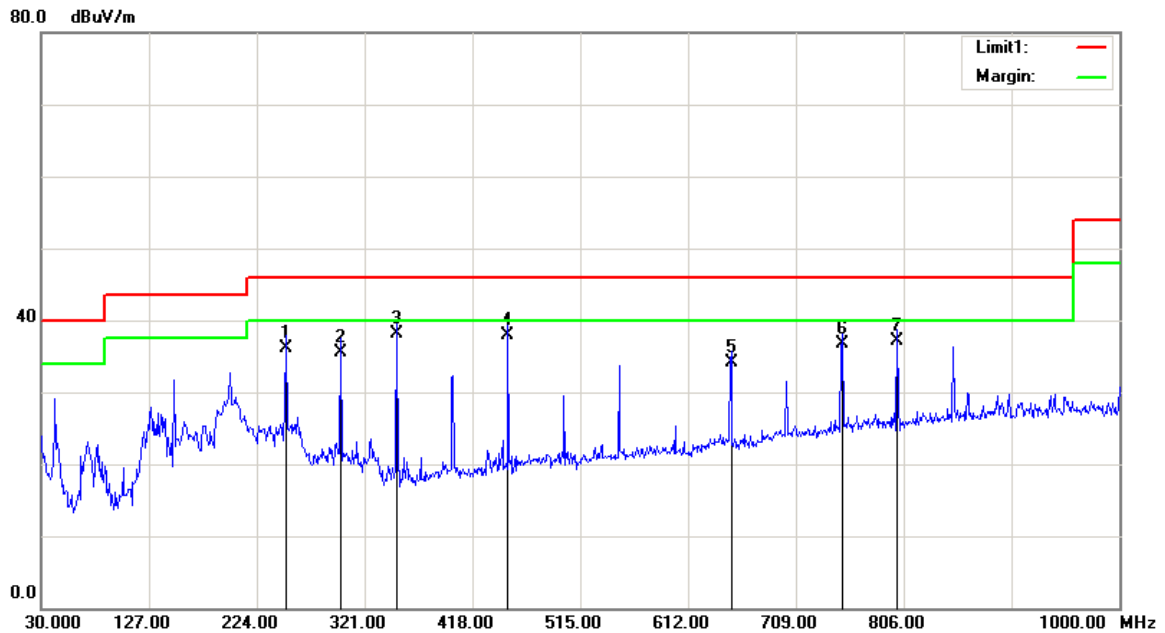
Test Data**Environmental Conditions**

Temperature:	27.1 °C
Relative Humidity:	48 %
ATM Pressure:	100.1 kPa

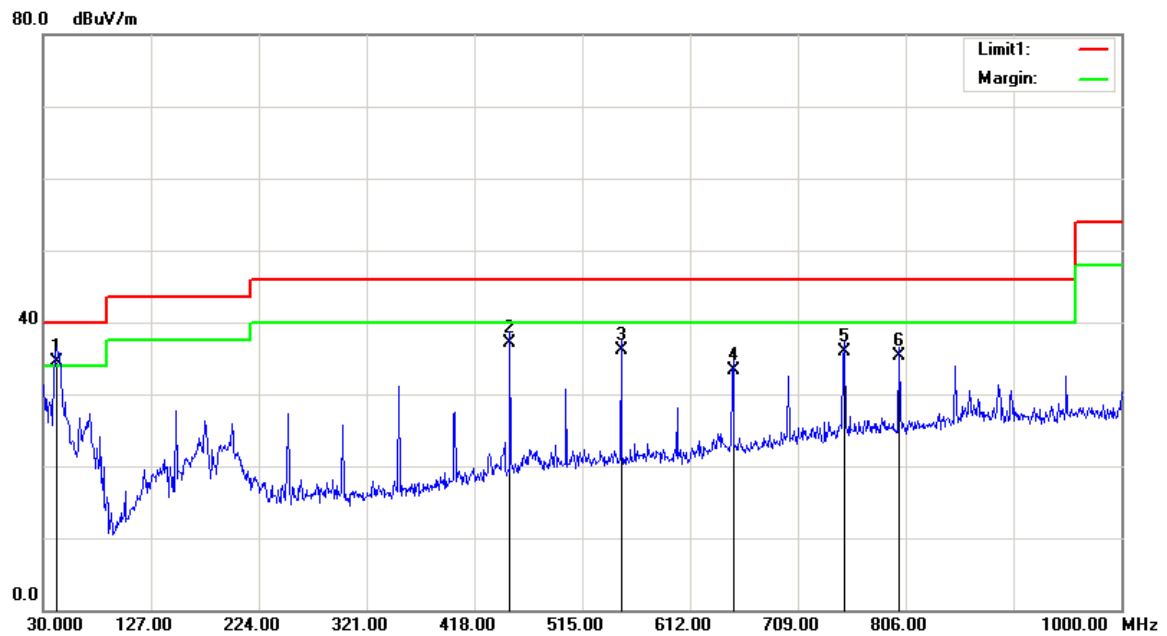
The testing was performed by Dean Liu on 2016-06-26.

1) 30MHz-1GHz(Middle channel is the worst)

Horizontal



Frequency (MHz)	Receiver Reading (dBμV)	Detector	Correction Factor (dB/m)	Cord. Amp. (dBμV/m)	Limit (dBμV/m)	Margin (dB)
250.1900	43.87	QP	-7.67	36.20	46.00	9.80
299.6600	41.40	QP	-5.80	35.60	46.00	10.40
350.1000	42.88	QP	-4.68	38.20	46.00	7.80
450.0100	40.39	QP	-2.49	37.90	46.00	8.10
650.8000	33.70	QP	0.40	34.10	46.00	11.90
750.7100	34.43	QP	2.37	36.80	46.00	9.20
800.1800	34.05	QP	3.15	37.20	46.00	8.80

Vertical

Frequency (MHz)	Receiver Reading (dBμV)	Detector	Correction Factor (dB/m)	Cord. Amp. (dBμV/m)	Limit (dBμV/m)	Margin (dB)
42.6100	42.84	QP	-8.34	34.50	40.00	5.50
450.0100	39.69	QP	-2.49	37.20	46.00	8.80
549.9200	37.29	QP	-1.19	36.10	46.00	9.90
650.8000	32.90	QP	0.40	33.30	46.00	12.70
750.7100	33.63	QP	2.37	36.00	46.00	10.00
800.1800	32.25	QP	3.15	35.40	46.00	10.60

Test Mode: Transmitting

BDR Mode (GFSK):

Frequency	Receiver		Rx Antenna		Cable loss (dB)	Amplifier Gain (dB)	Corrected Amplitude (dBμV/m)	FCC 15.247	
(MHz)	Reading (dBμV)	Detector (PK/QP/AV)	Polar (H/V)	Factor (dB)				Limit (dBμV/m)	Margin (dB)
Low Channel: 2402 MHz									
2402	66.16	PK	H	25.65	3.66	0.00	95.47	N/A	N/A
2402	65.75	AV	H	25.65	3.66	0.00	95.06	N/A	N/A
2402	68.61	PK	V	25.65	3.66	0.00	97.92	N/A	N/A
2402	68.12	AV	V	25.65	3.66	0.00	97.43	N/A	N/A
2390	15.12	PK	V	25.61	3.63	0.00	44.36	74.00	29.64
2390	13.3	AV	V	25.61	3.63	0.00	42.54	54.00	11.46
4804	30.81	PK	V	30.59	5.06	27.41	39.05	74.00	34.95
4804	18.46	AV	V	30.59	5.06	27.41	26.70	54.00	27.30
7206	29.97	PK	V	34.09	6.61	25.91	44.76	74.00	29.24
7206	17.56	AV	V	34.09	6.61	25.91	32.35	54.00	21.65
3100	35.43	PK	V	27.52	6.84	27.45	42.34	74.00	31.66
3100	22.85	AV	V	27.52	6.84	27.45	29.76	54.00	24.24
Middle Channel: 2441 MHz									
2441	66.01	PK	H	25.75	3.76	0.00	95.52	N/A	N/A
2441	65.73	AV	H	25.75	3.76	0.00	95.24	N/A	N/A
2441	68.49	PK	V	25.75	3.76	0.00	98.00	N/A	N/A
2441	67.73	AV	V	25.75	3.76	0.00	97.24	N/A	N/A
4882	30.69	PK	V	30.79	5.19	27.42	39.25	74.00	34.75
4882	18.36	AV	V	30.79	5.19	27.42	26.92	54.00	27.08
7323	30.22	PK	V	34.38	6.75	25.88	45.47	74.00	28.53
7323	17.79	AV	V	34.38	6.75	25.88	33.04	54.00	20.96
3100	35.23	PK	V	27.52	6.84	27.45	42.14	74.00	31.86
3100	22.49	AV	V	27.52	6.84	27.45	29.40	54.00	24.60
4315	32.9	PK	V	29.84	4.92	26.97	40.69	74.00	33.31
4315	20.51	AV	V	29.84	4.92	26.97	28.30	54.00	25.70
High Channel: 2480 MHz									
2480	65.83	PK	H	25.85	3.68	0.00	95.36	N/A	N/A
2480	65.51	AV	H	25.85	3.68	0.00	95.04	N/A	N/A
2480	68.06	PK	V	25.85	3.68	0.00	97.59	N/A	N/A
2480	67.28	AV	V	25.85	3.68	0.00	96.81	N/A	N/A
2483.5	27.09	PK	V	25.86	3.67	0.00	56.62	74.00	17.38
2483.5	14.14	AV	V	25.86	3.67	0.00	43.67	54.00	10.33
4960	30.3	PK	V	31.00	5.34	27.43	39.21	74.00	34.79
4960	18.12	AV	V	31.00	5.34	27.43	27.03	54.00	26.97
7440	30.08	PK	V	34.66	6.89	25.97	45.66	74.00	28.34
7440	17.69	AV	V	34.66	6.89	25.97	33.27	54.00	20.73
3100	34.49	PK	V	27.52	6.84	27.45	41.40	74.00	32.60

EDR Mode ($\pi/4$ -DQPSK):

Frequency (MHz)	Receiver		Rx Antenna		Cable loss (dB)	Amplifier Gain (dB)	Corrected Amplitude (dB μ V/m)	FCC 15.247	
	Reading (dB μ V)	Detector (PK/QP/AV)	Polar (H/V)	Factor (dB)				Limit (dB μ V/m)	Margin (dB)
Low Channel: 2402 MHz									
2402	66.57	PK	H	25.65	3.66	0.00	95.88	N/A	N/A
2402	63.45	AV	H	25.65	3.66	0.00	92.76	N/A	N/A
2402	68.69	PK	V	25.65	3.66	0.00	98.00	N/A	N/A
2402	65.53	AV	V	25.65	3.66	0.00	94.84	N/A	N/A
2390	25.19	PK	V	25.61	3.63	0.00	54.43	74.00	19.57
2390	13.32	AV	V	25.61	3.63	0.00	42.56	54.00	11.44
4804	30.38	PK	V	30.59	5.06	27.41	38.62	74.00	35.38
4804	18.11	AV	V	30.59	5.06	27.41	26.35	54.00	27.65
7206	29.32	PK	V	34.09	6.61	25.91	44.11	74.00	29.89
7206	16.75	AV	V	34.09	6.61	25.91	31.54	54.00	22.46
3100	34.97	PK	V	27.52	6.84	27.45	41.88	74.00	32.12
3100	32.48	AV	V	27.52	6.84	27.45	39.39	54.00	14.61
Middle Channel: 2441 MHz									
2441	66.83	PK	H	25.75	3.76	0.00	96.34	N/A	N/A
2441	63.76	AV	H	25.75	3.76	0.00	93.27	N/A	N/A
2441	69.08	PK	V	25.75	3.76	0.00	98.59	N/A	N/A
2441	65.95	AV	V	25.75	3.76	0.00	95.46	N/A	N/A
4882	30.74	PK	V	30.79	5.19	27.42	39.30	74.00	34.70
4882	18.38	AV	V	30.79	5.19	27.42	26.94	54.00	27.06
7323	29.91	PK	V	34.38	6.75	25.88	45.16	74.00	28.84
7323	17.41	AV	V	34.38	6.75	25.88	32.66	54.00	21.34
3100	34.61	PK	V	27.52	6.84	27.45	41.52	74.00	32.48
3100	27.23	AV	V	27.52	6.84	27.45	34.14	54.00	19.86
4315	33.65	PK	V	29.84	4.92	26.97	41.44	74.00	32.56
4315	21.28	AV	V	29.84	4.92	26.97	29.07	54.00	24.93
High Channel: 2480 MHz									
2480	66.86	PK	H	25.85	3.68	0.00	96.39	N/A	N/A
2480	63.79	AV	H	25.85	3.68	0.00	93.32	N/A	N/A
2480	69.08	PK	V	25.85	3.68	0.00	98.61	N/A	N/A
2480	66.04	AV	V	25.85	3.68	0.00	95.57	N/A	N/A
2483.5	26.18	PK	V	25.86	3.67	0.00	55.71	74.00	18.29
2483.5	14.31	AV	V	25.86	3.67	0.00	43.84	54.00	10.16
4960	30.77	PK	V	31.00	5.34	27.43	39.68	74.00	34.32
4960	18.35	AV	V	31.00	5.34	27.43	27.26	54.00	26.74
7440	30.24	PK	V	34.66	6.89	25.97	45.82	74.00	28.18
7440	17.69	AV	V	34.66	6.89	25.97	33.27	54.00	20.73
3100	33.92	PK	V	27.52	6.84	27.45	40.83	74.00	33.17
3100	21.67	AV	V	27.52	6.84	27.45	28.58	54.00	25.42

EDR Mode (8-DPSK):

Frequency (MHz)	Receiver		Rx Antenna		Cable loss (dB)	Amplifier Gain (dB)	Corrected Amplitude (dBμV/m)	FCC 15.247	
	Reading (dBμV)	Detector (PK/QP/AV)	Polar (H/V)	Factor (dB)				Limit (dBμV/m)	Margin (dB)
Low Channel: 2402 MHz									
2402	66.67	PK	H	25.65	3.66	0.00	95.98	N/A	N/A
2402	63.45	AV	H	25.65	3.66	0.00	92.76	N/A	N/A
2402	69.49	PK	V	25.65	3.66	0.00	98.80	N/A	N/A
2402	66.3	AV	V	25.65	3.66	0.00	95.61	N/A	N/A
2390	25.17	PK	V	25.61	3.63	0.00	54.41	74.00	19.59
2390	13.29	AV	V	25.61	3.63	0.00	42.53	54.00	11.47
4804	30.51	PK	V	30.59	5.06	27.41	38.75	74.00	35.25
4804	18.19	AV	V	30.59	5.06	27.41	26.43	54.00	27.57
7206	29.58	PK	V	34.09	6.61	25.91	44.37	74.00	29.63
7206	17.26	AV	V	34.09	6.61	25.91	32.05	54.00	21.95
3100	33.85	PK	V	27.52	6.84	27.45	40.76	74.00	33.24
3100	21.49	AV	V	27.52	6.84	27.45	28.40	54.00	25.60
Middle Channel: 2441 MHz									
2441	67.22	PK	H	25.75	3.76	0.00	96.73	N/A	N/A
2441	64.04	AV	H	25.75	3.76	0.00	93.55	N/A	N/A
2441	69.47	PK	V	25.75	3.76	0.00	98.98	N/A	N/A
2441	66.18	AV	V	25.75	3.76	0.00	95.69	N/A	N/A
4882	31.04	PK	V	30.79	5.19	27.42	39.60	74.00	34.40
4882	18.73	AV	V	30.79	5.19	27.42	27.29	54.00	26.71
7323	30.14	PK	V	34.38	6.75	25.88	45.39	74.00	28.61
7323	17.84	AV	V	34.38	6.75	25.88	33.09	54.00	20.91
3100	34.32	PK	V	27.52	6.84	27.45	41.23	74.00	32.77
3100	21.96	AV	V	27.52	6.84	27.45	28.87	54.00	25.13
4315	33.29	PK	V	29.84	4.92	26.97	41.08	74.00	32.92
4315	20.83	AV	V	29.84	4.92	26.97	28.62	54.00	25.38
High Channel: 2480 MHz									
2480	67.53	PK	H	25.85	3.68	0.00	97.06	N/A	N/A
2480	64.24	AV	H	25.85	3.68	0.00	93.77	N/A	N/A
2480	69.34	PK	V	25.85	3.68	0.00	98.87	N/A	N/A
2480	65.96	AV	V	25.85	3.68	0.00	95.49	N/A	N/A
2483.5	25.18	PK	V	25.86	3.67	0.00	54.71	74.00	19.29
2483.5	14.26	AV	V	25.86	3.67	0.00	43.79	54.00	10.21
4960	31.21	PK	V	31.00	5.34	27.43	40.12	74.00	33.88
4960	19.06	AV	V	31.00	5.34	27.43	27.97	54.00	26.03
7440	30.6	PK	V	34.66	6.89	25.97	46.18	74.00	27.82
7440	18.35	AV	V	34.66	6.89	25.97	33.93	54.00	20.07
3100	34.41	PK	V	27.52	6.84	27.45	41.32	74.00	32.68
3100	22.18	AV	V	27.52	6.84	27.45	29.09	54.00	24.91

FCC §15.247(a) (1) - CHANNEL SEPARATION TEST

Applicable Standard

Frequency hopping systems shall have hopping channel carrier frequencies separated by a minimum of 25 kHz or the 20dB bandwidth of the hopping channel, whichever is greater. Alternatively, frequency hopping systems operating in the 2400-2483.50 MHz band may have hopping channel carrier frequencies that are separated by 25 kHz or two-thirds of the 20dB bandwidth of the hopping channel, whichever is greater provided the systems operate with an output power no greater than 125 mW.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2015-11-23	2016-11-22
N/A	Coaxial Cable	0.1m	N/A	2016-05-06	2017-05-06
E-Microwave	DC Blocking	EMDCB-00036	0E01201047	2016-05-06	2017-05-06

* **Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Procedure

1. Set the EUT in transmitting mode, spectrum Bandwidth was set at 30 kHz, maxhold the channel.
2. Set the adjacent channel of the EUT maxhold another trace.
3. Measure the channel separation.

Test Data

Environmental Conditions

Temperature:	28.5 °C
Relative Humidity:	39 %
ATM Pressure:	100.1 kPa

The testing was performed by Dean Liu on 2016-06-26.

Test Result: Compliance.

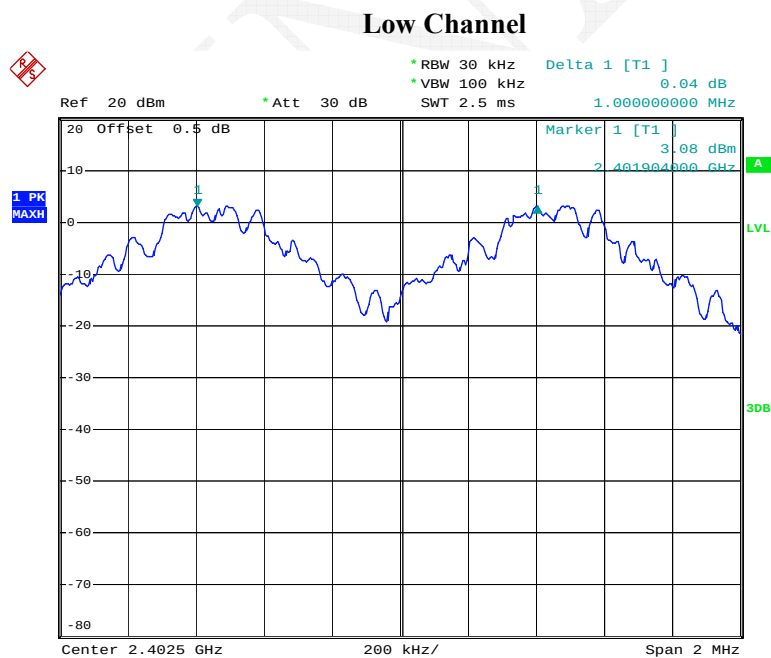
Please refer to following tables and plots

Test Mode: Transmitting

Mode	Channel	Frequency	Channel Separation	Limit	Result
		MHz	MHz	MHz	
BDR (GFSK)	Low	2402	1.000	0.69	Compliance
	Adjacent	2403			
	Middle	2441	1.004	0.69	
	Adjacent	2442			
	High	2480	1.004	0.69	
	Adjacent	2479			
EDR ($\pi/4$ -DQPSK)	Low	2402	1.000	0.90	Compliance
	Adjacent	2403			
	Middle	2441	1.000	0.91	
	Adjacent	2442			
	High	2480	1.000	0.91	
	Adjacent	2479			
EDR (8DPSK)	Low	2402	1.000	0.91	Compliance
	Adjacent	2403			
	Middle	2441	1.004	0.91	
	Adjacent	2442			
	High	2480	1.000	0.91	
	Adjacent	2479			

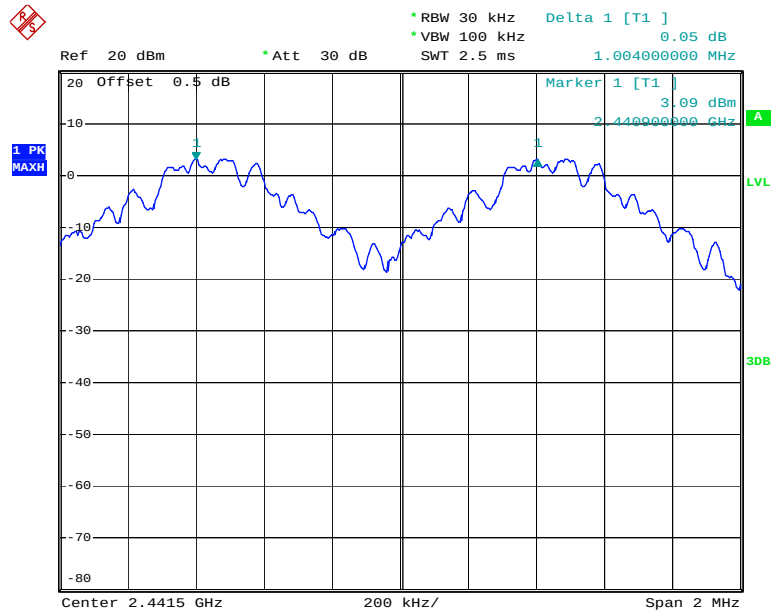
Note: Limit = $(2/3) \times 20\text{dB}$ bandwidth

BDR Mode (GFSK):



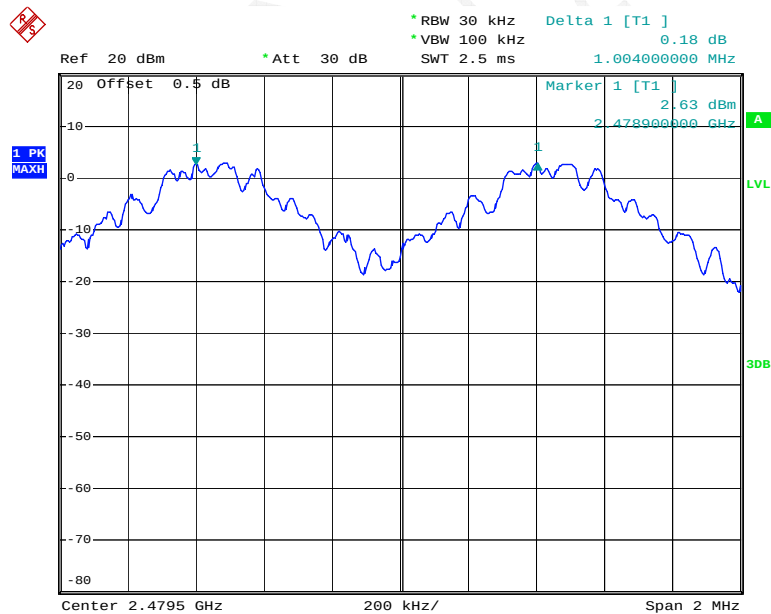
Date: 26.JUN.2016 11:43:36

Middle Channel



Date: 26.JUN.2016 11:44:53

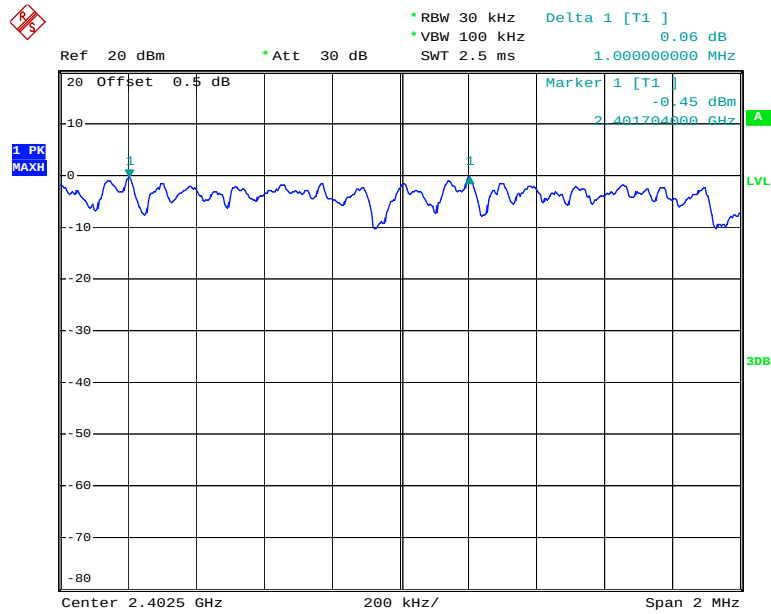
High Channel



Date: 26.JUN.2016 11:46:23

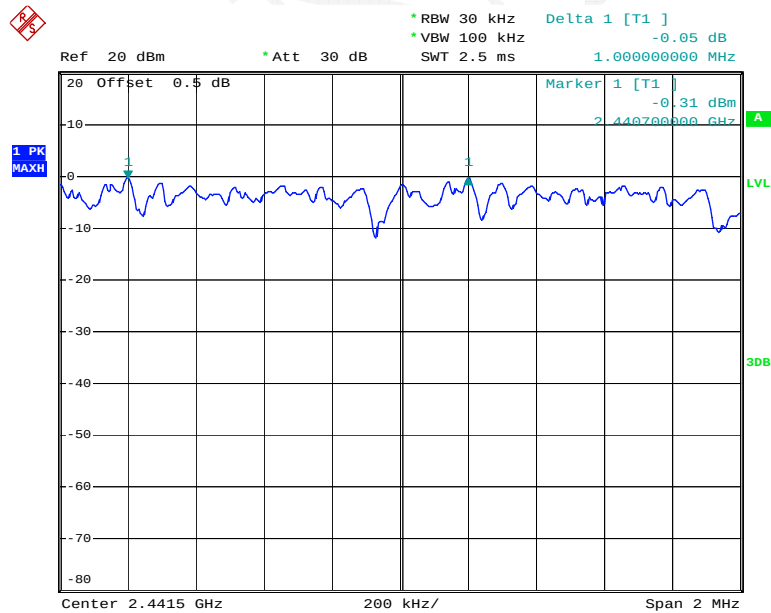
EDR Mode ($\pi/4$ -DQPSK):

Low Channel



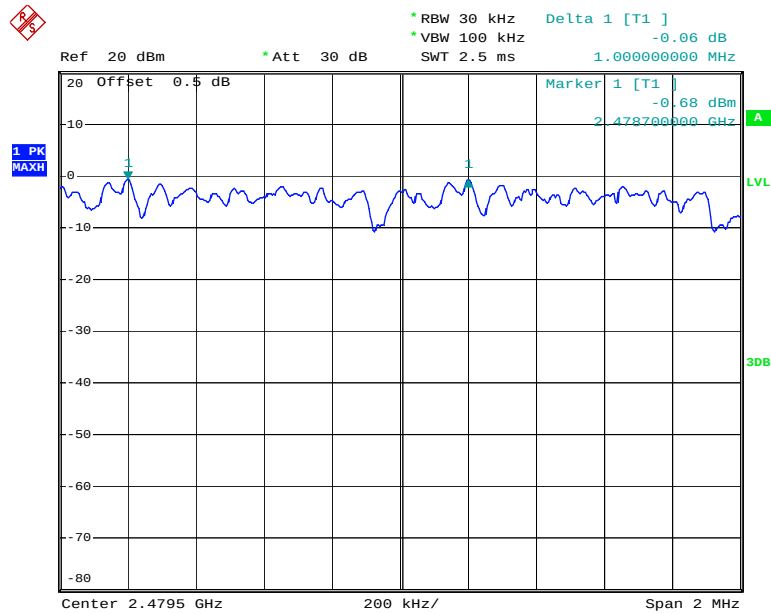
Date: 26.JUN.2016 11:48:07

Middle Channel



Date: 26.JUN.2016 11:49:59

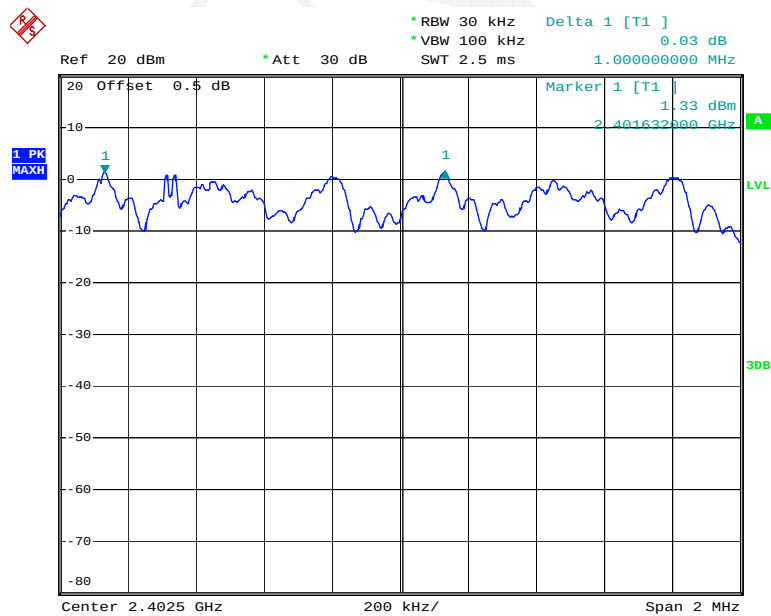
High Channel



Date: 26.JUN.2016 11:52:04

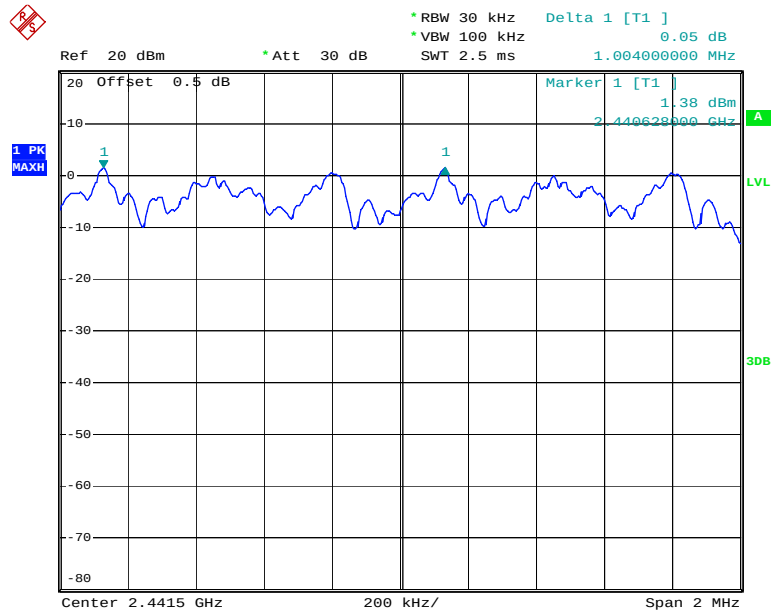
EDR Mode (8-DPSK):

Low Channel



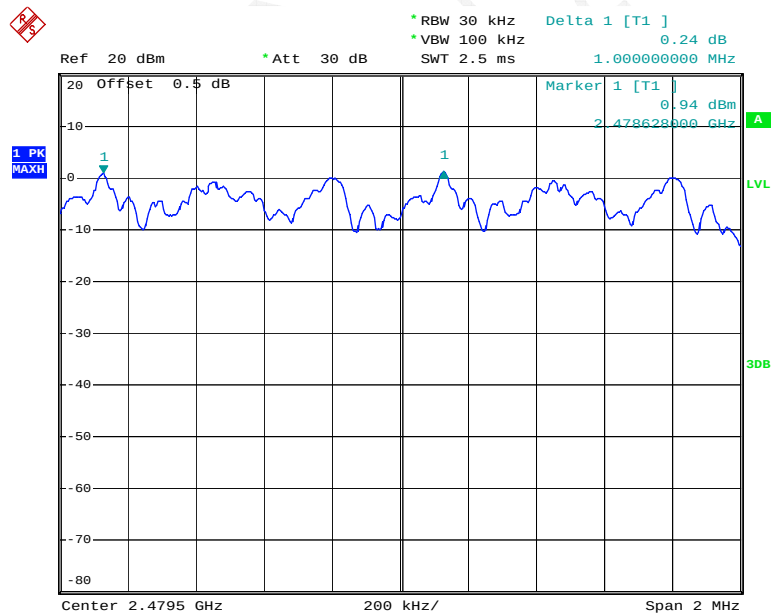
Date: 26.JUN.2016 11:53:21

Middle Channel



Date: 26.JUN.2016 11:54:30

High Channel



Date: 26.JUN.2016 11:56:36

FCC §15.247(a) (1) – 20 dB BANDWIDTH TESTING

Applicable Standard

Alternatively, frequency hopping systems operating in the 2400–2483.5 MHz band may have hopping channel carrier frequencies that are separated by 25 kHz or two-thirds of the 20 dB bandwidth of the hopping channel, whichever is greater, provided the systems operate with an output power no greater than 125 mW.

Test Procedure

1. Check the calibration of the measuring instrument using either an internal calibrator or a known signal from an external generator.
2. Position the EUT on the test table without connection to measurement instrument. Turn on the EUT. Then set it to any one convenient frequency within its operating range. Set a reference level on the measuring instrument equal to the highest peak value.
3. Measure the frequency difference of two frequencies that were attenuated 20 dB from the reference level. Record the frequency difference as the emission bandwidth.
4. Repeat above procedures until all frequencies measured were complete.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2015-11-23	2016-11-22
N/A	Coaxial Cable	0.1m	N/A	2016-05-06	2017-05-06
E-Microwave	DC Blocking	EMDCB-00036	0E01201047	2016-05-06	2017-05-06

* **Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Data

Environmental Conditions

Temperature:	28.5 °C
Relative Humidity:	39 %
ATM Pressure:	100.1 kPa

The testing was performed by Dean Liu on 2016-06-26.

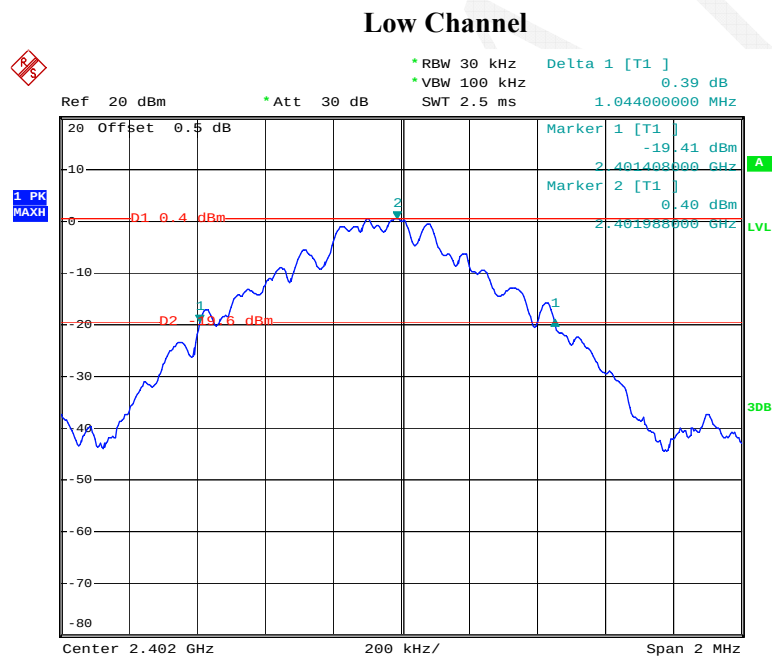
Test Result: Compliance.

Please refer to following tables and plots

Test Mode: Transmitting

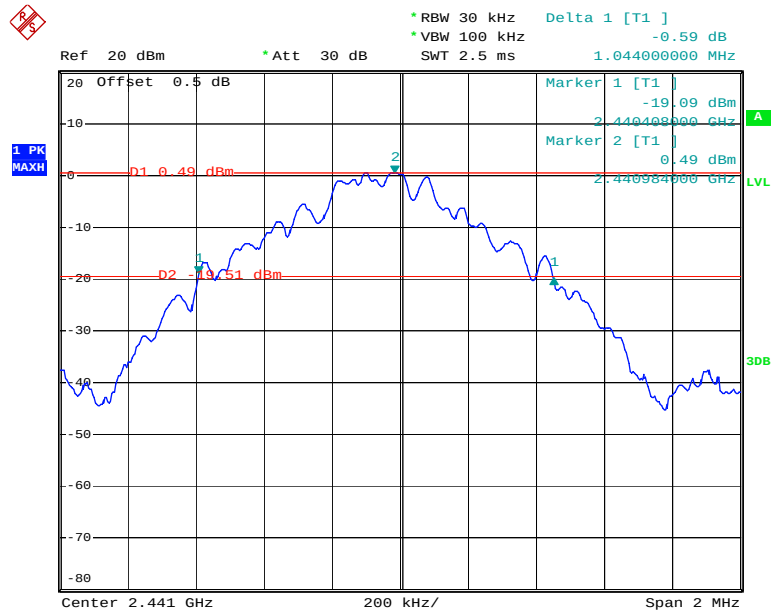
Mode	Channel	Frequency (MHz)	20 dB Bandwidth (MHz)
BDR Mode (GFSK)	Low	2402	1.04
	Middle	2441	1.04
	High	2480	1.04
EDR Mode ($\pi/4$ -DQPSK):	Low	2402	1.35
	Middle	2441	1.36
	High	2480	1.36
EDR Mode (8-DPSK):	Low	2402	1.37
	Middle	2441	1.37
	High	2480	1.37

BDR Mode (GFSK):



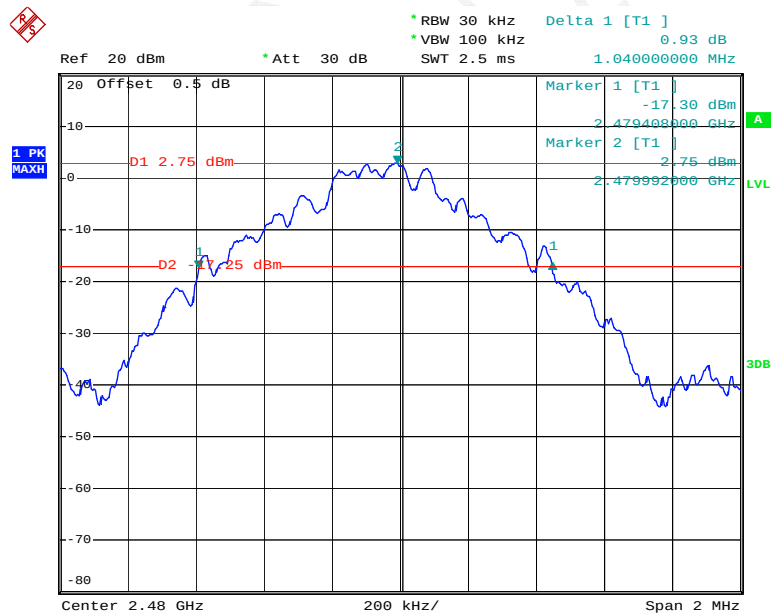
Date: 26.JUN.2016 11:15:44

Middle Channel



Date: 26.JUN.2016 11:27:57

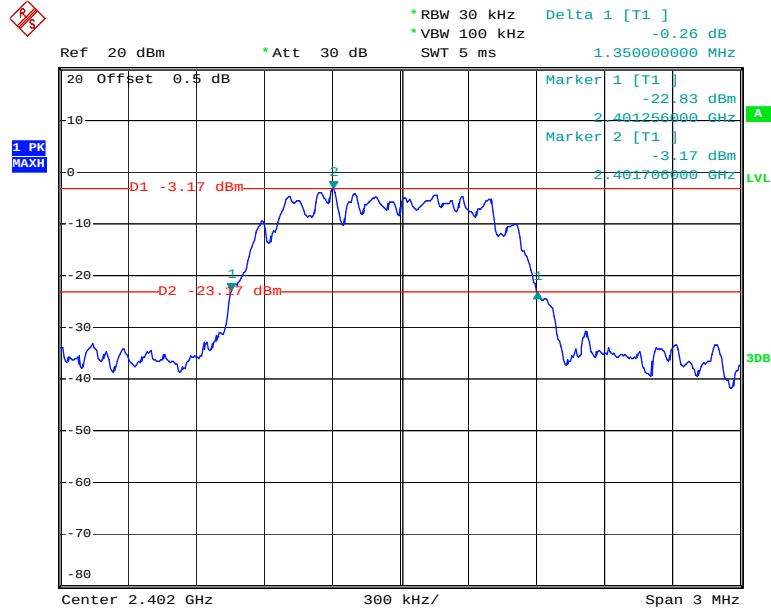
High Channel



Date: 26.JUN.2016 11:29:40

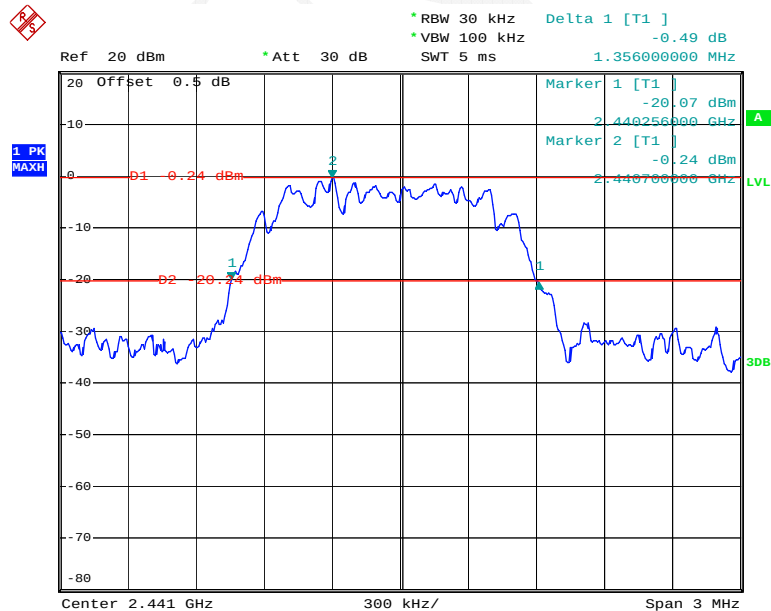
EDR Mode ($\pi/4$ -DQPSK):

Low Channel



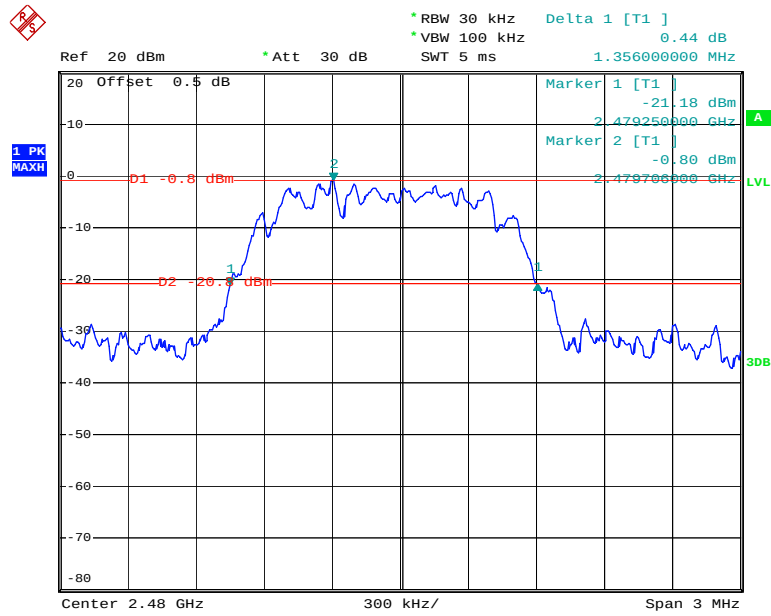
Date: 26.JUN.2016 11:31:20

Middle Channel



Date: 26.JUN.2016 11:38:14

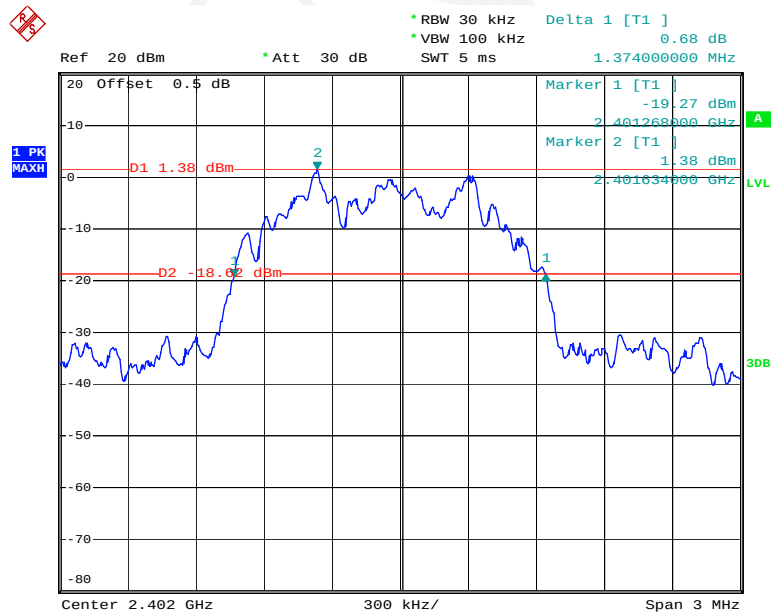
High Channel



Date: 26.JUN.2016 11:34:33

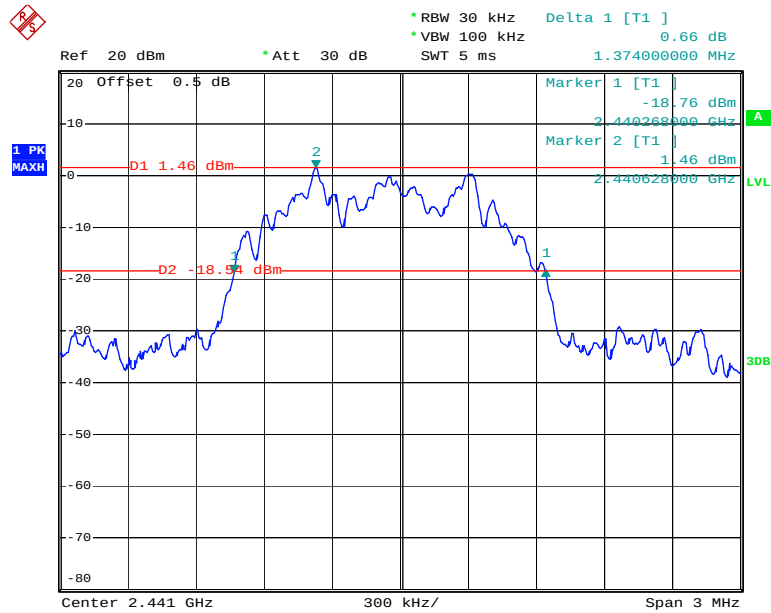
EDR Mode (8-DPSK):

Low Channel



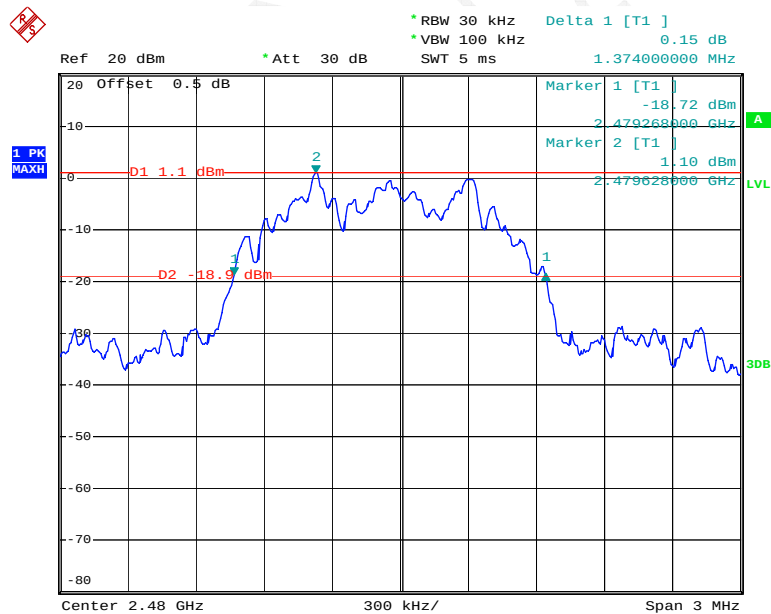
Date: 26.JUN.2016 11:36:27

Middle Channel



Date: 26.JUN.2016 11:39:33

High Channel



Date: 26.JUN.2016 11:40:41

FCC §15.247(a) (1) (iii) - QUANTITY OF HOPPING CHANNEL TEST

Applicable Standard

Frequency hopping systems in the 2400–2483.5 MHz band shall use at least 15 channels. The average time of occupancy on any channel shall not be greater than 0.4 seconds within a period of 0.4 seconds multiplied by the number of hopping channels employed. Frequency hopping systems may avoid or suppress transmissions on a particular hopping frequency provided that a minimum of 15 channels are used.

Test Procedure

1. Check the calibration of the measuring instrument (SA) using either an internal calibrator or a known signal from an external generator.
2. Set the EUT in hopping mode from first channel to last.
3. By using the Max-Hold function record the Quantity of the channel.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2015-11-23	2016-11-22
N/A	Coaxial Cable	0.1m	N/A	2016-05-06	2017-05-06
E-Microwave	DC Blocking	EMDCB-00036	0E01201047	2016-05-06	2017-05-06

* **Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Data

Environmental Conditions

Temperature:	27.9 °C
Relative Humidity:	46 %
ATM Pressure:	100 kPa

The testing was performed by Dean Liu on 2016-07-07.

Test Result: Compliance.

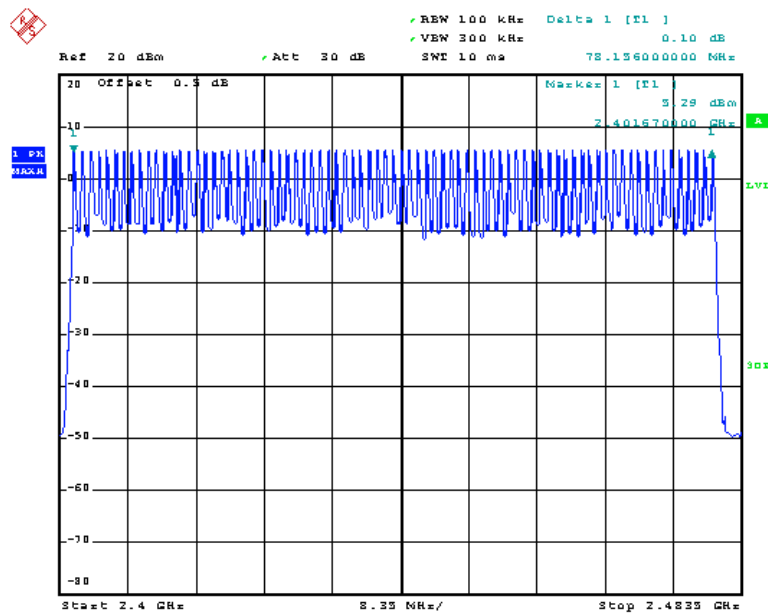
Please refer to following tables and plots

Test Mode: Transmitting

BDR Mode (GFSK):

Frequency Range (MHz)	Number of Hopping Channel	Limit
2400-2483.5	79	≥ 15

Number of Hopping Channels

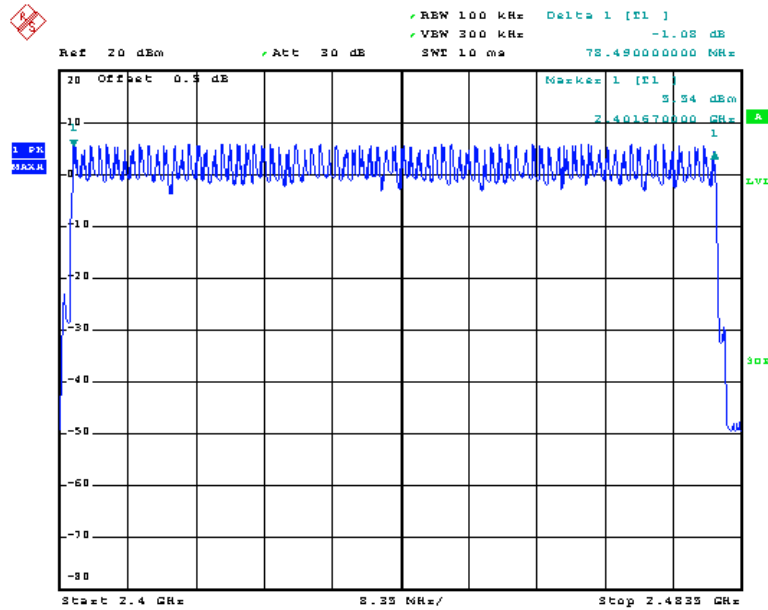


Date: 7.JUL.2016 22:52:51

EDR Mode ($\pi/4$ -DQPSK):

Frequency Range (MHz)	Number of Hopping Channel	Limit
2400-2483.5	79	≥ 15

Number of Hopping Channels

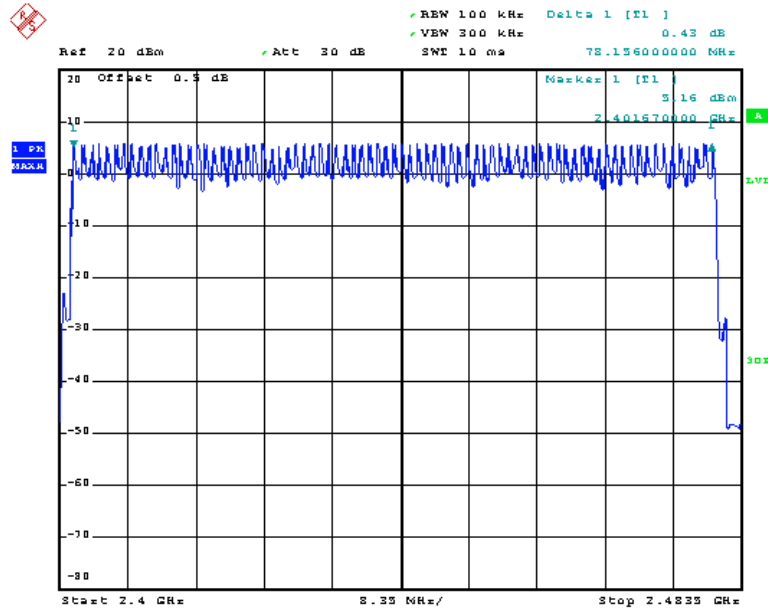


Date: 7.JUL.2016 23:40:30

EDR Mode (8-DPSK):

Frequency Range (MHz)	Number of Hopping Channel	Limit
2400-2483.5	79	≥15

Number of Hopping Channels



Date: 7.JUL.2016 23:47:38

FCC §15.247(a) (1) (iii) - TIME OF OCCUPANCY (DWELL TIME)**Applicable Standard**

Frequency hopping systems in the 2400-2483.5 MHz shall use at least 15 channels. The average time of occupancy on any channel shall not be greater than 0.4 seconds within a period of 0.4 seconds multiplied by the number of hopping channels employed. Frequency hopping systems may avoid or suppress transmissions on a particular hopping frequency provided that a minimum of 15 channels are used.

Test Procedure

The EUT was worked in channel hopping; Spectrum SPAN was set as 0. Sweep was set as $0.4 \times \text{channel no. (s)}$, the quantity of pulse was get from single sweep. In addition, the time of single pulses was tested.

Dwell Time= time slot length * hope rate/ number of hopping channels * 31.6s
Hop rate=1600/s

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2015-11-23	2016-11-22
N/A	Coaxial Cable	0.1m	N/A	2016-05-06	2017-05-06
E-Microwave	DC Blocking	EMDCB-00036	0E01201047	2016-05-06	2017-05-06

* **Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Data**Environmental Conditions**

Temperature:	27.9°C
Relative Humidity:	46 %
ATM Pressure:	100 kPa

The testing was performed by Dean Liu on 2016-07-07.

Test Result: Compliance.

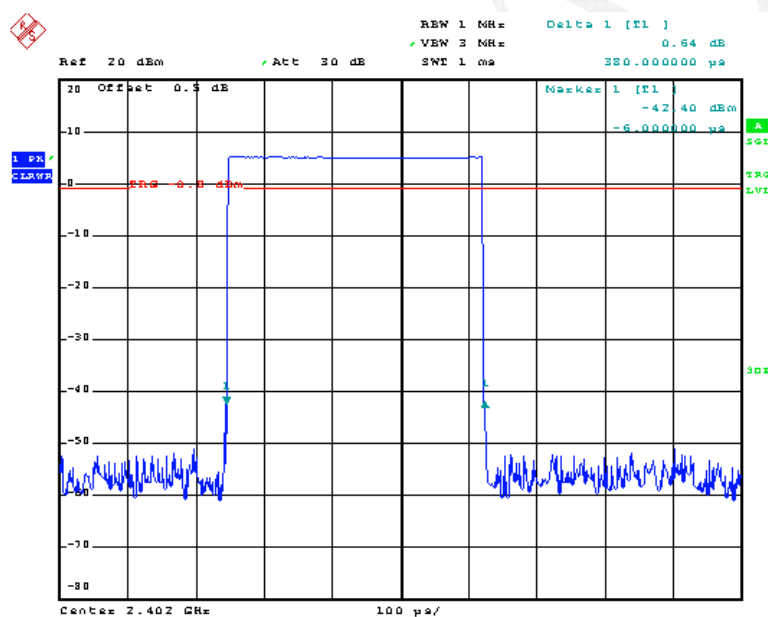
Please refer to following tables and plots

Test Mode: Transmitting

BDR Mode (GFSK):

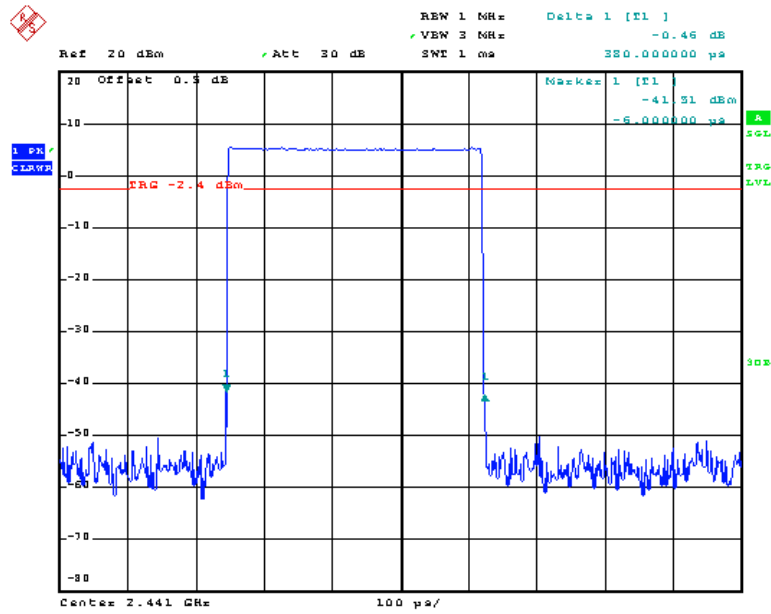
Mode	Channel	Pulse Width (ms)	Dwell Time (s)	Limit (s)	Result
DH1	Low	0.380	0.122	0.4	Compliance
	Middle	0.380	0.122	0.4	Compliance
	High	0.384	0.123	0.4	Compliance
	Note: Dwell time=Pulse time (ms) $\times$ (1600/2/79) $\times$ 31.6 s				
DH3	Low	1.650	0.264	0.4	Compliance
	Middle	1.650	0.264	0.4	Compliance
	High	1.650	0.264	0.4	Compliance
	Note: Dwell time=Pulse time (ms) $\times$ (1600/4/79) $\times$ 31.6 s				
DH5	Low	2.910	0.310	0.4	Compliance
	Middle	2.900	0.309	0.4	Compliance
	High	2.910	0.310	0.4	Compliance
	Note: Dwell time=Pulse time (ms) $\times$ (1600/6/79) $\times$ 31.6 s				

DH1: Low Channel



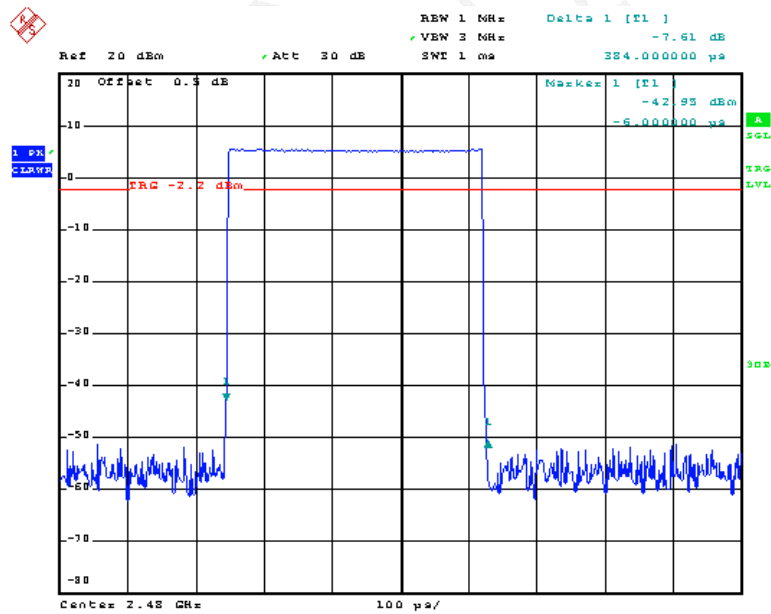
Date: 7.JUL.2016 23:15:28

DH1: Middle Channel



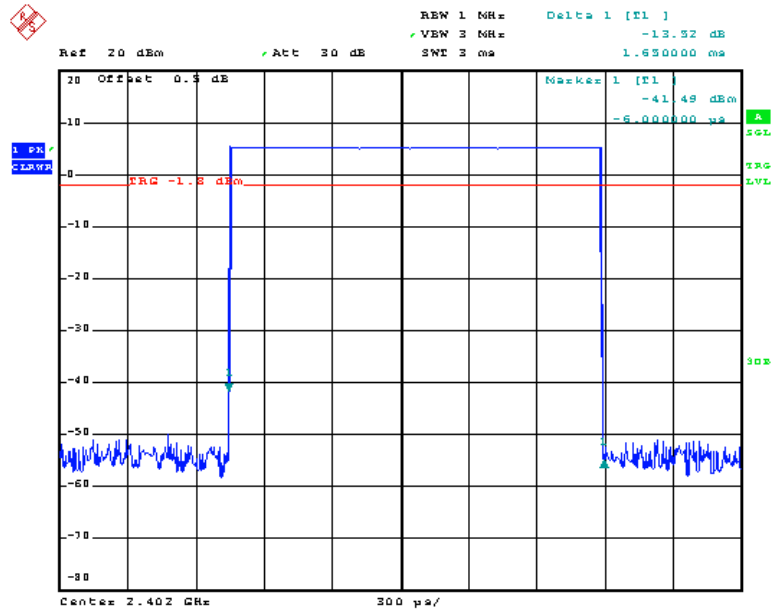
Date: 7.JUL.2016 23:15:38

DH1: High Channel



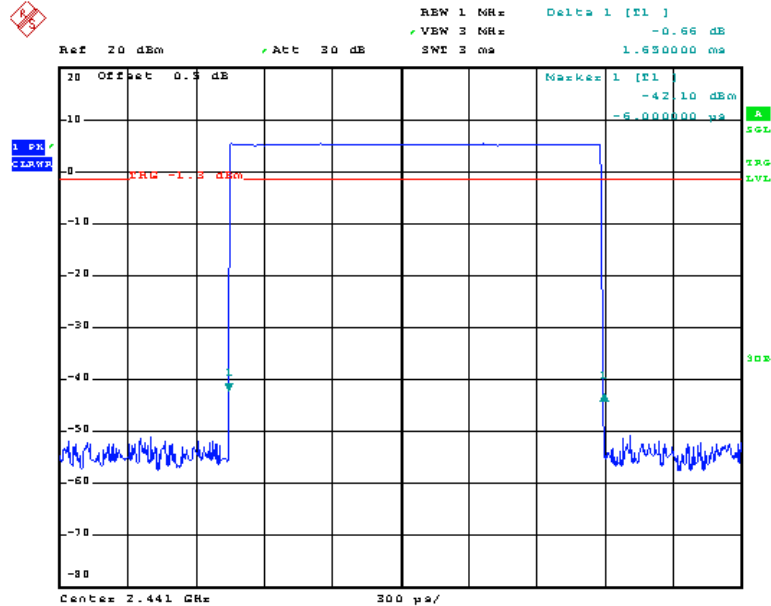
Date: 7.JUL.2016 23:15:48

DH3: Low Channel



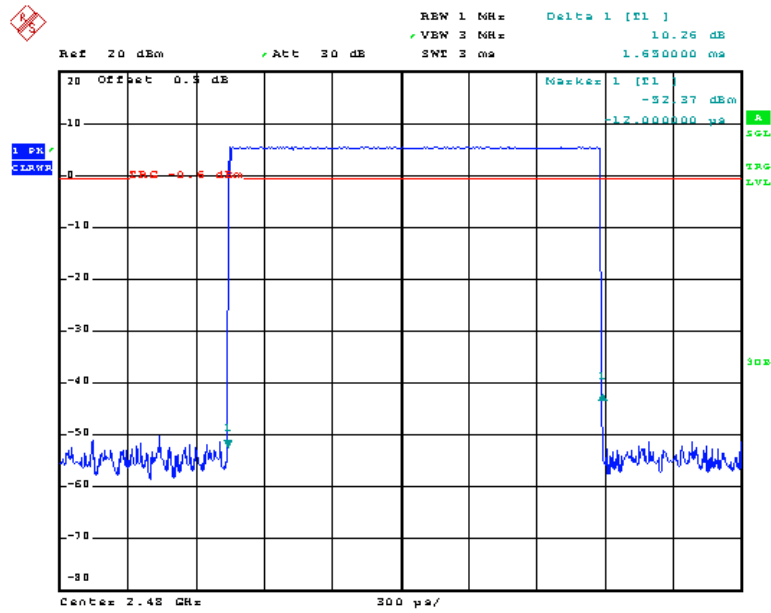
Date: 7.JUL.2016 23:19:39

DH3: Middle Channel



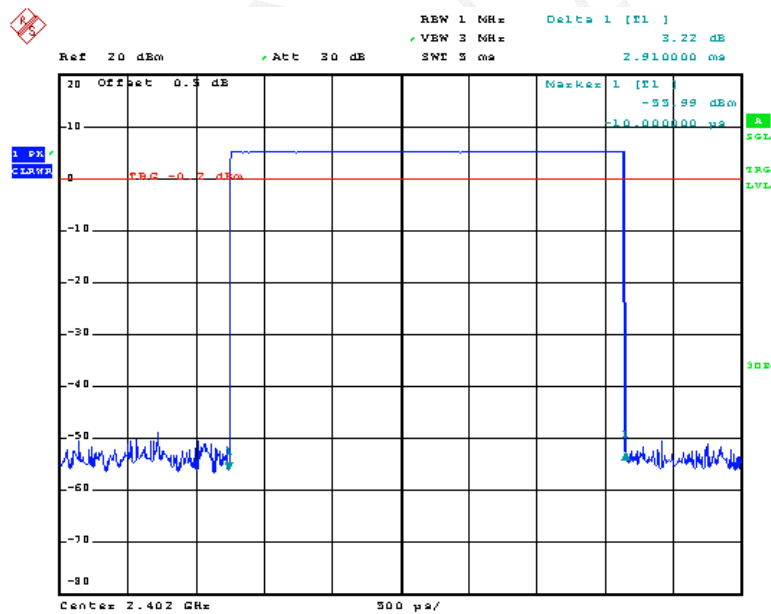
Date: 7.JUL.2016 23:19:48

DH3: High Channel



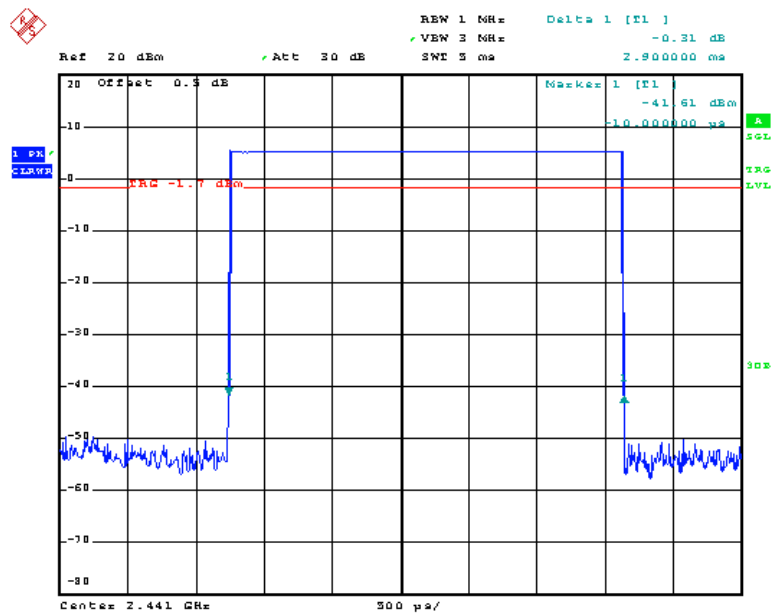
Date: 7.JUL.2016 23:19:56

DH5: Low Channel



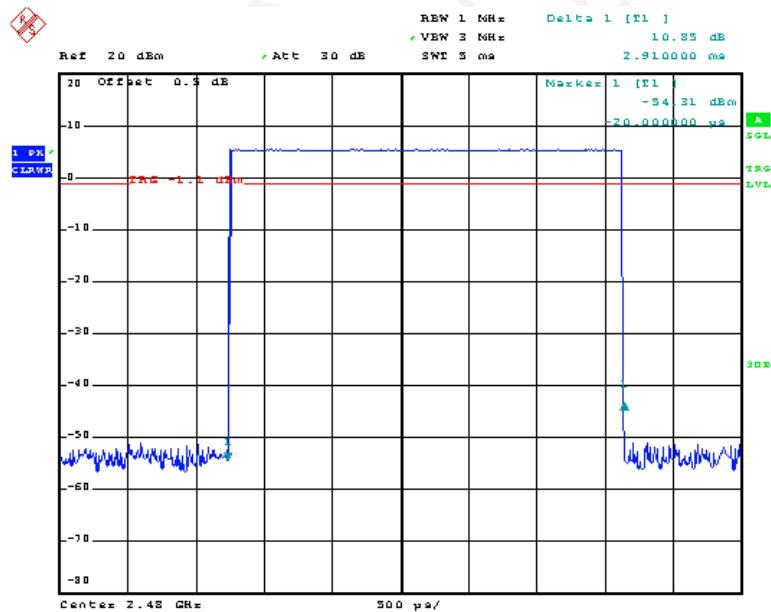
Date: 7.JUL.2016 23:22:44

DH5: Middle Channel



Date: 7.JUL.2016 23:22:52

DH5: High Channel

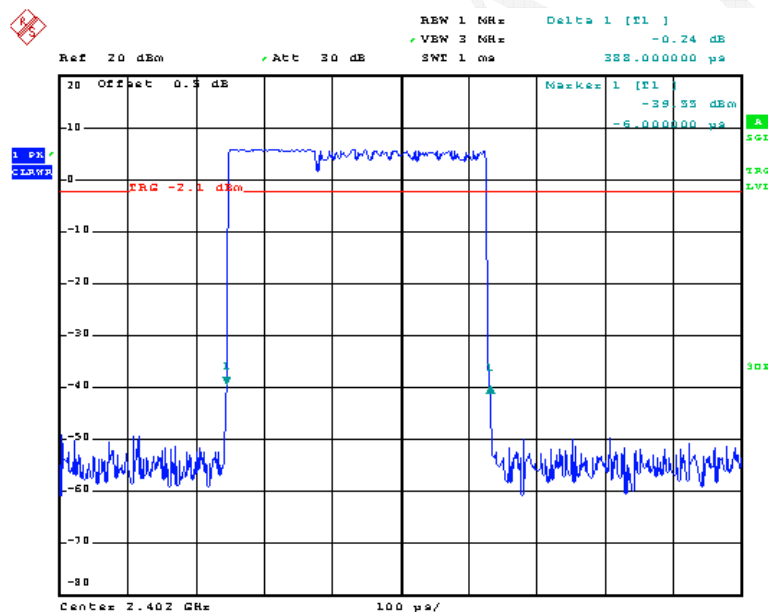


Date: 7.JUL.2016 23:23:01

EDR Mode ($\pi/4$ -DQPSK):

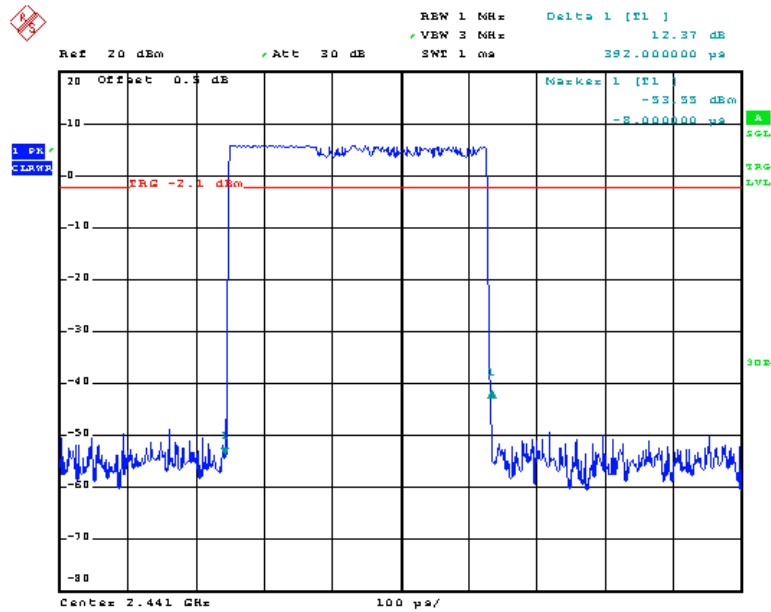
Mode	Channel	Pulse Width (ms)	Dwell Time (s)	Limit (s)	Result
2DH1	Low	0.388	0.124	0.4	Compliance
	Middle	0.392	0.125	0.4	Compliance
	High	0.394	0.126	0.4	Compliance
	Note: Dwell time=Pulse time (ms) × (1600/2/79) ×31.6 s				
2DH3	Low	1.656	0.265	0.4	Compliance
	Middle	1.656	0.265	0.4	Compliance
	High	1.656	0.265	0.4	Compliance
	Note: Dwell time=Pulse time (ms) × (1600/4/79) ×31.6 s				
2DH5	Low	2.910	0.310	0.4	Compliance
	Middle	2.920	0.311	0.4	Compliance
	High	2.910	0.310	0.4	Compliance
	Note: Dwell time=Pulse time (ms) × (1600/6/79) ×31.6 s				

2DH1: Low Channel



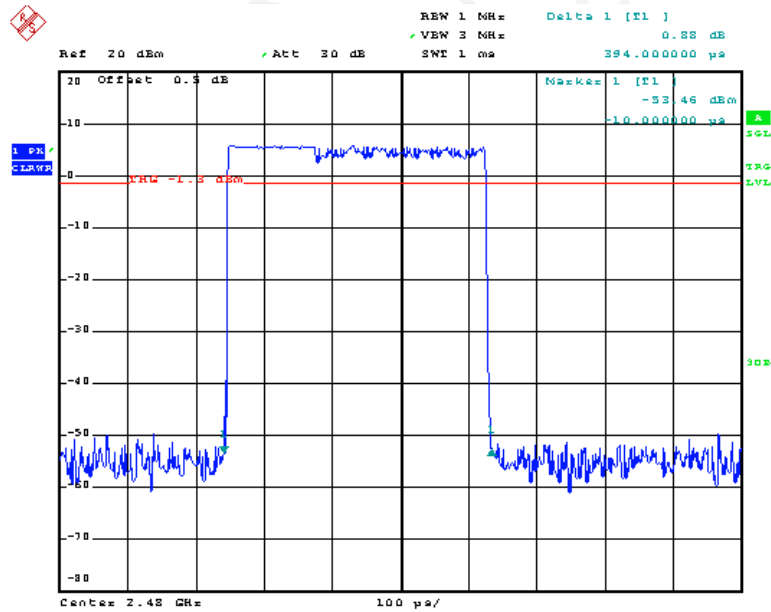
Date: 7.JUL.2016 23:25:30

2DH1: Middle Channel



Date: 7.JUL.2016 23:25:39

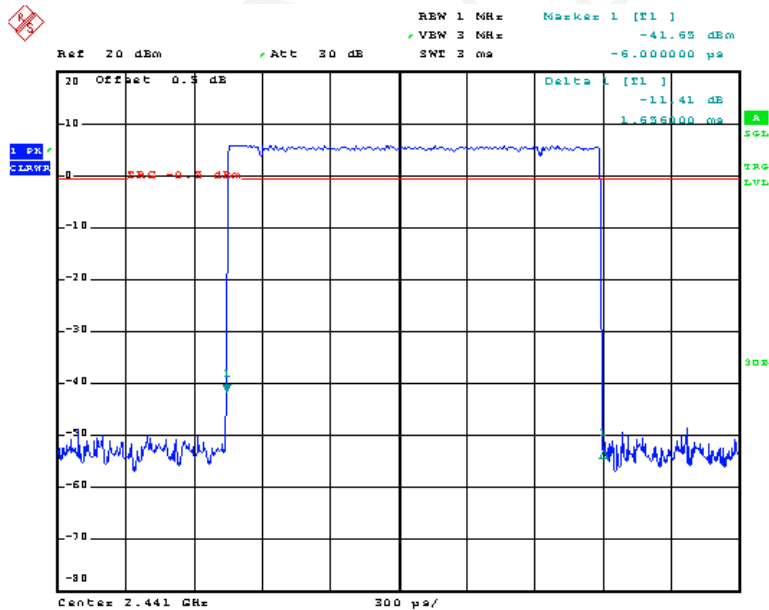
2DH1: High Channel



Date: 7.JUL.2016 23:25:47

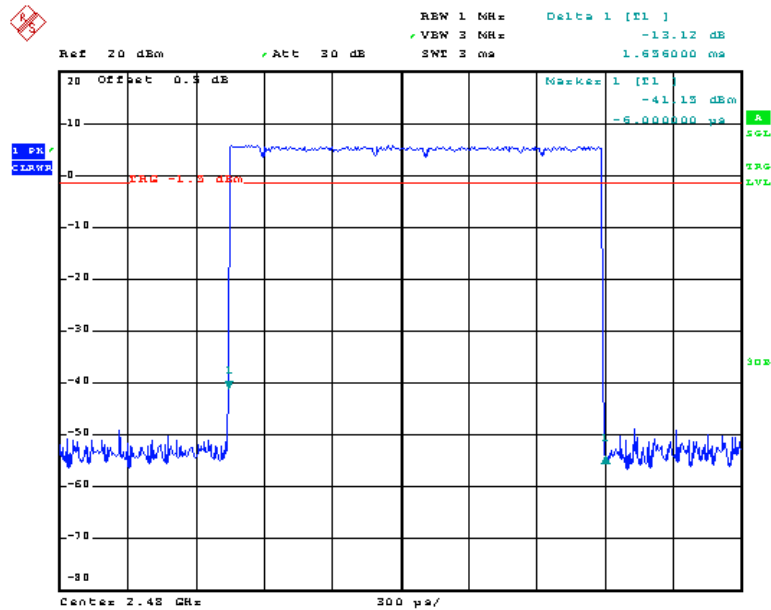
REF 1 MHz Delta 1 [F1]
 Ref 20 dBm Att 30 dB SWI 3 ms Delta 1.656000 ms
 Offset 0.3 dB Marker 1 [F1]
 -49.60 dBm
 -12.000000 us
 S2C -0.5 dBm
 Center 2.402 GHz 300 ps/

2DH3: Middle Channel



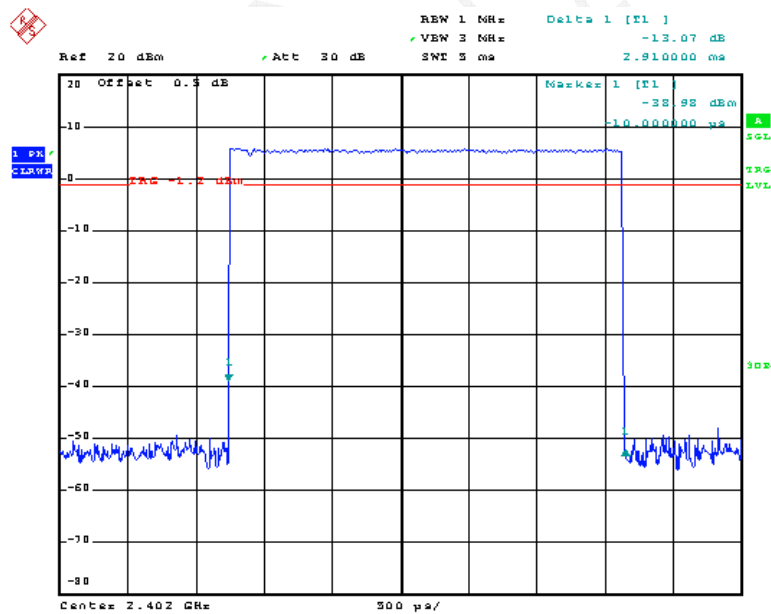
Page 47 of 64

2DH3: High Channel



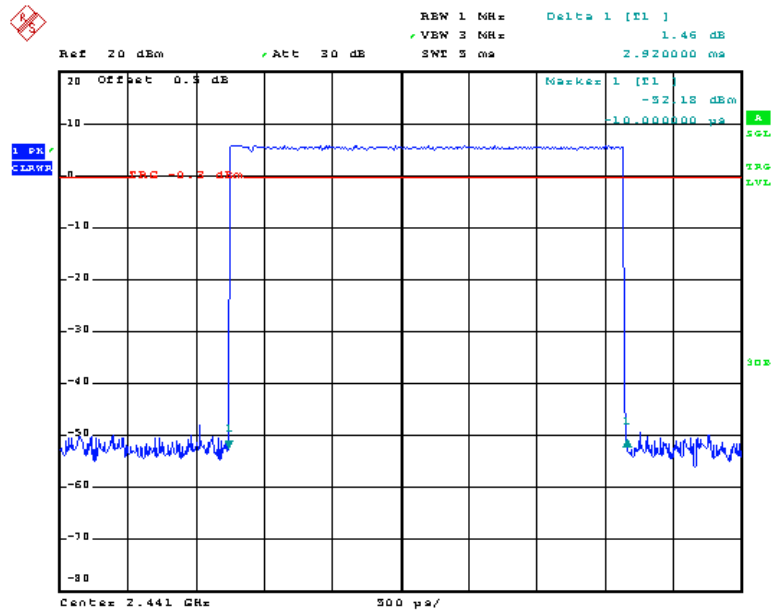
Date: 7.JUL.2016 23:31:58

2DH5: Low Channel



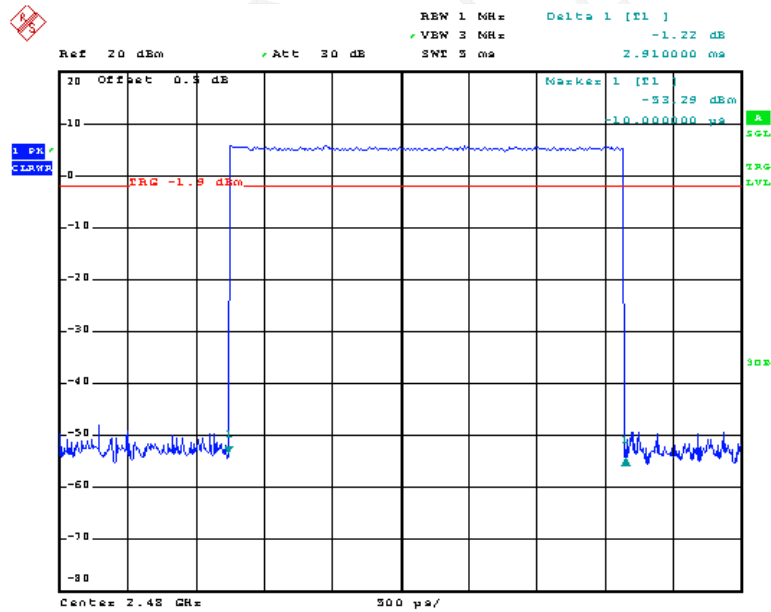
Date: 7.JUL.2016 23:33:56

2DH5: Middle Channel



Date: 7.JUL.2016 23:34:32

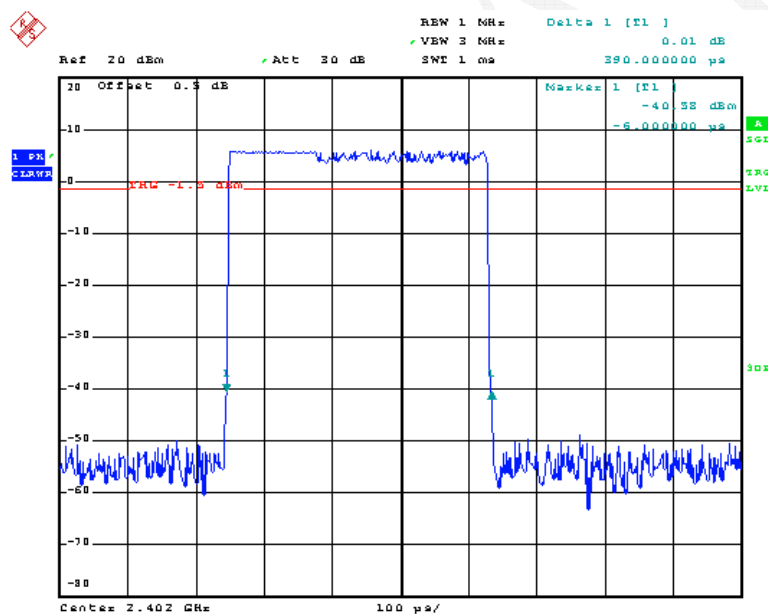
2DH5: High Channel



Date: 7.JUL.2016 23:34:41

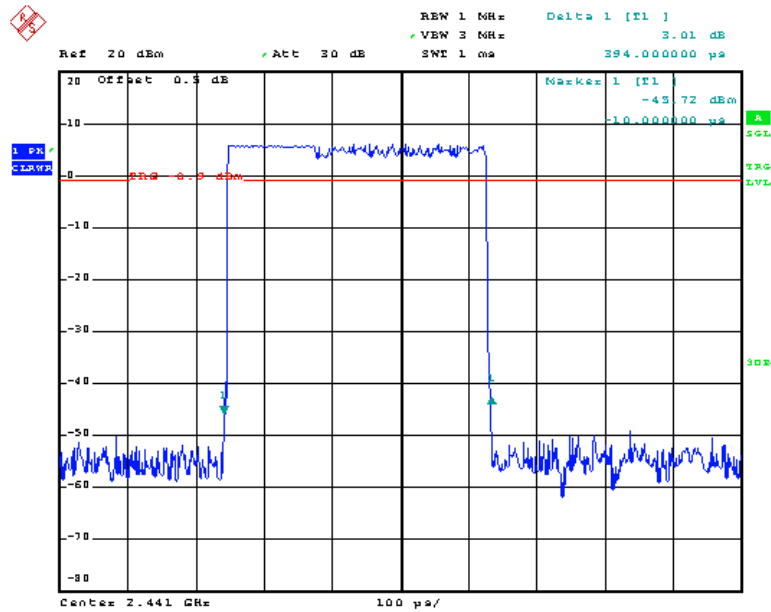
EDR Mode (8-DPSK):

Mode	Channel	Pulse Width (ms)	Dwell Time (s)	Limit (s)	Result
3DH1	Low	0.390	0.125	0.4	Compliance
	Middle	0.394	0.126	0.4	Compliance
	High	0.388	0.124	0.4	Compliance
	Note: Dwell time=Pulse time (ms) $\times$ (1600/2/79) $\times$ 31.6 s				
3DH3	Low	1.656	0.265	0.4	Compliance
	Middle	1.656	0.265	0.4	Compliance
	High	1.650	0.264	0.4	Compliance
	Note: Dwell time=Pulse time (ms) $\times$ (1600/4/79) $\times$ 31.6 s				
3DH5	Low	2.910	0.310	0.4	Compliance
	Middle	2.910	0.310	0.4	Compliance
	High	2.910	0.310	0.4	Compliance
	Note: Dwell time=Pulse time (ms) $\times$ (1600/6/79) $\times$ 31.6 s				

3DH1: Low Channel

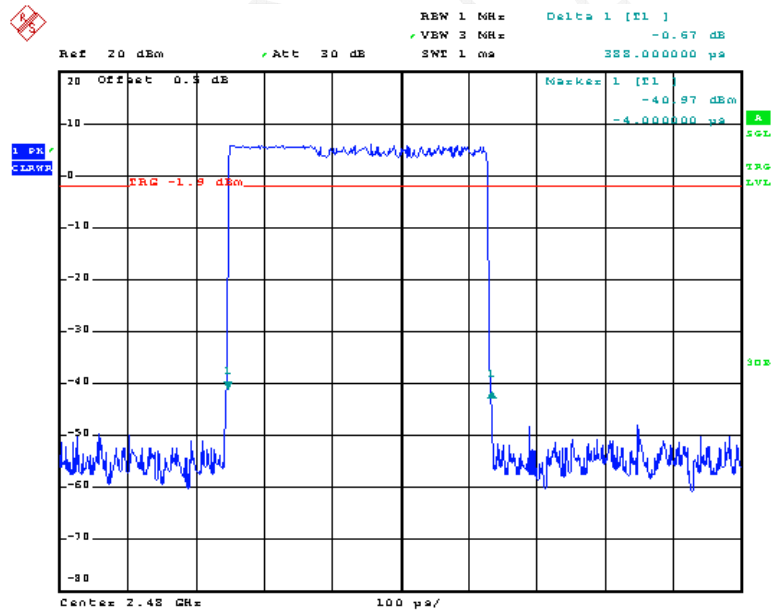
Date: 7.JUL.2016 23:48:00

3DH1: Middle Channel



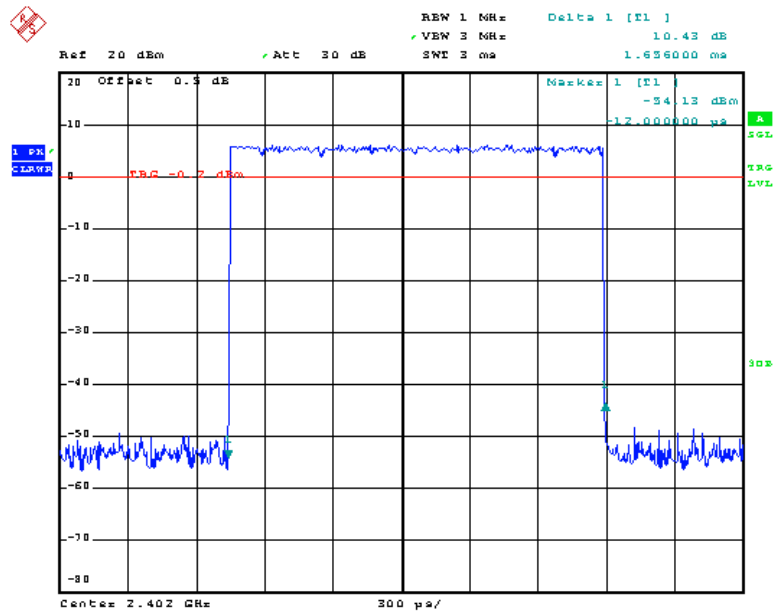
Date: 7.JUL.2016 23:48:16

3DH1: High Channel



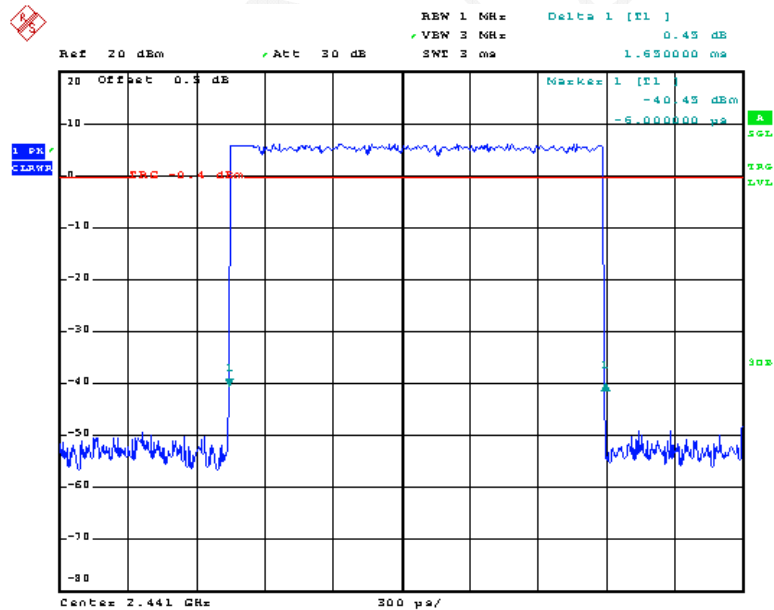
Date: 7.JUL.2016 23:48:24

3DH3: Low Channel



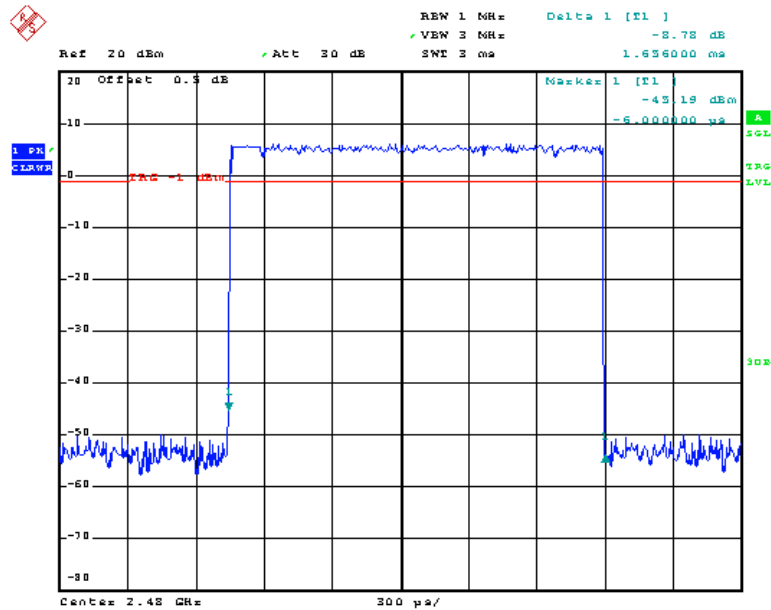
Date: 7.JUL.2016 23:49:47

3DH3: Middle Channel



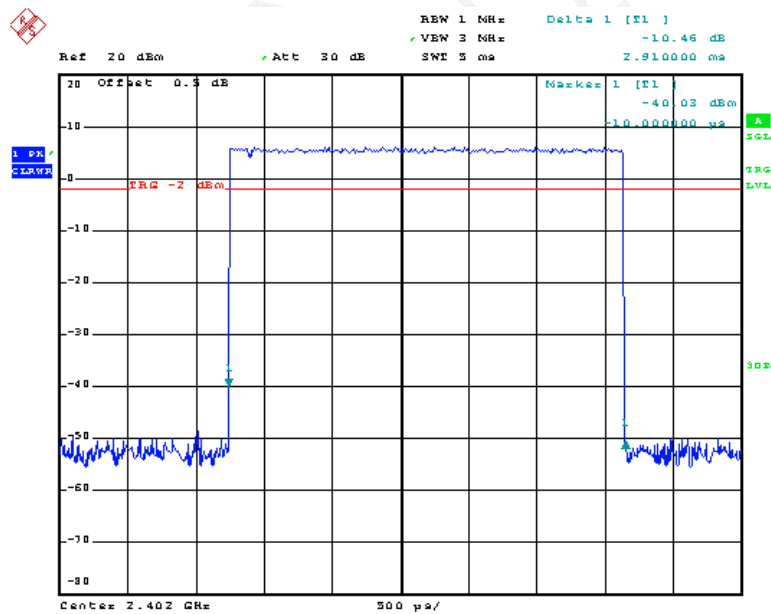
Date: 7.JUL.2016 23:49:56

3DH3: High Channel



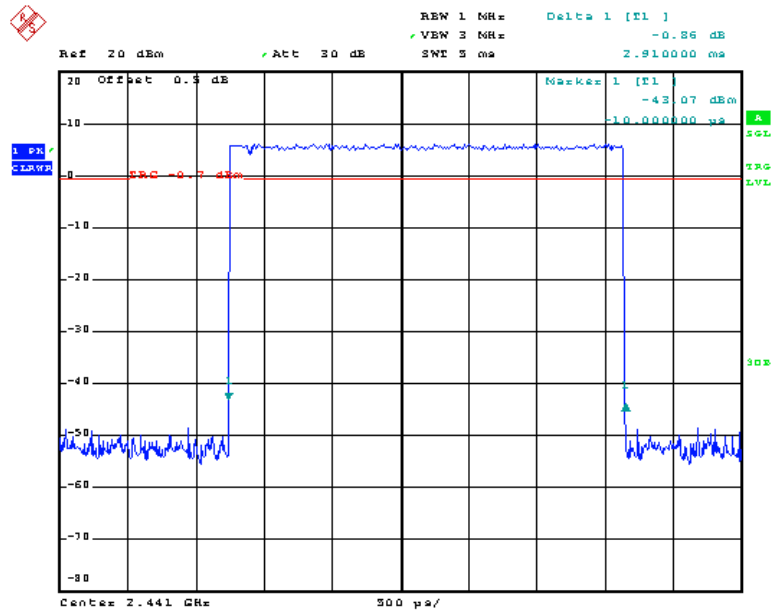
Date: 7.JUL.2016 23:50:31

3DH5: Low Channel



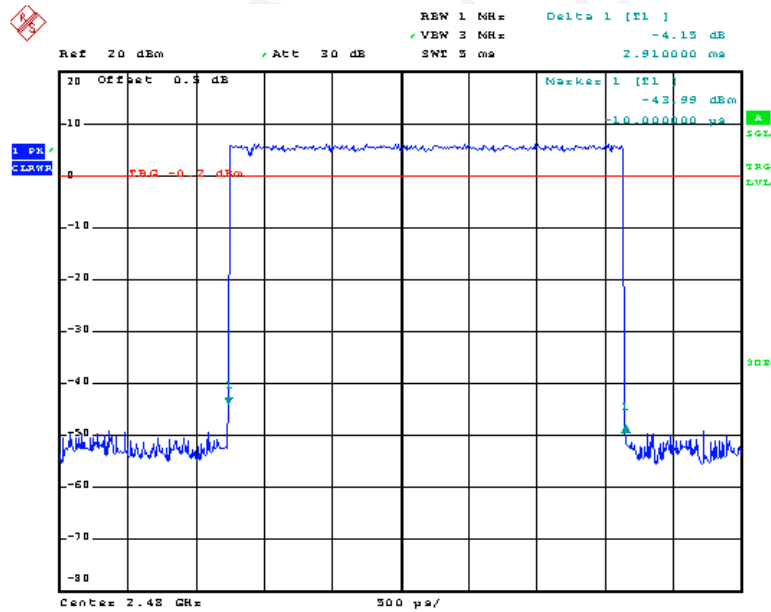
Date: 7.JUL.2016 23:54:08

3DH5: Middle Channel



Date: 7.JUL.2016 23:54:16

3DH5: High Channel



Date: 7.JUL.2016 23:54:25

FCC §15.247(b) (1) - PEAK OUTPUT POWER MEASUREMENT**Applicable Standard**

According to §15.247(b) (1), for frequency hopping systems operating in the 2400–2483.5 MHz band employing at least 75 non-overlapping hopping channels, and all frequency hopping systems in the 5725–5850 MHz band: 1 watt. For all other frequency hopping systems in the 2400–2483.5 MHz band: 0.125 watts

Test Procedure

1. Place the EUT on a bench and set in transmitting mode.
2. Remove the antenna from the EUT and then connect a low loss RF cable from the antenna port to one test equipment.
3. Add a correction factor to the display.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2015-11-23	2016-11-22
N/A	Coaxial Cable	0.1m	N/A	2016-05-06	2017-05-06
E-Microwave	DC Blocking	EMDCB-00036	0E01201047	2016-05-06	2017-05-06

* **Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Data**Environmental Conditions**

Temperature:	28.5 °C
Relative Humidity:	39 %
ATM Pressure:	100.1 kPa

The testing was performed by Dean Liu on 2016-06-26.

Test Result: Compliance.

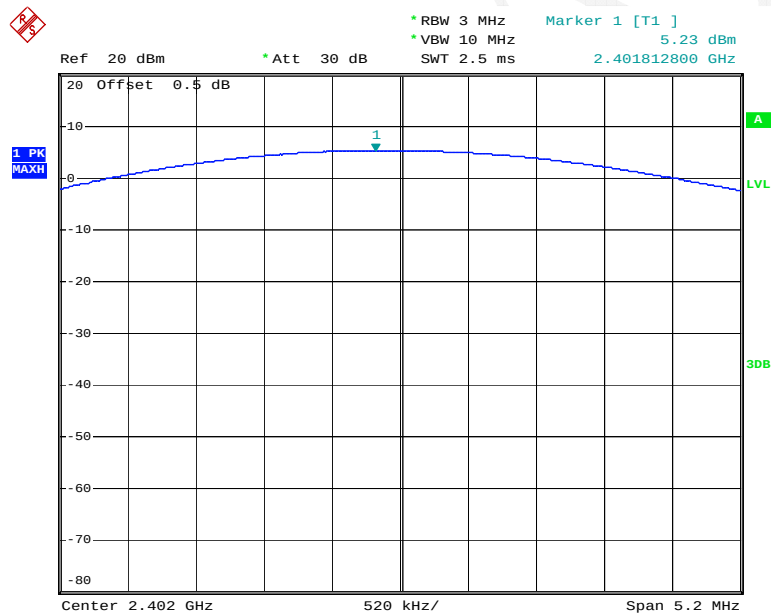
Test Mode: Transmitting

Mode	Channel	Frequency (MHz)	Output power (dBm)	Limit (dBm)
BDR Mode (GFSK)	Low	2402	5.23	30
	Middle	2441	5.41	30
	High	2480	5.01	30
EDR Mode ($\pi/4$ -DQPSK)	Low	2402	6.3	30
	Middle	2441	6.51	30
	High	2480	6.11	30
EDR Mode (8-DPSK)	Low	2402	6.57	30
	Middle	2441	6.66	30
	High	2480	6.42	30

Note: The data above was tested in conducted mode.

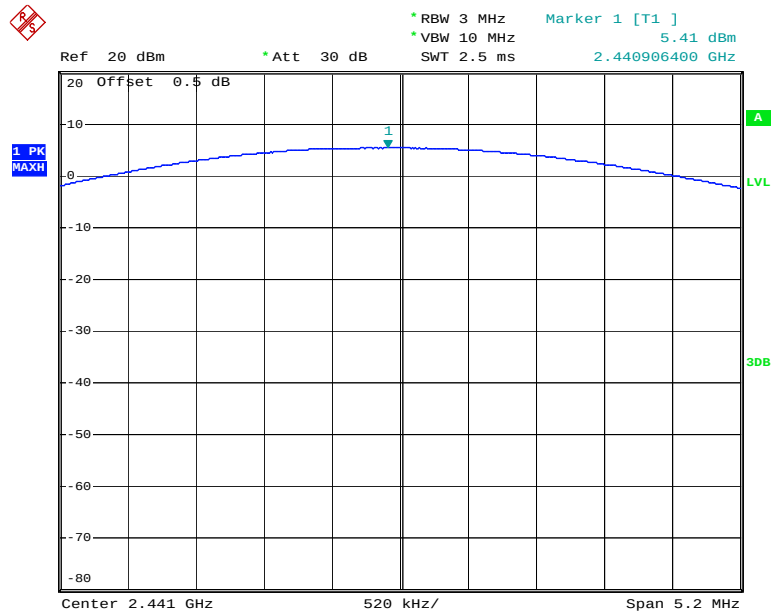
BDR Mode (GFSK):

Output Power, Low Channel



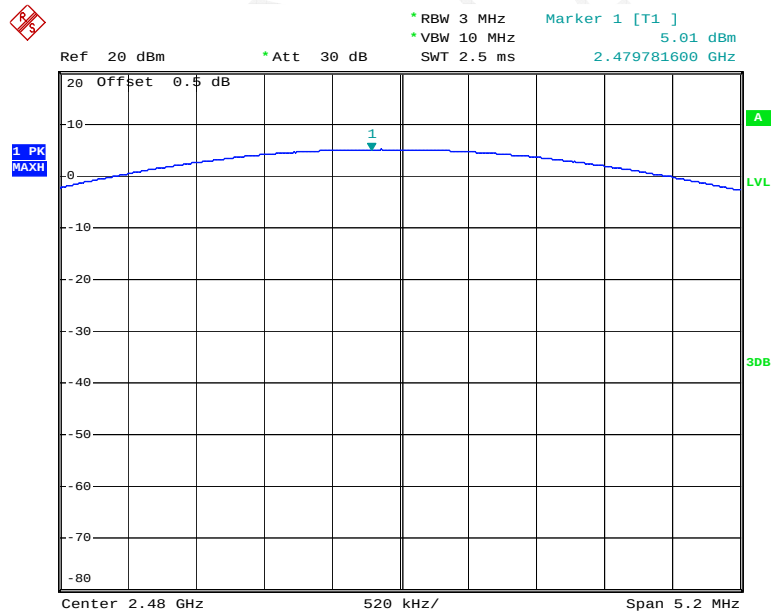
Date: 26.JUN.2016 11:17:08

Output Power, Middle Channel



Date: 26.JUN.2016 11:28:25

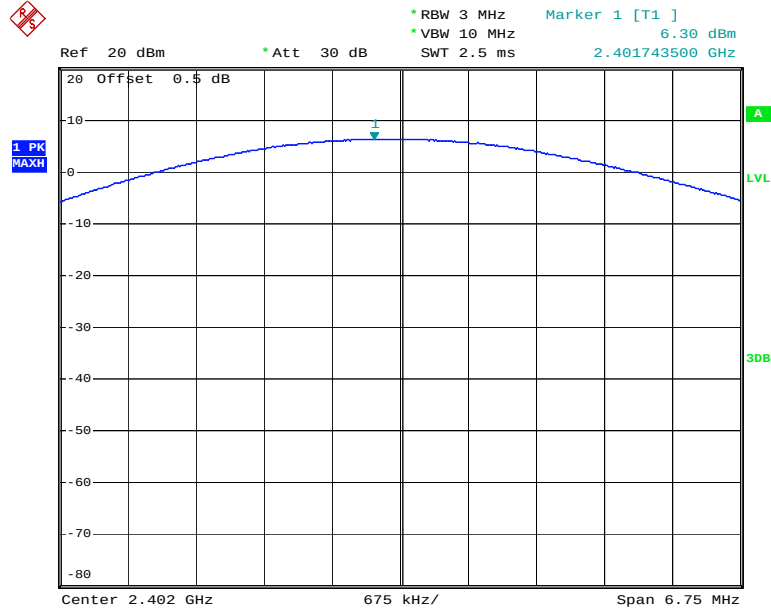
Output Power, High Channel



Date: 26.JUN.2016 11:30:06

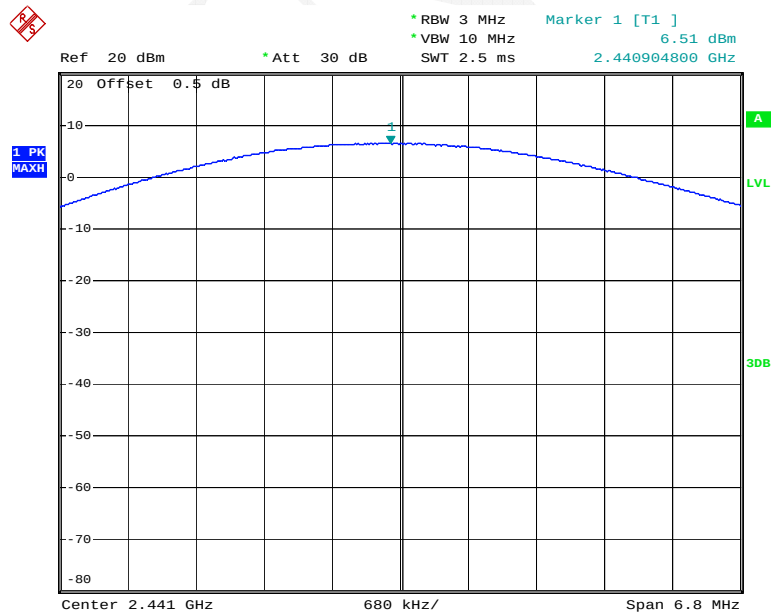
EDR Mode ($\pi/4$ -DQPSK):

Output Power, Low Channel



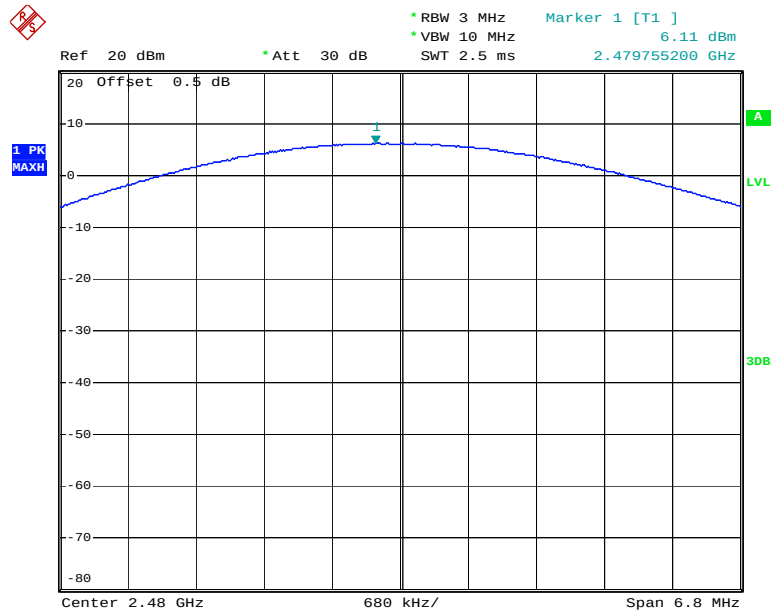
Date: 26.JUN.2016 11:31:47

Output Power, Middle Channel



Date: 26.JUN.2016 11:38:40

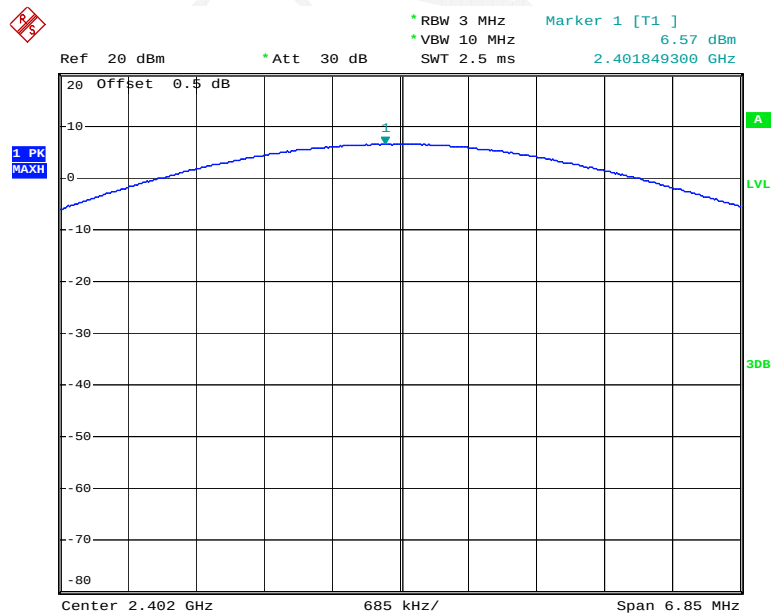
Output Power, High Channel



Date: 26.JUN.2016 11:34:59

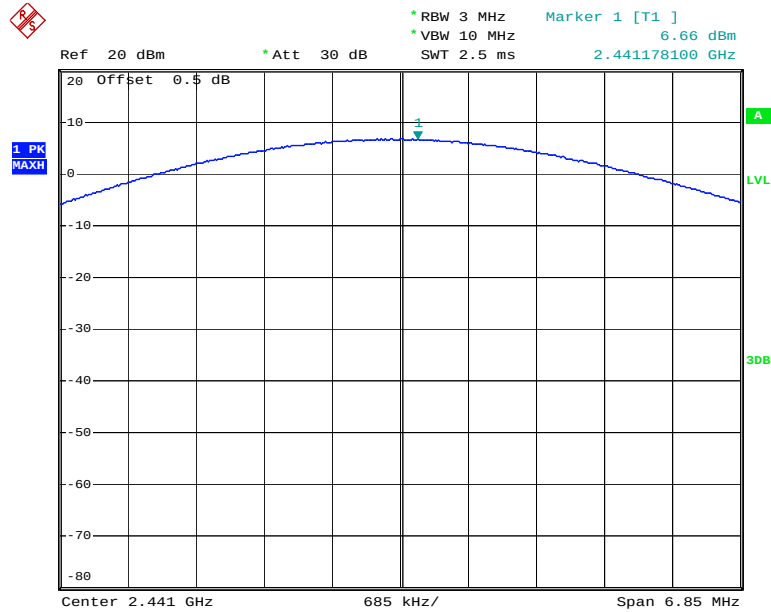
EDR Mode (8-DPSK):

Output Power, Low Channel



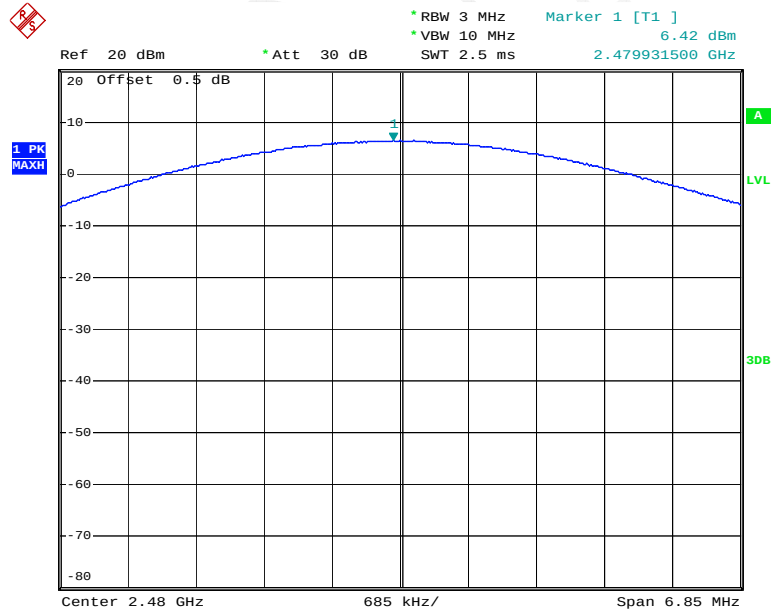
Date: 26.JUN.2016 11:36:54

Output Power, Middle Channel



Date: 26.JUN.2016 11:40:00

Output Power, High Channel



Date: 26.JUN.2016 11:41:08

FCC §15.247(d) - BAND EDGES TESTING

Applicable Standard

In any 100 kHz bandwidth outside the frequency band in which the spread spectrum or digitally modulated intentional radiator is operating, the radio frequency power that is produced by the intentional radiator shall be at least 20 dB below that in the 100 kHz bandwidth within the band that contains the highest level of the desired power, based on either an RF conducted or a radiated measurement, provided the transmitter demonstrates compliance with the peak conducted power limits. If the transmitter complies with the conducted power limits based on the use of RMS averaging over a time interval, as permitted under paragraph (b)(3) of this section, the attenuation required under this paragraph shall be 30 dB instead of 20 dB. Attenuation below the general limits specified in §15.209(a) is not required. In addition, radiated emissions which fall in the restricted bands, as defined in §15.205(a), must also comply with the radiated emission limits specified in §15.209(a) (see §15.205(c)).

Test Procedure

1. Check the calibration of the measuring instrument using either an internal calibrator or a known signal from an external generator.
2. Remove the antenna from the EUT and then connect to a low loss RF cable from the antenna port to a EMI test receiver, then turn on the EUT and make it operate in transmitting mode. Then set it to Low Channel and High Channel within its operating range, and make sure the instrument is operated in its linear range.
3. Set both RBW and VBW of spectrum analyzer to 100 kHz with a convenient frequency span including 100 kHz bandwidth from band edge.
4. Measure the highest amplitude appearing on spectral display and set it as a reference level. Plot the graph with marking the highest point and edge frequency.
5. Repeat above procedures until all measured frequencies were complete.

Test Equipment List and Details

Manufacturer	Description	Model	Serial Number	Calibration Date	Calibration Due Date
R&S	Spectrum Analyzer	FSP 38	100478	2015-11-23	2016-11-22
N/A	Coaxial Cable	0.1m	N/A	2016-05-06	2017-05-06
E-Microwave	DC Blocking	EMDCB-00036	0E01201047	2016-05-06	2017-05-06

* **Statement of Traceability:** Bay Area Compliance Laboratories Corp. (Dongguan) attests that all calibrations have been performed, traceable to National Primary Standards and International System of Units (SI).

Test Data

Environmental Conditions

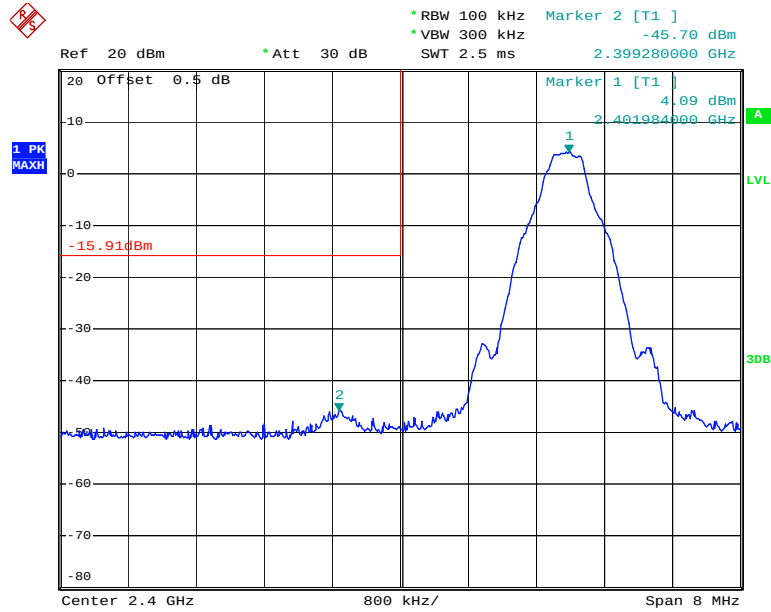
Temperature:	28.5 °C
Relative Humidity:	39 %
ATM Pressure:	100.1 kPa

The testing was performed by Dean Liu on 2016-06-26.

Test Result: Compliance

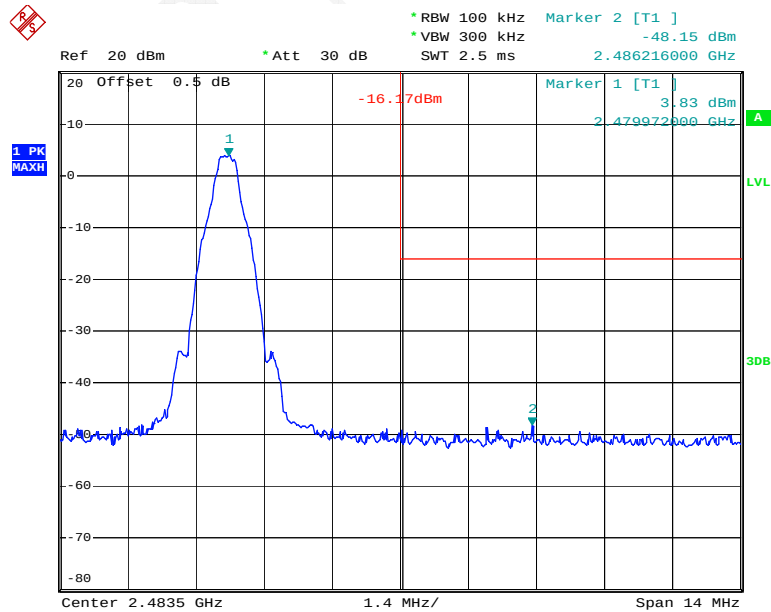
BDR Mode (GFSK):

Band Edge, Left Side



Date: 26.JUN.2016 11:18:02

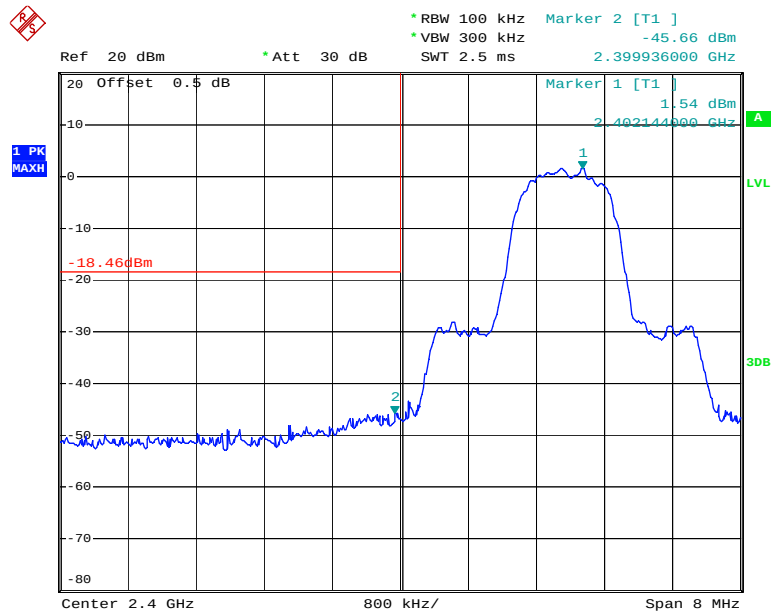
Band Edge, Right Side



Date: 26.JUN.2016 11:30:24

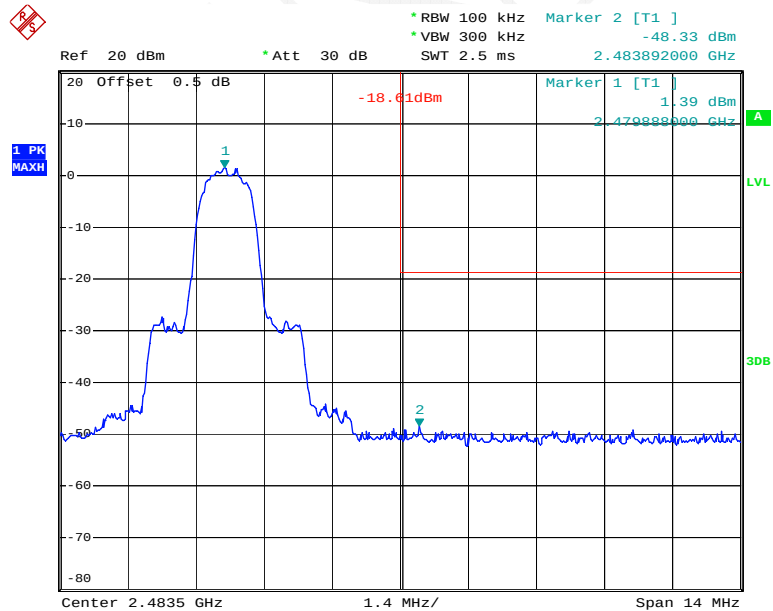
EDR Mode ($\pi/4$ -DQPSK):

Band Edge, Left Side



Date: 26.JUN.2016 11:32:04

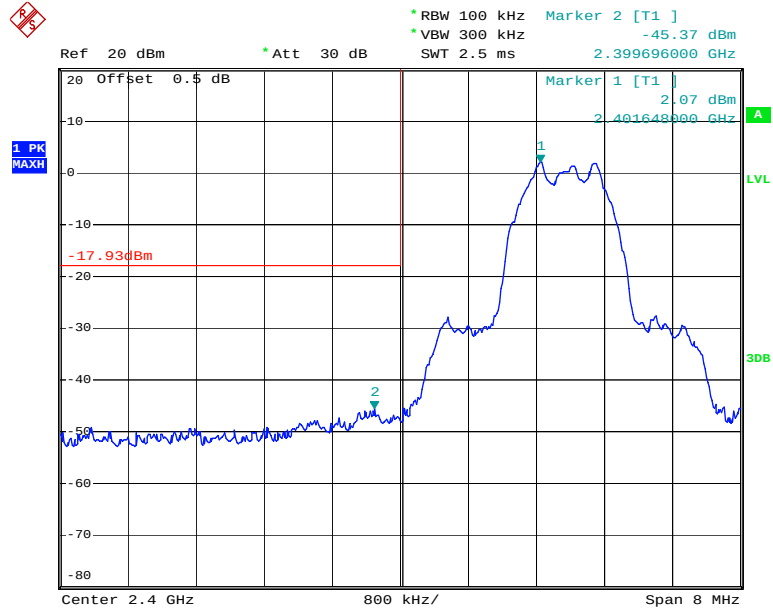
Band Edge, Right Side



Date: 26.JUN.2016 11:35:22

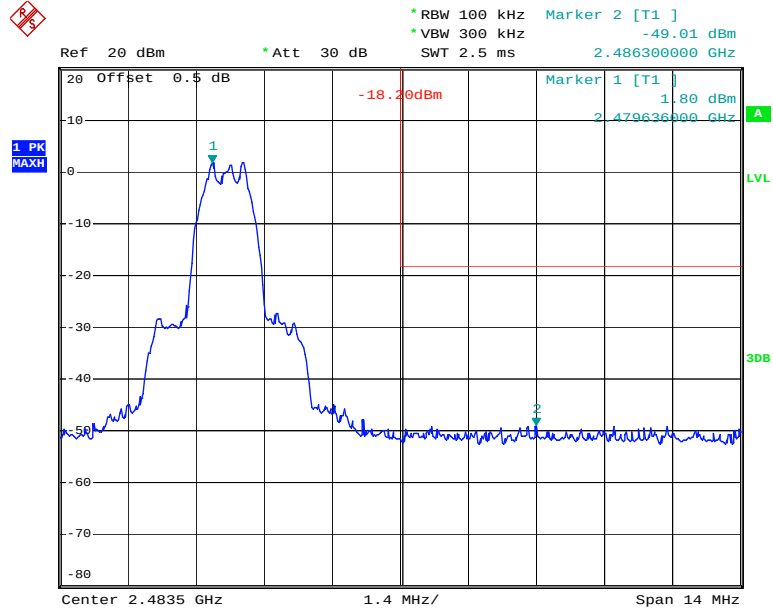
EDR Mode (8-DPSK):

Band Edge, Left Side



Date: 26.JUN.2016 11:37:11

Band Edge, Right Side



Date: 26.JUN.2016 11:41:25

*****END OF REPORT*****