

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Average / Port 1 + Port 2 / 3GHz~6GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5255	-72.96	-71.87	-45.37	-41.25	4.12
5295	-71.90	-71.36	-44.61	-41.25	3.36
5340	-75.74	-72.77	-47.00	-41.25	5.75
5250 (Straddle Channel)	-72.67	-72.18	-45.41	-41.25	4.16

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Peak / Port 1 + Port 2 / 3GHz~6GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5255	-60.27	-58.15	-32.07	-21.25	10.82
5295	-58.63	-57.70	-31.13	-21.25	9.88
5340	-63.61	-64.59	-37.06	-21.25	15.81
5250 (Straddle Channel)	-59.66	-58.77	-32.18	-21.25	10.93

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Average / Port 1 + Port 2 / 3GHz~6GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5270	-73.47	-72.70	-46.06	-41.25	4.81
5300	-74.45	-70.93	-45.33	-41.25	4.08
5325	-73.62	-71.93	-45.68	-41.25	4.43
5250 (Straddle Channel)	-73.51	-72.64	-46.04	-41.25	4.79

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Peak / Port 1 + Port 2 / 3GHz~6GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5270	-61.20	-61.13	-34.15	-21.25	12.90
5300	-64.44	-63.39	-36.87	-21.25	15.62
5325	-64.20	-63.94	-37.06	-21.25	15.81
5250 (Straddle Channel)	-61.19	-60.54	-33.84	-21.25	12.59

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Average / Port 1 + Port 2 / 6GHz~9GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5255	-78.83	-78.15	-51.47	-41.25	10.22
5295	-78.47	-78.19	-51.32	-41.25	10.07
5340	-79.44	-79.04	-52.23	-41.25	10.98
5250 (Straddle Channel)	-78.85	-78.40	-51.61	-41.25	10.36

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Peak / Port 1 + Port 2 / 6GHz~9GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5255	-66.28	-65.99	-39.12	-21.25	17.87
5295	-66.68	-66.05	-39.34	-21.25	18.09
5340	-67.35	-67.43	-40.38	-21.25	19.13
5250 (Straddle Channel)	-66.20	-66.51	-39.34	-21.25	18.09

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Average / Port 1 + Port 2 / 6GHz~9GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5270	-78.43	-77.93	-51.16	-41.25	9.91
5300	-79.48	-78.95	-52.20	-41.25	10.95
5325	-79.66	-79.38	-52.51	-41.25	11.26
5250 (Straddle Channel)	-78.23	-77.74	-50.97	-41.25	9.72

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Peak / Port 1 + Port 2 / 6GHz~9GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5270	-65.80	-65.60	-38.69	-21.25	17.44
5300	-66.90	-66.47	-39.67	-21.25	18.42
5325	-66.77	-66.95	-39.85	-21.25	18.60
5250 (Straddle Channel)	-66.24	-64.53	-38.29	-21.25	17.04

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Average / Port 1 + Port 2 / 9GHz~18GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5255	-76.38	-76.23	-49.29	-41.25	8.04
5295	-76.25	-76.27	-49.25	-41.25	8.00
5340	-76.22	-76.21	-49.20	-41.25	7.95
5250 (Straddle Channel)	-76.22	-76.28	-49.24	-41.25	7.99

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Peak / Port 1 + Port 2 / 9GHz~18GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5255	-64.58	-63.38	-36.93	-21.25	15.68
5295	-64.04	-64.58	-37.29	-21.25	16.04
5340	-64.10	-64.46	-37.27	-21.25	16.02
5250 (Straddle Channel)	-64.34	-65.13	-37.71	-21.25	16.46

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Average / Port 1 + Port 2 / 9GHz~18GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5270	-76.02	-76.06	-49.03	-41.25	7.78
5300	-76.25	-76.10	-49.16	-41.25	7.91
5325	-76.17	-76.12	-49.13	-41.25	7.88
5250 (Straddle Channel)	-76.08	-76.26	-49.16	-41.25	7.91

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Peak / Port 1 + Port 2 / 9GHz~18GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5270	-64.38	-64.61	-37.48	-21.25	16.23
5300	-63.84	-63.94	-36.88	-21.25	15.63
5325	-65.00	-63.75	-37.32	-21.25	16.07
5250 (Straddle Channel)	-64.43	-63.58	-36.97	-21.25	15.72

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Average / Port 1 + Port 2 / 18GHz~40GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5255	-72.02	-72.10	-45.05	-41.25	3.80
5295	-72.04	-71.93	-44.97	-41.25	3.72
5340	-72.07	-72.04	-45.04	-41.25	3.79
5250 (Straddle Channel)	-71.87	-72.08	-44.96	-41.25	3.71

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Peak / Port 1 + Port 2 / 18GHz~40GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5255	-60.18	-60.29	-33.22	-21.25	11.97
5295	-59.35	-60.50	-32.88	-21.25	11.63
5340	-59.60	-60.02	-32.79	-21.25	11.54
5250 (Straddle Channel)	-60.30	-60.05	-33.16	-21.25	11.91

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Average / Port 1 + Port 2 / 18GHz~40GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5270	-72.08	-72.00	-45.03	-41.25	3.78
5300	-71.97	-72.09	-45.02	-41.25	3.77
5325	-72.02	-71.99	-44.99	-41.25	3.74
5250 (Straddle Channel)	-72.13	-72.14	-45.12	-41.25	3.87

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Peak / Port 1 + Port 2 / 18GHz~40GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5270	-60.33	-60.05	-33.18	-21.25	11.93
5300	-58.73	-60.27	-32.42	-21.25	11.17
5325	-59.43	-60.12	-32.75	-21.25	11.50
5250 (Straddle Channel)	-60.16	-60.57	-33.35	-21.25	12.10



Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Average / Port 1 + Port 2 / 1GHz~3GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5480	-83.57	-83.52	-56.53	-41.25	15.28
5575	-83.59	-83.64	-56.60	-41.25	15.35
5715	-83.56	-83.62	-56.58	-41.25	15.33
5725 (Straddle Channel)	-83.38	-83.53	-56.44	-41.25	15.19

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Peak / Port 1 + Port 2 / 1GHz~3GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5480	-65.98	-70.89	-40.76	-21.25	19.51
5575	-71.95	-71.99	-44.96	-21.25	23.71
5715	-71.61	-72.48	-45.01	-21.25	23.76
5725 (Straddle Channel)	-71.20	-72.37	-44.74	-21.25	23.49

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Average / Port 1 + Port 2 / 1GHz~3GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5495	-82.76	-83.63	-56.16	-41.25	14.91
5550	-83.61	-83.51	-56.55	-41.25	15.30
5700	-83.52	-83.58	-56.54	-41.25	15.29
5720 (Straddle Channel)	-83.42	-83.49	-56.44	-41.25	15.19

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Peak / Port 1 + Port 2 / 1GHz~3GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5495	-70.84	-72.05	-44.39	-21.25	23.14
5550	-71.38	-71.62	-44.49	-21.25	23.24
5700	-69.06	-69.71	-42.36	-21.25	21.11
5720 (Straddle Channel)	-71.64	-71.36	-44.49	-21.25	23.24

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Average / Port 1 + Port 2 / 3GHz~6GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5480	-74.61	-71.73	-45.93	-41.25	4.68
5575	-71.88	-72.09	-44.97	-41.25	3.72
5715	-75.10	-71.91	-46.21	-41.25	4.96
5725 (Straddle Channel)	-72.03	-72.10	-45.05	-41.25	3.80

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Peak / Port 1 + Port 2 / 3GHz~6GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5480	-63.98	-64.05	-37.00	-21.25	15.75
5575	-61.73	-62.52	-35.10	-21.25	13.85
5715	-63.90	-63.60	-36.74	-21.25	15.49
5725 (Straddle Channel)	-60.09	-60.51	-33.28	-21.25	12.03

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Average / Port 1 + Port 2 / 3GHz~6GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5495	-74.47	-72.88	-46.59	-41.25	5.34
5550	-72.99	-72.43	-45.69	-41.25	4.44
5700	-74.71	-73.44	-47.02	-41.25	5.77
5720 (Straddle Channel)	-68.46	-70.35	-42.29	-41.25	1.04

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Peak / Port 1 + Port 2 / 3GHz~6GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5495	-64.74	-64.53	-37.62	-21.25	16.37
5550	-61.89	-60.86	-34.33	-21.25	13.08
5700	-64.70	-64.45	-37.56	-21.25	16.31
5720 (Straddle Channel)	-57.98	-58.48	-31.21	-21.25	9.96

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Average / Port 1 + Port 2 / 6GHz~9GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5480	-76.91	-76.83	-49.86	-41.25	8.61
5575	-75.46	-75.51	-48.47	-41.25	7.22
5715	-76.78	-76.88	-49.82	-41.25	8.57
5725 (Straddle Channel)	-75.69	-76.36	-49.00	-41.25	7.75

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Peak / Port 1 + Port 2 / 6GHz~9GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5480	-65.90	-65.16	-38.50	-21.25	17.25
5575	-63.83	-62.93	-36.35	-21.25	15.10
5715	-66.05	-65.43	-38.72	-21.25	17.47
5725 (Straddle Channel)	-62.70	-64.67	-36.56	-21.25	15.31

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Average / Port 1 + Port 2 / 6GHz~9GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5495	-76.91	-76.93	-49.91	-41.25	8.66
5550	-74.05	-73.37	-46.69	-41.25	5.44
5700	-76.76	-76.78	-49.76	-41.25	8.51
5720 (Straddle Channel)	-73.92	-74.93	-47.39	-41.25	6.14

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Peak / Port 1 + Port 2 / 6GHz~9GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5495	-65.31	-66.34	-38.78	-21.25	17.53
5550	-61.92	-62.72	-35.29	-21.25	14.04
5700	-65.79	-65.65	-38.71	-21.25	17.46
5720 (Straddle Channel)	-61.62	-63.77	-35.55	-21.25	14.30

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Average / Port 1 + Port 2 / 9GHz~18GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5480	-76.18	-76.08	-49.12	-41.25	7.87
5575	-76.03	-76.18	-49.09	-41.25	7.84
5715	-76.04	-76.12	-49.07	-41.25	7.82
5725 (Straddle Channel)	-76.04	-75.99	-49.00	-41.25	7.75

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Peak / Port 1 + Port 2 / 9GHz~18GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5480	-63.87	-65.29	-37.51	-21.25	16.26
5575	-64.63	-65.05	-37.82	-21.25	16.57
5715	-64.34	-65.14	-37.71	-21.25	16.46
5725 (Straddle Channel)	-63.63	-64.50	-37.03	-21.25	15.78

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Average / Port 1 + Port 2 / 9GHz~18GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5495	-76.14	-76.14	-49.13	-41.25	7.88
5550	-76.14	-76.10	-49.11	-41.25	7.86
5700	-76.18	-76.13	-49.14	-41.25	7.89
5720 (Straddle Channel)	-76.21	-76.19	-49.19	-41.25	7.94

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Peak / Port 1 + Port 2 / 9GHz~18GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5495	-64.33	-64.70	-37.50	-21.25	16.25
5550	-64.81	-63.39	-37.03	-21.25	15.78
5700	-64.44	-64.37	-37.39	-21.25	16.14
5720 (Straddle Channel)	-64.96	-64.95	-37.94	-21.25	16.69

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Average / Port 1 + Port 2 / 18GHz~40GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5480	-72.06	-72.05	-45.04	-41.25	3.79
5575	-72.14	-71.99	-45.05	-41.25	3.80
5715	-72.07	-72.05	-45.05	-41.25	3.80
5725 (Straddle Channel)	-71.96	-72.06	-45.00	-41.25	3.75

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Peak / Port 1 + Port 2 / 18GHz~40GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5480	-60.14	-60.59	-33.35	-21.25	12.10
5575	-60.40	-60.78	-33.58	-21.25	12.33
5715	-60.62	-61.13	-33.86	-21.25	12.61
5725 (Straddle Channel)	-60.29	-60.61	-33.44	-21.25	12.19

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Average / Port 1 + Port 2 / 18GHz~40GHz

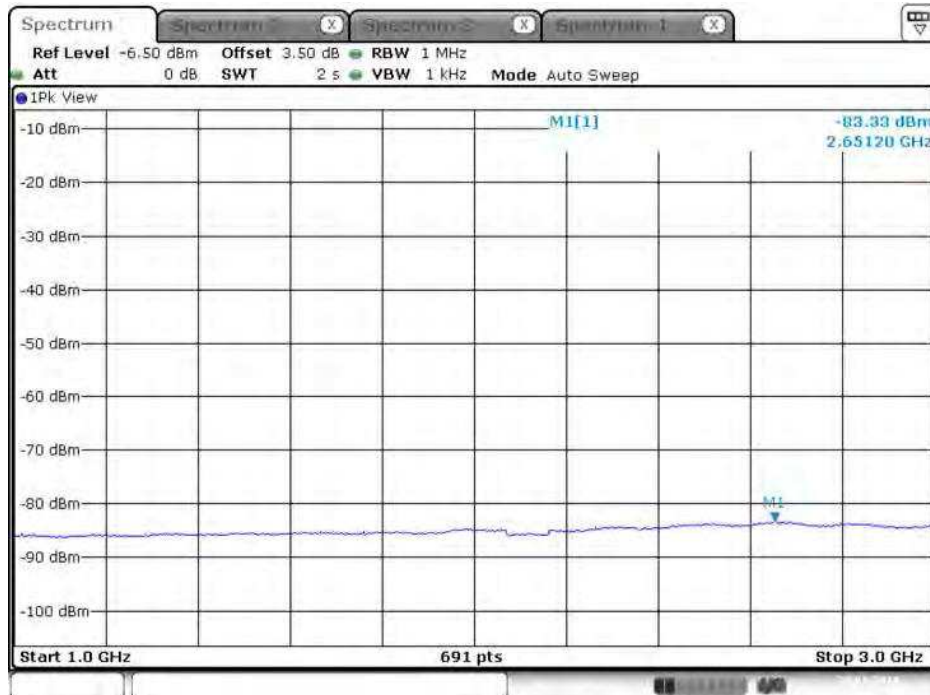
Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5495	-72.12	-72.01	-45.05	-41.25	3.80
5550	-72.11	-72.13	-45.11	-41.25	3.86
5700	-72.07	-72.10	-45.07	-41.25	3.82
5720 (Straddle Channel)	-71.96	-72.14	-45.04	-41.25	3.79

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Peak / Port 1 + Port 2 / 18GHz~40GHz

Frequency (MHz)	Chain(TX1) Spurious Level (dBm)	Chain(TX2) Spurious Level (dBm)	Total Spurious Level (dBm)	Limit (dBm)	Margin (dBm)
5495	-60.81	-60.51	-33.65	-21.25	12.40
5550	-60.39	-60.06	-33.21	-21.25	11.96
5700	-59.96	-60.26	-33.10	-21.25	11.85
5720 (Straddle Channel)	-61.00	-60.23	-33.59	-21.25	12.34

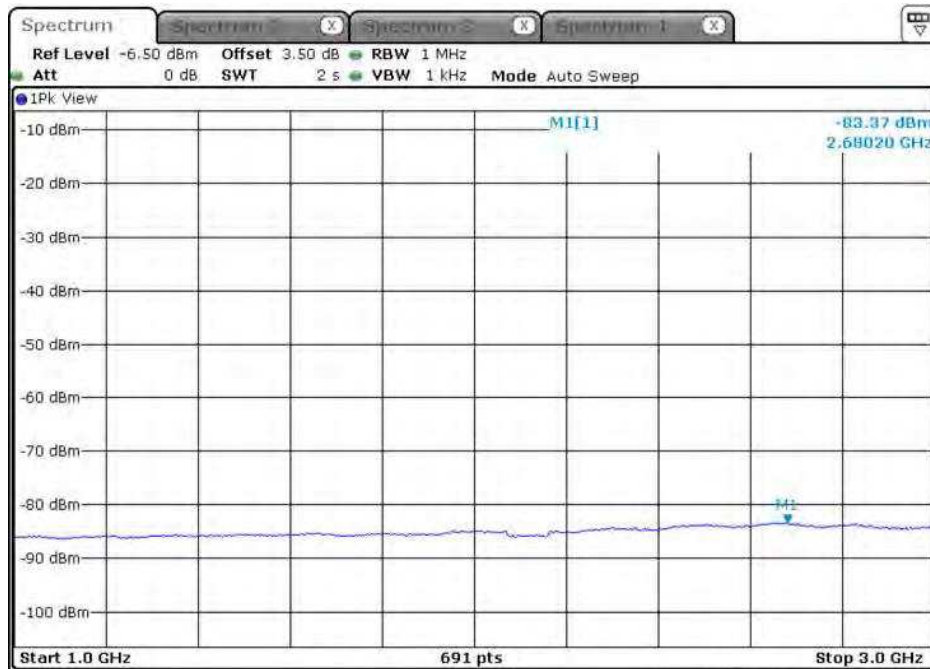
For Antenna 1:

Plot on Configuration QPSK, 10M / 5255 MHz / Average / Port 1 / 1GHz~3GHz



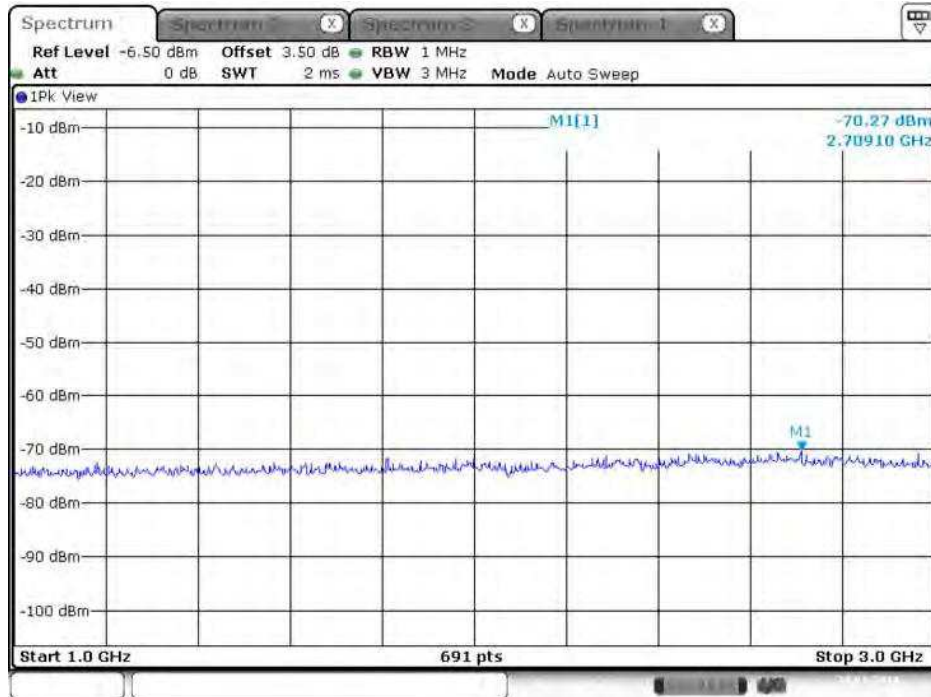
Date: 24.JAN.2018 16:42:13

Plot on Configuration QPSK, 10M / 5255 MHz / Average / Port 2 / 1GHz~3GHz



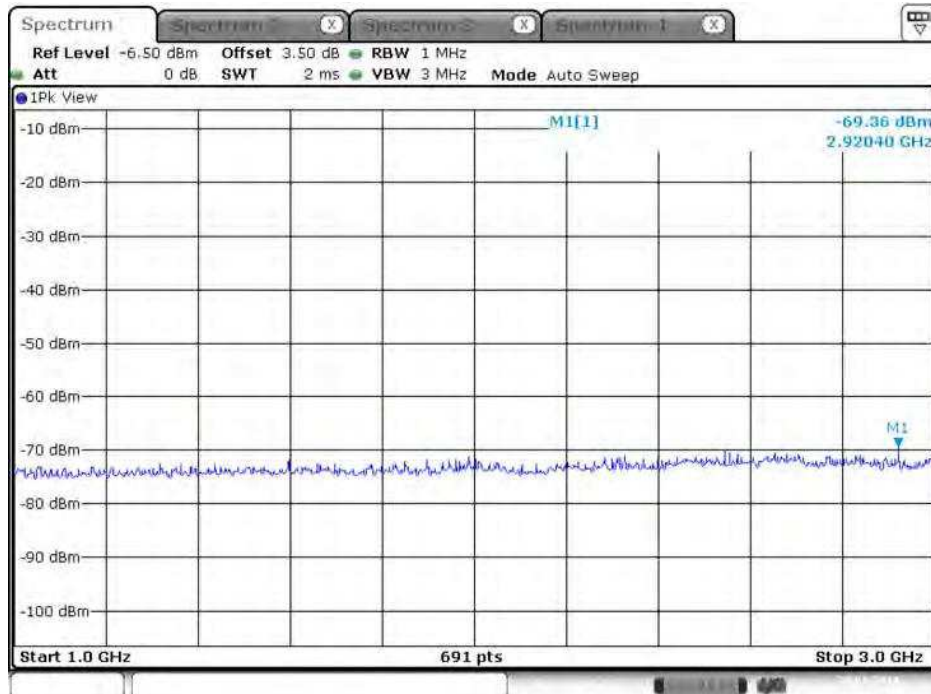
Date: 24.JAN.2018 17:32:19

Plot on Configuration QPSK, 10M / 5255 MHz / Peak / Port 1 / 1GHz~3GHz



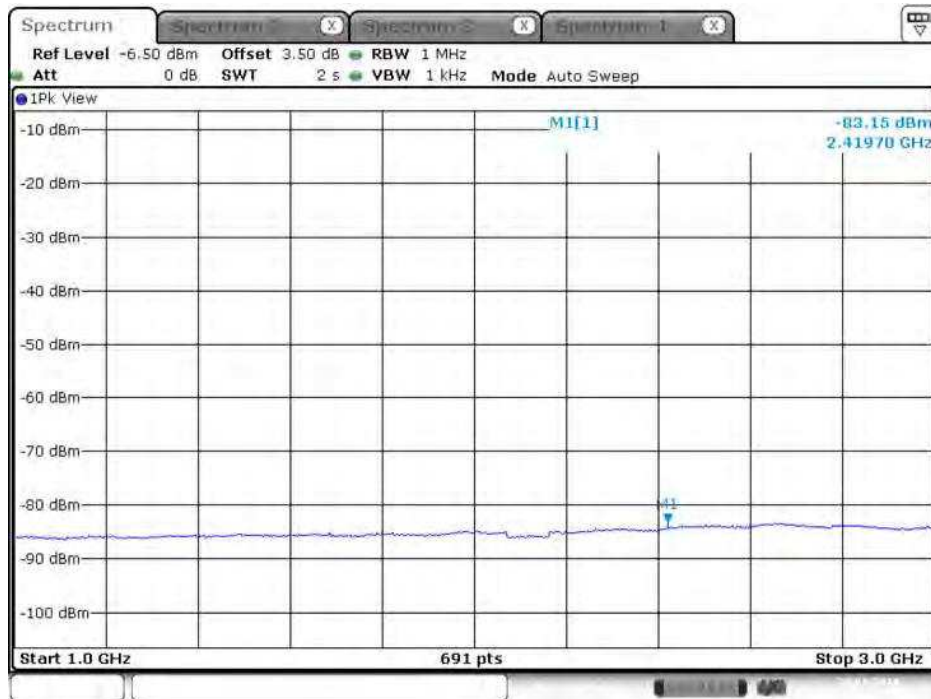
Date: 24.JAN.2018 16:43:40

Plot on Configuration QPSK, 10M / 5255 MHz / Peak / Port 2 / 1GHz~3GHz



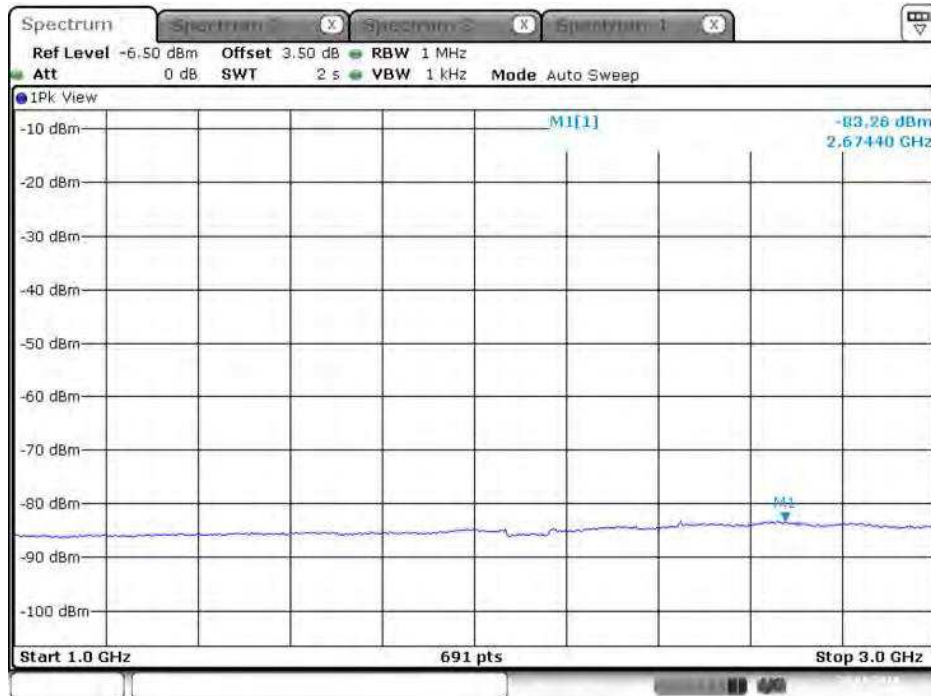
Date: 24.JAN.2018 17:33:09

Plot on Configuration QPSK, 10M / 5295 MHz / Average / Port 1 / 1GHz~3GHz



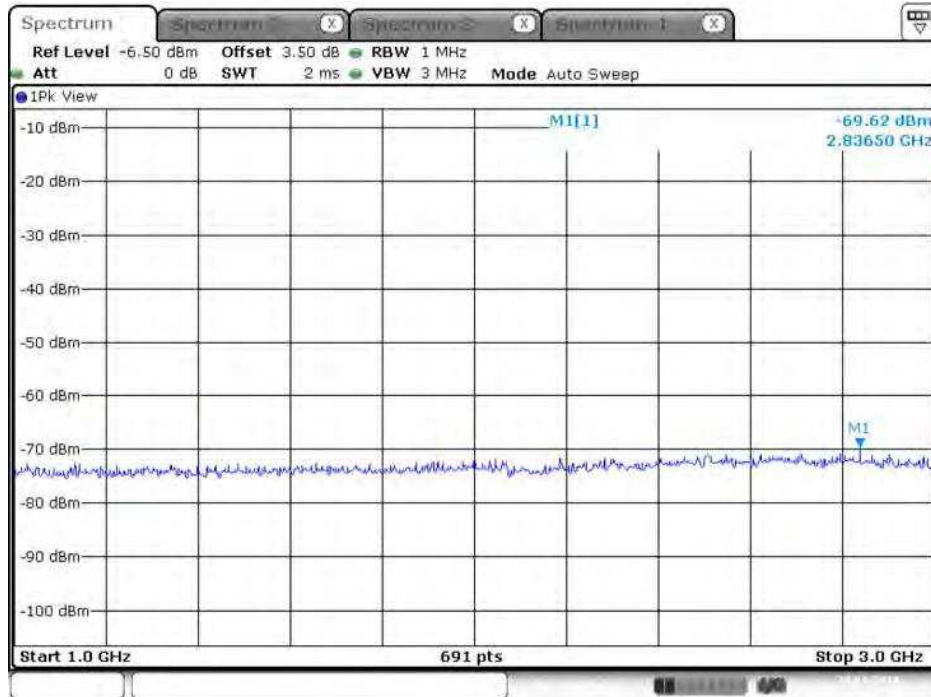
Date: 24.JAN.2018 17:37:20

Plot on Configuration QPSK, 10M / 5295 MHz / Average / Port 2 / 1GHz~3GHz



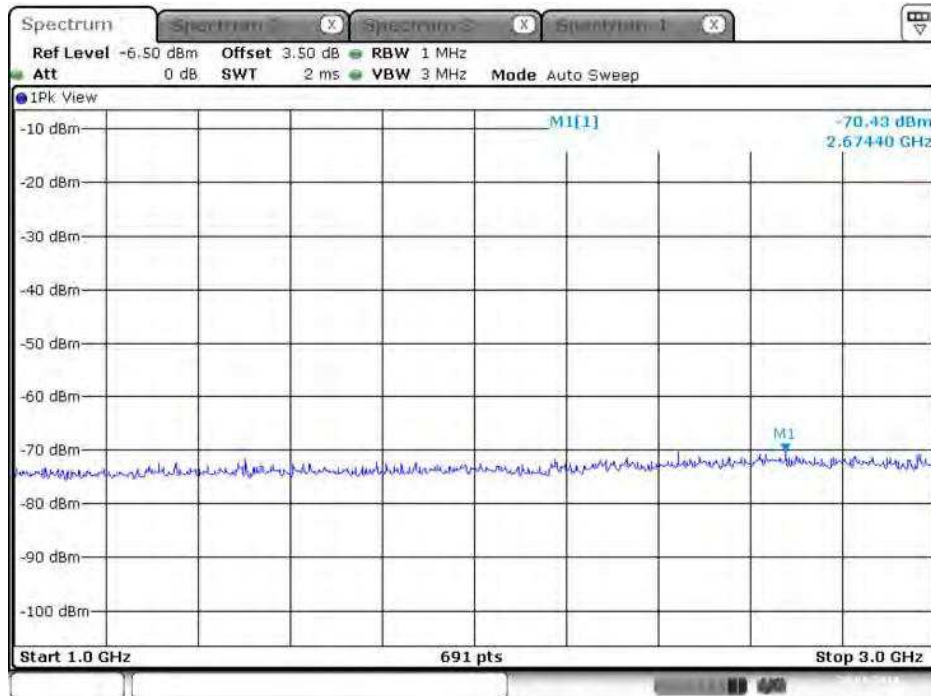
Date: 24.JAN.2018 18:08:41

Plot on Configuration QPSK, 10M / 5295 MHz / Peak / Port 1 / 1GHz~3GHz



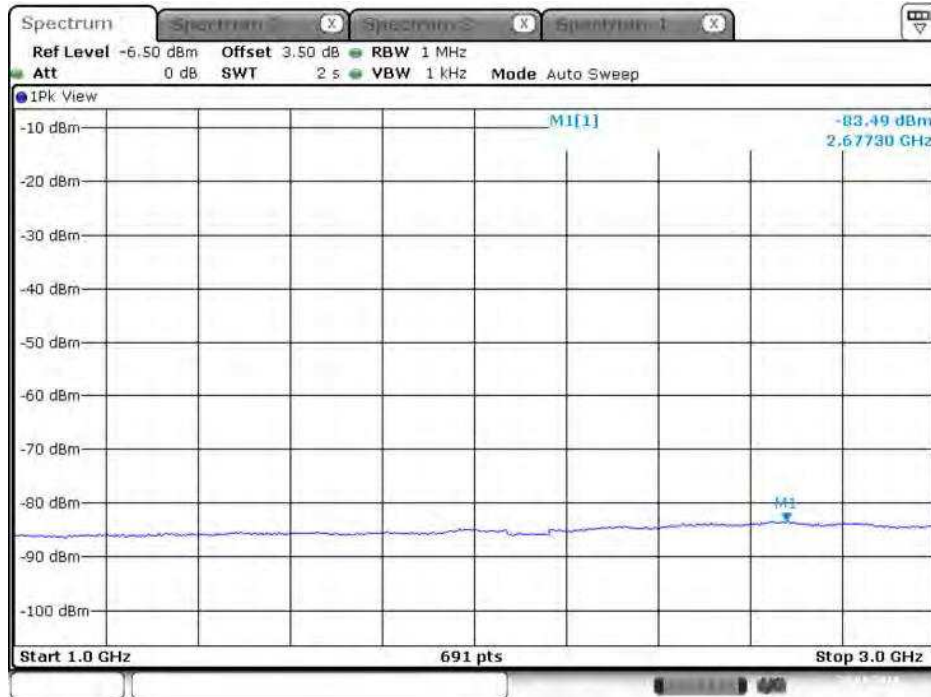
Date: 24.JAN.2018 17:38:57

Plot on Configuration QPSK, 10M / 5295 MHz / Peak / Port 2 / 1GHz~3GHz



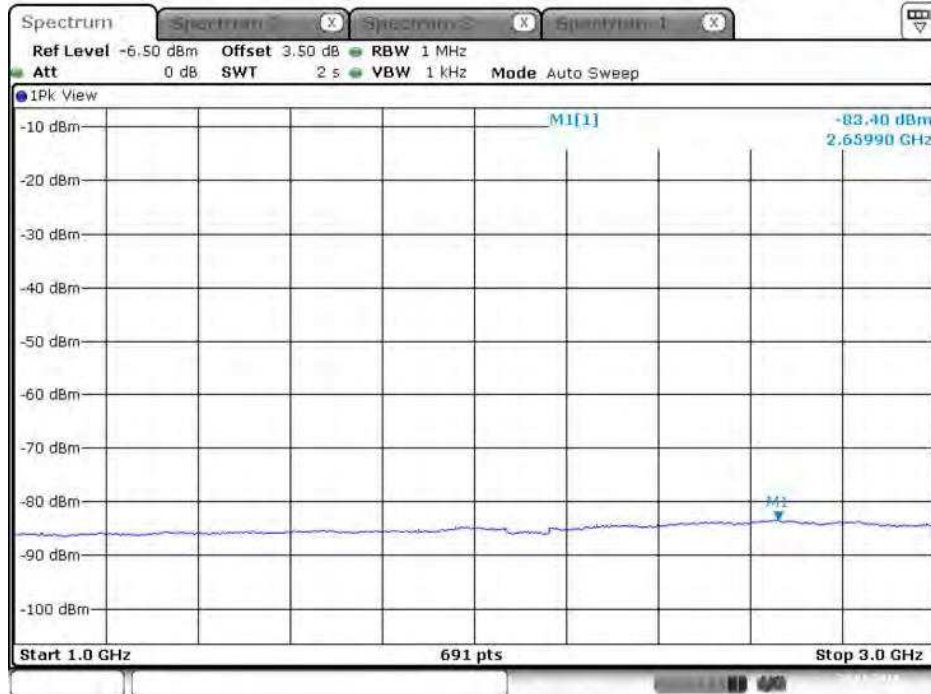
Date: 24.JAN.2018 18:09:21

Plot on Configuration QPSK, 10M / 5340 MHz / Average / Port 1 / 1GHz~3GHz



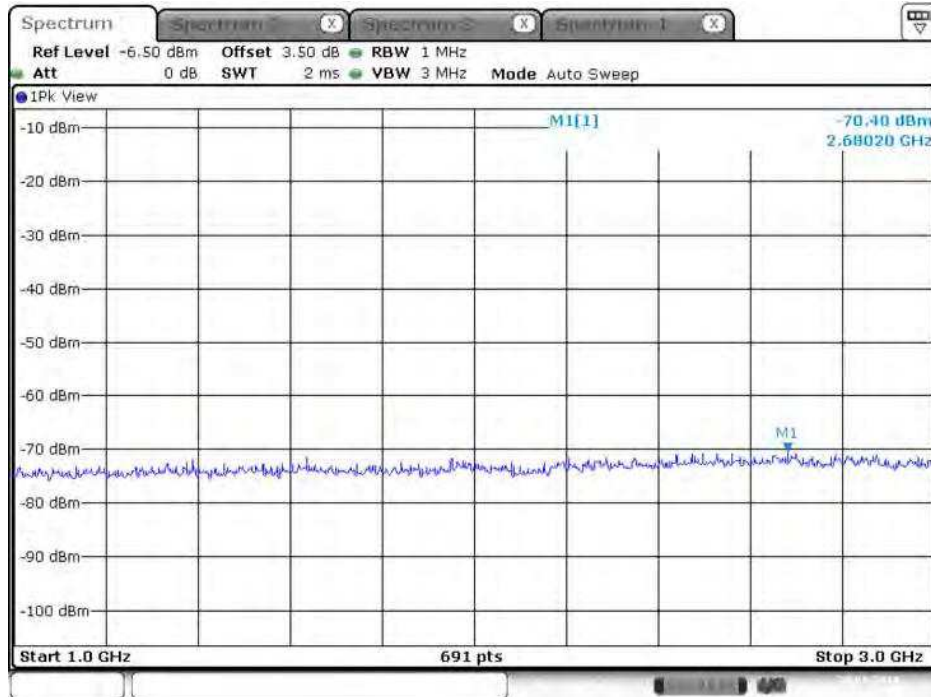
Date: 24.JAN.2018 18:53:09

Plot on Configuration QPSK, 10M / 5340 MHz / Average / Port 2 / 1GHz~3GHz



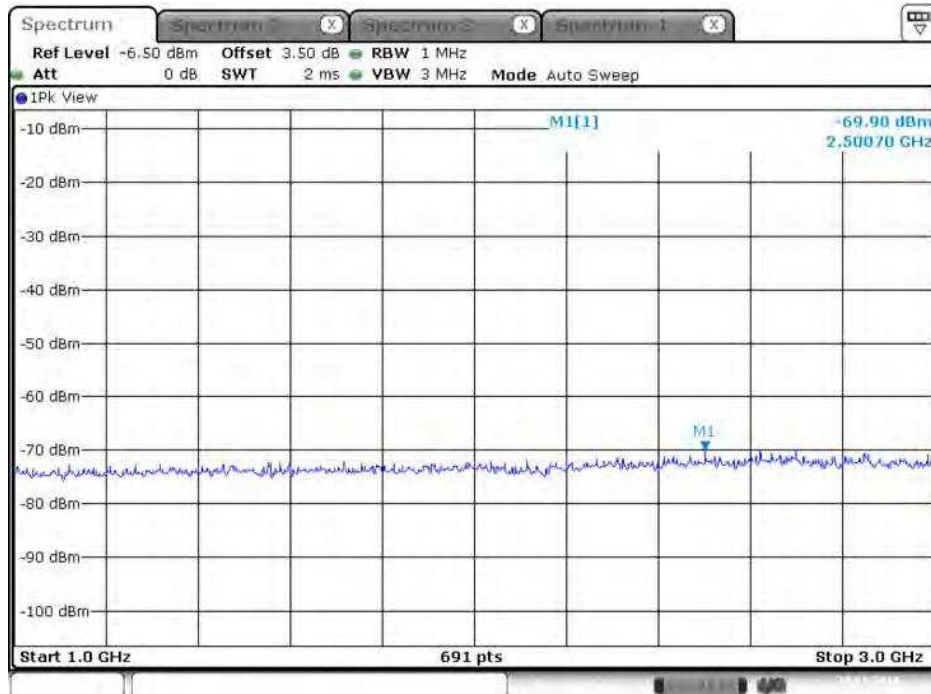
Date: 24.JAN.2018 19:18:28

Plot on Configuration QPSK, 10M / 5340 MHz / Peak / Port 1 / 1GHz~3GHz



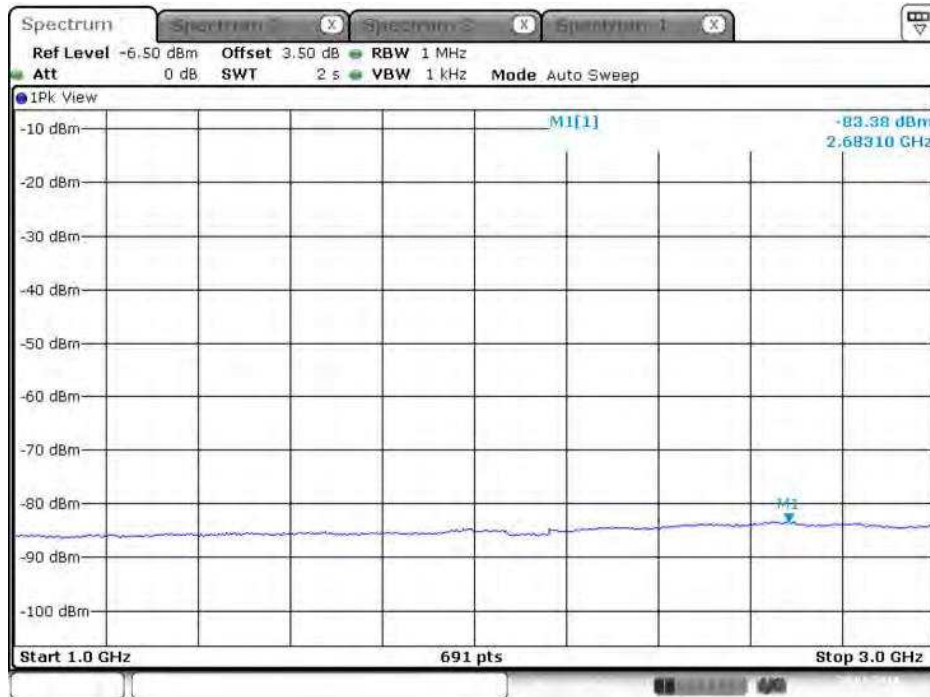
Date: 24.JAN.2018 18:54:08

Plot on Configuration QPSK, 10M / 5340 MHz / Peak / Port 2 / 1GHz~3GHz



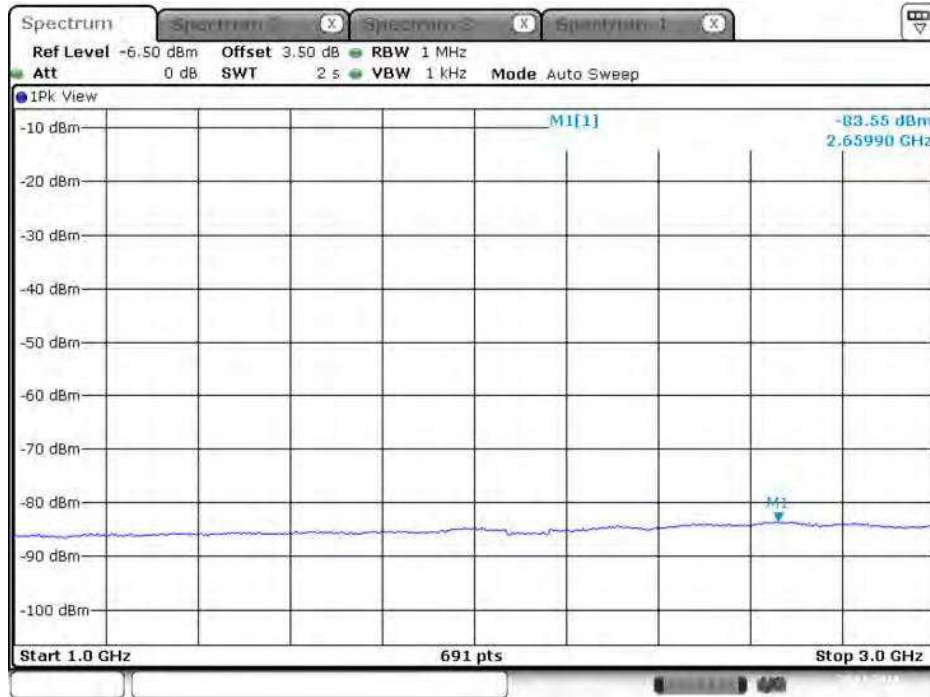
Date: 24.JAN.2018 19:19:11

Plot on Configuration QPSK, 10M / 5250 MHz (Straddle Channel) / Average / Port 1 / 1GHz~3GHz



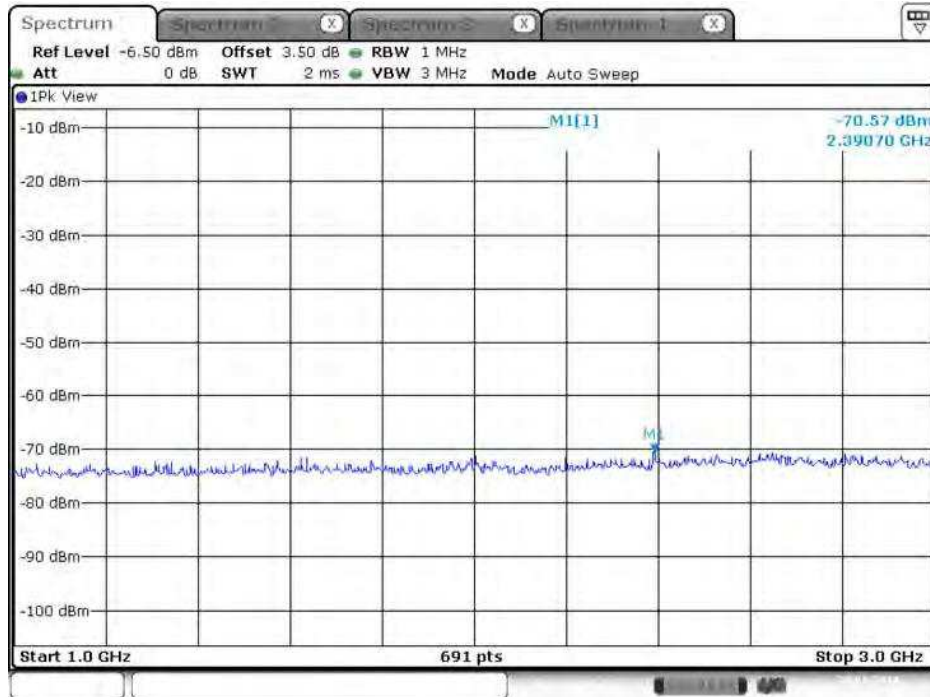
Date: 24.JAN.2018 19:22:24

Plot on Configuration QPSK, 10M / 5250 MHz (Straddle Channel) / Average / Port 2 / 1GHz~3GHz



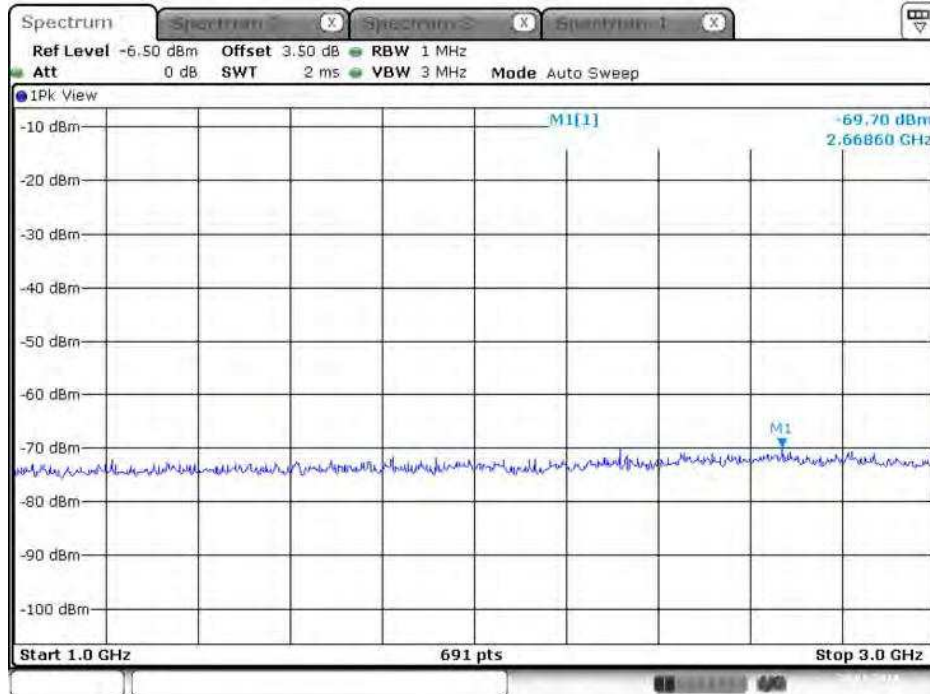
Date: 24.JAN.2018 19:47:27

Plot on Configuration QPSK, 10M / 5250 MHz (Straddle Channel) / Peak / Port 1 / 1GHz~3GHz



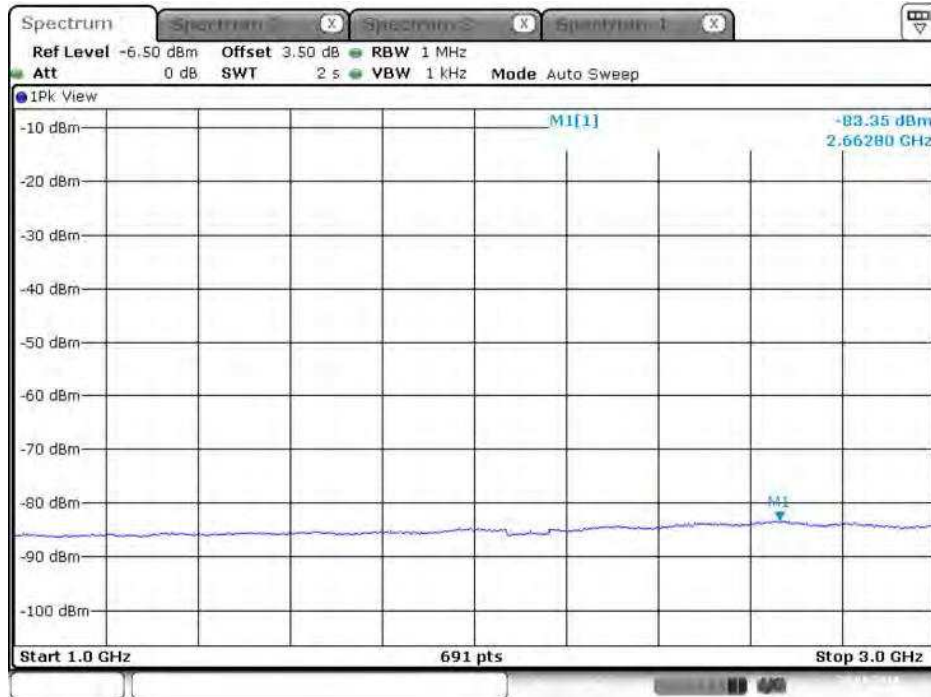
Date: 24.JAN.2018 19:23:19

Plot on Configuration QPSK, 10M / 5250 MHz (Straddle Channel) / Peak / Port 2 / 1GHz~3GHz



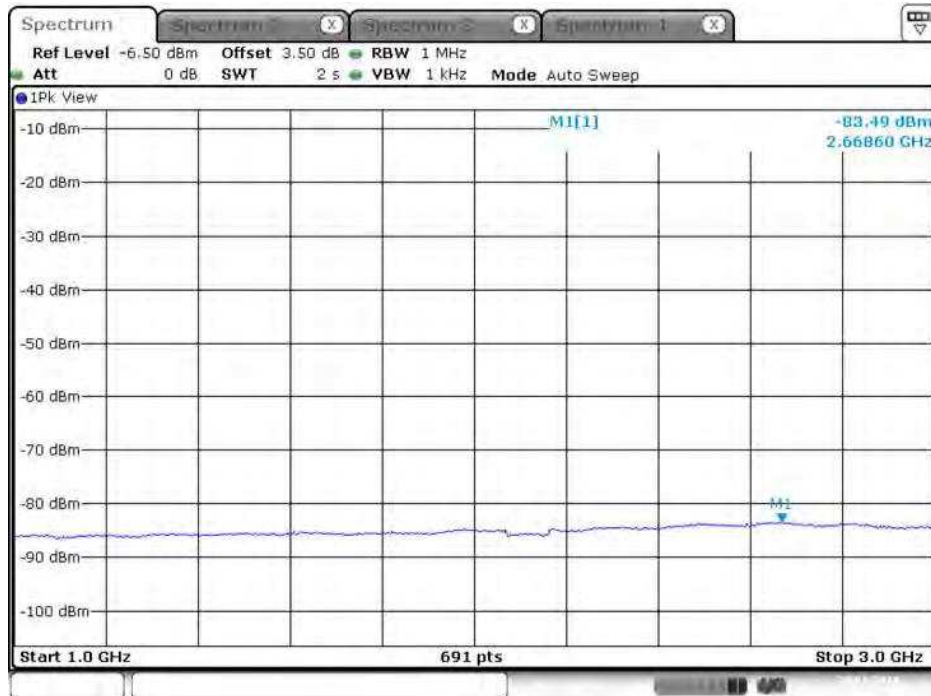
Date: 24.JAN.2018 19:48:37

Plot on Configuration QPSK, 40M / 5270 MHz / Average / Port 1 / 1GHz~3GHz



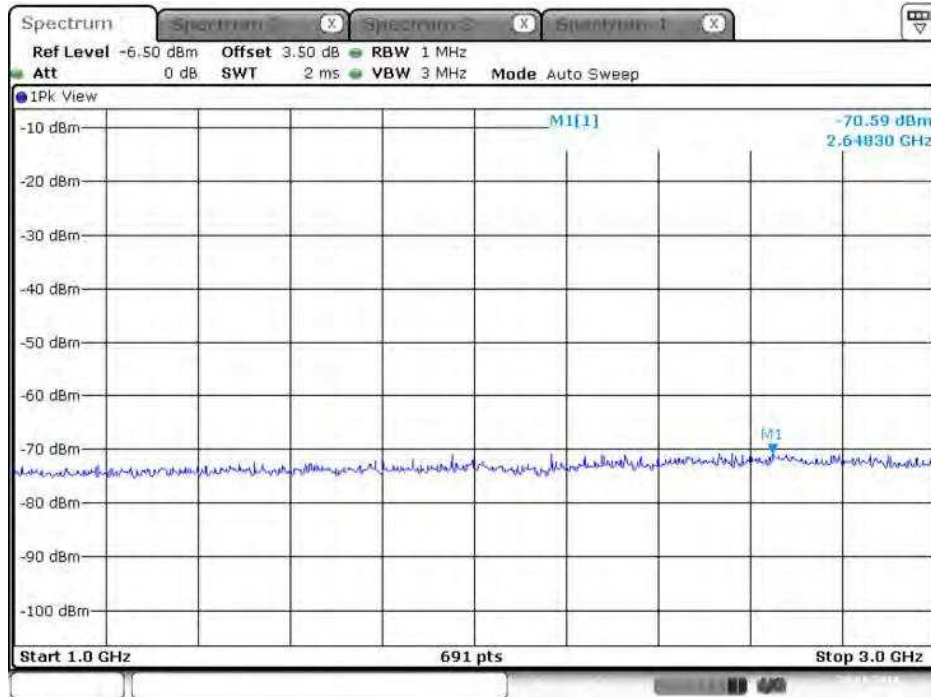
Date: 24.JAN.2018 19:51:42

Plot on Configuration QPSK, 40M / 5270 MHz / Average / Port 2 / 1GHz~3GHz



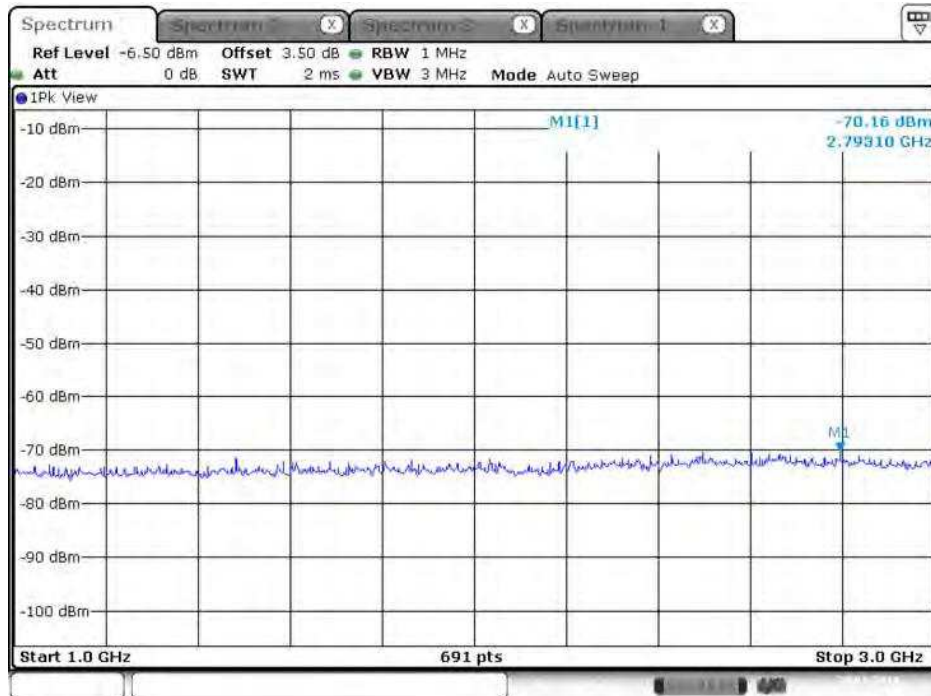
Date: 24.JAN.2018 20:20:52

Plot on Configuration QPSK, 40M / 5270 MHz / Peak / Port 1 / 1GHz~3GHz



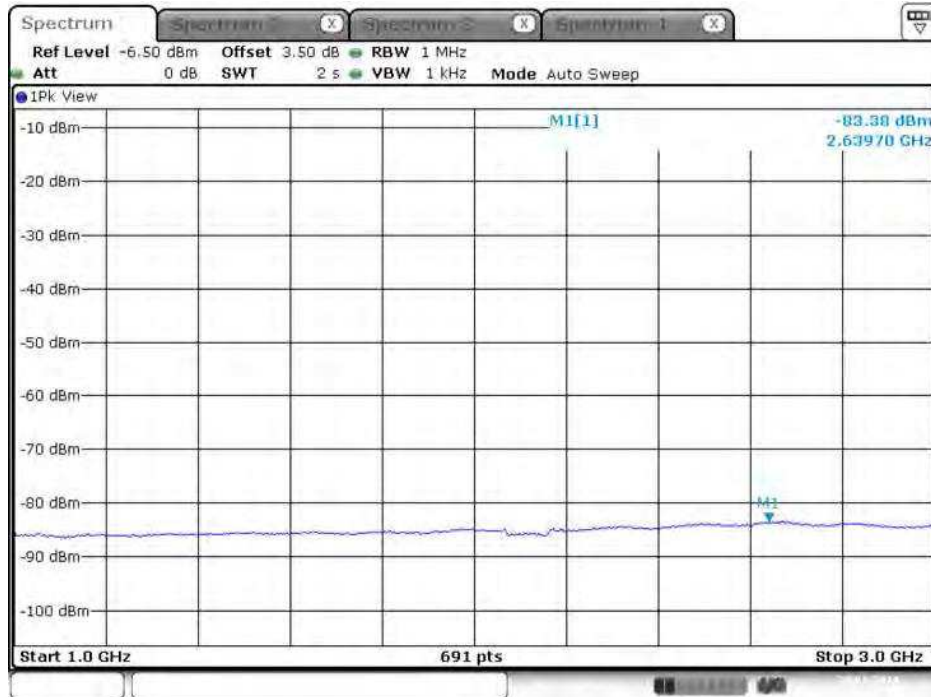
Date: 24.JAN.2018 19:52:25

Plot on Configuration QPSK, 40M / 5270 MHz / Peak / Port 2 / 1GHz~3GHz



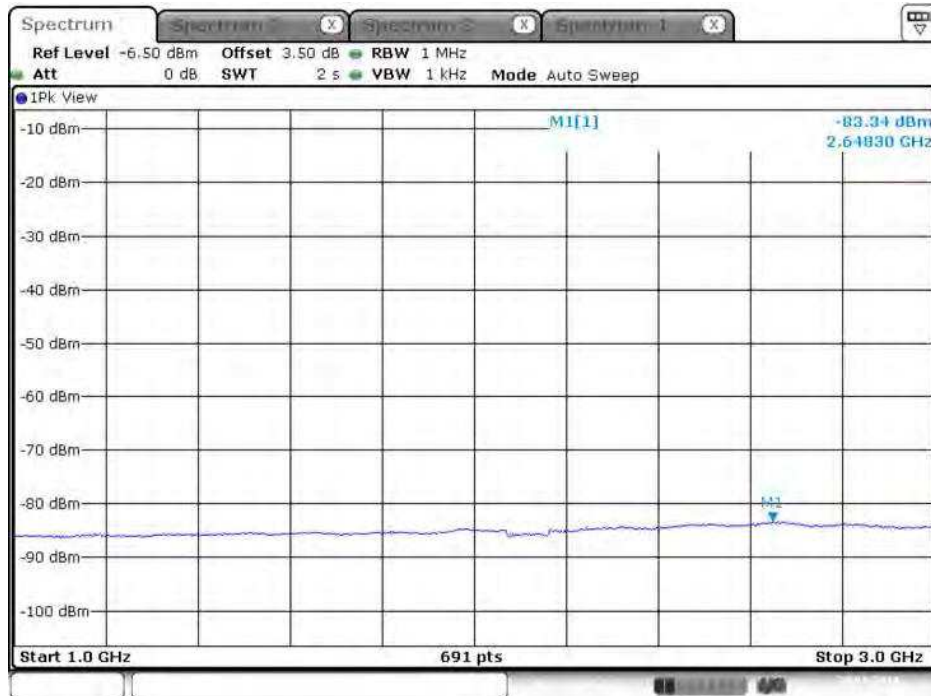
Date: 24.JAN.2018 20:22:01

Plot on Configuration QPSK, 40M / 5300 MHz / Average / Port 1 / 1GHz~3GHz



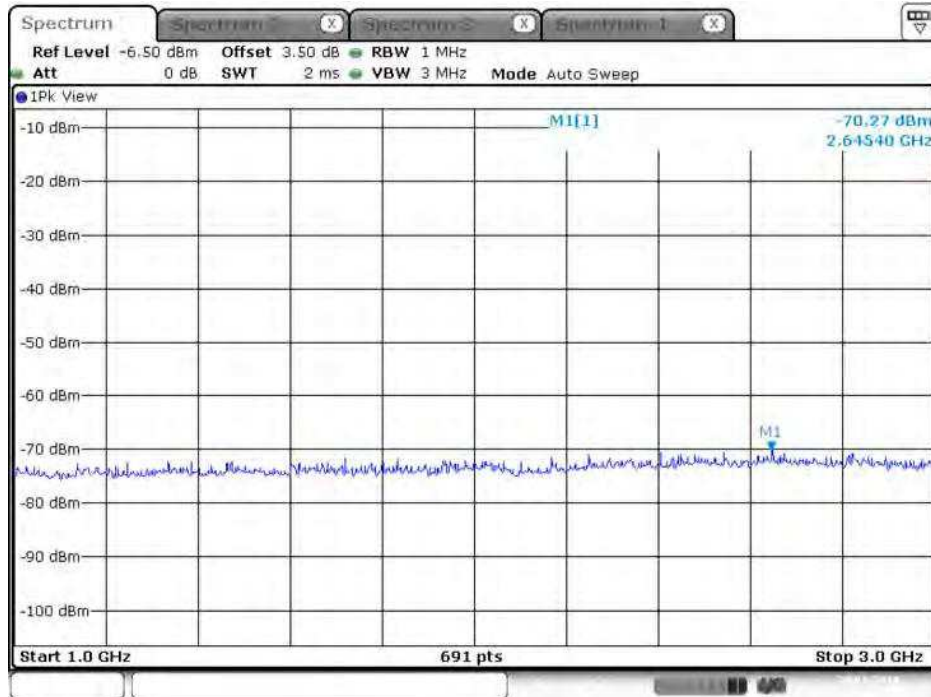
Date: 24.JAN.2018 20:28:43

Plot on Configuration QPSK, 40M / 5300 MHz / Average / Port 2 / 1GHz~3GHz



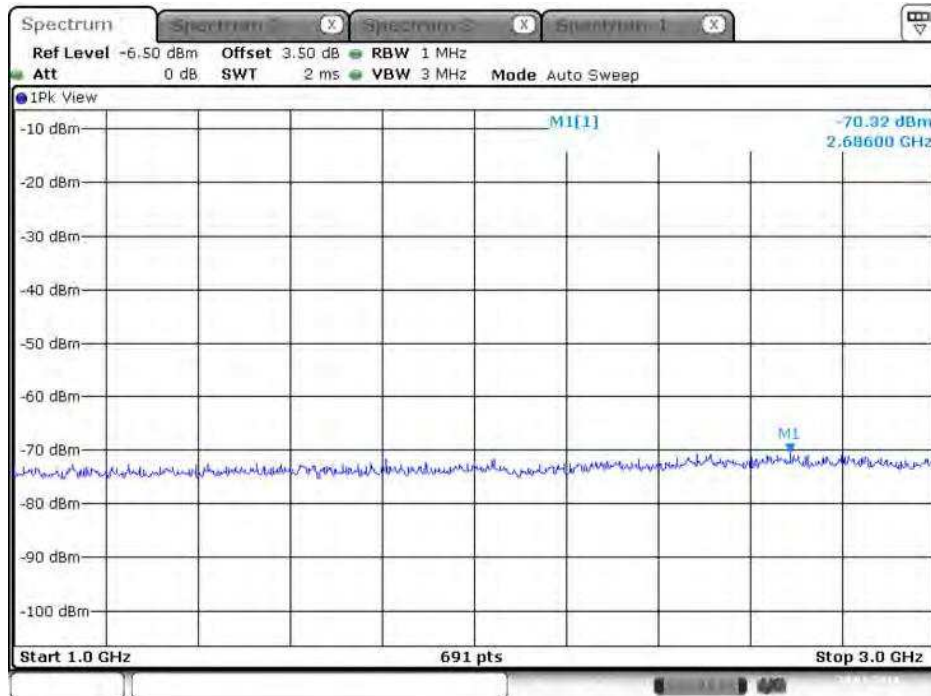
Date: 24.JAN.2018 20:50:34

Plot on Configuration QPSK, 40M / 5300 MHz / Peak / Port 1 / 1GHz~3GHz



Date: 24.JAN.2018 20:29:41

Plot on Configuration QPSK, 40M / 5300 MHz / Peak / Port 2 / 1GHz~3GHz



Date: 24.JAN.2018 20:51:39