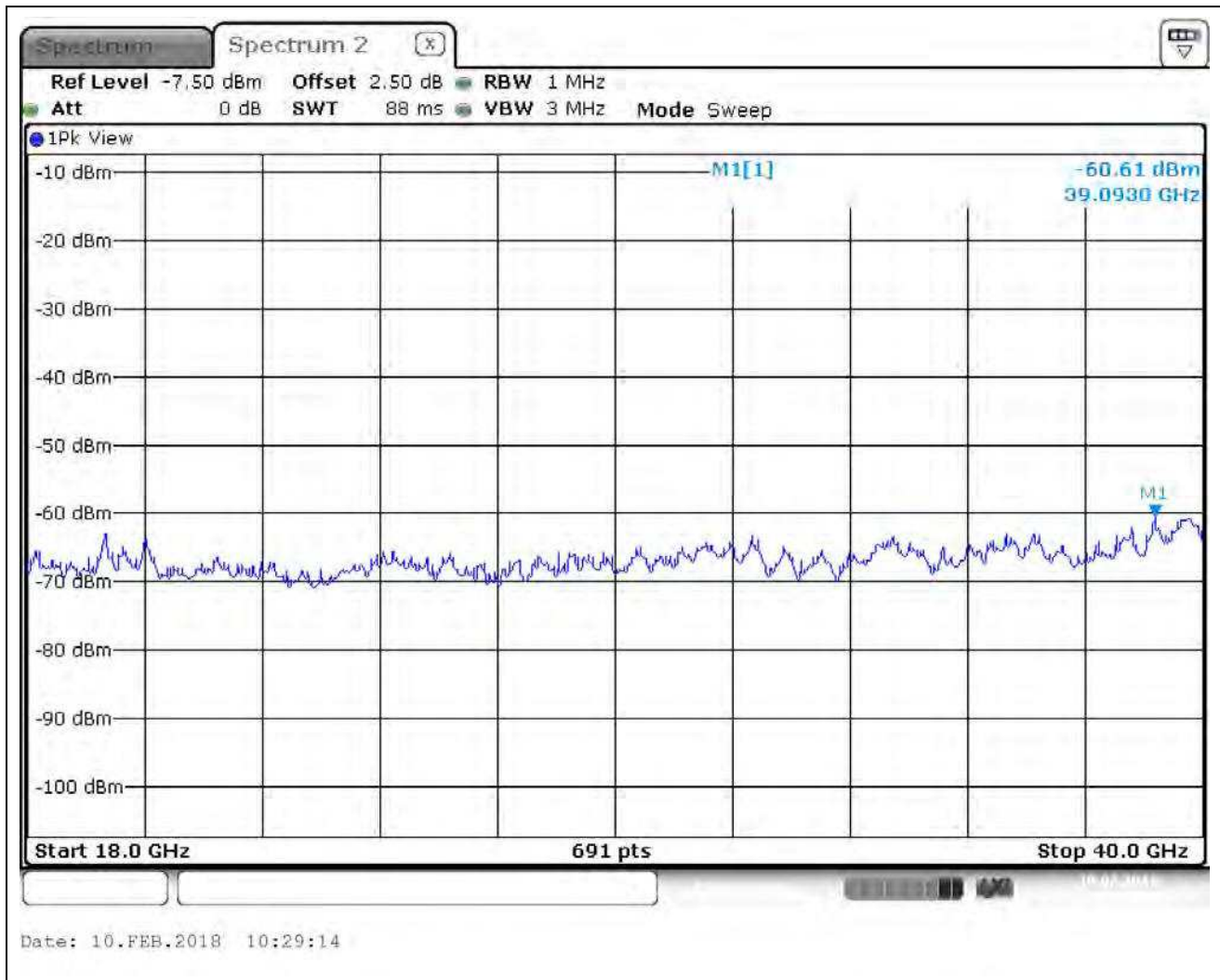
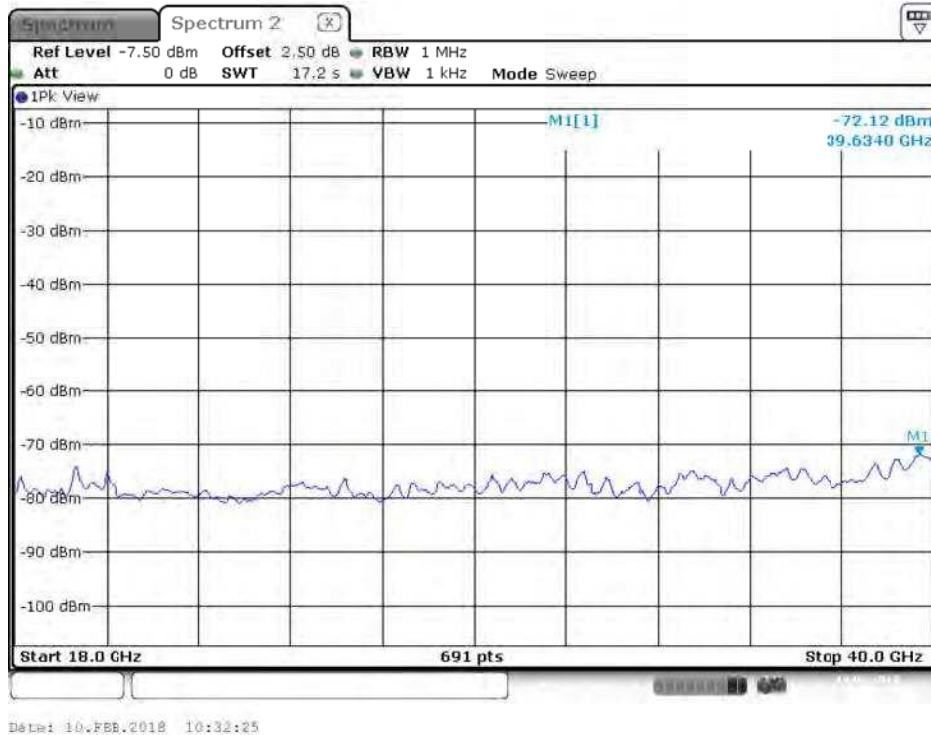
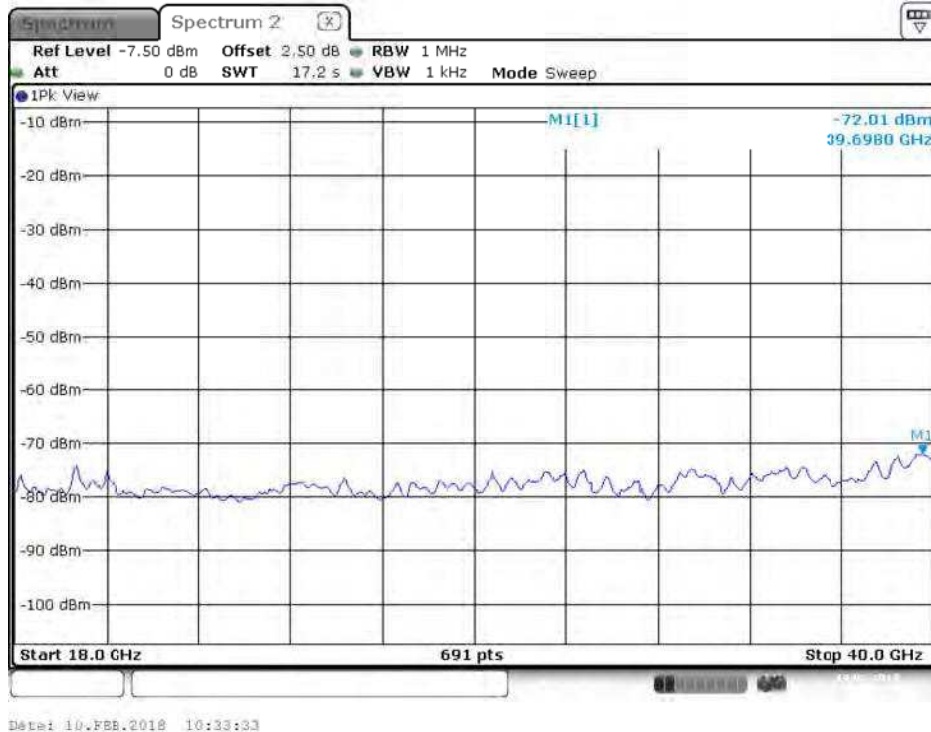




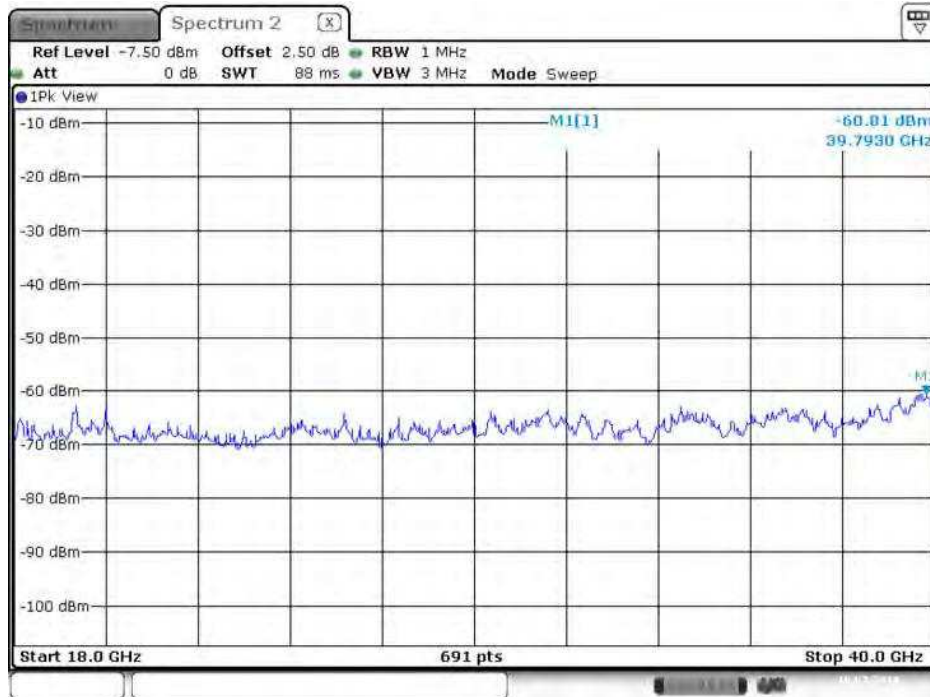
Plot on Configuration QPSK, 40M / 5495 MHz / Average / Port 1 / 18GHz~40GHz



Plot on Configuration QPSK, 40M / 5495 MHz / Average / Port 2 / 18GHz~40GHz

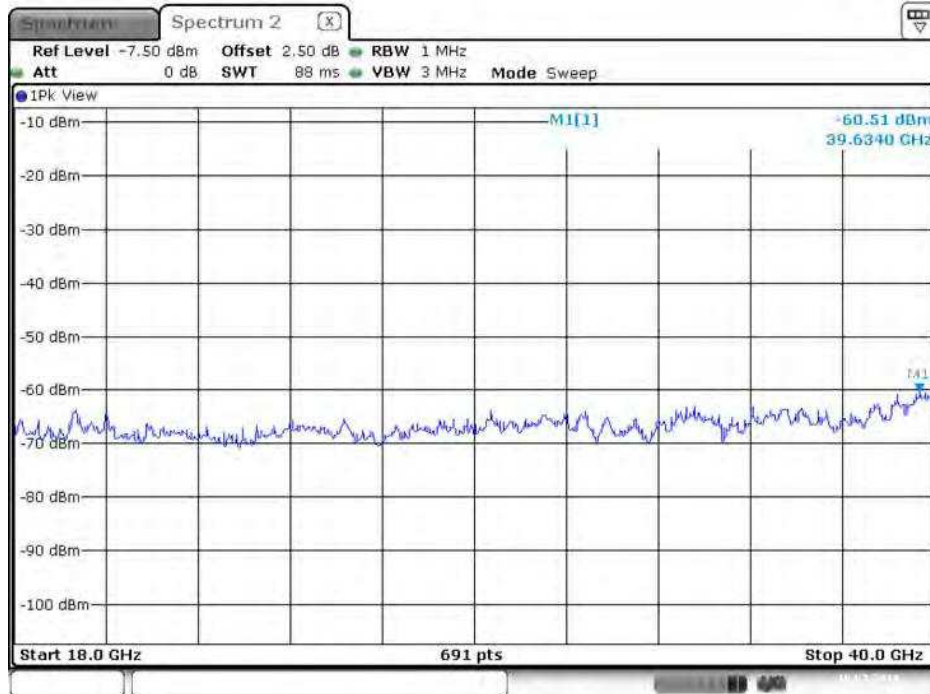


Plot on Configuration QPSK, 40M / 5495 MHz / Peak / Port 1 / 18GHz~40GHz



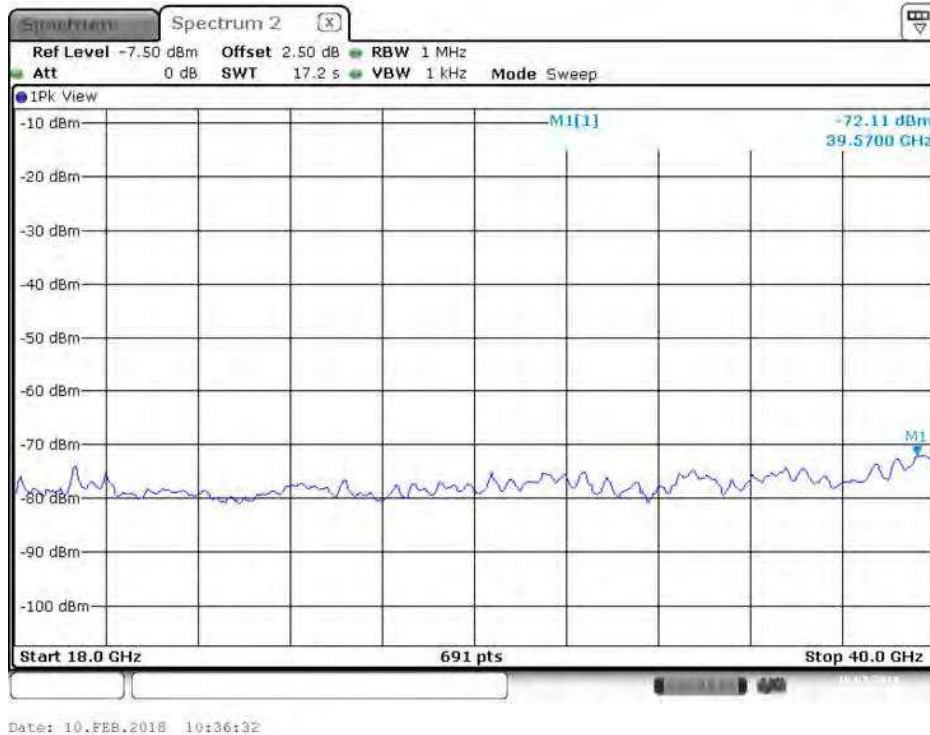
Date: 10.FEB.2018 10:32:33

Plot on Configuration QPSK, 40M / 5495 MHz / Peak / Port 2 / 18GHz~40GHz

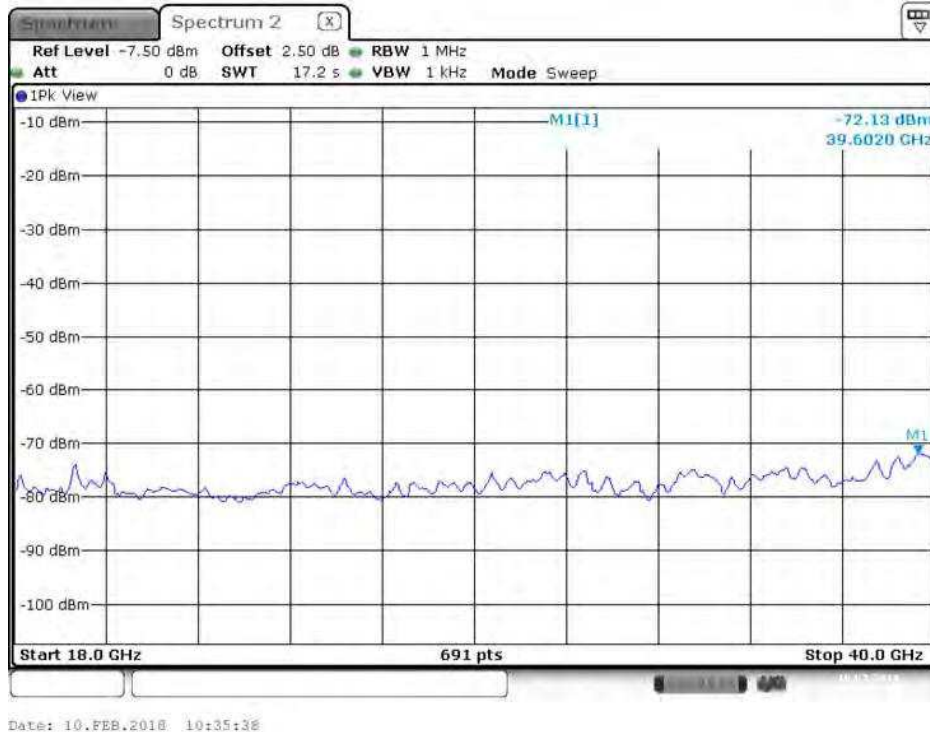


Date: 10.FEB.2018 10:33:42

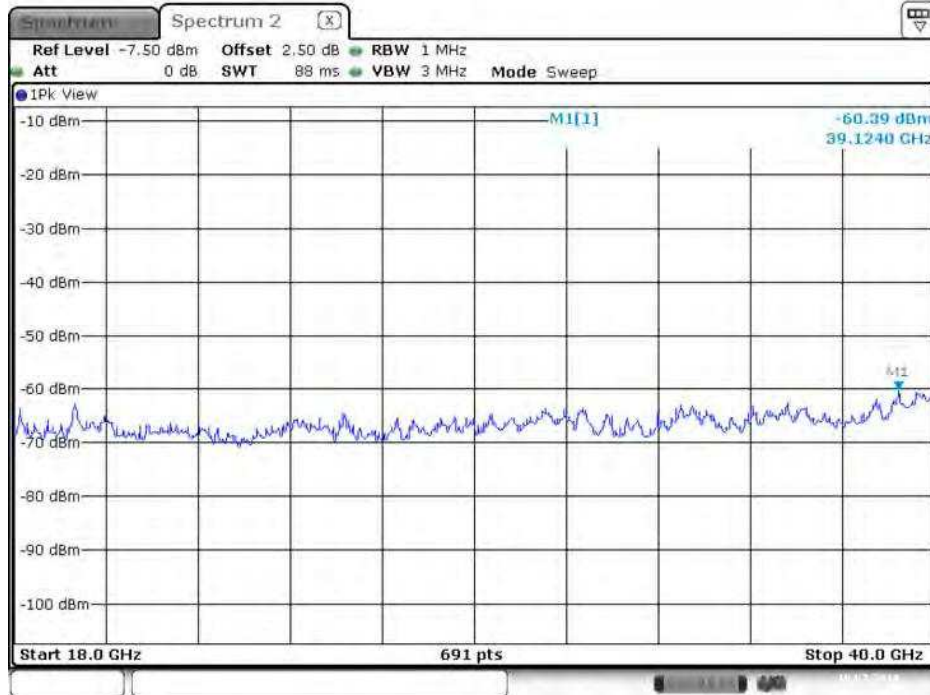
Plot on Configuration QPSK, 40M / 5550 MHz / Average / Port 1 / 18GHz~40GHz



Plot on Configuration QPSK, 40M / 5550 MHz / Average / Port 2 / 18GHz~40GHz

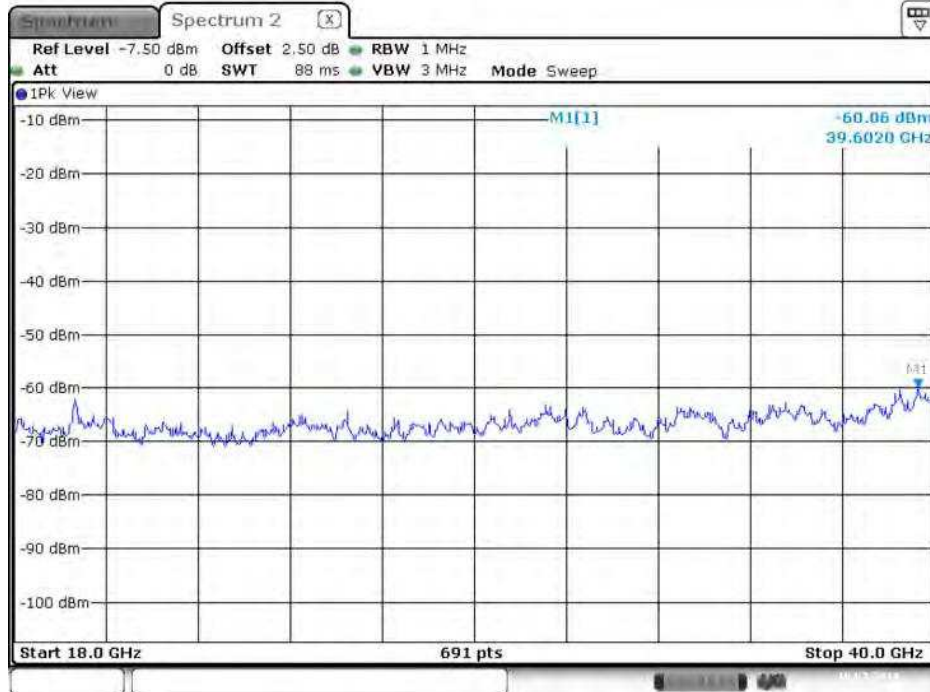


Plot on Configuration QPSK, 40M / 5550 MHz / Peak / Port 1 /18GHz~40GHz



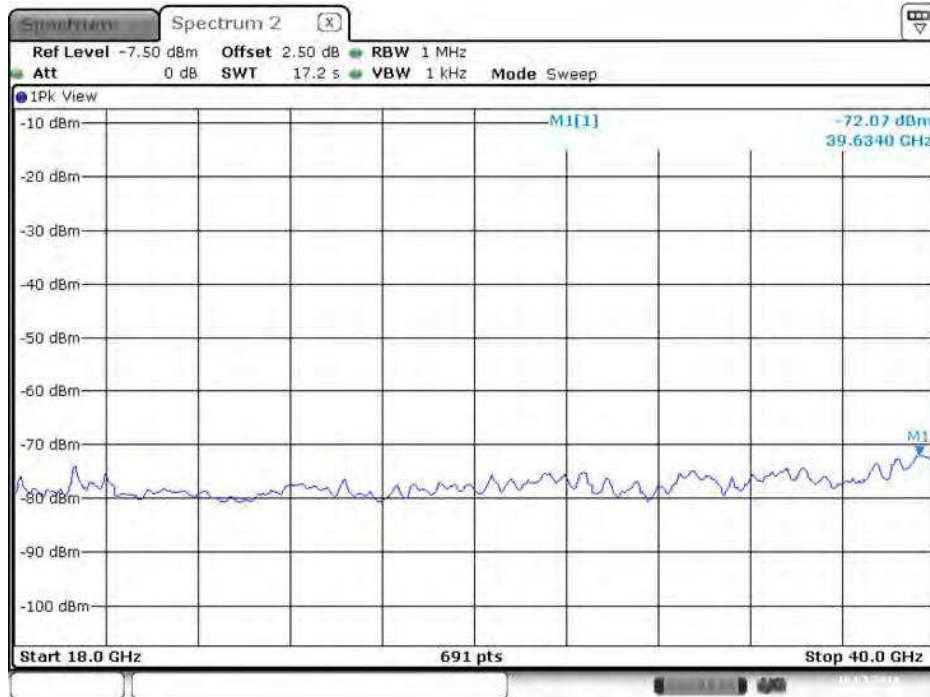
Date: 10.FEB.2018 10:36:41

Plot on Configuration QPSK, 40M / 5550 MHz / Peak / Port 2 / 18GHz~40GHz



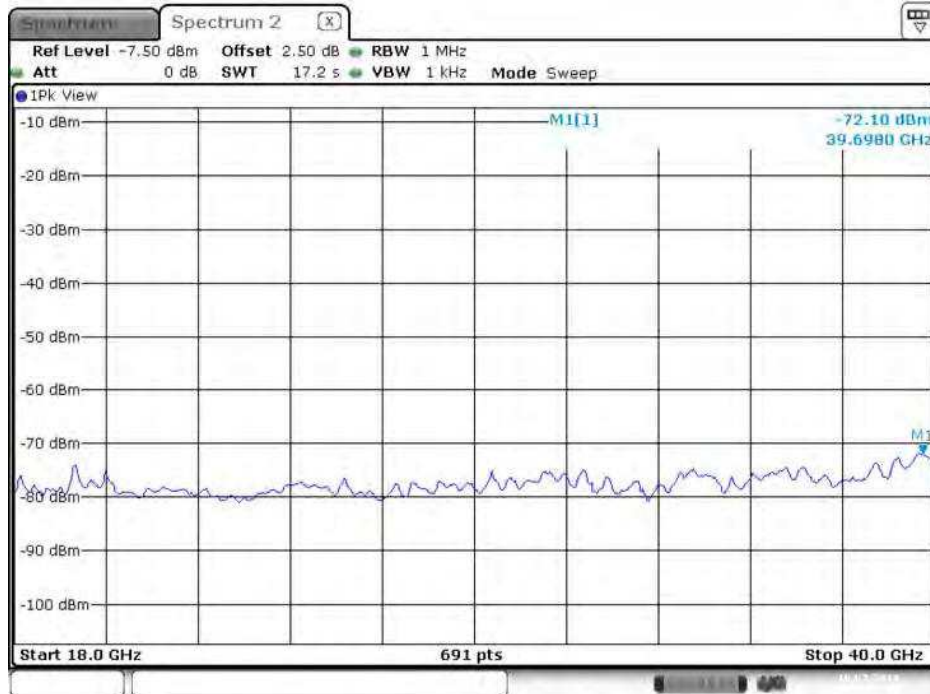
Date: 10.FEB.2018 10:35:46

Plot on Configuration QPSK, 40M / 5700 MHz / Average / Port 1 / 18GHz~40GHz



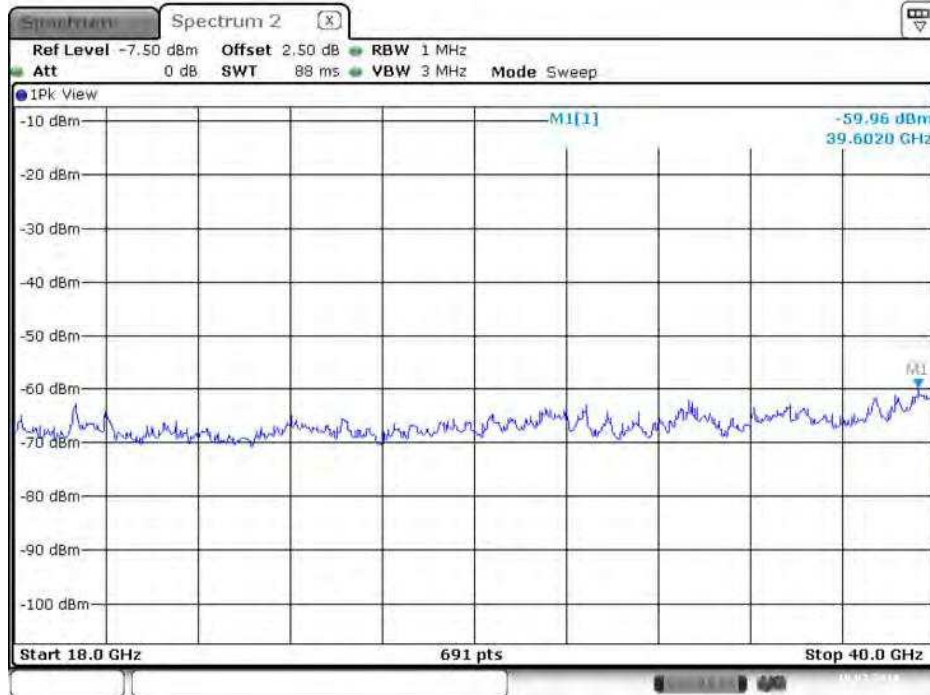
Date: 10.FEB.2018 10:37:42

Plot on Configuration QPSK, 40M / 5700 MHz / Average / Port 2 / 18GHz~40GHz



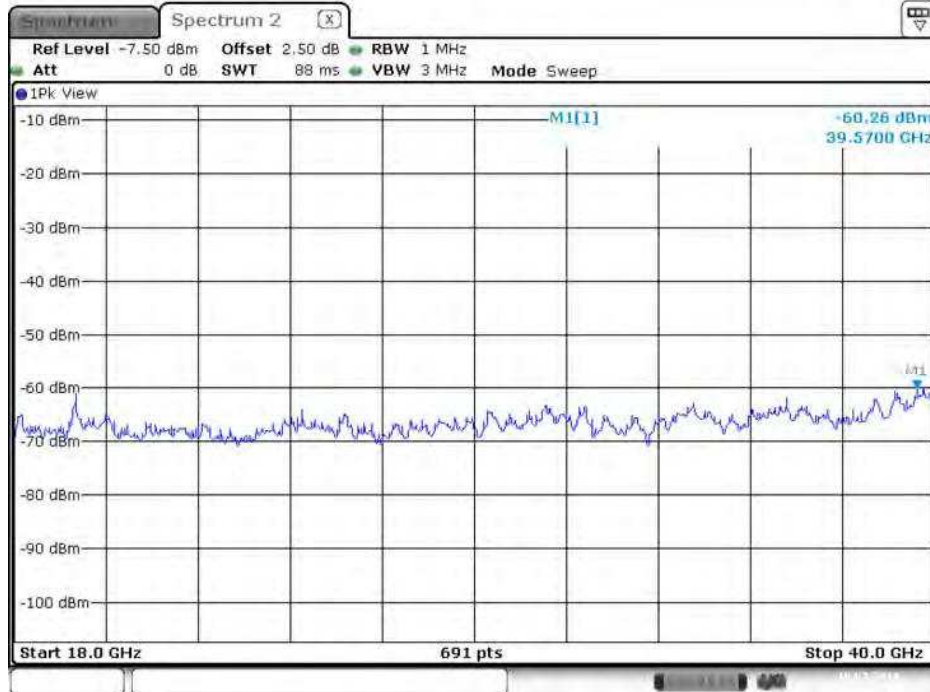
Date: 10.FEB.2018 10:38:32

Plot on Configuration QPSK, 40M / 5700 MHz / Peak / Port 1 / 18GHz~40GHz



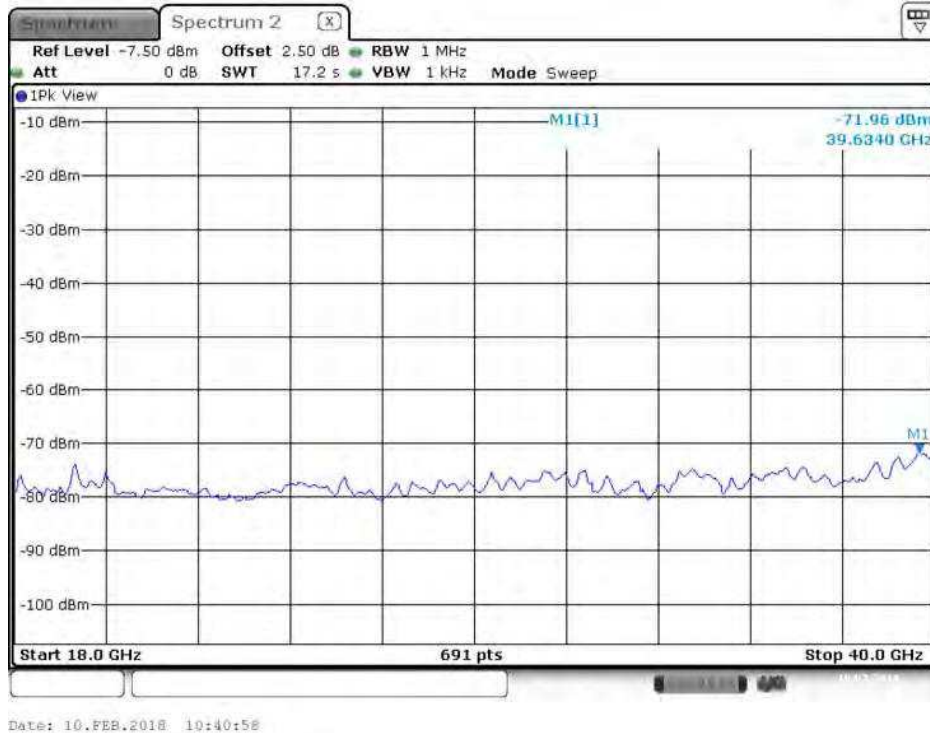
Date: 10.FEB.2018 10:37:53

Plot on Configuration QPSK, 40M / 5700 MHz / Peak / Port 2 / 18GHz~40GHz

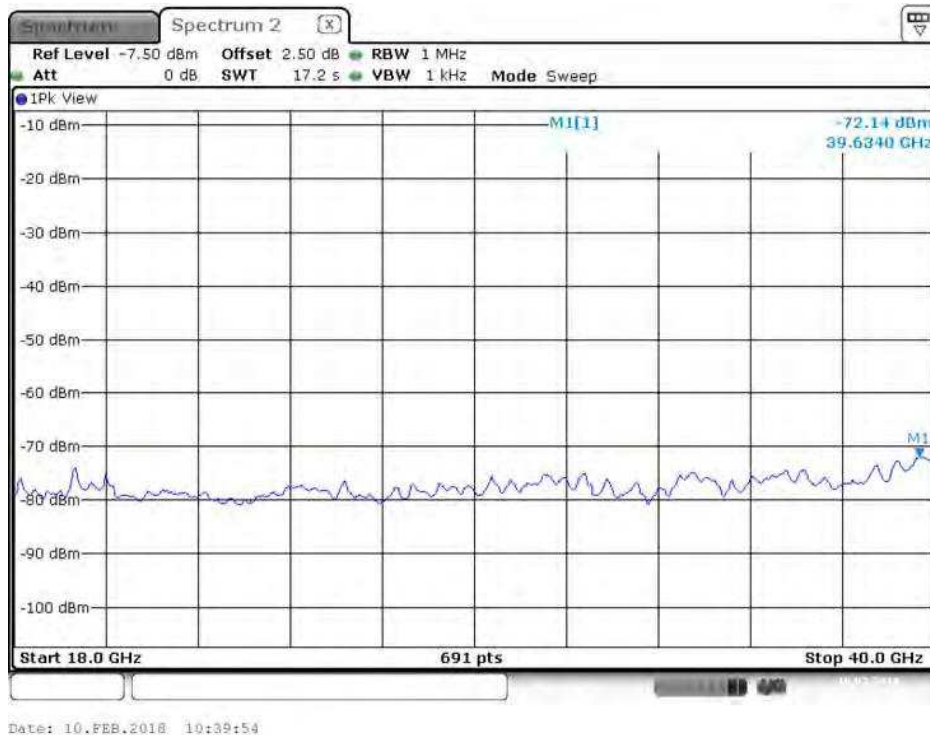


Date: 10.FEB.2018 10:38:42

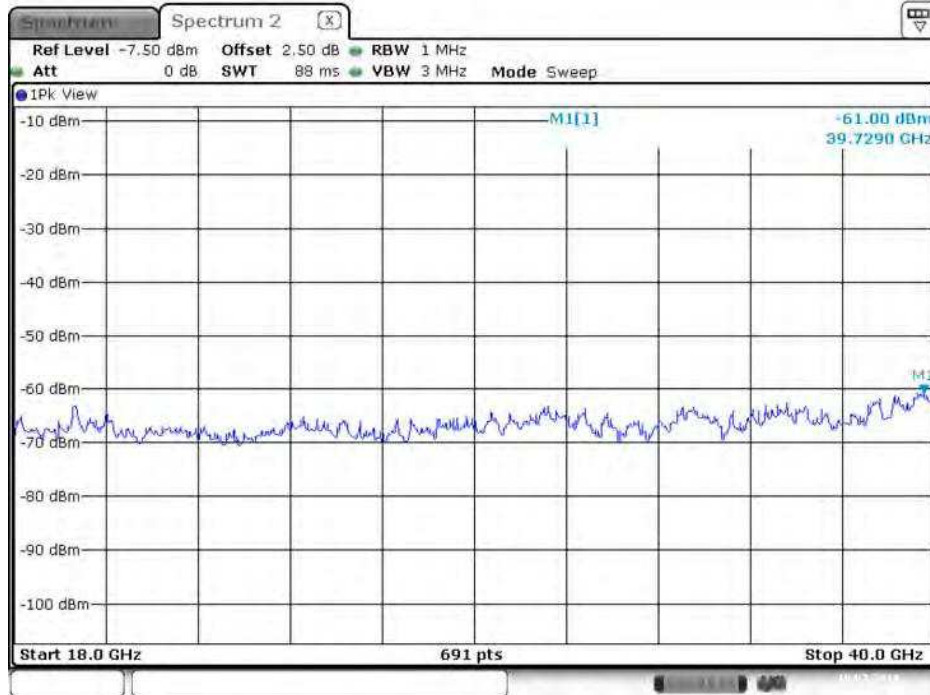
Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Average / Port 1 / 18GHz~40GHz



Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Average / Port 2 / 18GHz~40GHz

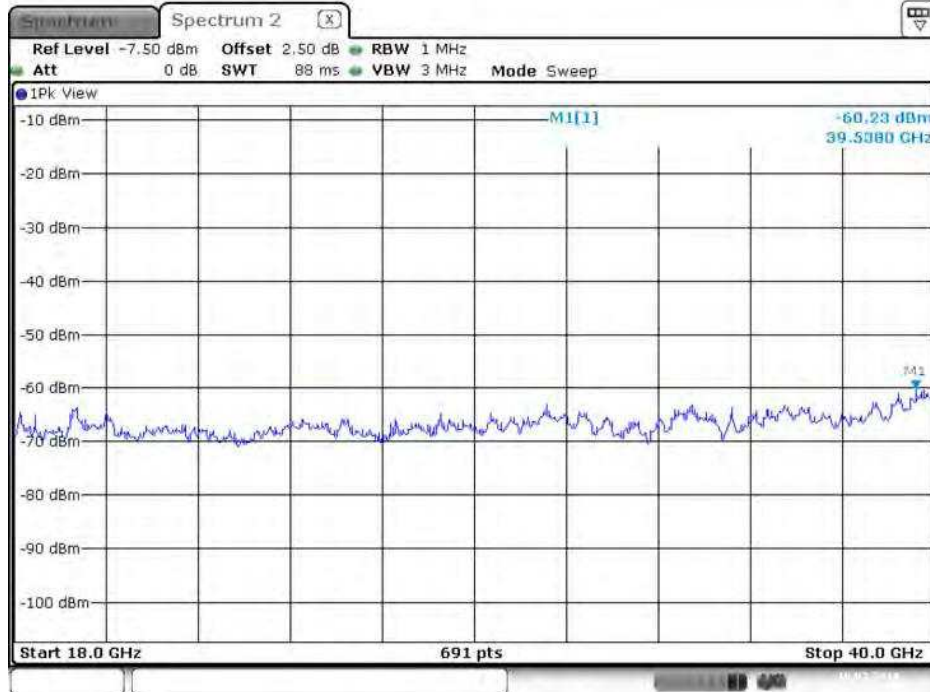


Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Peak / Port 1 / 18GHz~40GHz



Date: 10.FEB.2018 10:41:12

Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Peak / Port 2 / 18GHz~40GHz



Date: 10.FEB.2018 10:40:01



Conducted Bandedge above 1GHz Result

Appendix D.2

For Conducted Bandedge

For Antenna 1:

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Average / Port 1 + Port 2

Frequency(MHz)	Port 1 (TX1) Bandedge Level (dBm)	Port 2 (TX2) Bandedge Level (dBm)	Total Bandedge Level (dBm)	Limit (dBm)	Margin (dB)
5255	-50.53	-50.77	-45.64	-41.25	4.39
5295	-50.73	-50.55	-45.63	-41.25	4.38
5340	-46.65	-45.97	-41.29	-41.25	0.04
5250 (Straddle Channel)	-49.66	-49.83	-44.73	-41.25	3.48

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Average / Port 1 + Port 2

Frequency(MHz)	Port 1 (TX1) Bandedge Level (dBm)	Port 2 (TX2) Bandedge Level (dBm)	Total Bandedge Level (dBm)	Limit (dBm)	Margin (dB)
5270	-50.80	-50.36	-45.56	-41.25	4.31
5300	-47.51	-47.19	-42.34	-41.25	1.09
5325	-47.26	-46.59	-41.90	-41.25	0.65
5250 (Straddle Channel)	-50.00	-49.74	-44.86	-41.25	3.61



Conducted Bandedge above 1GHz Result

Appendix D.2

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Peak / Port 1 + Port 2

Frequency(MHz)	Port 1 (TX1) Bandedge Level (dBm)	Port 2 (TX2) Bandedge Level (dBm)	Total Bandedge Level (dBm)	Limit (dBm)	Margin (dB)
5255	-38.26	-37.81	-33.02	-21.25	11.77
5295	-37.24	-37.69	-32.45	-21.25	11.20
5340	-26.36	-26.96	-21.64	-21.25	0.39
5250 (Straddle Channel)	-38.01	-37.65	-32.82	-21.25	11.57

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Peak / Port 1 + Port 2

Frequency(MHz)	Port 1 (TX1) Bandedge Level (dBm)	Port 2 (TX2) Bandedge Level (dBm)	Total Bandedge Level (dBm)	Limit (dBm)	Margin (dB)
5270	-37.58	-37.90	-32.73	-21.25	11.48
5300	-34.91	-32.42	-28.48	-21.25	7.23
5325	-34.87	-33.22	-28.96	-21.25	7.71
5250 (Straddle Channel)	-35.26	-35.54	-30.39	-21.25	9.14



Conducted Bandedge above 1GHz Result

Appendix D.2

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Average / Port 1 + Port 2

Frequency(MHz)	Port 1 (TX1) Bandedge Level (dBm)	Port 2 (TX2) Bandedge Level (dBm)	Total Bandedge Level (dBm)	Limit (dBm)	Margin (dB)
5480	-48.20	-47.39	-42.77	-41.25	1.52
5575	-49.26	-50.31	-44.74	-41.25	3.49
5715	-47.79	-46.28	-41.96	-41.25	0.71
5725 (Straddle Channel)	-50.34	-50.39	-45.35	-41.25	4.10

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Average / Port 1 + Port 2

Frequency(MHz)	Port 1 (TX1) Bandedge Level (dBm)	Port 2 (TX2) Bandedge Level (dBm)	Total Bandedge Level (dBm)	Limit (dBm)	Margin (dB)
5495	-46.61	-46.21	-41.40	-41.25	0.15
5550	-50.34	-49.97	-45.14	-41.25	3.89
5700	-47.19	-46.17	-41.64	-41.25	0.39
5720 (Straddle Channel)	-50.15	-50.25	-45.19	-41.25	3.94



Conducted Bandedge above 1GHz Result

Appendix D.2

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Peak / Port 1 + Port 2

Frequency(MHz)	Port 1 (TX1) Bandedge Level (dBm)	Port 2 (TX2) Bandedge Level (dBm)	Total Bandedge Level (dBm)	Limit (dBm)	Margin (dB)
5480	-26.67	-26.26	-21.45	-21.25	0.20
5575	-37.54	-36.65	-32.06	-21.25	10.81
5715	-31.34	-27.28	-23.84	-21.25	2.59
5725 (Straddle Channel)	-36.25	-36.43	-31.33	-21.25	10.08

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Peak / Port 1 + Port 2

Frequency(MHz)	Port 1 (TX1) Bandedge Level (dBm)	Port 2 (TX2) Bandedge Level (dBm)	Total Bandedge Level (dBm)	Limit (dBm)	Margin (dB)
5495	-34.16	-32.71	-28.36	-21.25	7.11
5550	-36.79	-36.78	-31.77	-21.25	10.52
5700	-34.57	-33.58	-29.04	-21.25	7.79
5720 (Straddle Channel)	-27.75	-28.58	-23.13	-21.25	1.88

For Antenna 2:

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Average / Port 1 + Port 2

Frequency(MHz)	Port 1 (TX1) Bandedge Level (dBm)	Port 2 (TX2) Bandedge Level (dBm)	Total Bandedge Level (dBm)	Limit (dBm)	Margin (dB)
5255	-69.15	-69.01	-42.07	-41.25	0.82
5295	-69.26	-68.79	-42.01	-41.25	0.76
5340	-68.99	-68.43	-41.69	-41.25	0.44
5250 (Straddle Channel)	-69.39	-69.16	-42.26	-41.25	1.01

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Average / Port 1 + Port 2

Frequency(MHz)	Port 1 (TX1) Bandedge Level (dBm)	Port 2 (TX2) Bandedge Level (dBm)	Total Bandedge Level (dBm)	Limit (dBm)	Margin (dB)
5270	-70.15	-69.18	-42.63	-41.25	1.38
5300	-69.50	-68.30	-41.85	-41.25	0.60
5325	-68.04	-68.80	-41.39	-41.25	0.14
5250 (Straddle Channel)	-70.05	-69.69	-42.86	-41.25	1.61



Conducted Bandedge above 1GHz Result

Appendix D.2

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Peak / Port 1 + Port 2

Frequency(MHz)	Port 1 (TX1) Bandedge Level (dBm)	Port 2 (TX2) Bandedge Level (dBm)	Total Bandedge Level (dBm)	Limit (dBm)	Margin (dB)
5255	-56.14	-56.04	-29.08	-21.25	7.83
5295	-56.11	-55.85	-28.97	-21.25	7.72
5340	-56.07	-57.78	-29.83	-21.25	8.58
5250 (Straddle Channel)	-56.59	-55.65	-29.08	-21.25	7.83

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Peak / Port 1 + Port 2

Frequency(MHz)	Port 1 (TX1) Bandedge Level (dBm)	Port 2 (TX2) Bandedge Level (dBm)	Total Bandedge Level (dBm)	Limit (dBm)	Margin (dB)
5270	-57.86	-56.51	-30.12	-21.25	8.87
5300	-58.33	-58.10	-31.20	-21.25	9.95
5325	-57.10	-56.10	-29.56	-21.25	8.31
5250 (Straddle Channel)	-57.37	-58.14	-30.73	-21.25	9.48



Conducted Bandedge above 1GHz Result

Appendix D.2

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Average / Port 1 + Port 2

Frequency(MHz)	Port 1 (TX1) Bandedge Level (dBm)	Port 2 (TX2) Bandedge Level (dBm)	Total Bandedge Level (dBm)	Limit (dBm)	Margin (dB)
5480	-69.02	-68.47	-41.73	-41.25	0.48
5575	-69.07	-68.84	-41.94	-41.25	0.69
5715	-68.20	-68.71	-41.44	-41.25	0.19
5725 (Straddle Channel)	-70.11	-69.97	-43.03	-41.25	1.78

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Average / Port 1 + Port 2

Frequency(MHz)	Port 1 (TX1) Bandedge Level (dBm)	Port 2 (TX2) Bandedge Level (dBm)	Total Bandedge Level (dBm)	Limit (dBm)	Margin (dB)
5495	-68.86	-68.20	-41.51	-41.25	0.26
5550	-69.34	-69.08	-42.20	-41.25	0.95
5700	-68.80	-68.62	-41.70	-41.25	0.45
5720 (Straddle Channel)	-69.54	-70.09	-42.80	-41.25	1.55



Conducted Bandedge above 1GHz Result

Appendix D.2

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 10M / Peak / Port 1 + Port 2

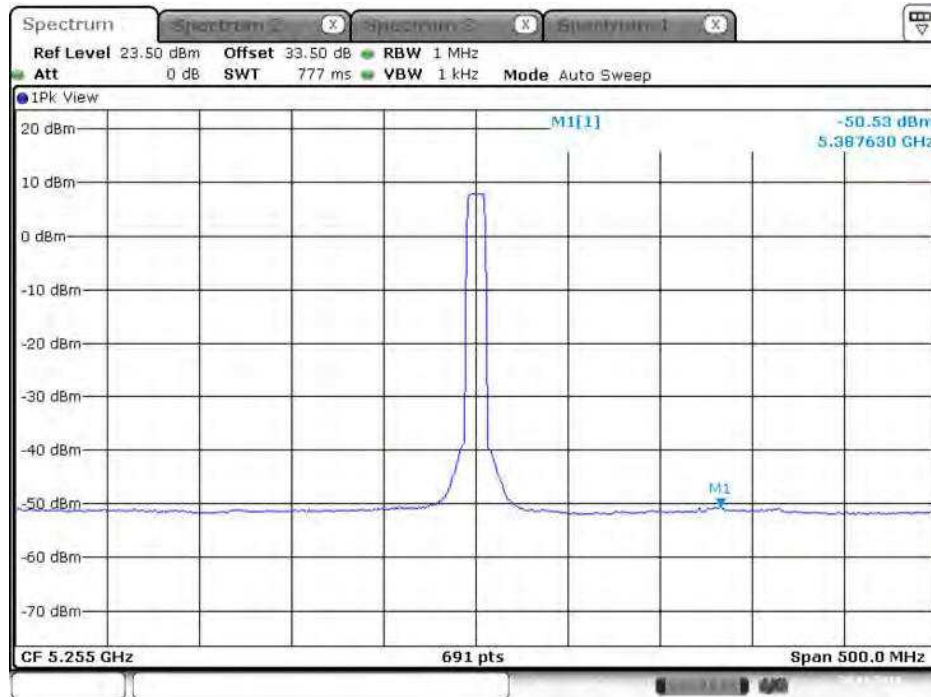
Frequency(MHz)	Port 1 (TX1) Bandedge Level (dBm)	Port 2 (TX2) Bandedge Level (dBm)	Total Bandedge Level (dBm)	Limit (dBm)	Margin (dB)
5480	-57.68	-57.36	-30.51	-21.25	9.26
5575	-56.34	-55.64	-28.97	-21.25	7.72
5715	-55.93	-57.06	-29.45	-21.25	8.20
5725 (Straddle Channel)	-59.56	-59.51	-32.52	-21.25	11.27

Temperature	22 °C	Humidity	54%
Test Engineer	Ron Huang / Serway Li	Configurations	QPSK, 40M / Peak / Port 1 + Port 2

Frequency(MHz)	Port 1 (TX1) Bandedge Level (dBm)	Port 2 (TX2) Bandedge Level (dBm)	Total Bandedge Level (dBm)	Limit (dBm)	Margin (dB)
5495	-56.47	-54.91	-28.61	-21.25	7.36
5550	-56.65	-56.65	-29.64	-21.25	8.39
5700	-56.40	-56.31	-29.34	-21.25	8.09
5720 (Straddle Channel)	-57.73	-55.37	-29.38	-21.25	8.13

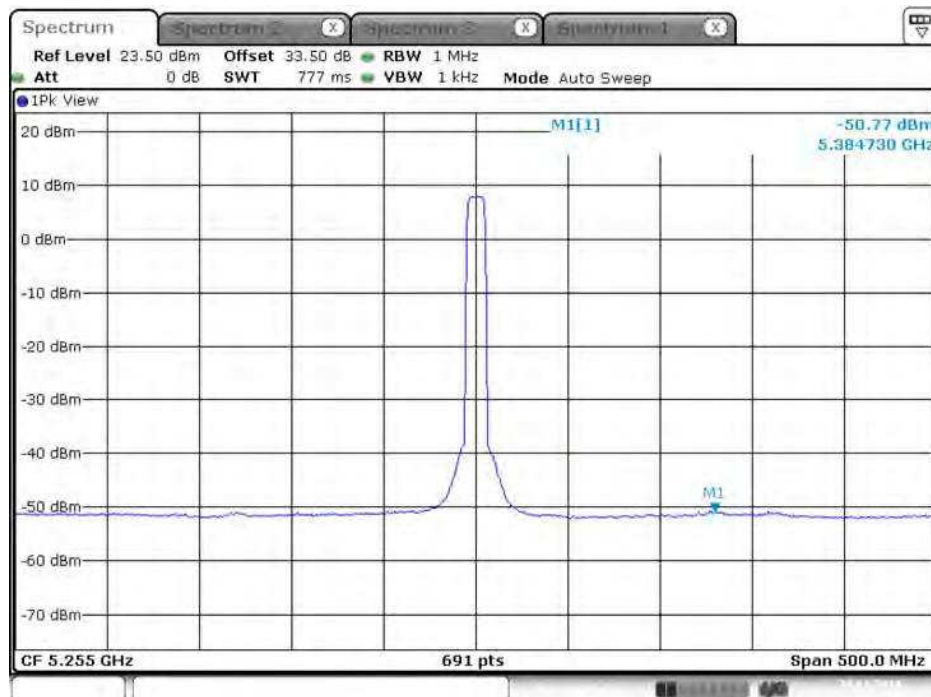
For Antenna 1:

Plot on Configuration QPSK, 10M / 5255 MHz / Average / Port 1 (TX1)



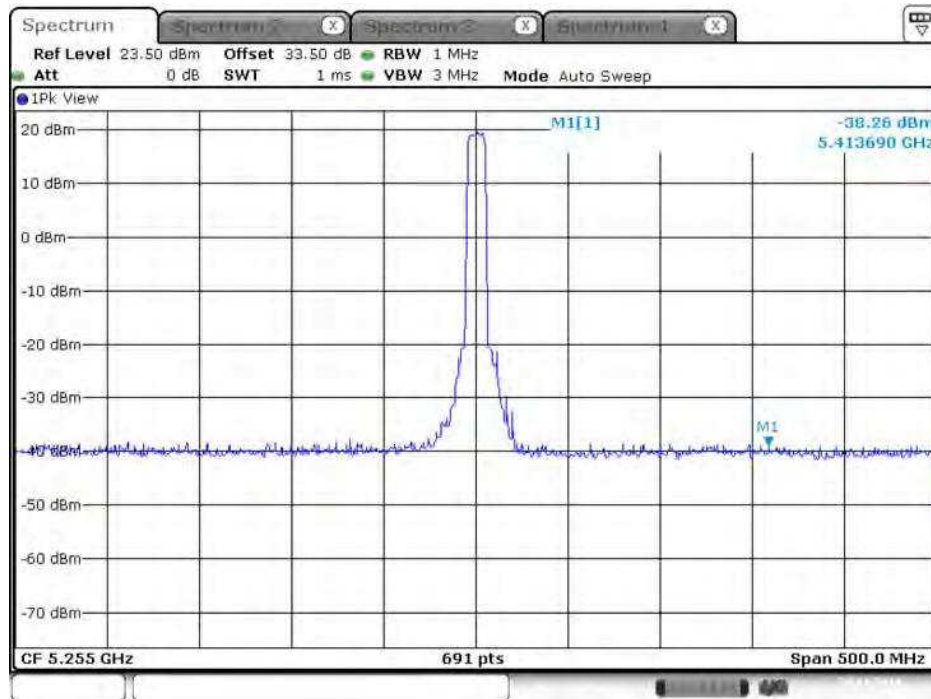
Date: 24.JAN.2018 14:07:24

Plot on Configuration QPSK, 10M / 5255 MHz / Average / Port 2 (TX2)



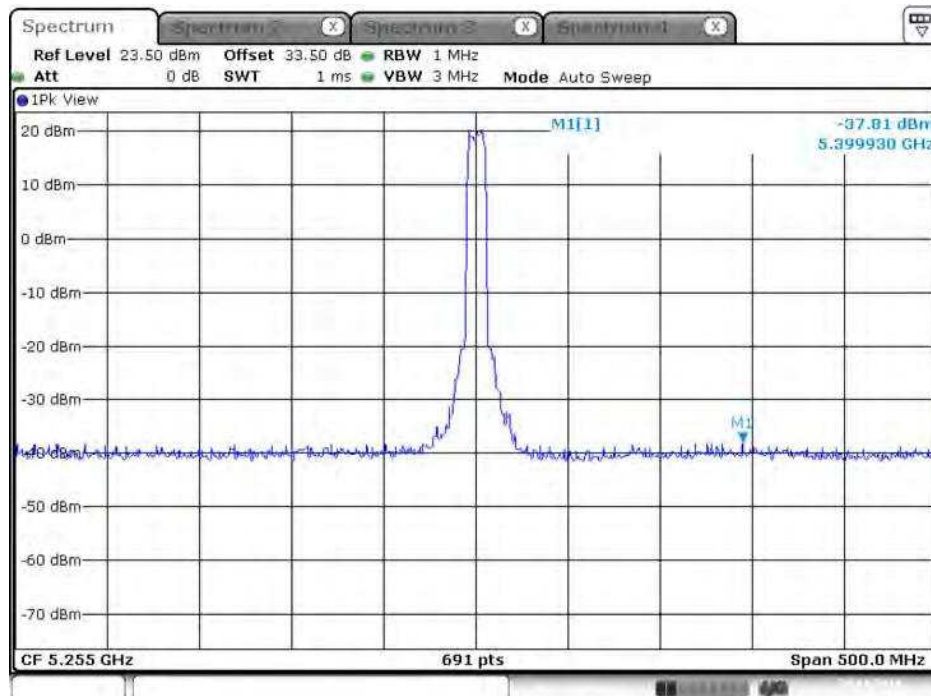
Date: 24.JAN.2018 14:10:29

Plot on Configuration QPSK, 10M / 5255 MHz / Peak / Port 1 (TX1)



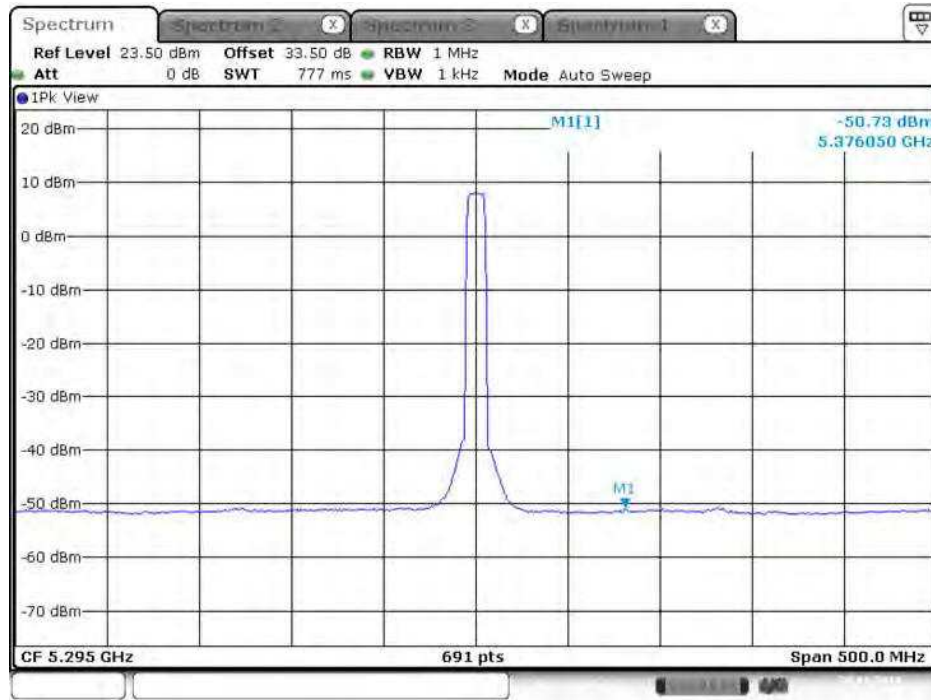
Date: 24.JAN.2018 14:08:41

Plot on Configuration QPSK, 10M / 5255 MHz / Peak / Port 2 (TX2)



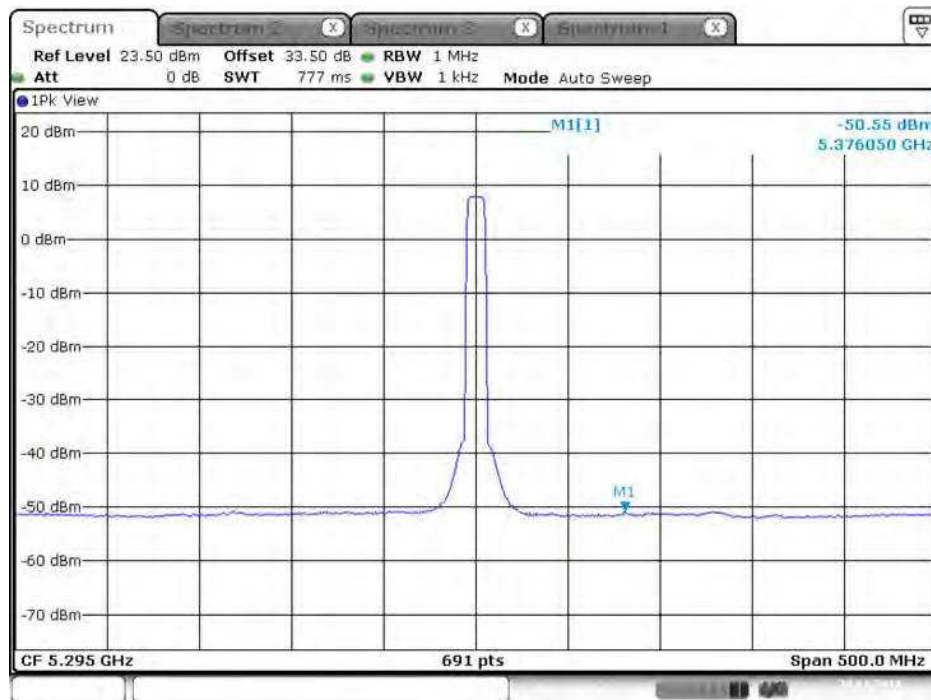
Date: 24.JAN.2018 14:12:53

Plot on Configuration QPSK, 10M / 5295 MHz/ Average / Port 1 (TX1)



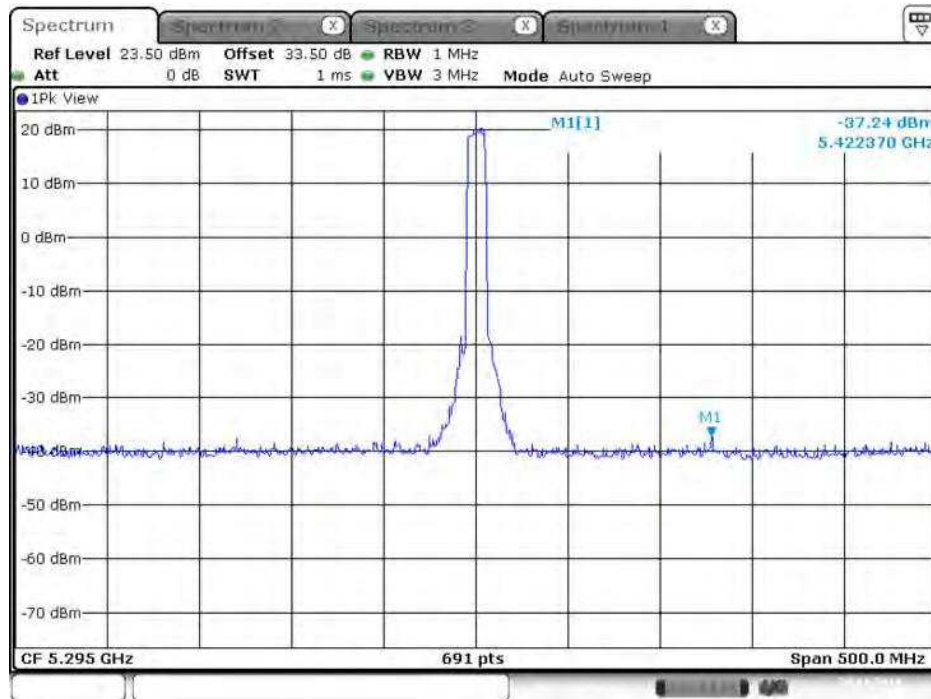
Date: 24.JAN.2018 14:17:55

Plot on Configuration QPSK, 10M / 5295 MHz/ Average / Port 2 (TX2)



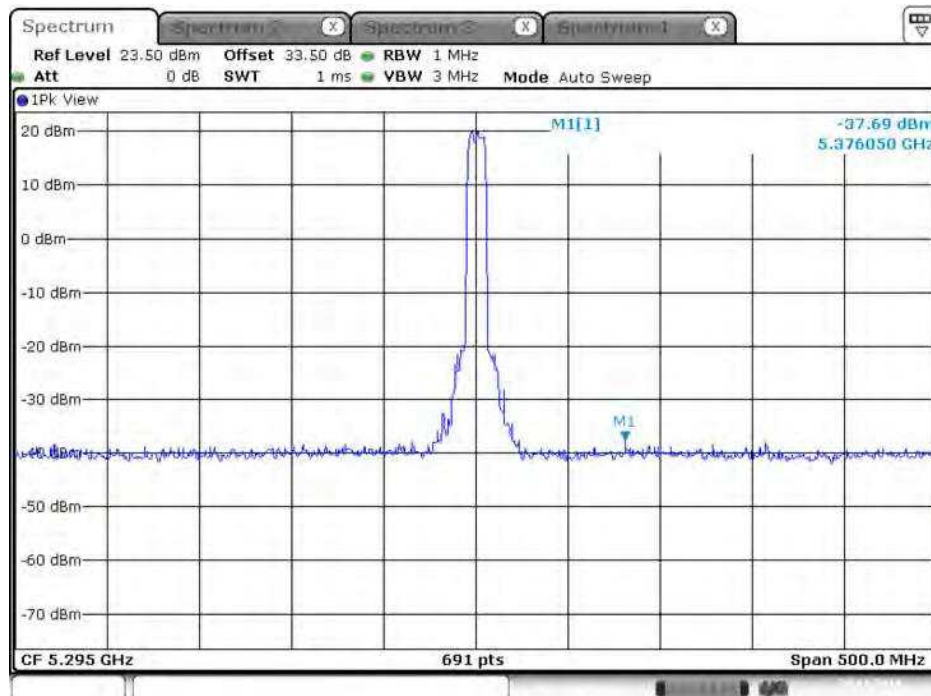
Date: 24.JAN.2018 14:21:44

Plot on Configuration QPSK, 10M / 5295 MHz/ Peak / Port 1 (TX1)



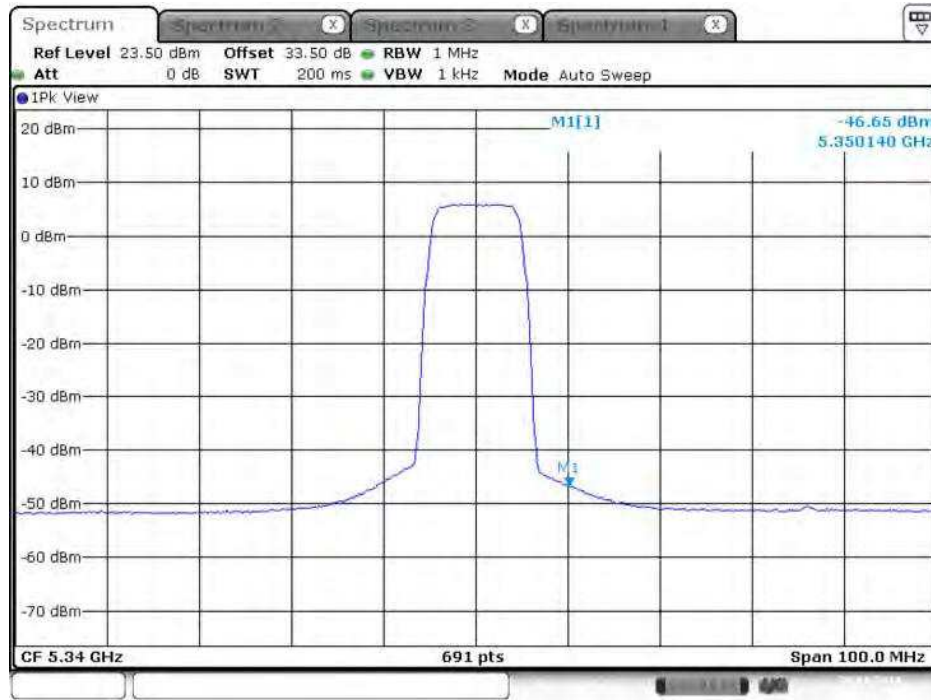
Date: 24.JAN 2018 14:19:46

Plot on Configuration QPSK, 10M / 5295 MHz / Peak / Port 2 (TX2)



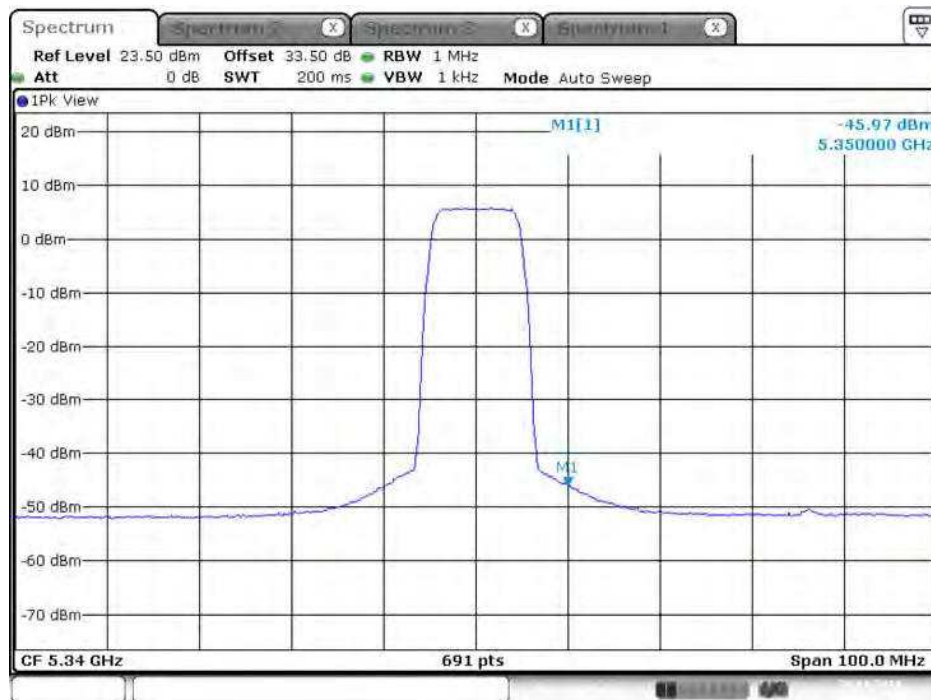
Date: 24.JAN 2018 14:23:18

Plot on Configuration QPSK, 10M / 5340 MHz / Average / Port 1 (TX1)



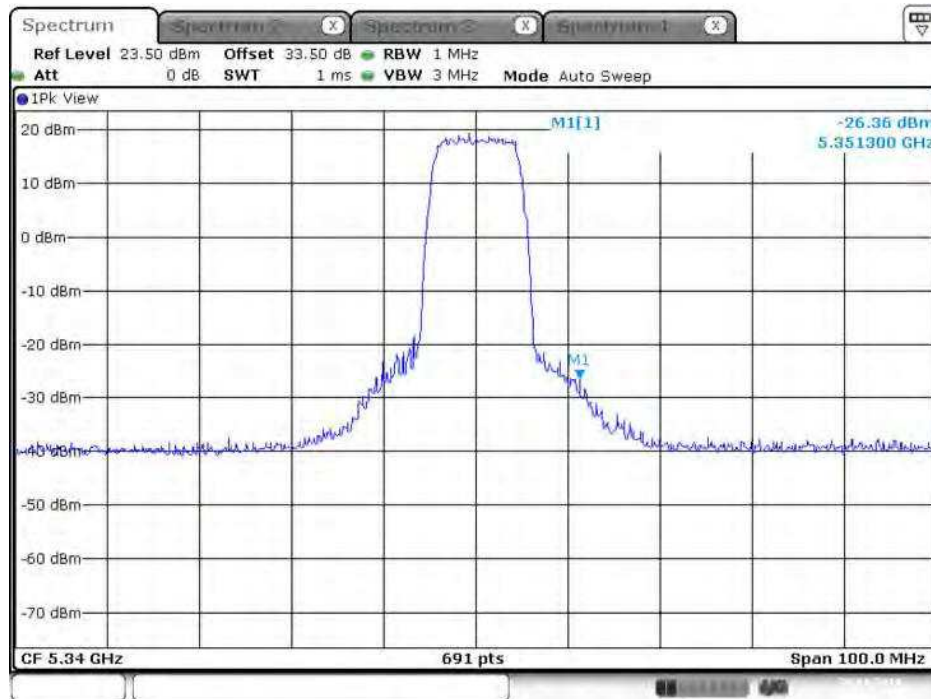
Date: 24.JAN.2018 14:36:05

Plot on Configuration QPSK, 10M / 5340 MHz / Average / Port 2 (TX2)



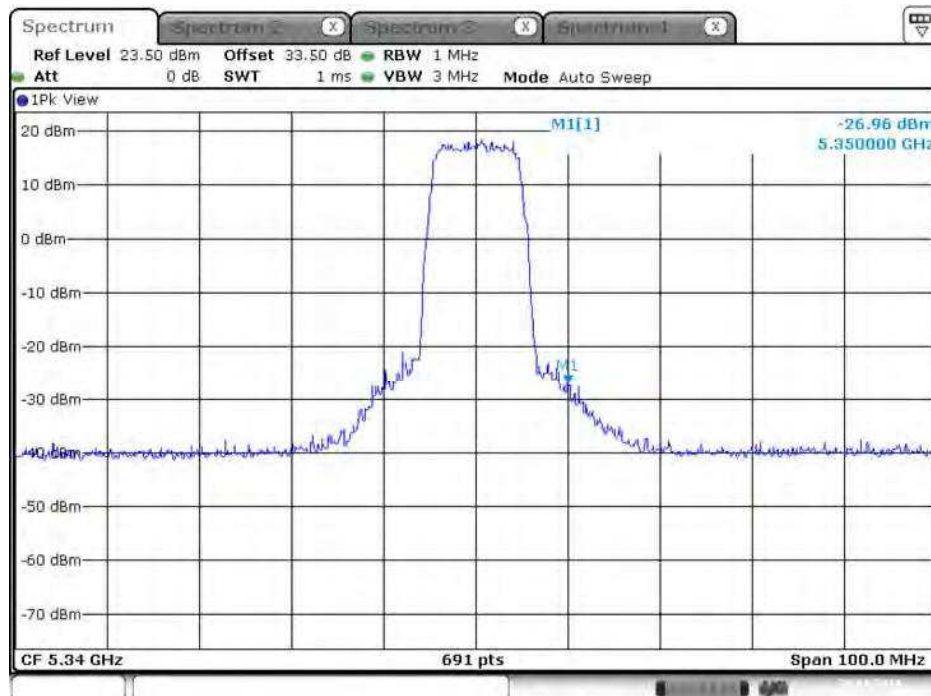
Date: 24.JAN.2018 14:42:26

Plot on Configuration QPSK, 10M / 5340 MHz / Peak / Port 1 (TX1)



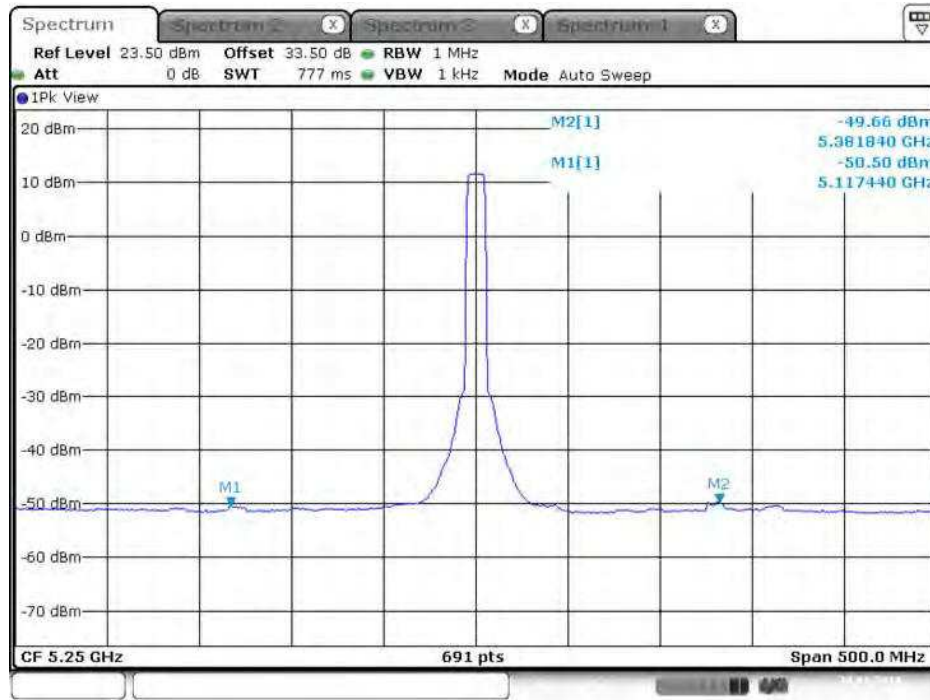
Date: 24.JAN.2018 14:38:41

Plot on Configuration QPSK, 10M / 5340 MHz / Peak / Port 2 (TX2)



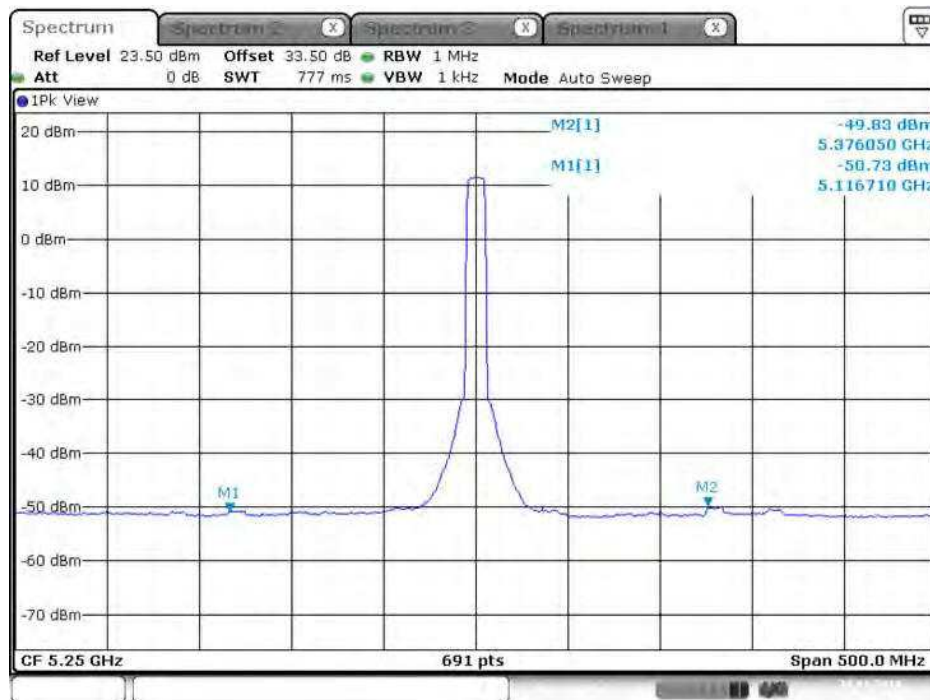
Date: 24.JAN.2018 14:44:51

Plot on Configuration QPSK, 10M / 5250 MHz (Straddle Channel) / Average / Port 1 (TX1)



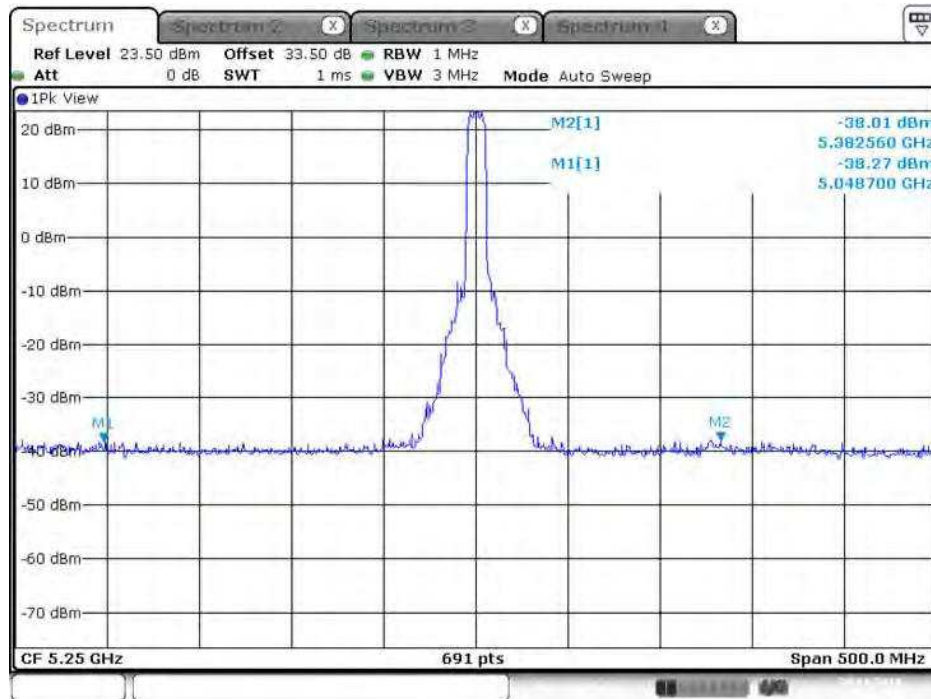
Date: 24.JAN.2018 14:58:24

Plot on Configuration QPSK, 10M / 5250 MHz (Straddle Channel) / Average / Port 2 (TX2)



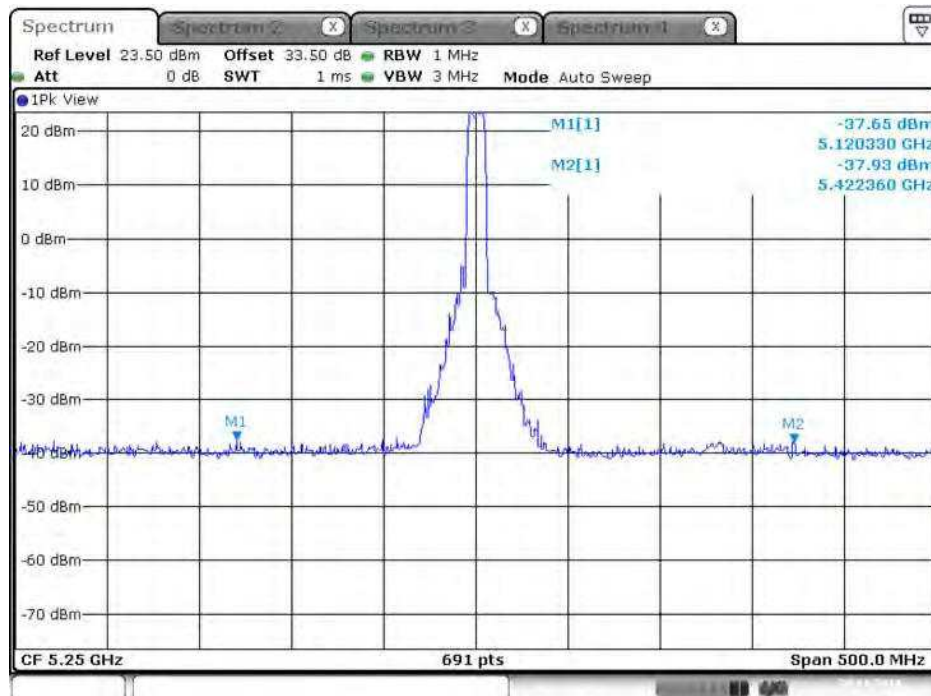
Date: 24.JAN.2018 15:03:39

Plot on Configuration QPSK, 10M / 5250 MHz (Straddle Channel) / Peak / Port 1 (TX1)



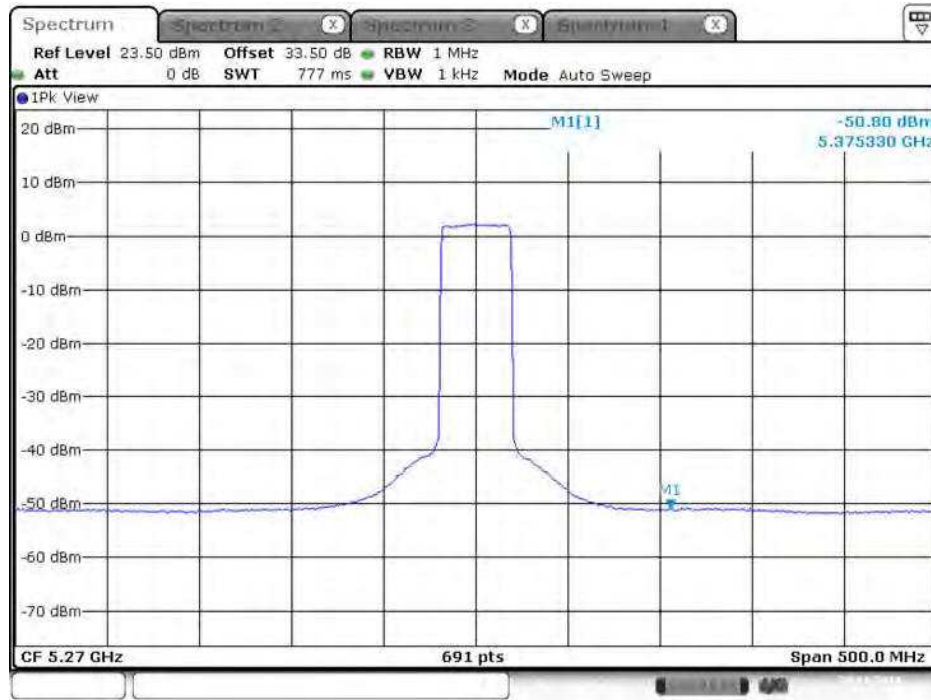
Date: 24.JAN 2018 15:01:29

Plot on Configuration QPSK, 10M / 5250 MHz (Straddle Channel) / Peak / Port 2 (TX2)



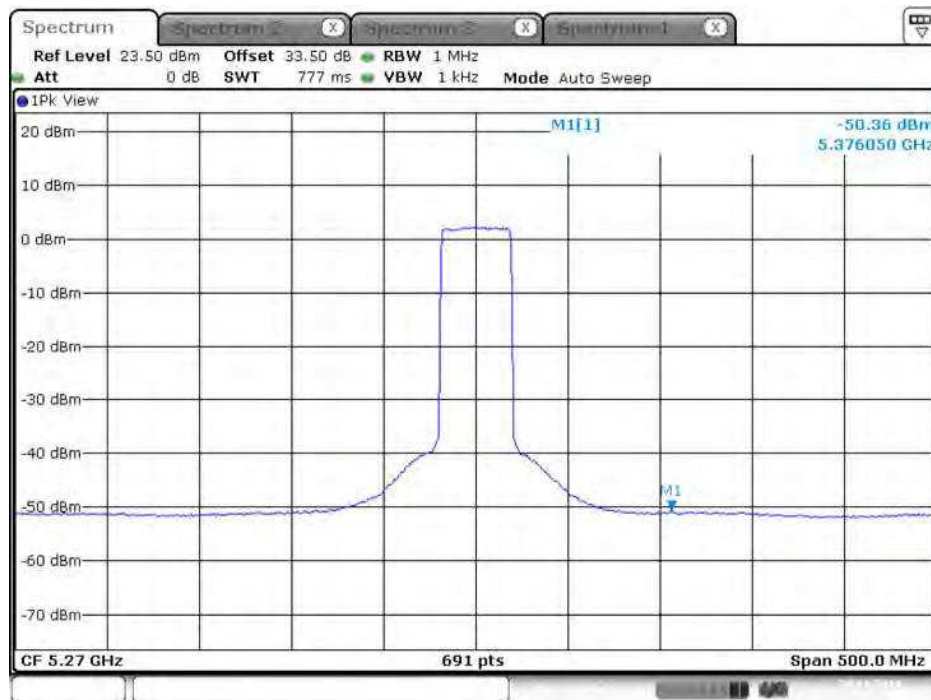
Date: 24.JAN 2018 15:05:37

Plot on Configuration QPSK, 40M / 5270 MHz / Average / Port 1 (TX1)



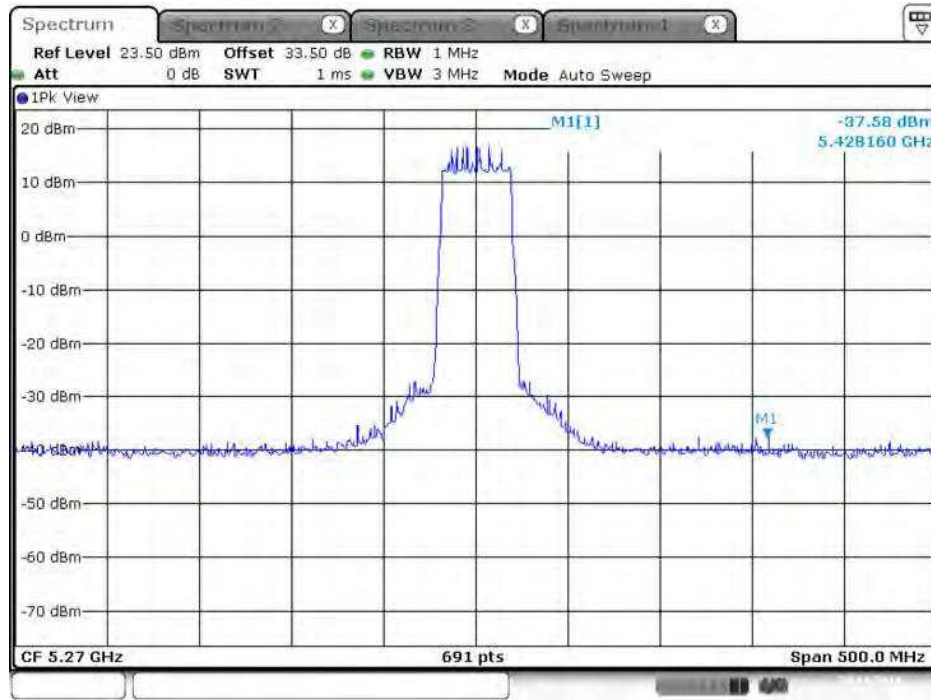
Date: 24.JAN.2018 15:13:47

Plot on Configuration QPSK, 40M / 5270 MHz / Average / Port 2 (TX2)



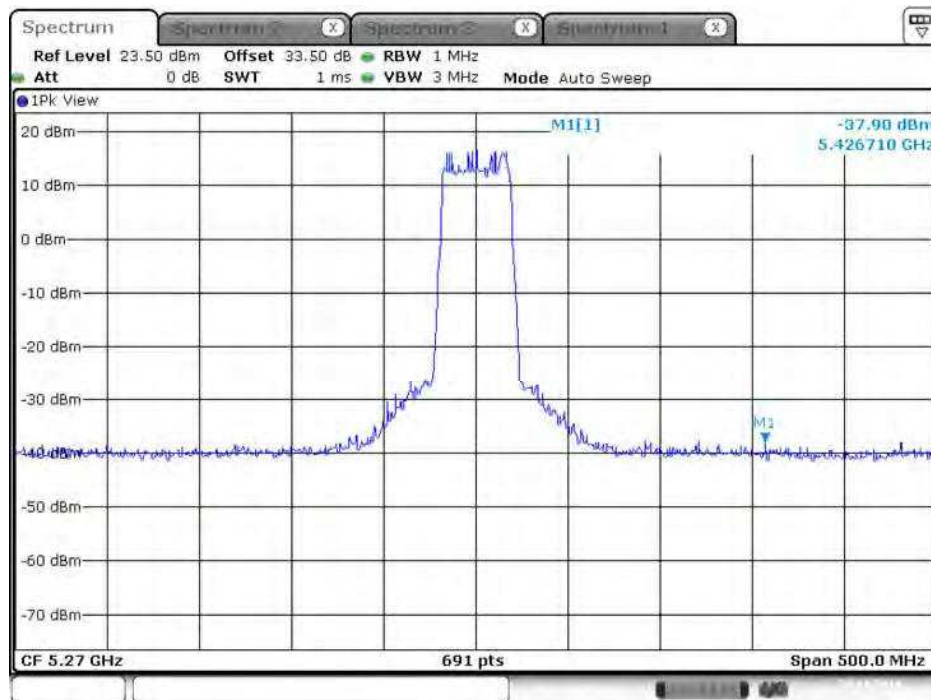
Date: 24.JAN.2018 15:17:11

Plot on Configuration QPSK, 40M / 5270 MHz / Peak / Port 1 (TX1)



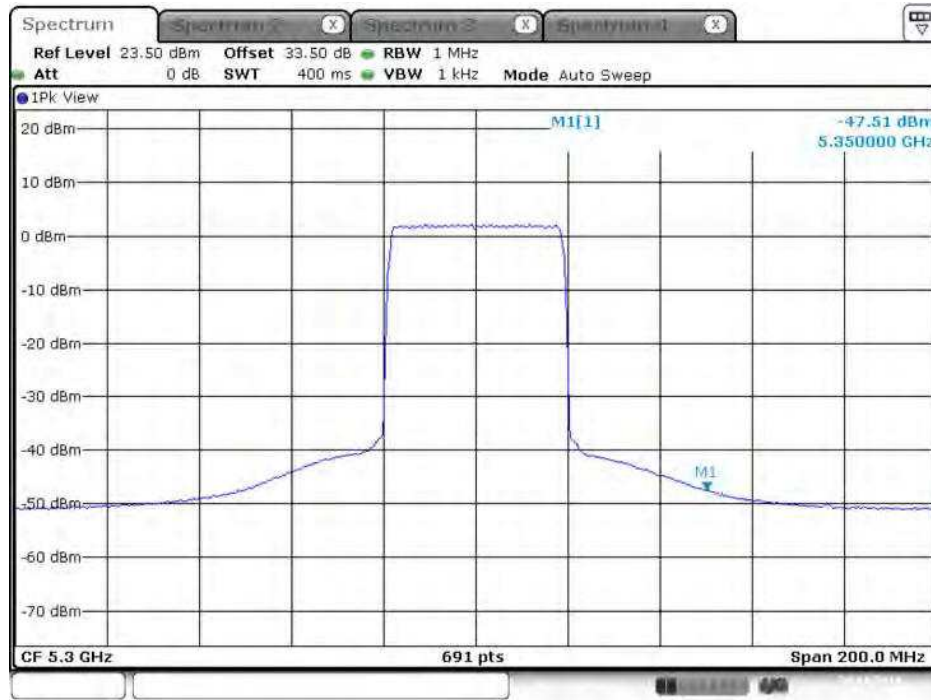
Date: 24.JAN.2018 15:15:35

Plot on Configuration QPSK, 40M / 5270 MHz / Peak / Port 2 (TX2)



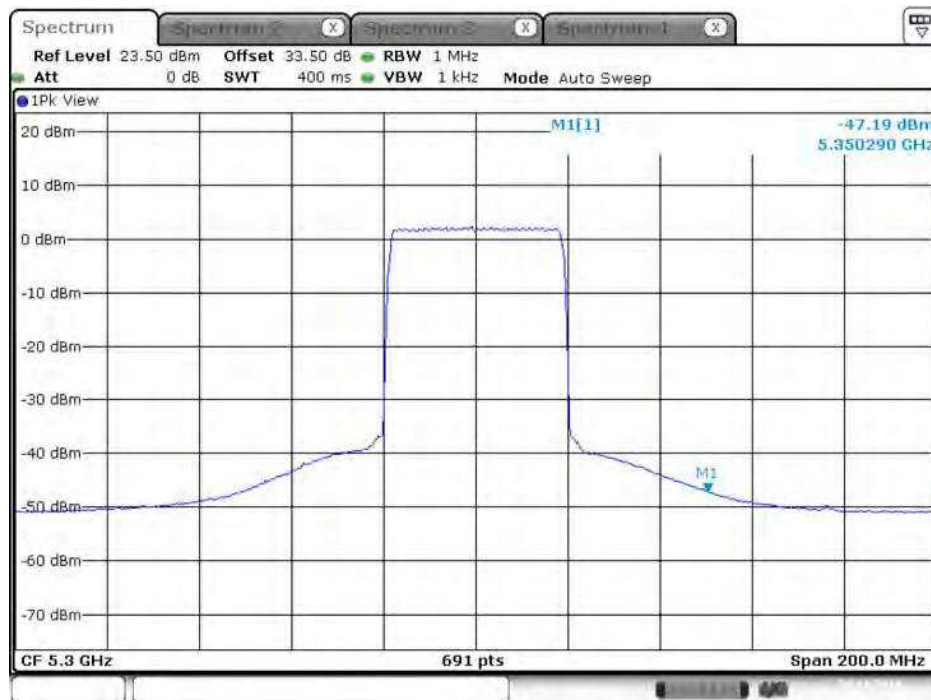
Date: 24.JAN.2018 15:18:35

Plot on Configuration QPSK, 40M / 5300 MHz / Average / Port 1 (TX1)



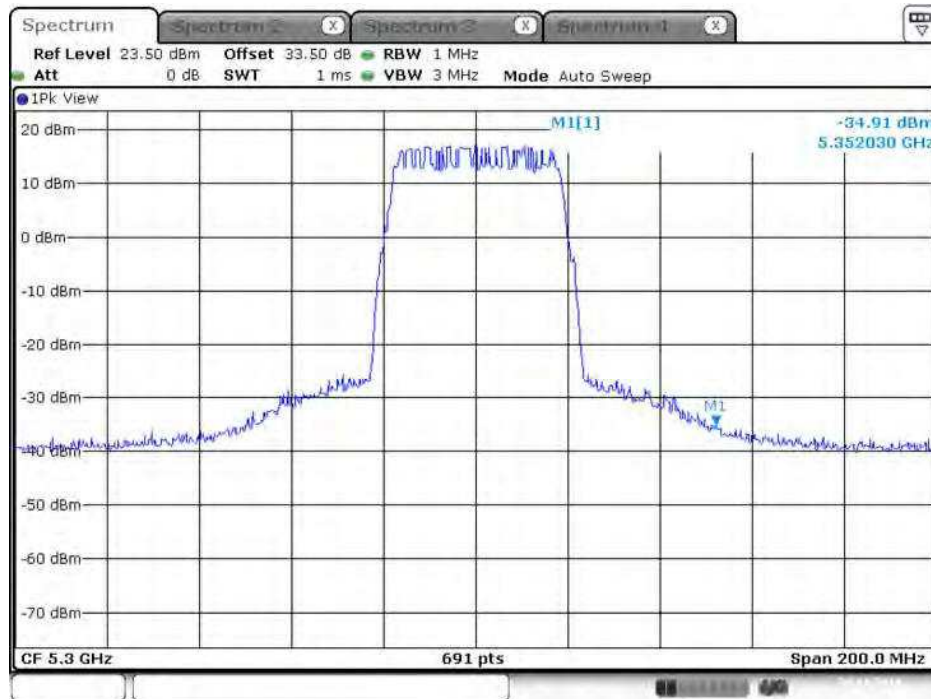
Date: 24.JAN 2018 15:24:07

Plot on Configuration QPSK, 40M / 5300 MHz / Average / Port 2 (TX2)



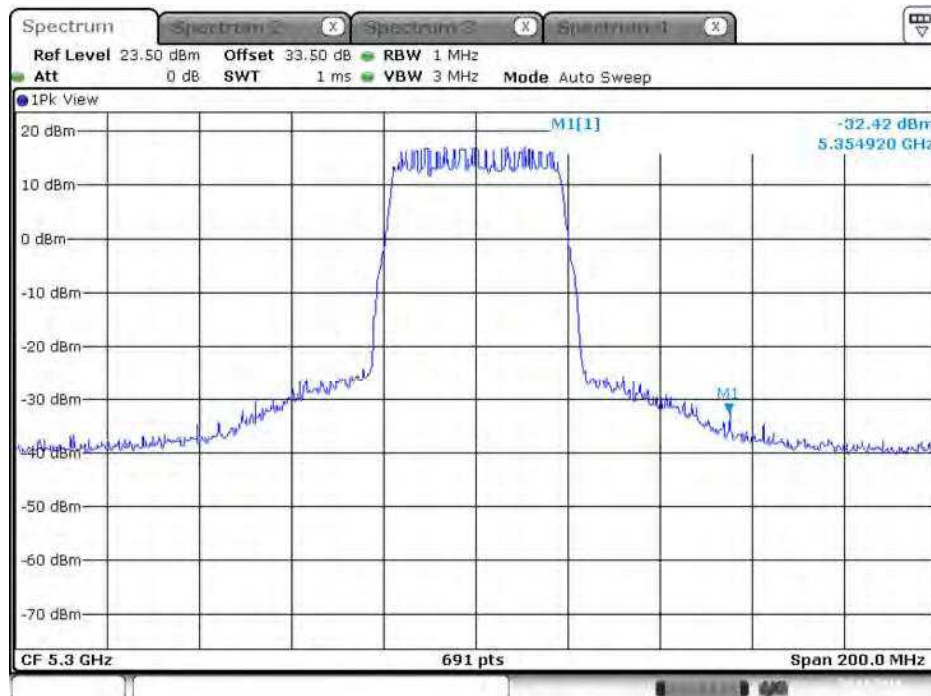
Date: 24.JAN 2018 15:27:12

Plot on Configuration QPSK, 40M / 5300 MHz / Peak / Port 1 (TX1)



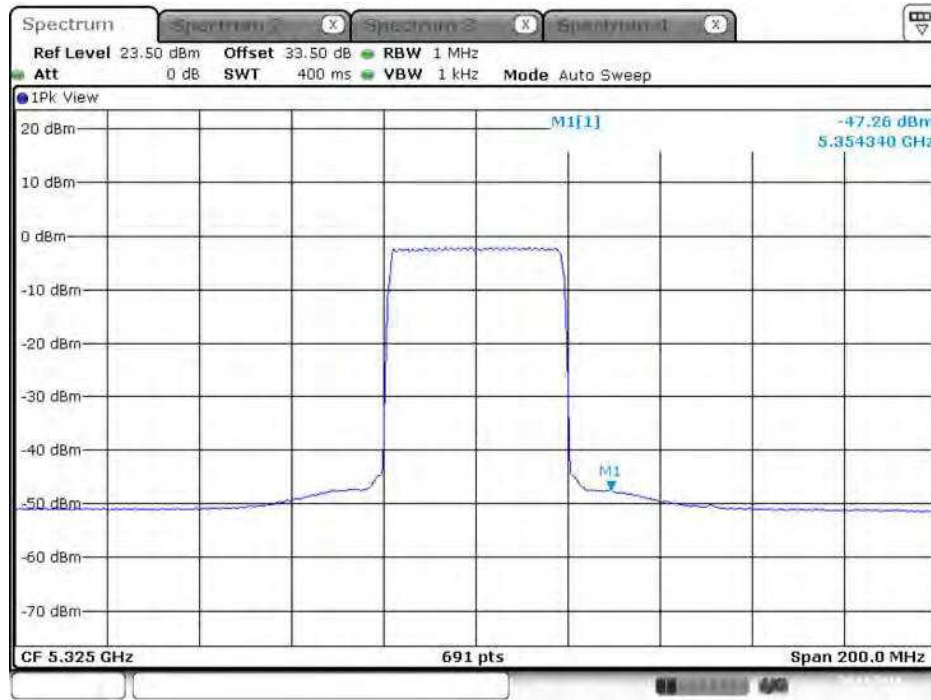
Date: 24.JAN 2018 15:25:23

Plot on Configuration QPSK, 40M / 5300 MHz / Peak / Port 2 (TX2)



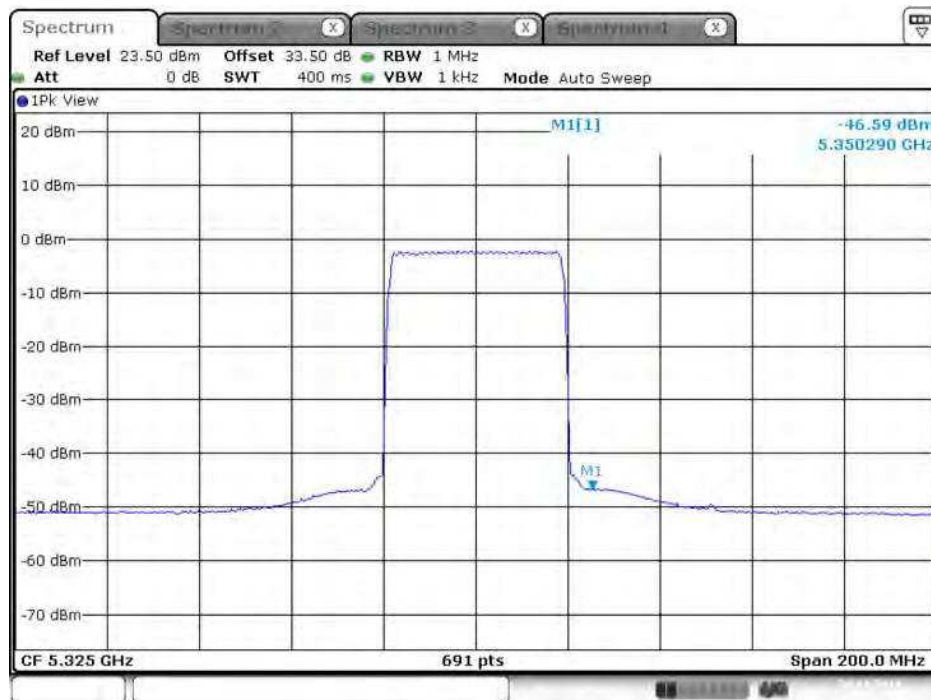
Date: 24.JAN 2018 15:27:59

Plot on Configuration QPSK, 40M / 5325 MHz / Average / Port 1 (TX1)



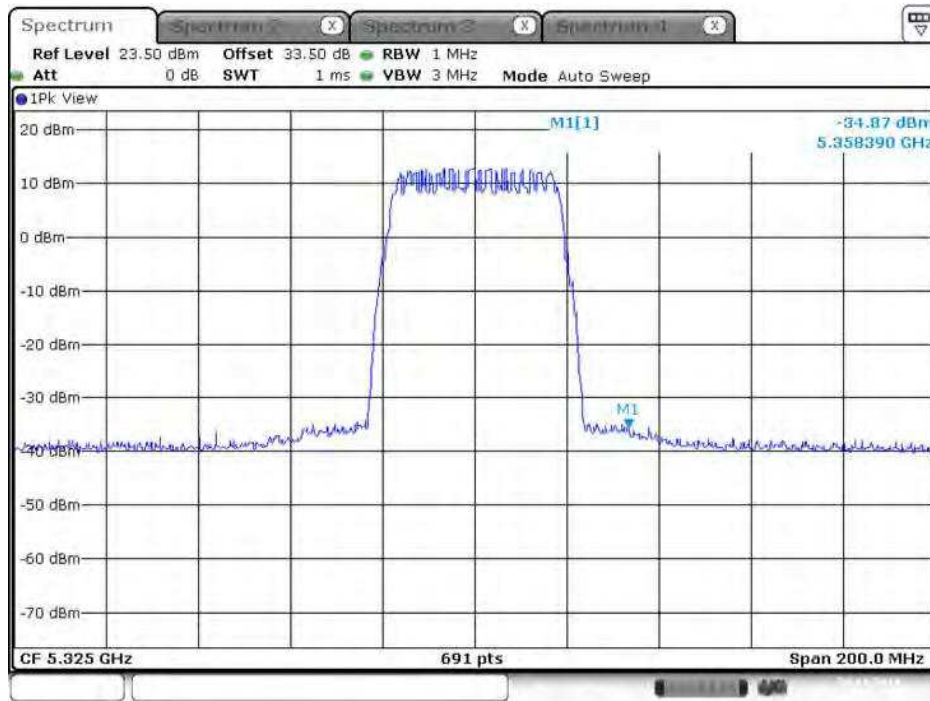
Date: 24.JAN.2018 15:37:16

Plot on Configuration QPSK, 40M / 5325 MHz / Average / Port 2 (TX2)



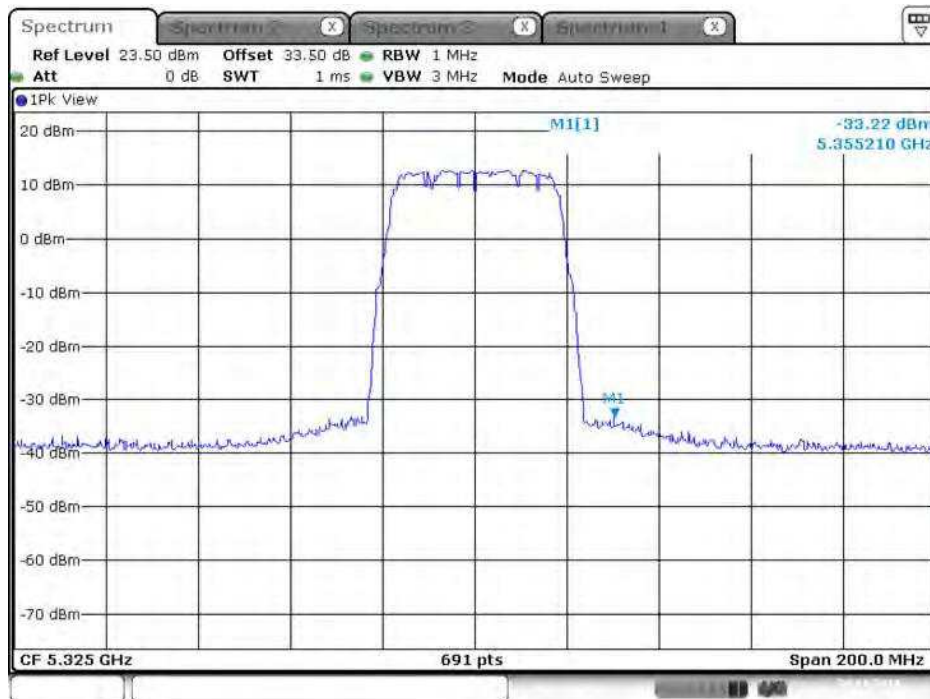
Date: 24.JAN.2018 15:32:32

Plot on Configuration QPSK, 40M / 5325 MHz / Peak / Port 1 (TX1)



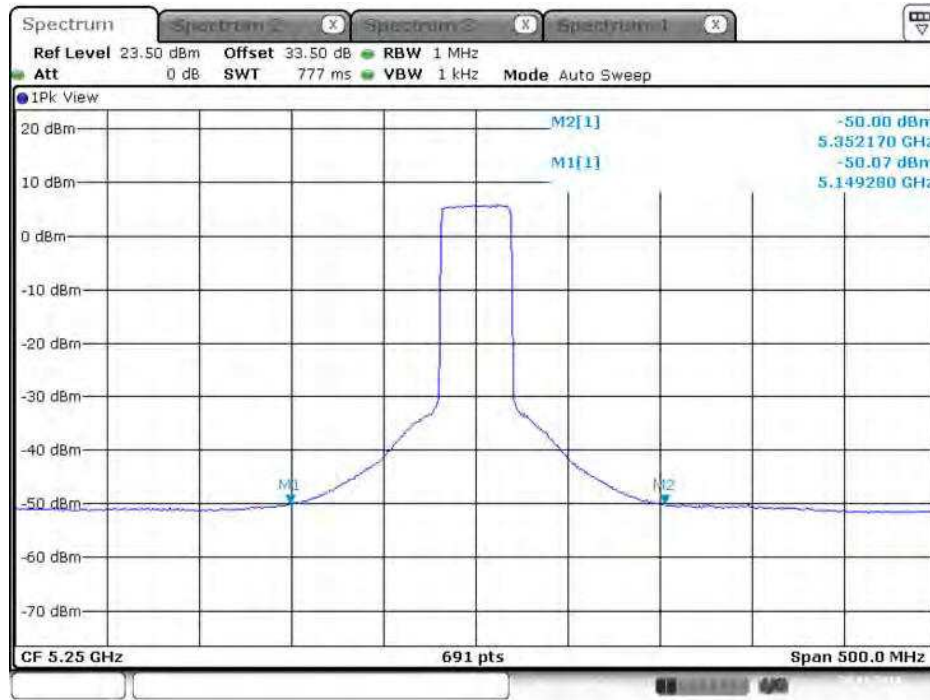
Date: 24.JAN 2018 15:38:57

Plot on Configuration QPSK, 40M / 5325 MHz / Peak / Port 2 (TX2)



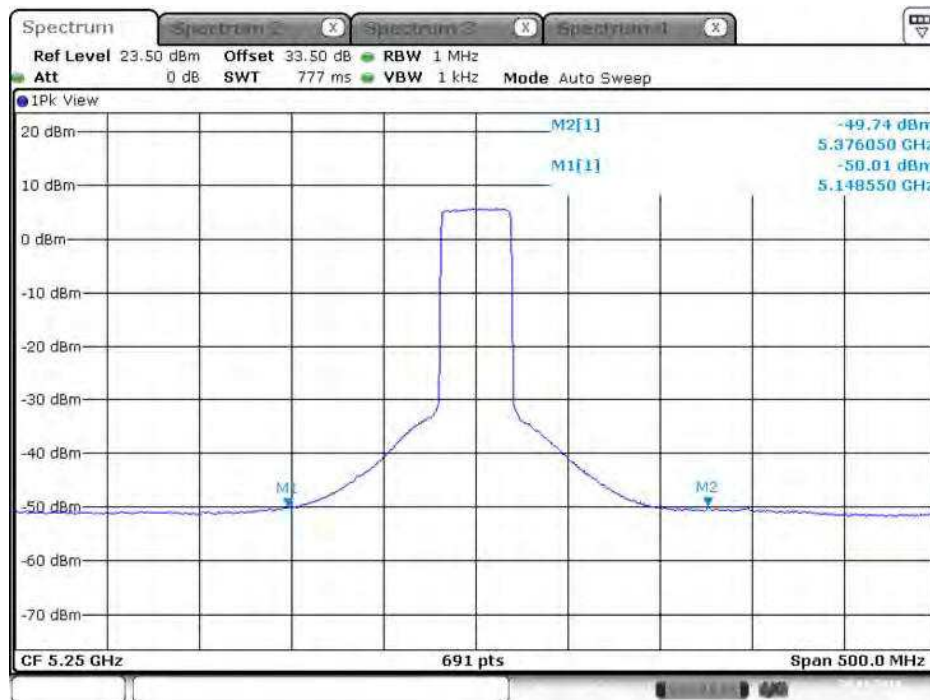
Date: 24.JAN 2018 15:34:12

Plot on Configuration QPSK, 40M / 5250 MHz (Straddle Channel) / Average / Port 1 (TX1)



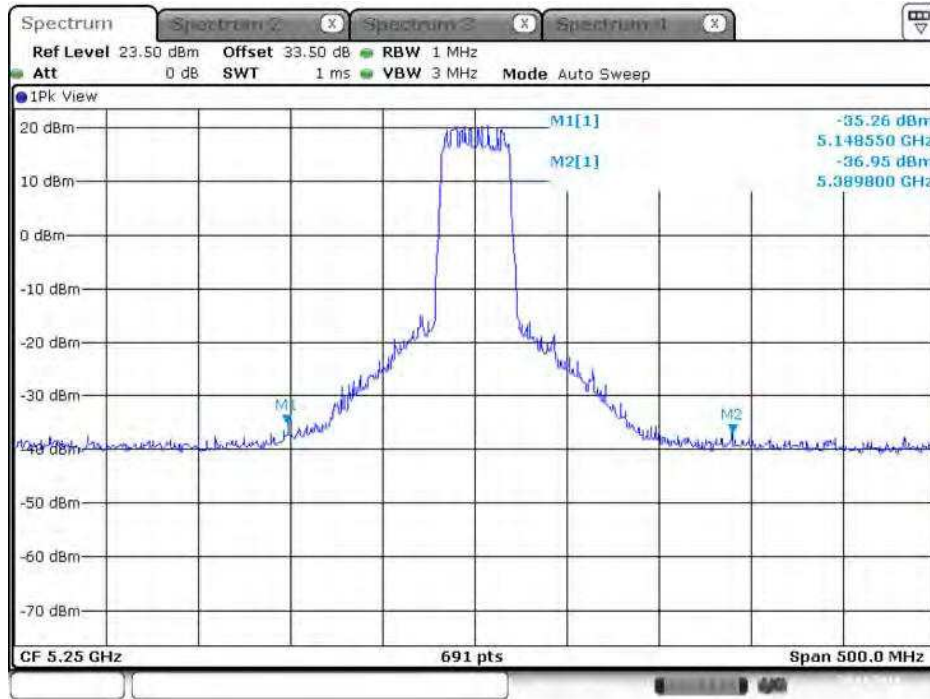
Date: 24.JAN.2018 15:43:54

Plot on Configuration QPSK, 40M / 5250 MHz (Straddle Channel) / Average / Port 2 (TX2)



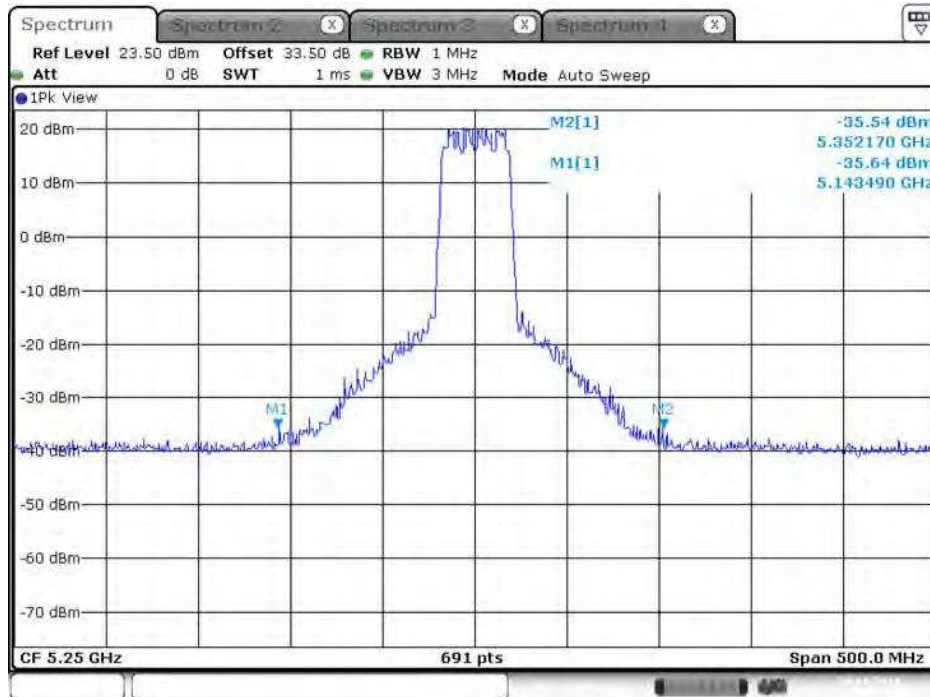
Date: 24.JAN.2018 15:50:23

Plot on Configuration QPSK, 40M / 5250 MHz (Straddle Channel) / Peak / Port 1 (TX1)



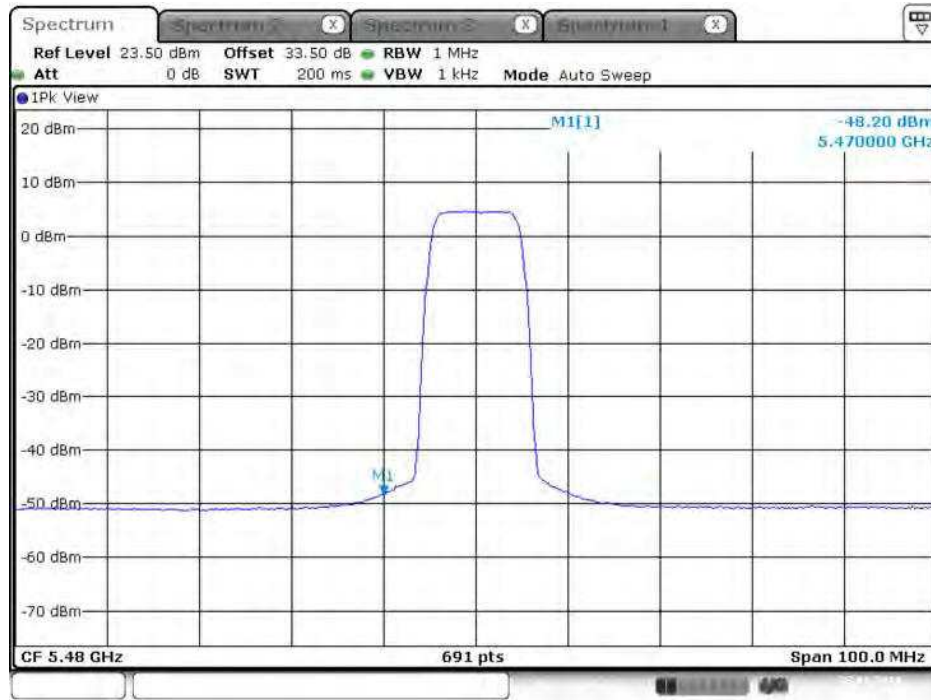
Date: 24.JAN.2018 15:46:57

Plot on Configuration QPSK, 40M / 5250 MHz (Straddle Channel) / Peak / Port 2 (TX2)



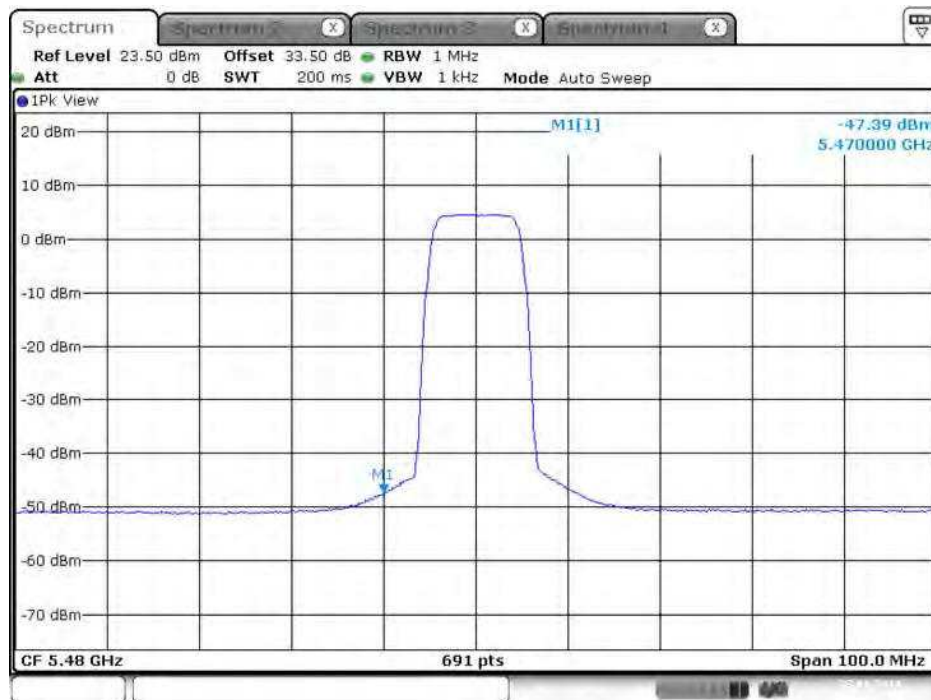
Date: 24.JAN.2018 15:52:24

Plot on Configuration QPSK, 10M / 5480 MHz / Average / Port 1 (TX1)



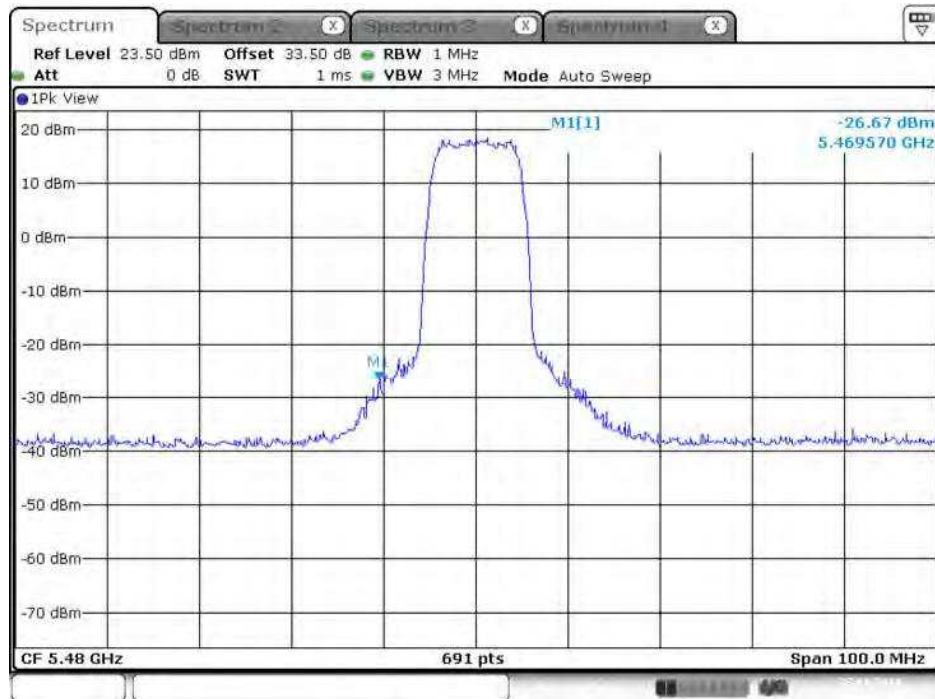
Date: 23.JAN.2018 11:47:23

Plot on Configuration QPSK, 10M / 5480 MHz / Average / Port 2 (TX2)



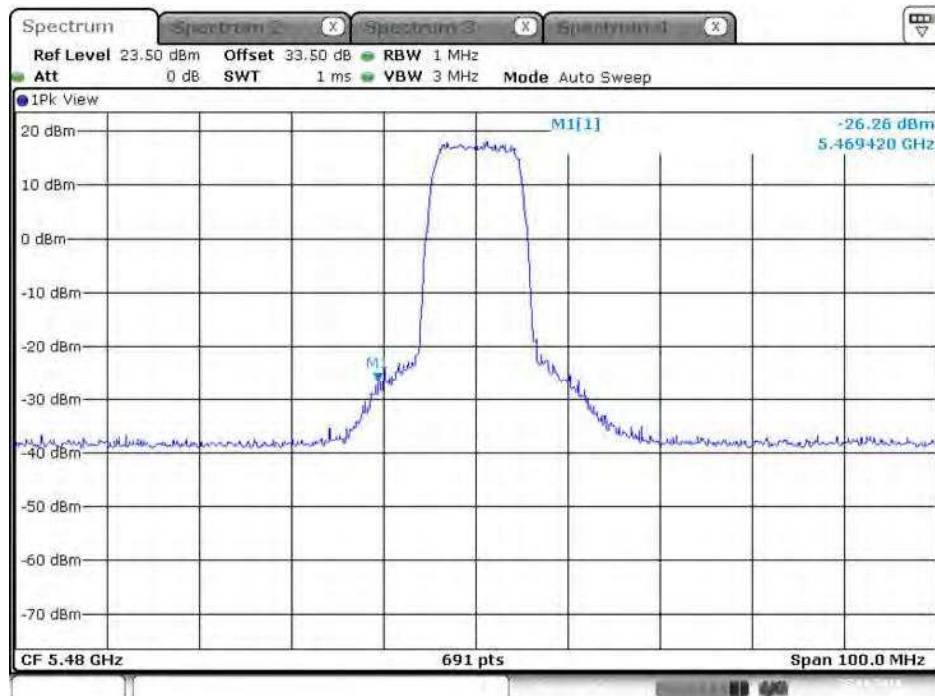
Date: 23.JAN.2018 11:45:44

Plot on Configuration QPSK, 10M / 5480 MHz / Peak / Port 1 (TX1)



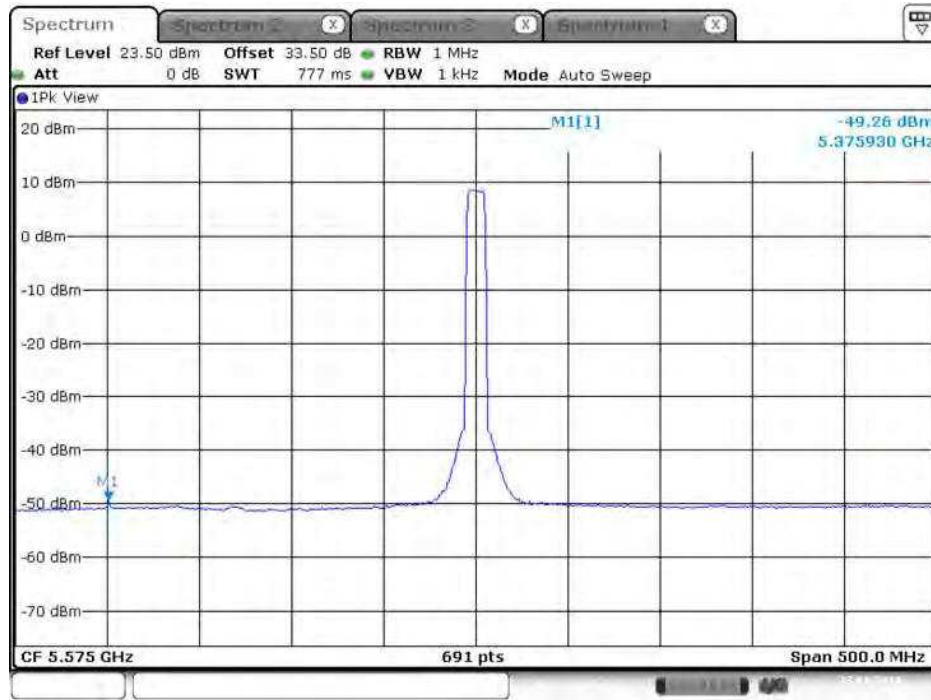
Date: 23.JAN 2018 11:34:13

Plot on Configuration QPSK, 10M / 5480 MHz / Peak / Port 2 (TX2)



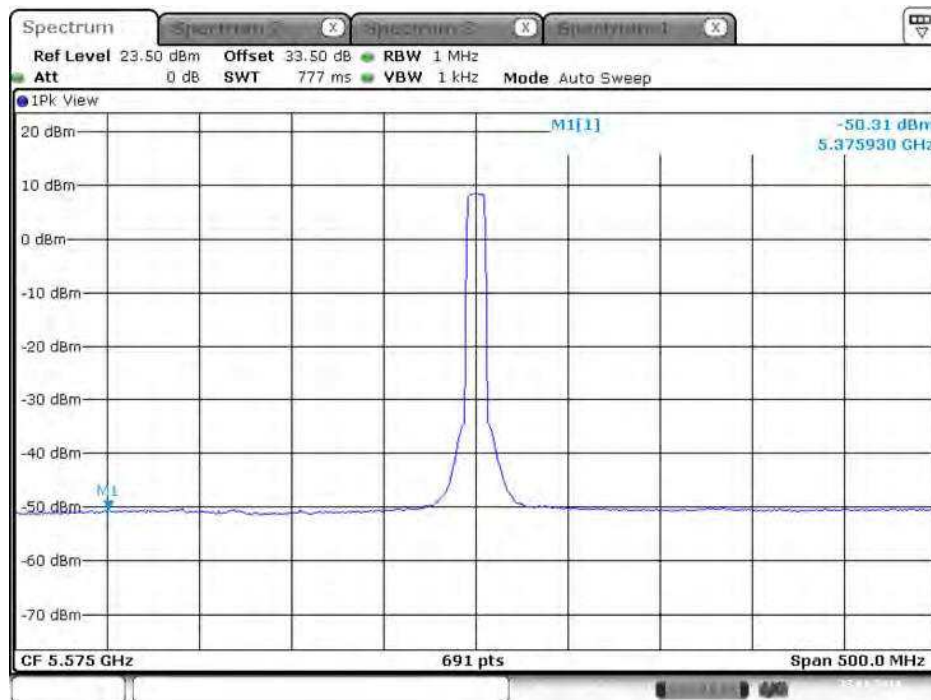
Date: 23.JAN 2018 11:44:11

Plot on Configuration QPSK, 10M / 5575 MHz/ Average / Port 1 (TX1)



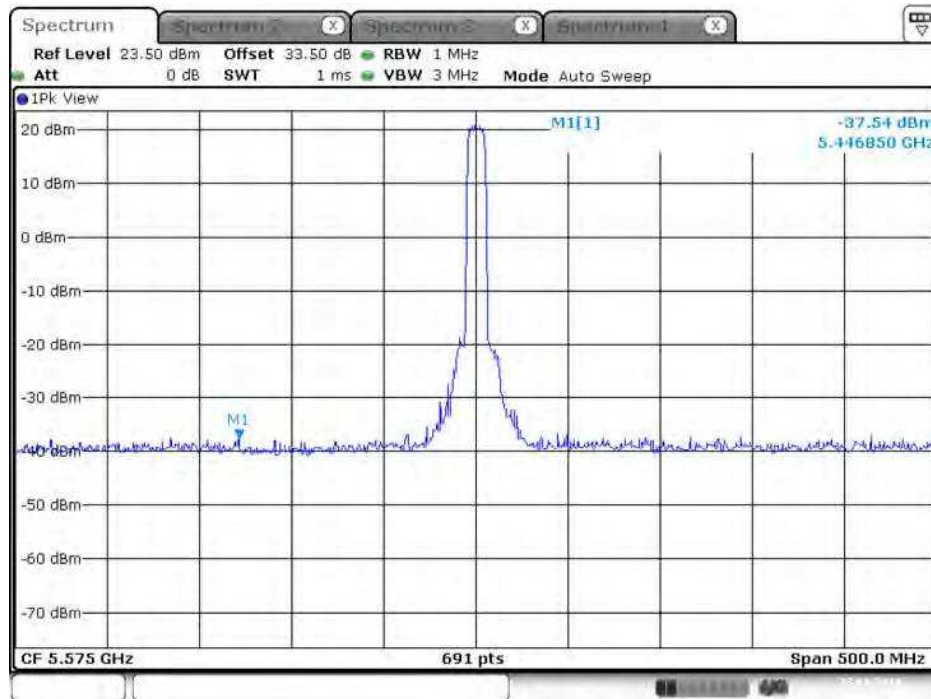
Date: 23.JAN 2018 12:05:50

Plot on Configuration QPSK, 10M / 5575 MHz/ Average / Port 2 (TX2)



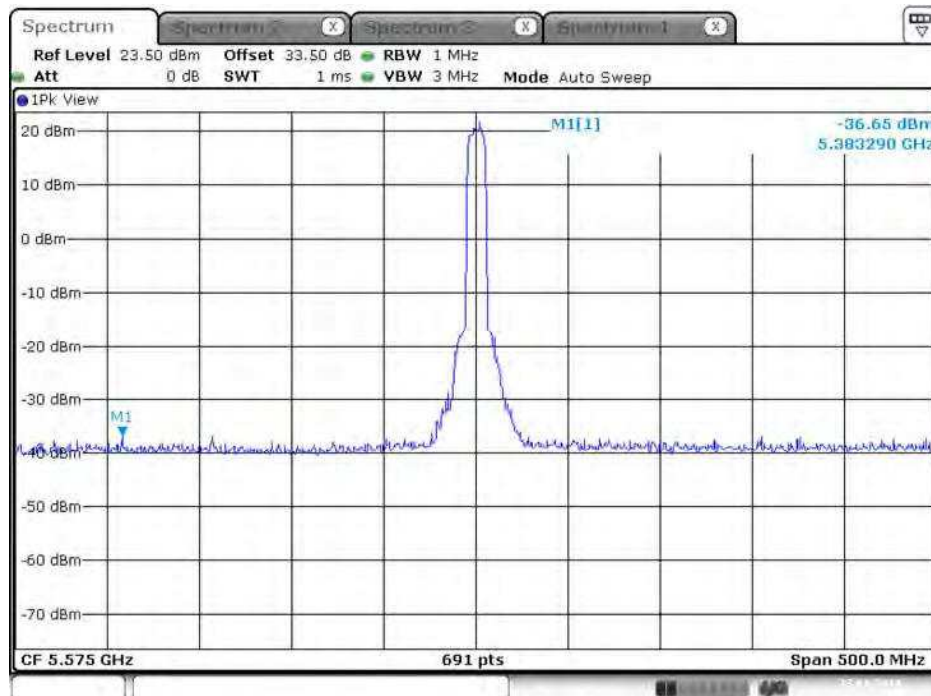
Date: 23.JAN 2018 12:02:09

Plot on Configuration QPSK, 10M / 5575 MHz/ Peak / Port 1 (TX1)



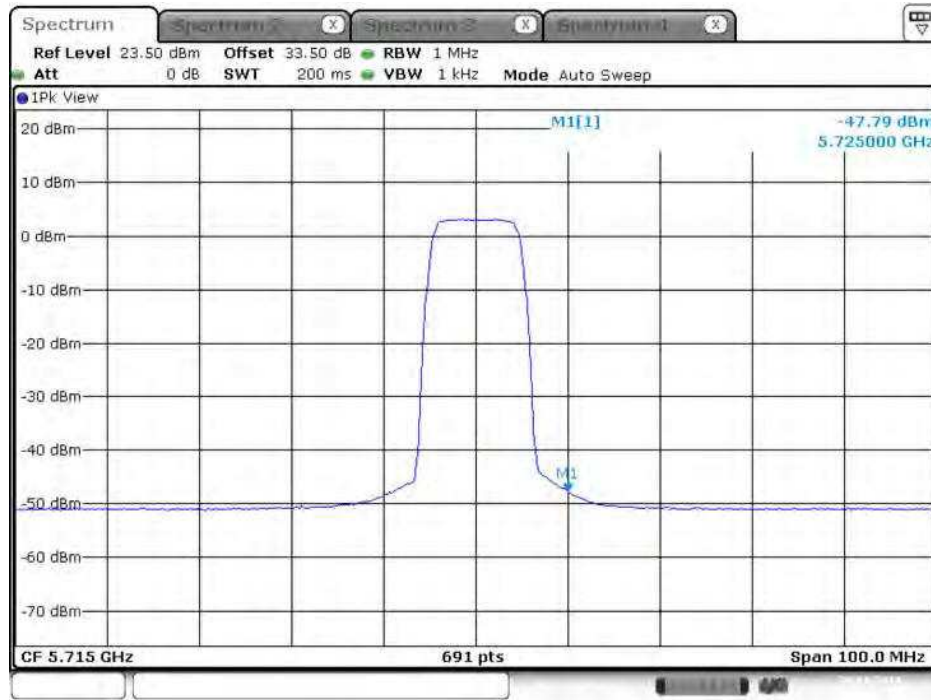
Date: 23.JAN 2018 12:07:33

Plot on Configuration QPSK, 10M / 5575 MHz / Peak / Port 2 (TX2)



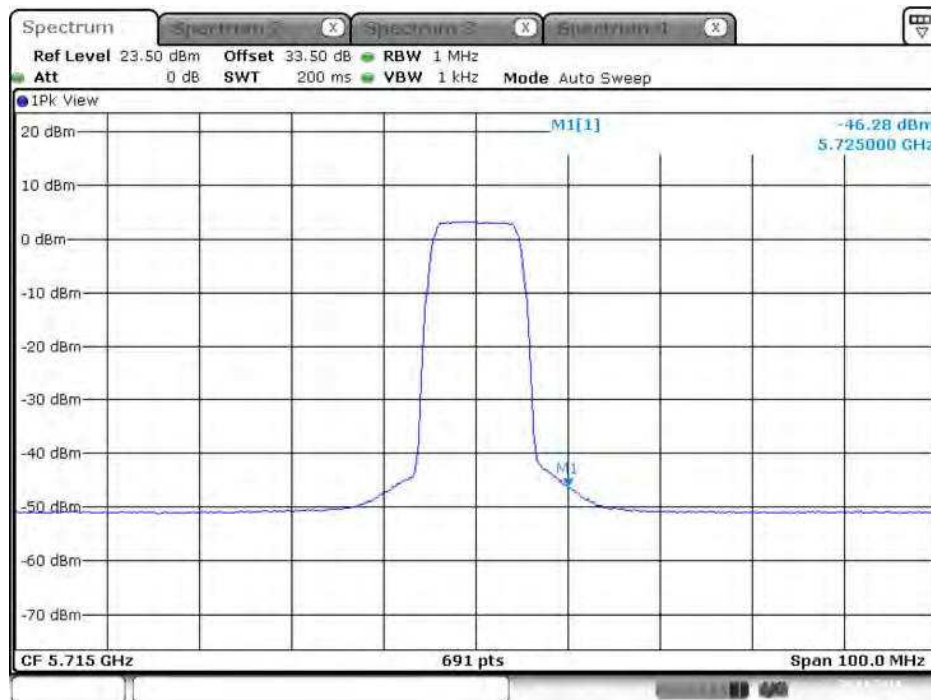
Date: 23.JAN 2018 12:03:39

Plot on Configuration QPSK, 10M / 5715 MHz / Average / Port 1 (TX1)



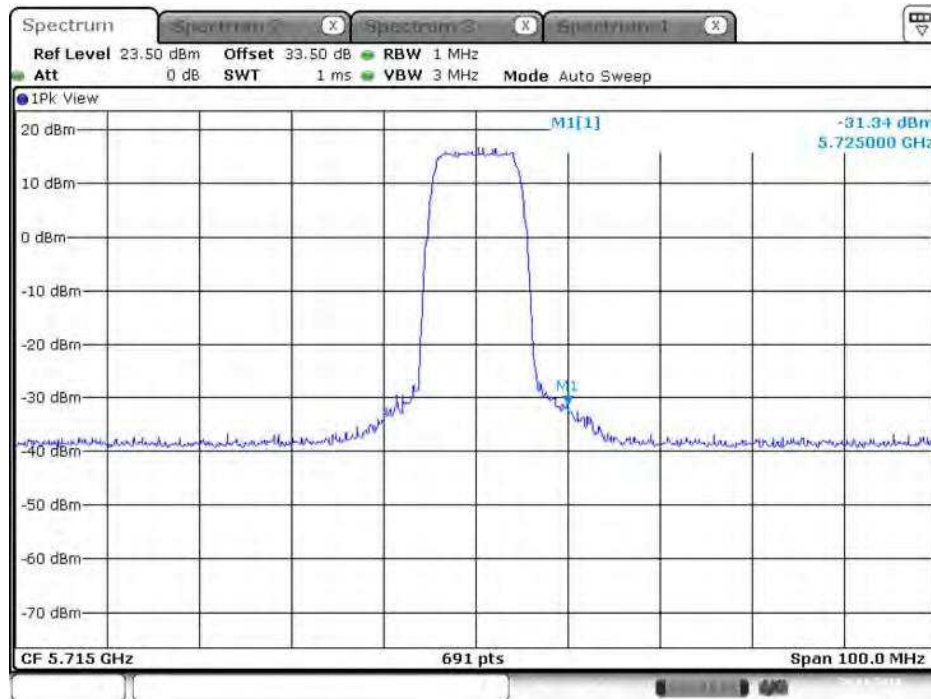
Date: 24.JAN.2018 10:10:44

Plot on Configuration QPSK, 10M / 5715 MHz / Average / Port 2 (TX2)



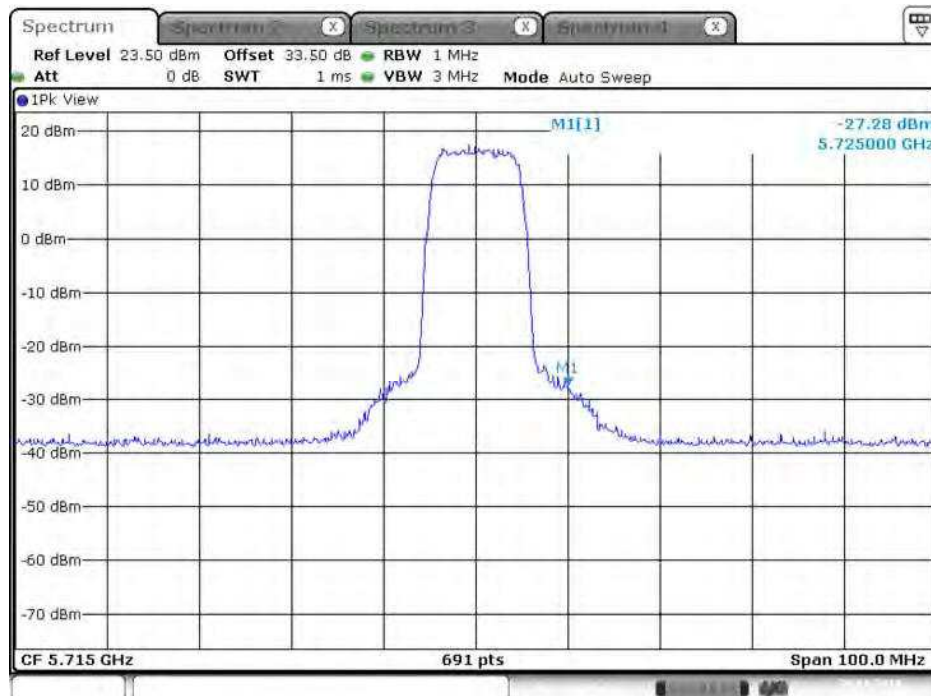
Date: 24.JAN.2018 10:00:38

Plot on Configuration QPSK, 10M / 5715 MHz / Peak / Port 1 (TX1)



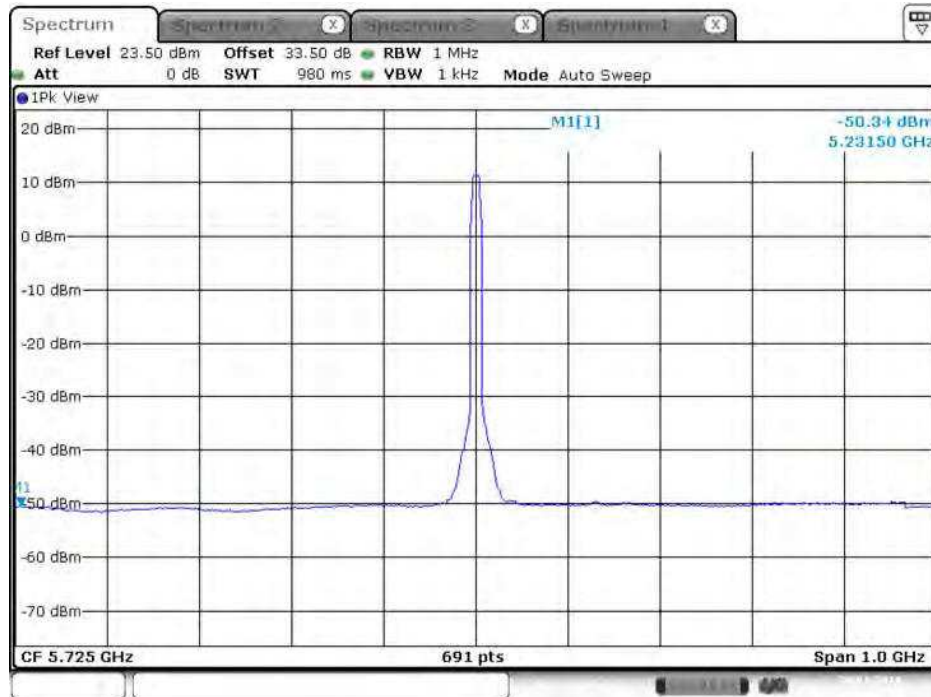
Date: 24.JAN 2018 10:12:26

Plot on Configuration QPSK, 10M / 5715 MHz / Peak / Port 2 (TX2)



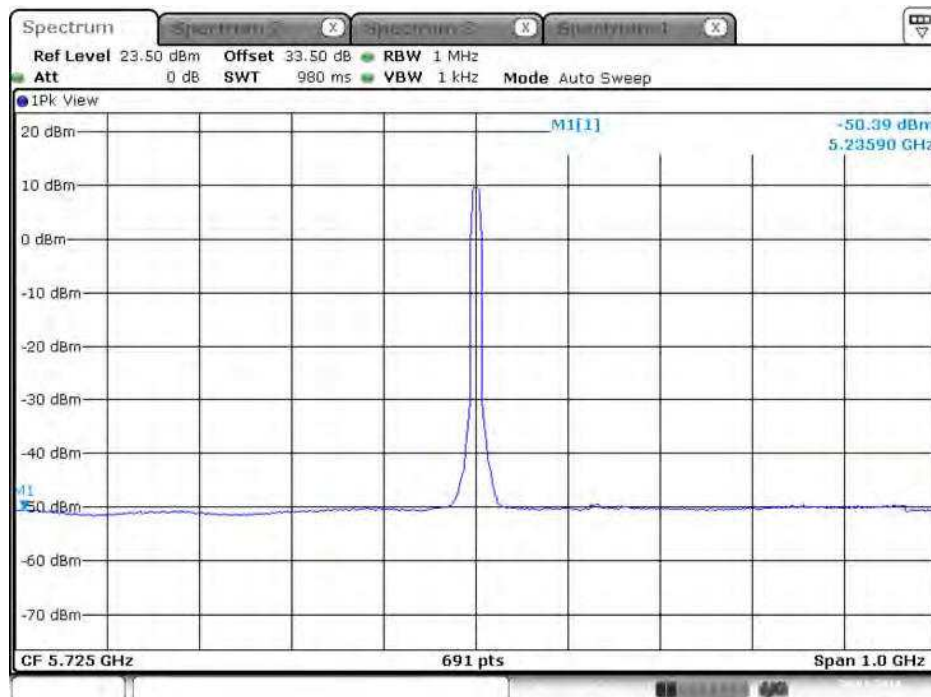
Date: 24.JAN 2018 10:04:27

Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Average / Port 1 (TX1)



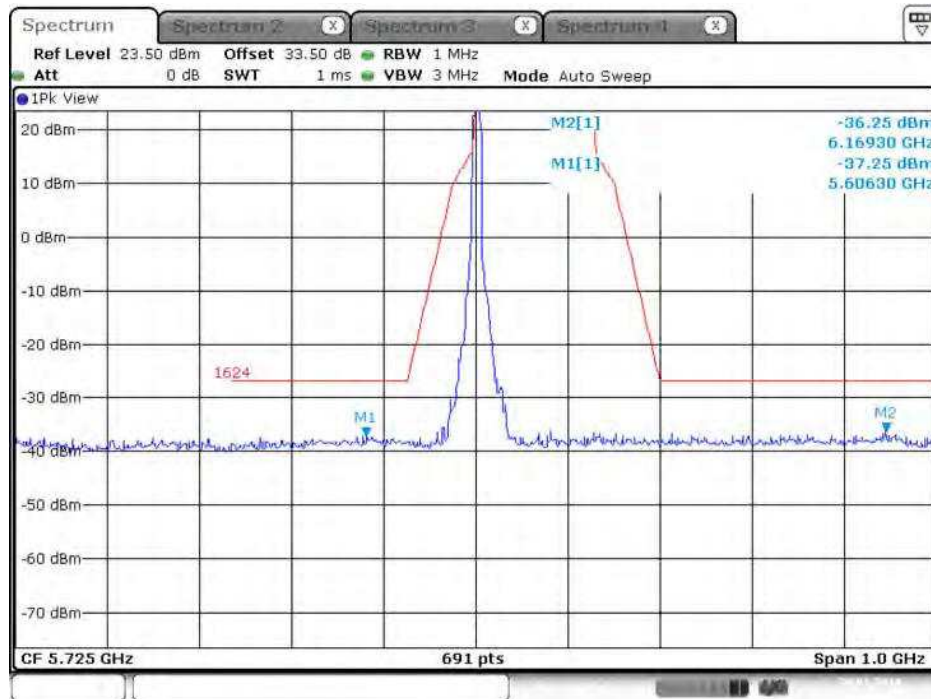
Date: 24.JAN.2018 00:45:14

Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Average / Port 2 (TX2)



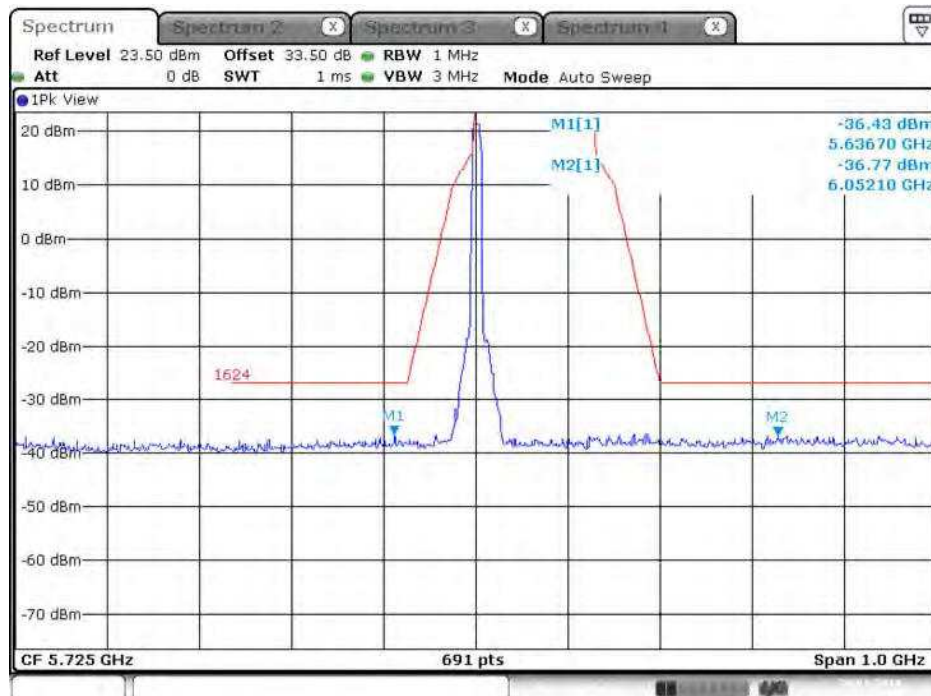
Date: 24.JAN.2018 00:41:58

Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Peak / Port 1 (TX1)



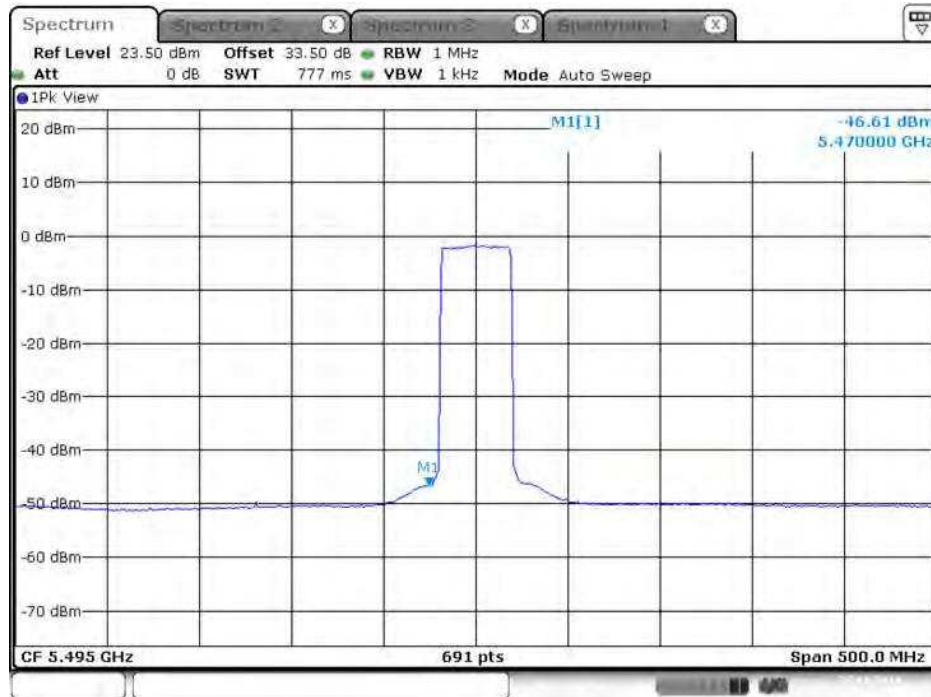
Date: 24.JAN.2018 00:44:31

Plot on Configuration QPSK, 10M / 5725 MHz (Straddle Channel) / Peak / Port 2 (TX2)



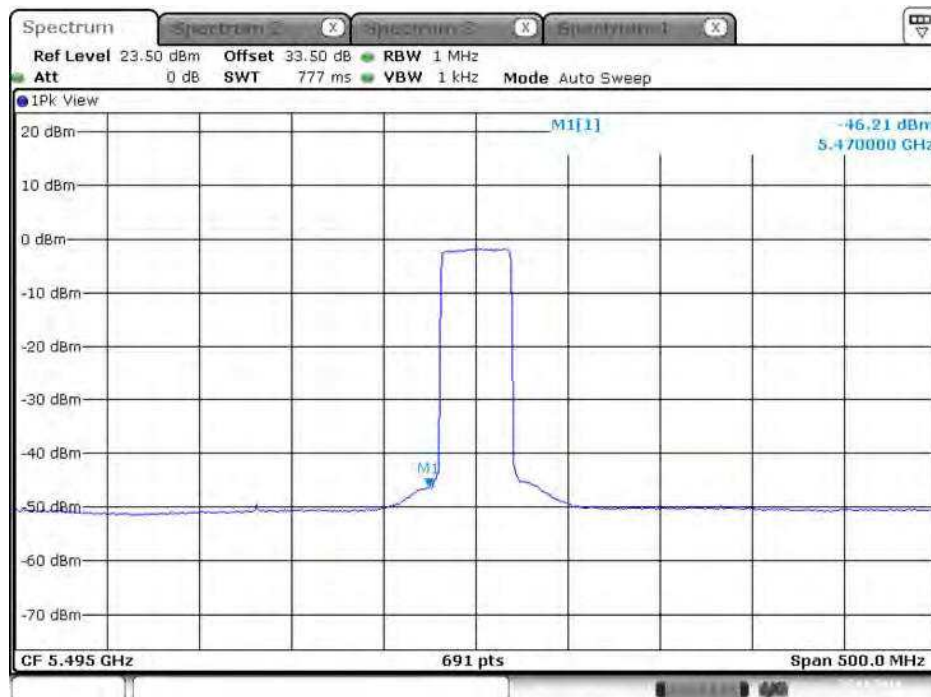
Date: 24.JAN.2018 00:43:07

Plot on Configuration QPSK, 40M / 5495 MHz / Average / Port 1 (TX1)



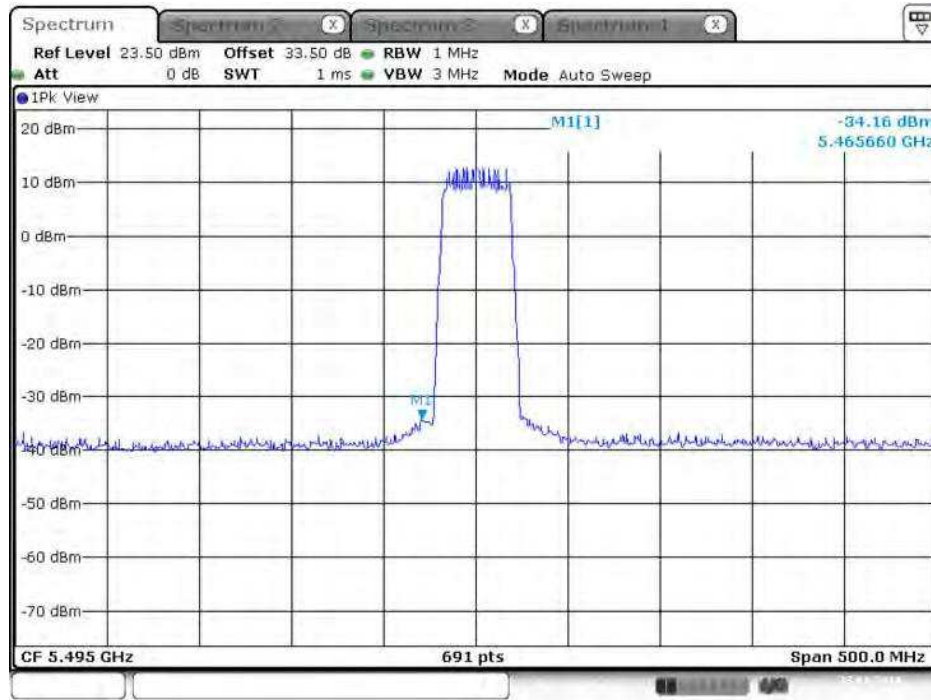
Date: 23.JAN 2018 16:46:44

Plot on Configuration QPSK, 40M / 5495 MHz / Average / Port 2 (TX2)



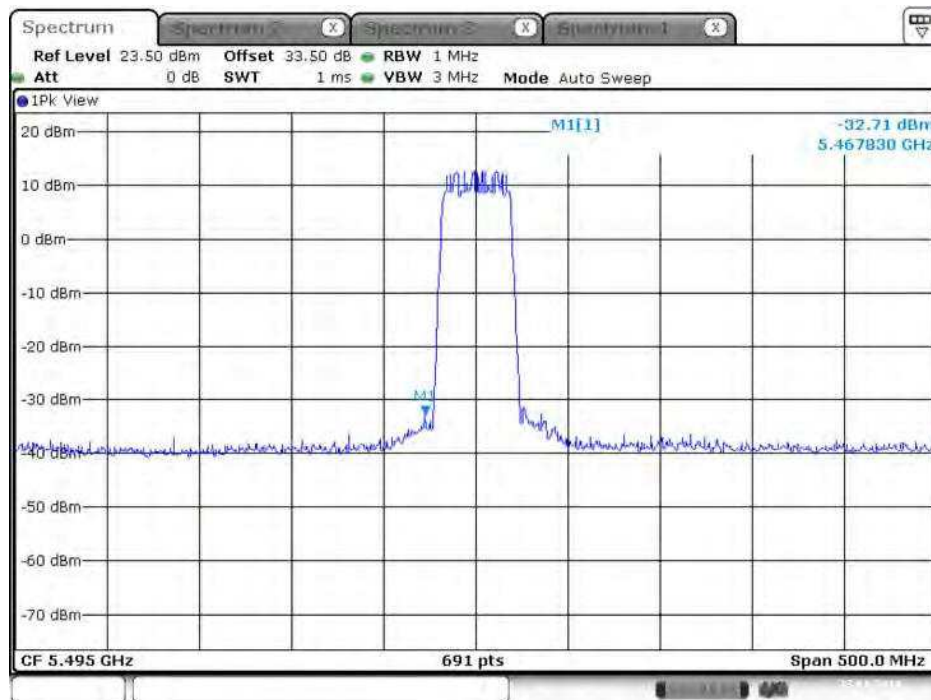
Date: 23.JAN 2018 16:50:03

Plot on Configuration QPSK, 40M / 5495 MHz / Peak / Port 1 (TX1)



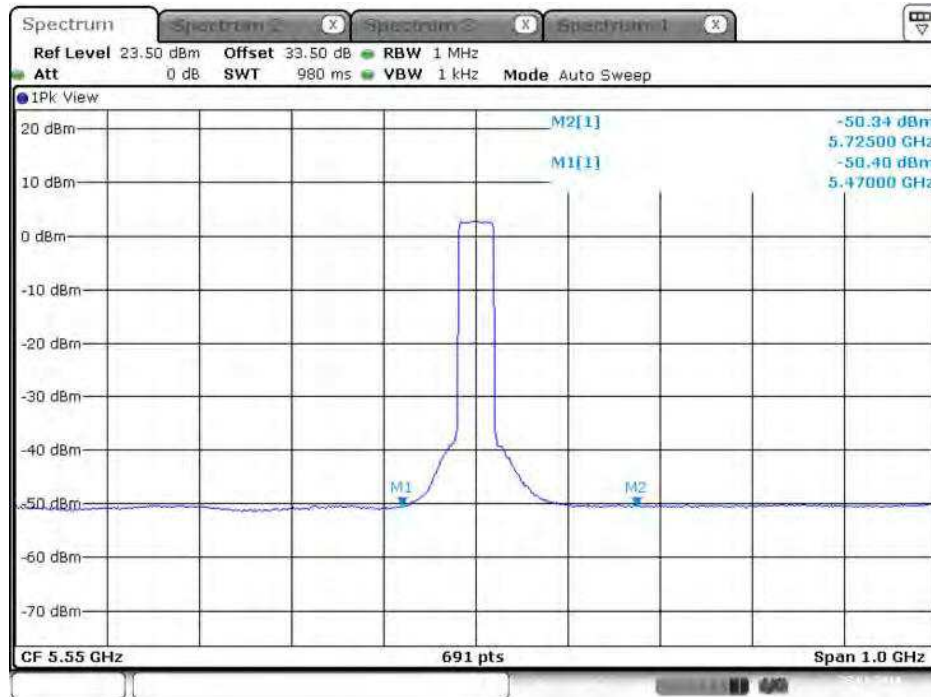
Date: 23.JAN.2018 16:48:08

Plot on Configuration QPSK, 40M / 5495 MHz / Peak / Port 2 (TX2)



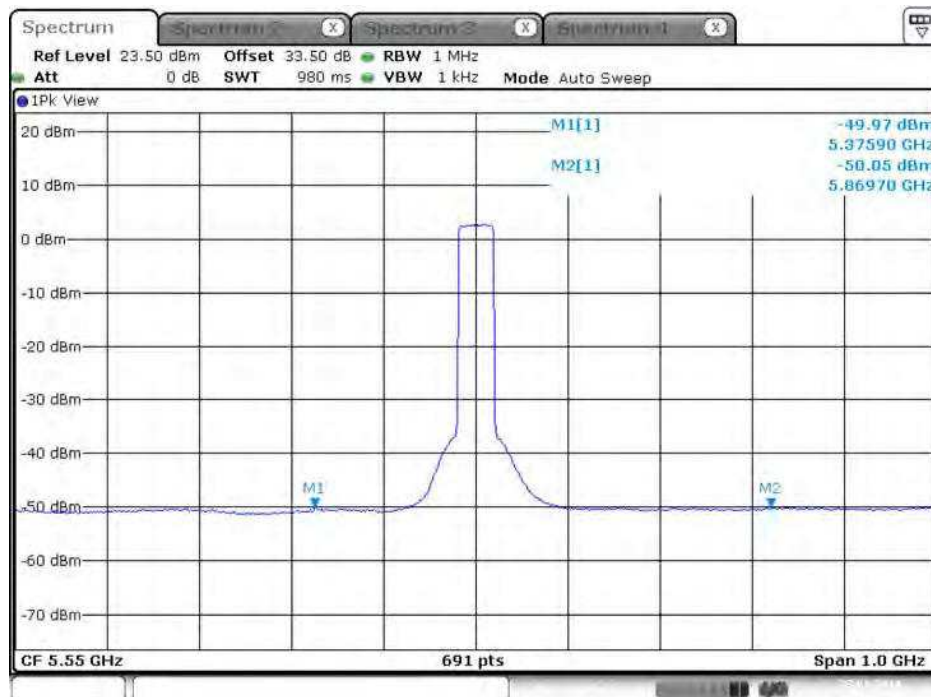
Date: 23.JAN.2018 16:50:53

Plot on Configuration QPSK, 40M / 5550 MHz / Average / Port 1 (TX1)



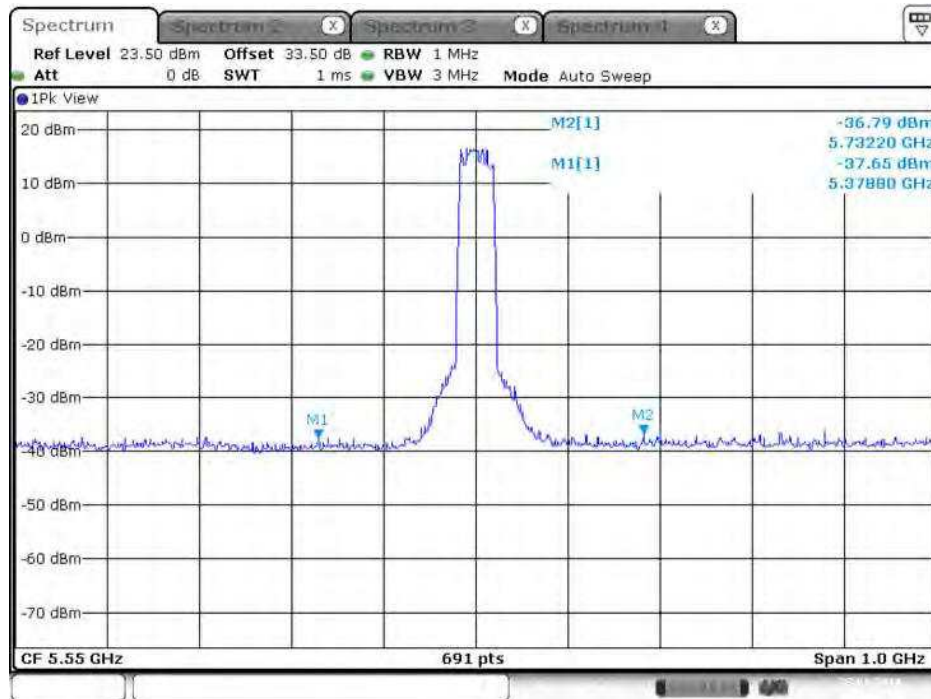
Date: 23.JAN.2018 18:22:15

Plot on Configuration QPSK, 40M / 5550 MHz / Average / Port 2 (TX2)



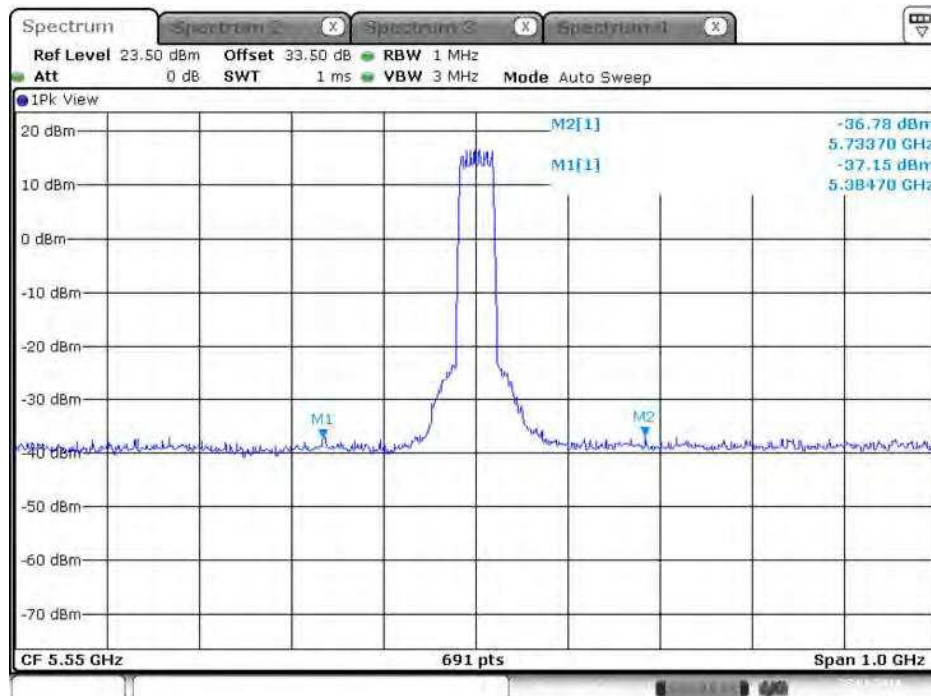
Date: 23.JAN.2018 18:27:39

Plot on Configuration QPSK, 40M / 5550 MHz / Peak / Port 1 (TX1)



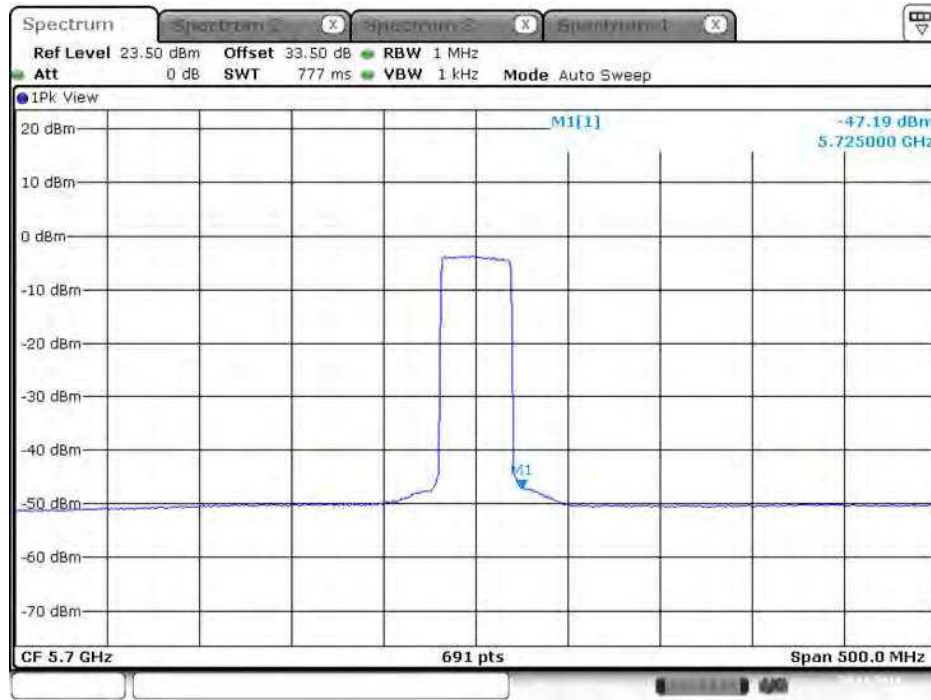
Date: 23.JAN 2018 18:24:20

Plot on Configuration QPSK, 40M / 5550 MHz / Peak / Port 2 (TX2)



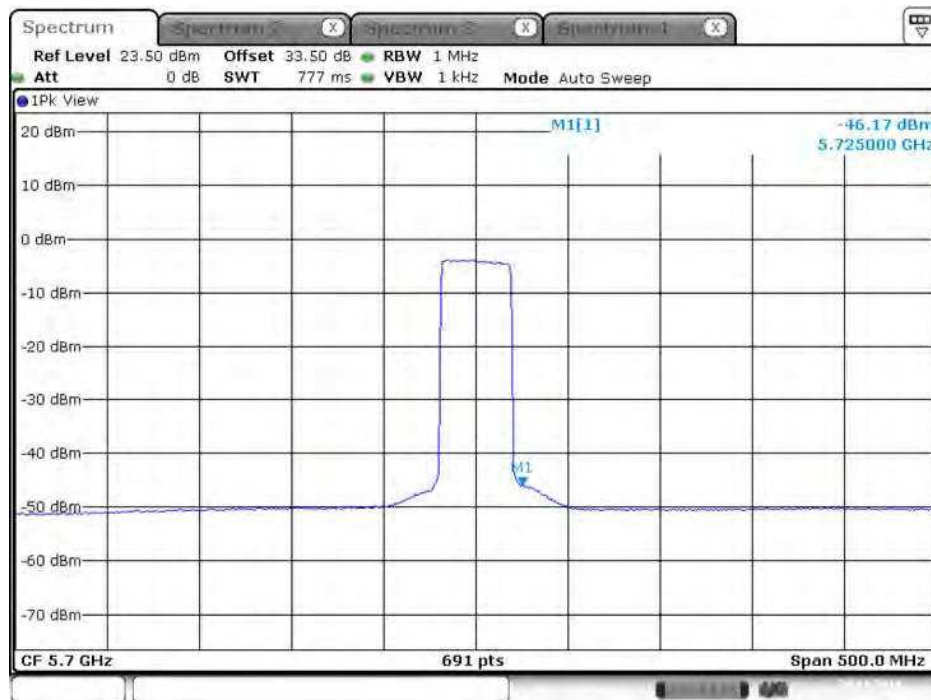
Date: 23.JAN 2018 18:29:10

Plot on Configuration QPSK, 40M / 5700 MHz / Average / Port 1 (TX1)



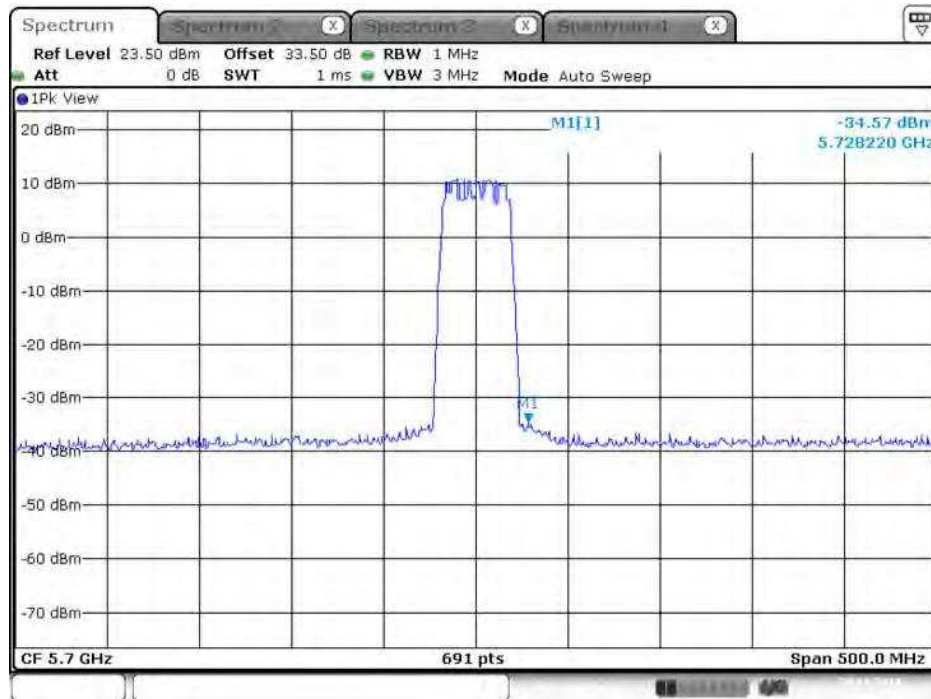
Date: 24.JAN.2018 00:12:12

Plot on Configuration QPSK, 40M / 5700 MHz / Average / Port 2 (TX2)



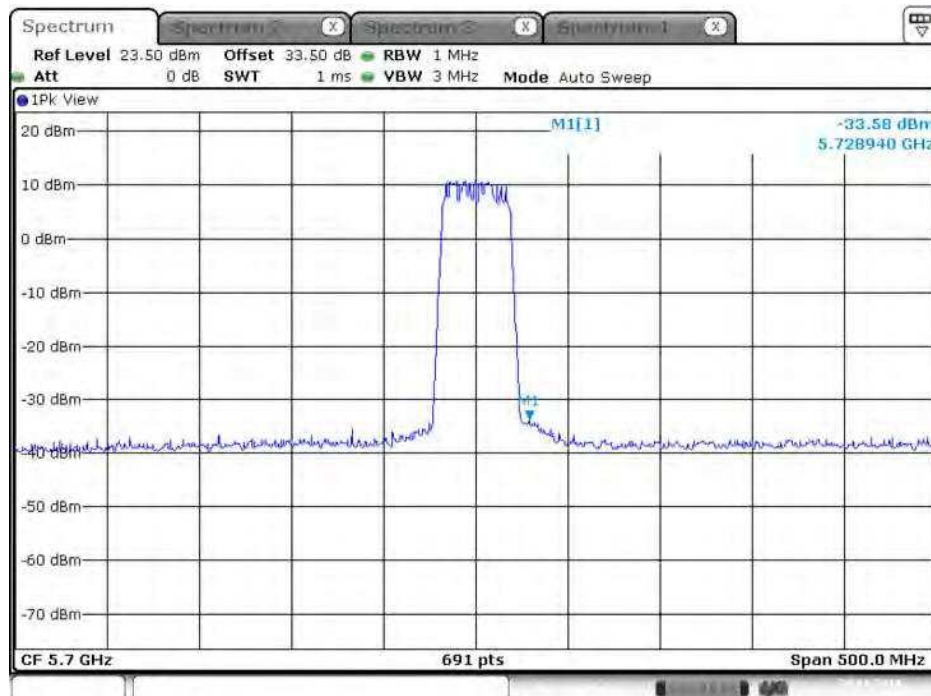
Date: 24.JAN.2018 00:10:16

Plot on Configuration QPSK, 40M / 5700 MHz / Peak / Port 1 (TX1)



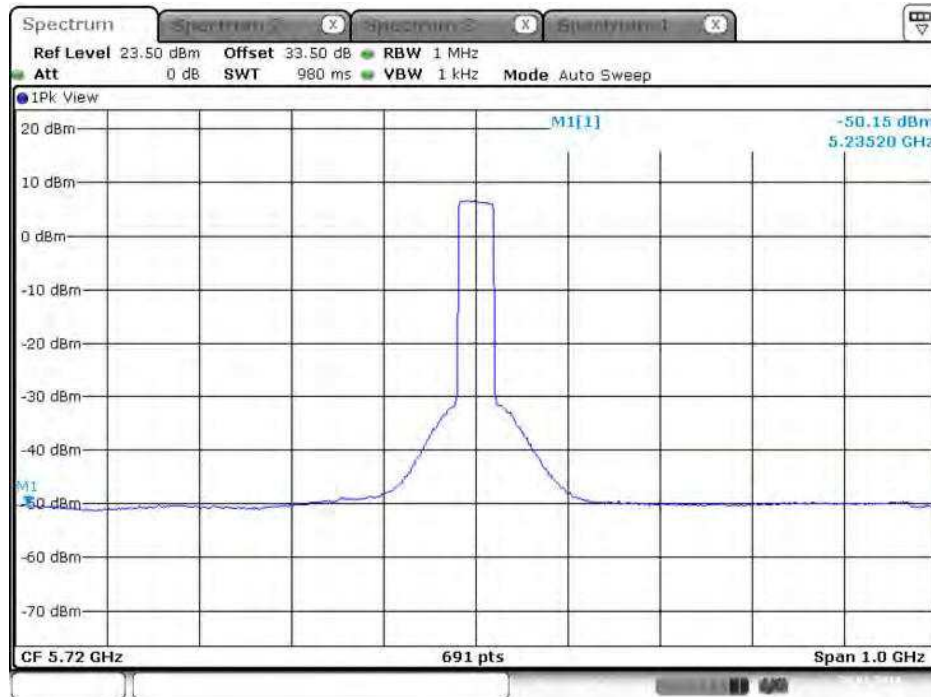
Date: 24.JAN.2018 00:12:54

Plot on Configuration QPSK, 40M / 5700 MHz / Peak / Port 2 (TX2)



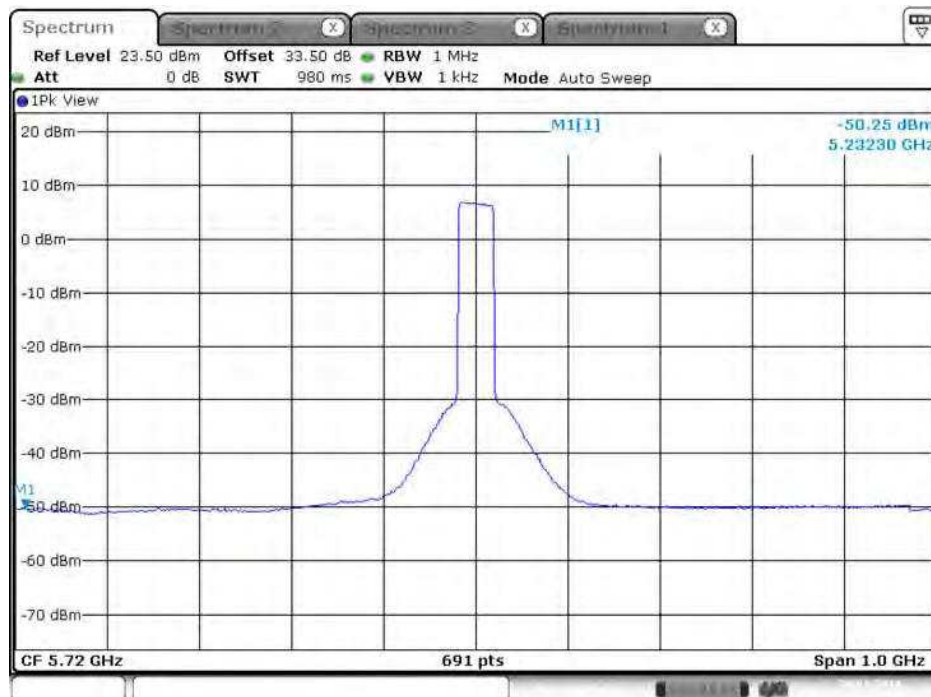
Date: 24.JAN.2018 00:11:02

Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Average / Port 1 (TX1)



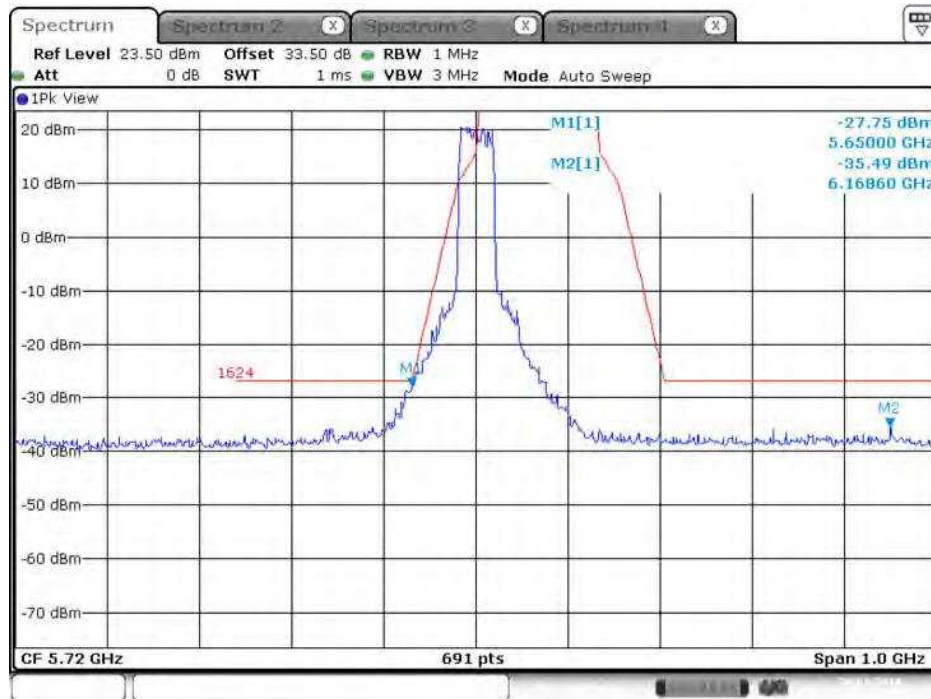
Date: 24.JAN.2018 01:03:32

Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Average / Port 2 (TX2)



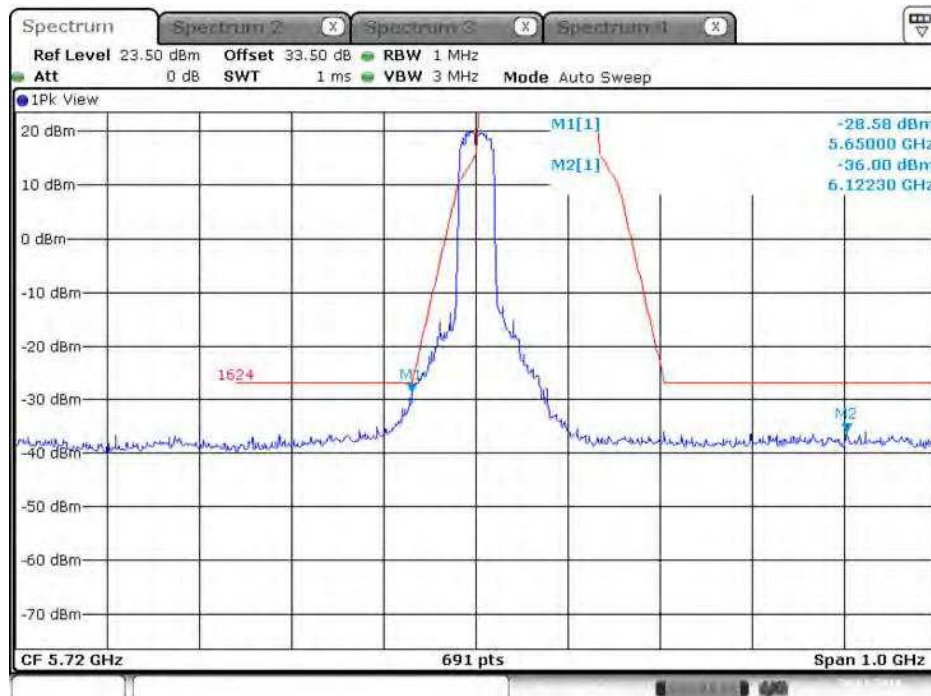
Date: 24.JAN.2018 00:49:18

Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Peak / Port 1 (TX1)



Date: 24.JAN.2018 01:02:56

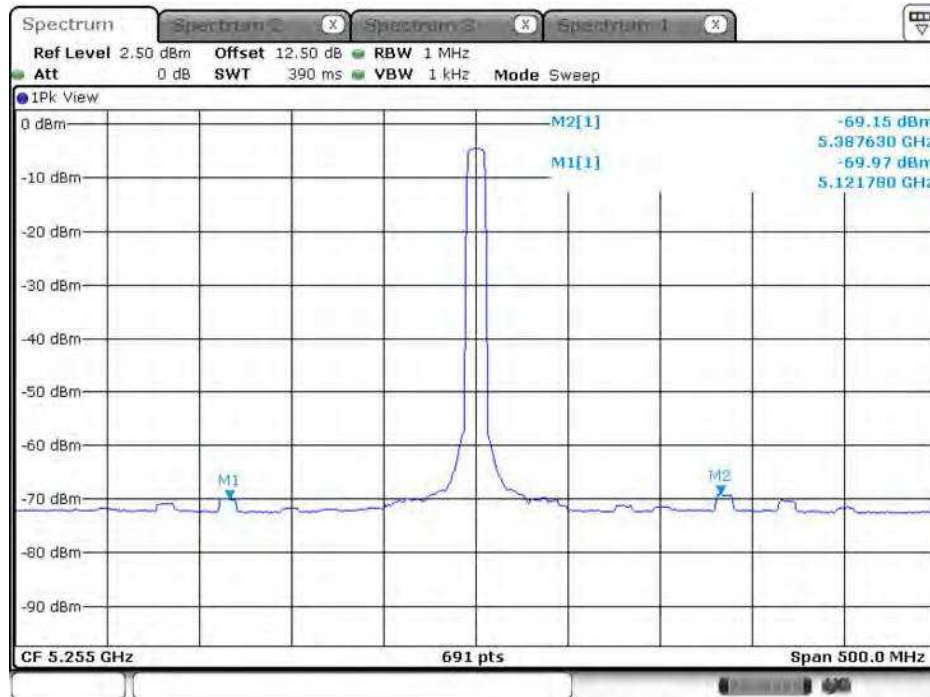
Plot on Configuration QPSK, 40M / 5720 MHz (Straddle Channel) / Peak / Port 2 (TX2)



Date: 24.JAN.2018 01:01:02

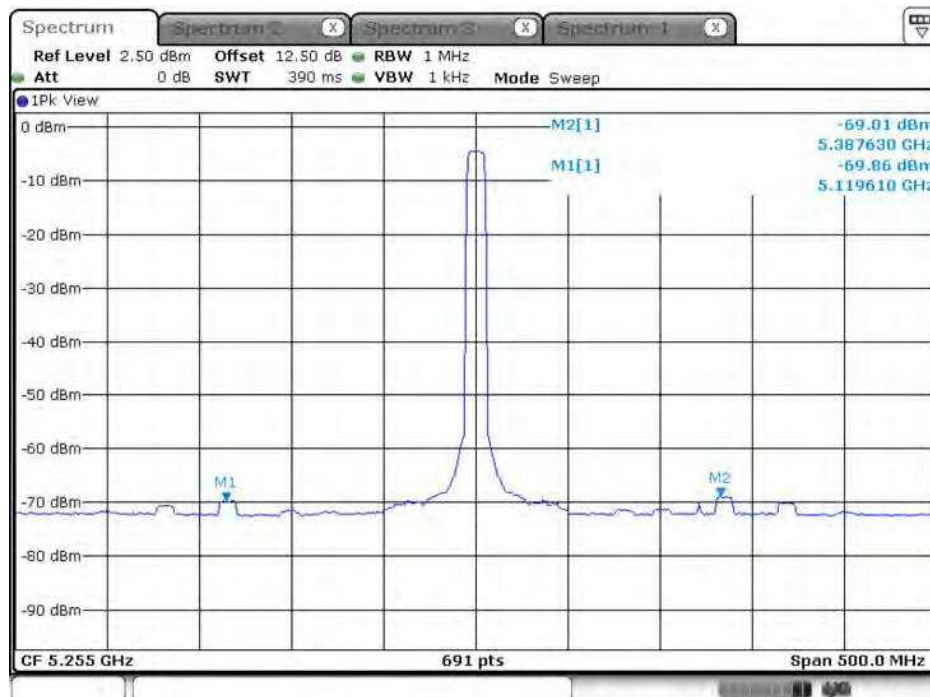
For Antenna 2:

Plot on Configuration QPSK, 10M / 5255 MHz / Average / Port 1 (TX1)



Date: 9.FEB.2018 15:15:18

Plot on Configuration QPSK, 10M / 5255 MHz / Average / Port 2 (TX2)



Date: 9.FEB.2018 15:19:53