



Antenna Part Specification

Customer name:	EDIFIER
Project name:	Neobuds Evo
Material category:	BT antenna
Antenna type:	monopole antenna
Version:	V1.0
Manufacturer name:	Shenzhen Cicent Communication Technology Co., Ltd.
Address:	505-506 ,A Block,Donglian Building,Chuangye 2 road,Baoan District,Shenzhen,Guangdong,P.R china
Date:	2024.07.06



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Change record			
Compile / change date	Reason for change	Changed content	Version
2024.07.06	First edition	First edition	V1.0

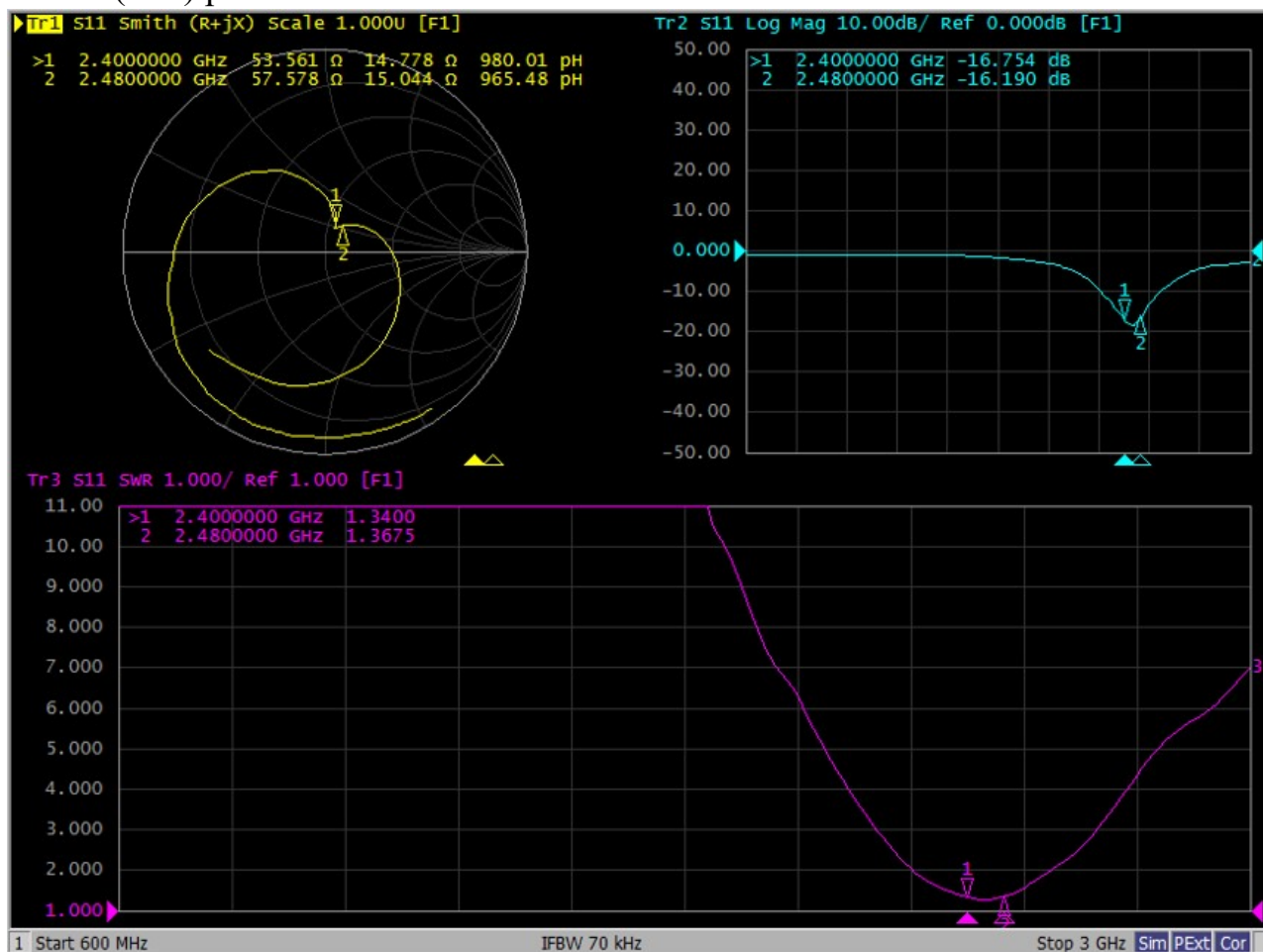


I: The report of passive data



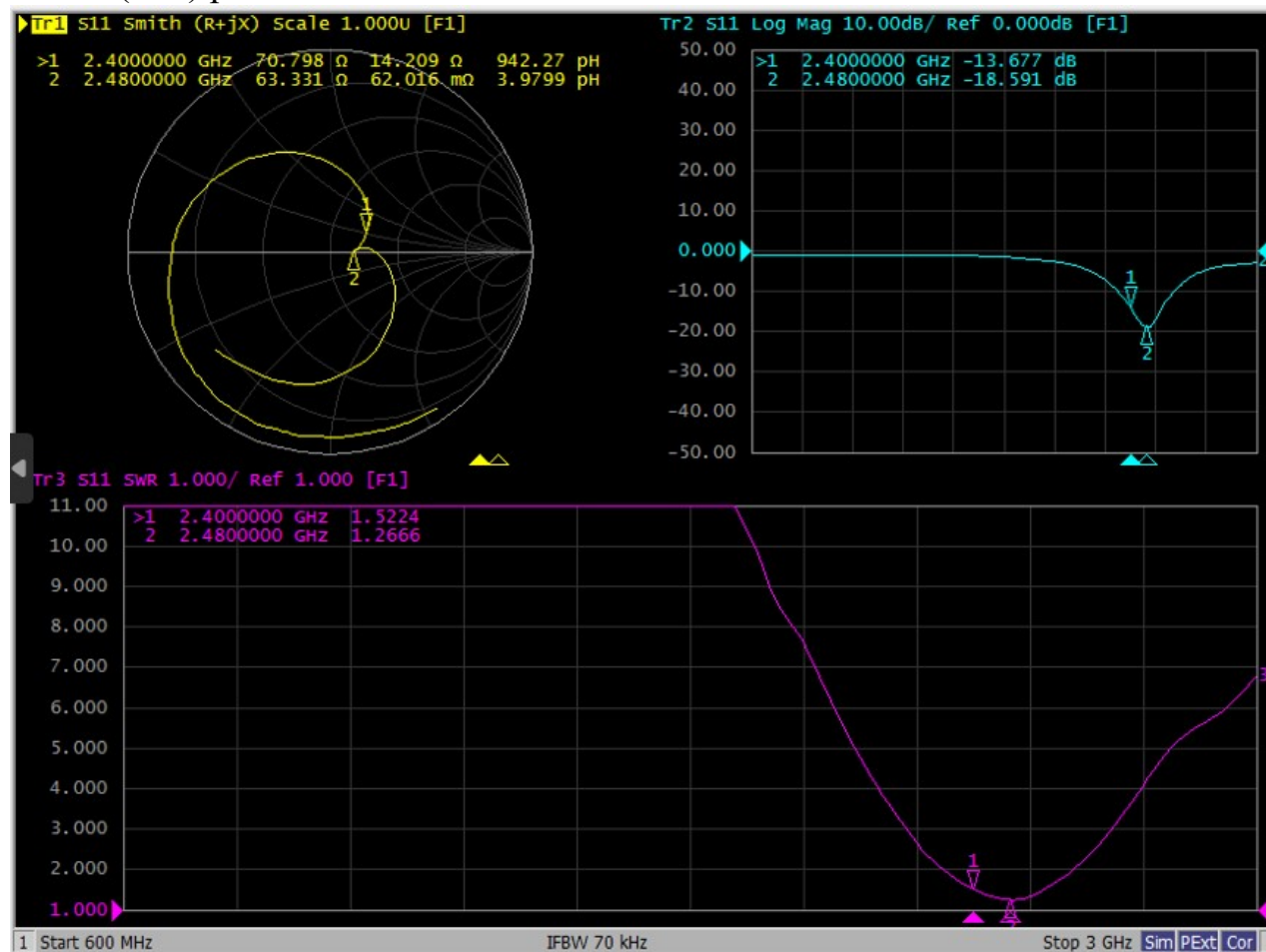
Angilent E5071C

VSWR(S11) parameter (L) :





VSWR(S11) parameter (R) :





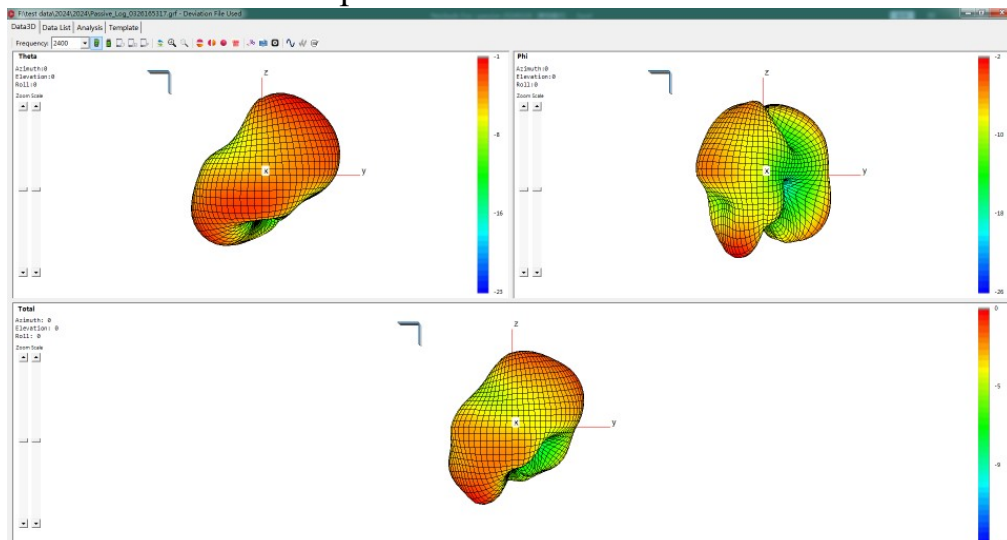
Efficiency:

2400-2480 MHz (L)			
Frequency (MHz)	Gain (dBi)	Efficiency (dB)	Efficiency
2400	-0.2	-3.7	43.0
2410	-0.1	-3.6	43.4
2420	0.0	-3.6	44.0
2430	0.0	-3.7	42.5
2440	-0.1	-3.9	40.8
2450	0.1	-3.8	41.8
2460	0.1	-3.9	40.6
2470	0.5	-3.6	43.3
2480	0.7	-3.4	46.0
Average value	0.11	-3.68	42.83

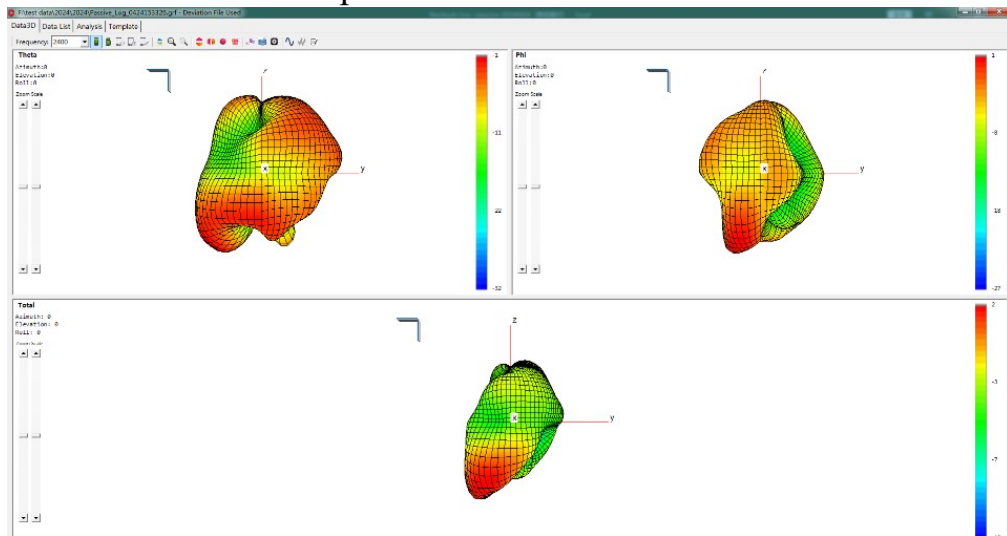
2400-2480 MHz (R)			
Frequency (MHz)	Gain (dBi)	Efficiency (dB)	Efficiency
2400	-0.6	-3.8	42.1
2410	-0.5	-3.9	40.9
2420	-0.6	-4.0	39.4
2430	-0.4	-3.9	40.4
2440	-0.5	-4.0	39.5
2450	-0.2	-3.9	41.0
2460	0.2	-3.5	44.4
2470	0.1	-3.7	42.8
2480	0.0	-3.7	42.3
Average value	-0.28	-3.83	41.42



3D Antenna radiation pattern (L) :



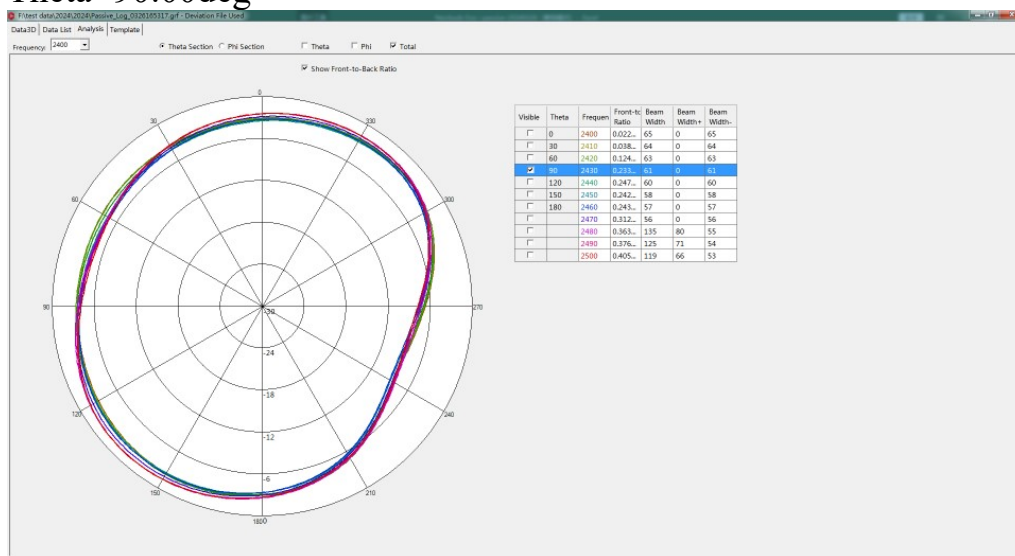
3D Antenna radiation pattern (R) :



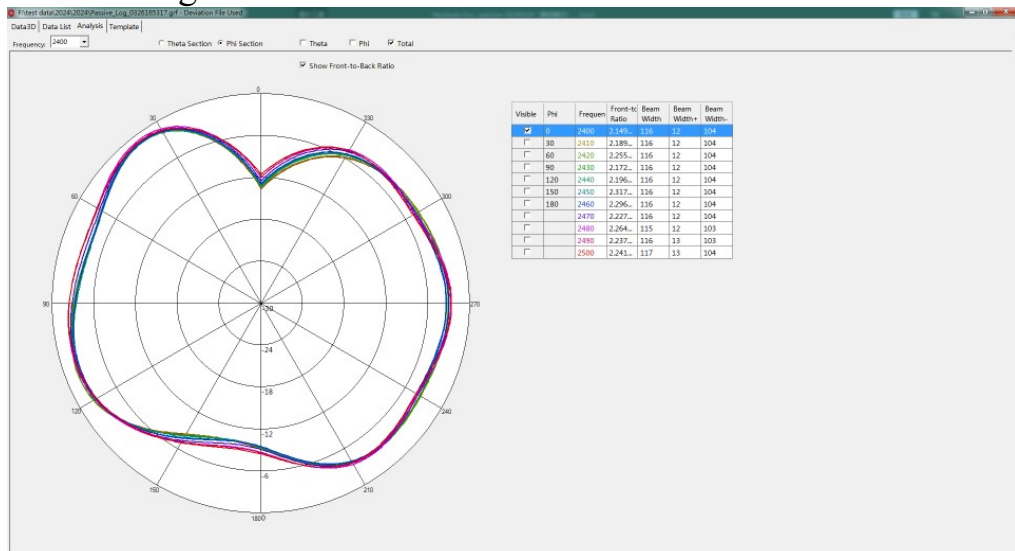


Antenna radiation pattern (L) :

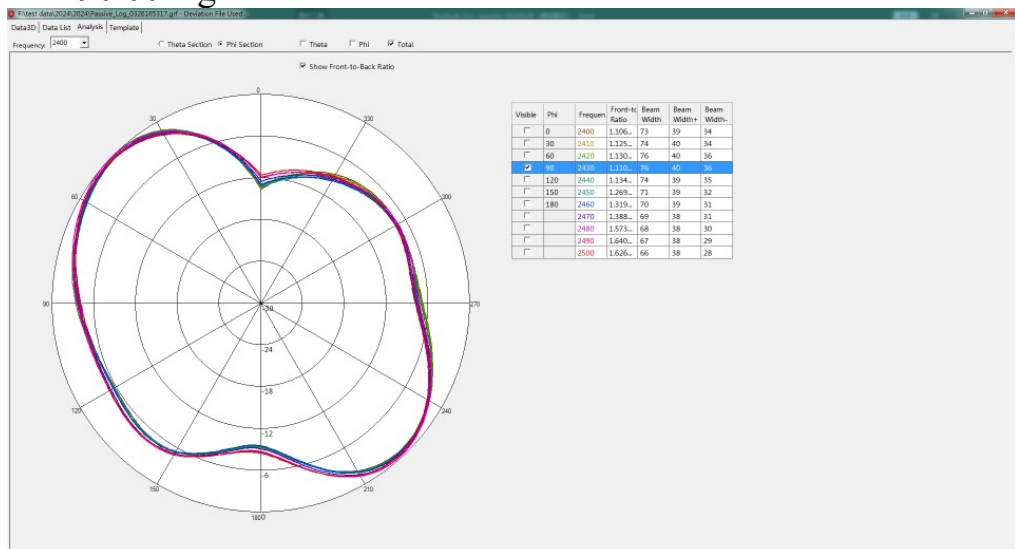
Theta=90.00deg



Phi=0.00deg



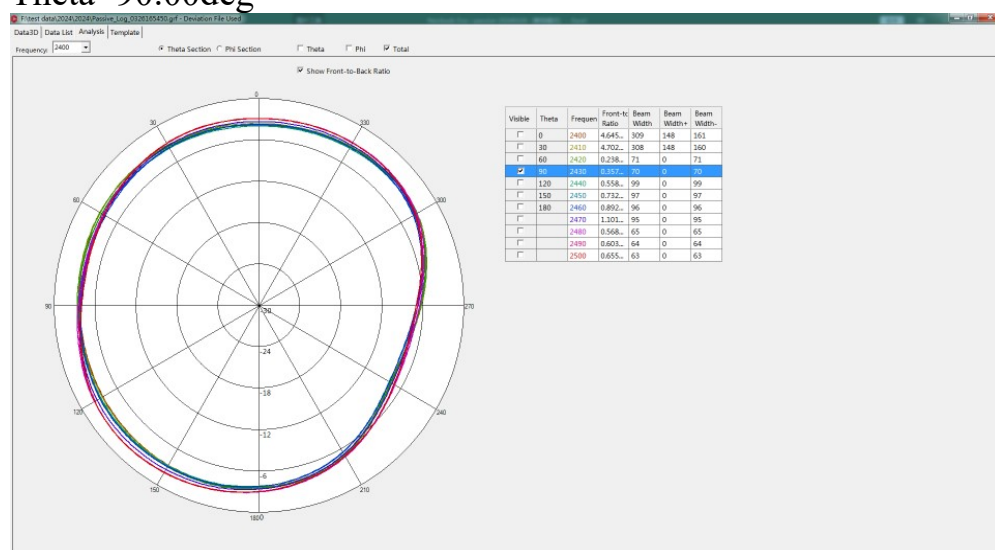
Phi=90.00deg



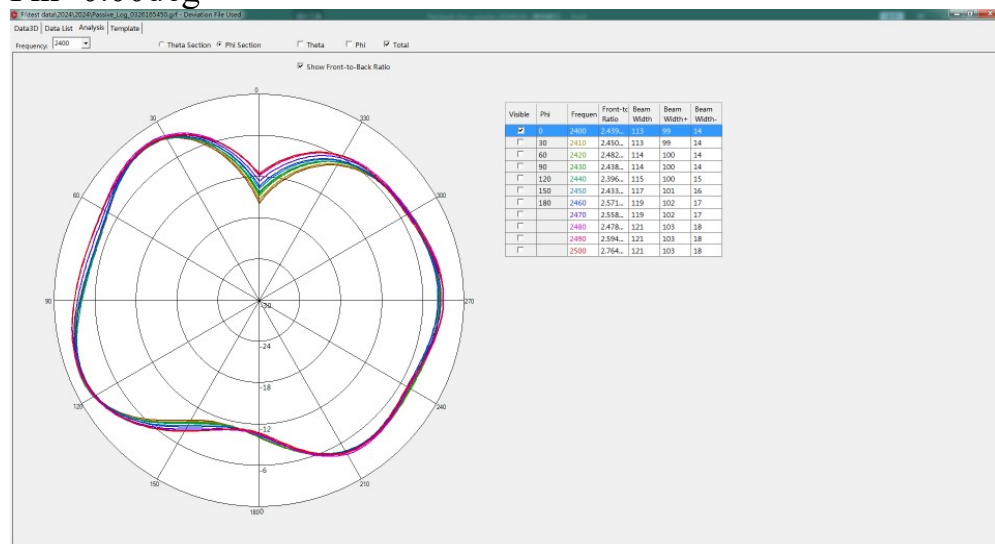


Antenna radiation pattern (R) :

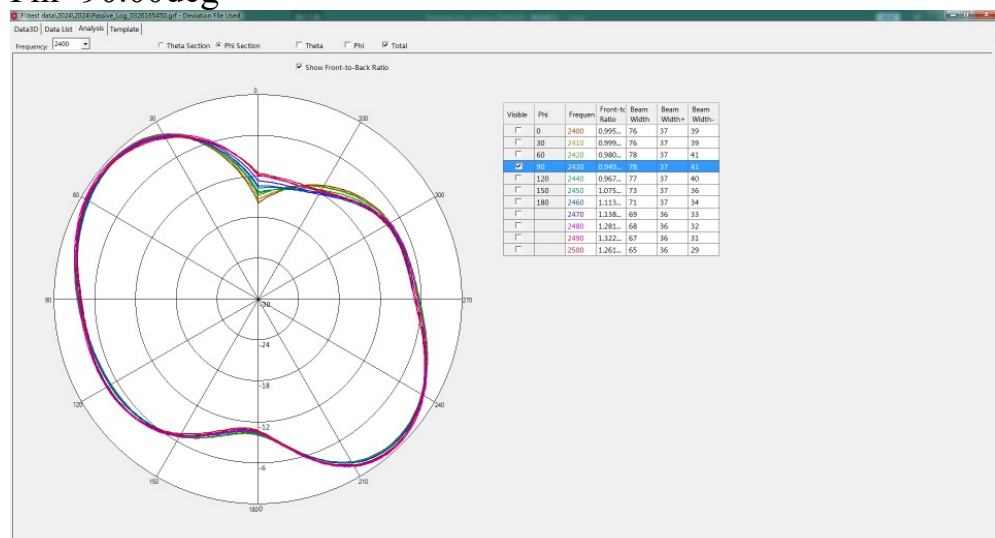
Theta=90.00deg



Phi=0.00deg



Phi=90.00deg





II: 3D Active test report of antenna

Free space	Channel	TRP (dBm)	TIS (dBm)
L	CH 0	12.4	-94.1
	CH 39	12.4	-95.0
	CH 78	12.2	-94.4

Headform	Channel	TRP (dBm)	TIS (dBm)
L	CH 0	1.9	-85.7
	CH 39	2.7	-86.0
	CH 78	2.5	-85.1

Free space	Channel	TRP (dBm)	TIS (dBm)
R	CH 0	12.4	-94.3
	CH 39	12.3	-94.7
	CH 78	12.1	-94.4

Headform	Channel	TRP (dBm)	TIS (dBm)
R	CH 0	2.0	-85.0
	CH 39	2.0	-85.4
	CH 78	1.5	-84.4



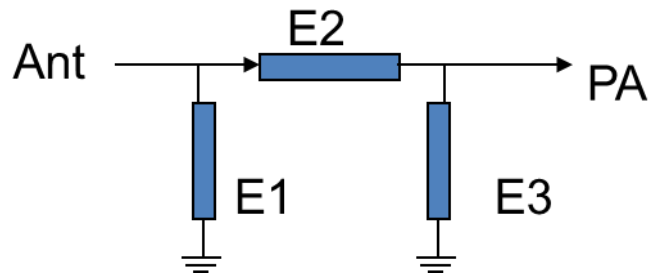
OTA Standard Chamber



III: Matching circuit

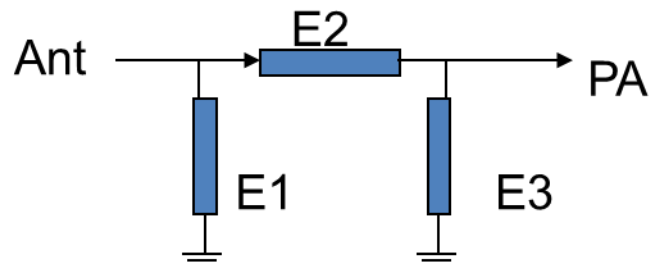
L:

Element	Value
E1	N/A
E2	0Ω
E3	N/A



R:

Element	Value
E1	N/A
E2	0Ω
E3	N/A





IV: Structure file

