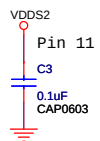
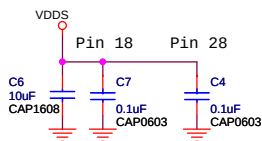


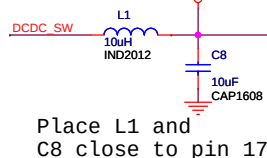
VDDS2 Decoupling Capacitors



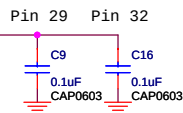
VDDS Decoupling Capacitors



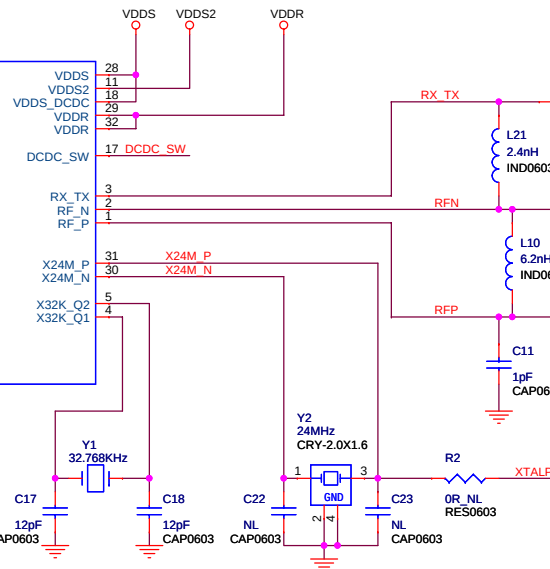
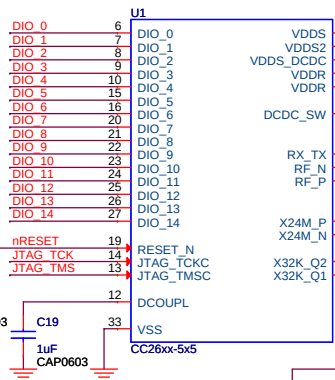
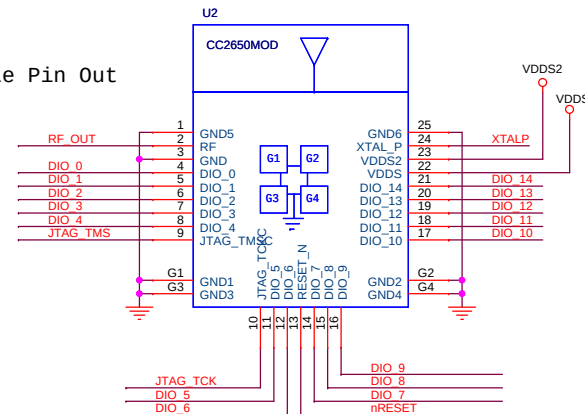
DC-DC Regulator



VDDR Decoupling Capacitors



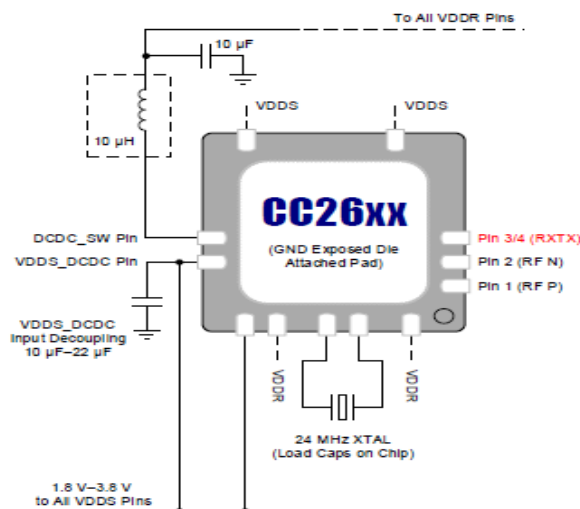
Module Pin Out



TP1 are placed in the bottom layer.

R1, C15 and C20 for antenna matching

Internal DC-DC Regulator



Texas Instruments

Title		Module
Size	Document Number	Rev
A3	CC2650MOD-SCH	D02
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