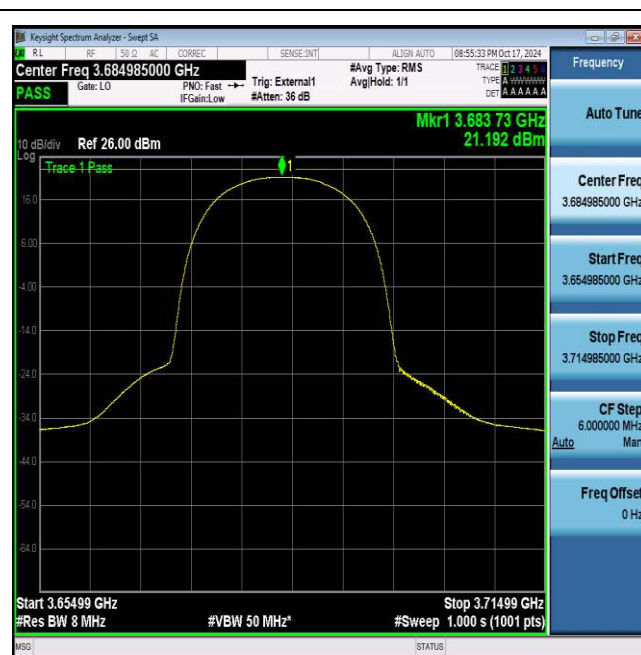




NTNV_N48_PC3_30_30_H_TID1_NS_01_3654.985_3714.985_#

1



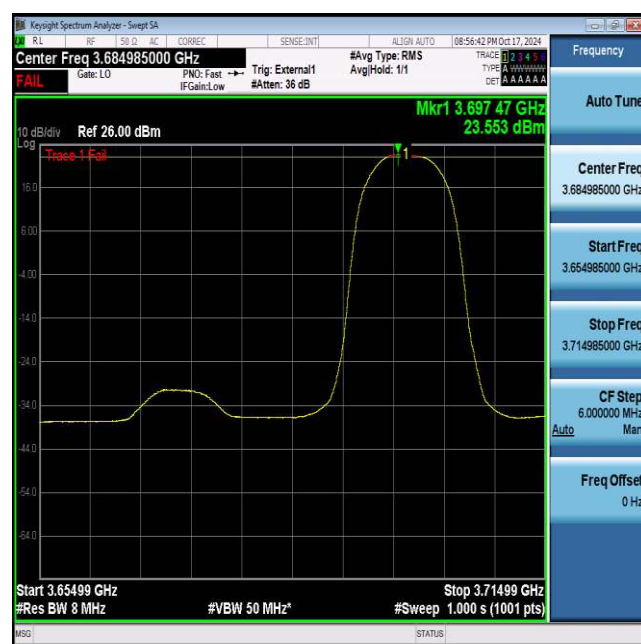
NTNV_N48_PC3_30_30_H_TID2_NS_01_3654.985_3714.985_#

1



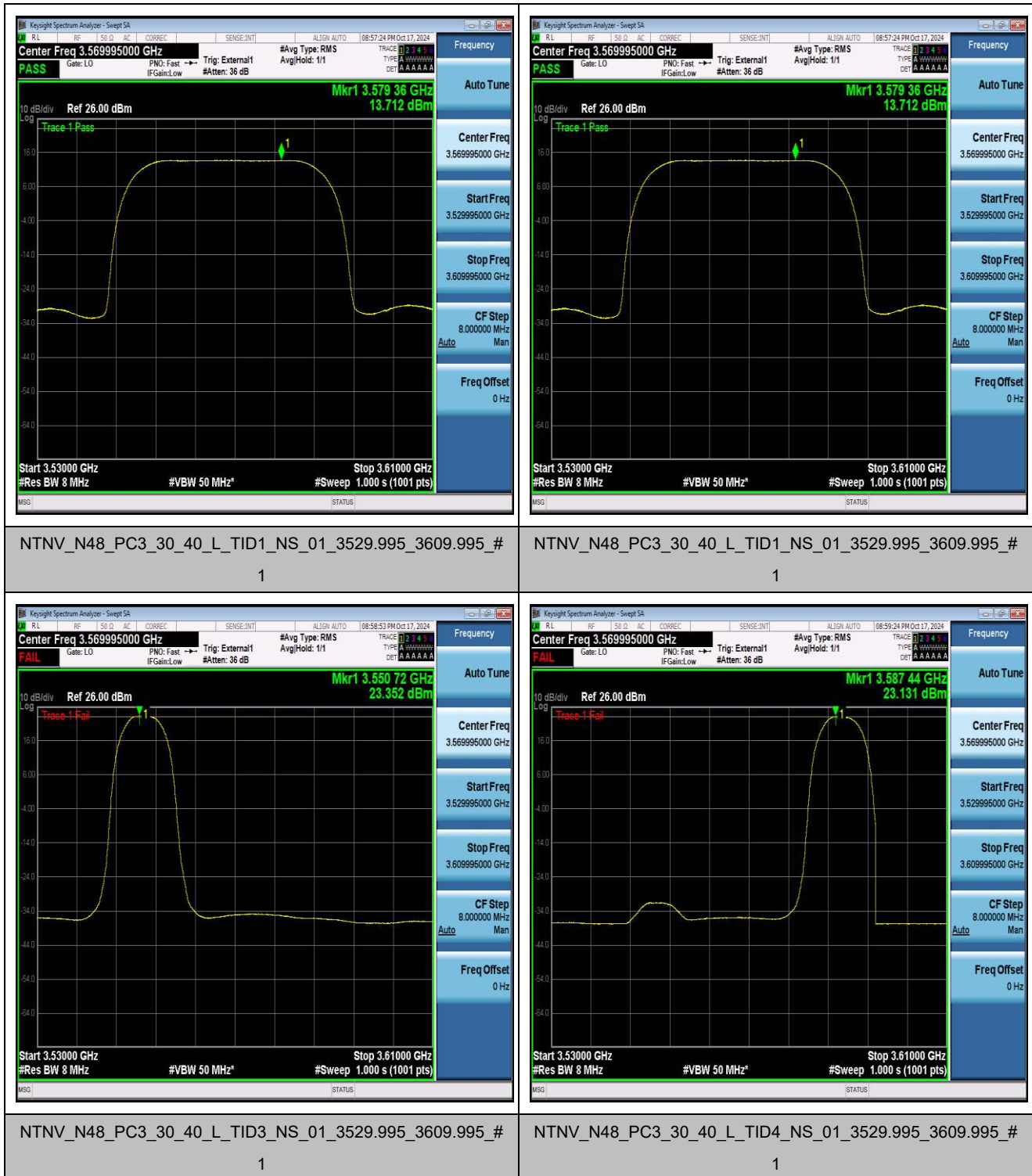
NTNV_N48_PC3_30_30_H_TID3_NS_01_3654.985_3714.985_#

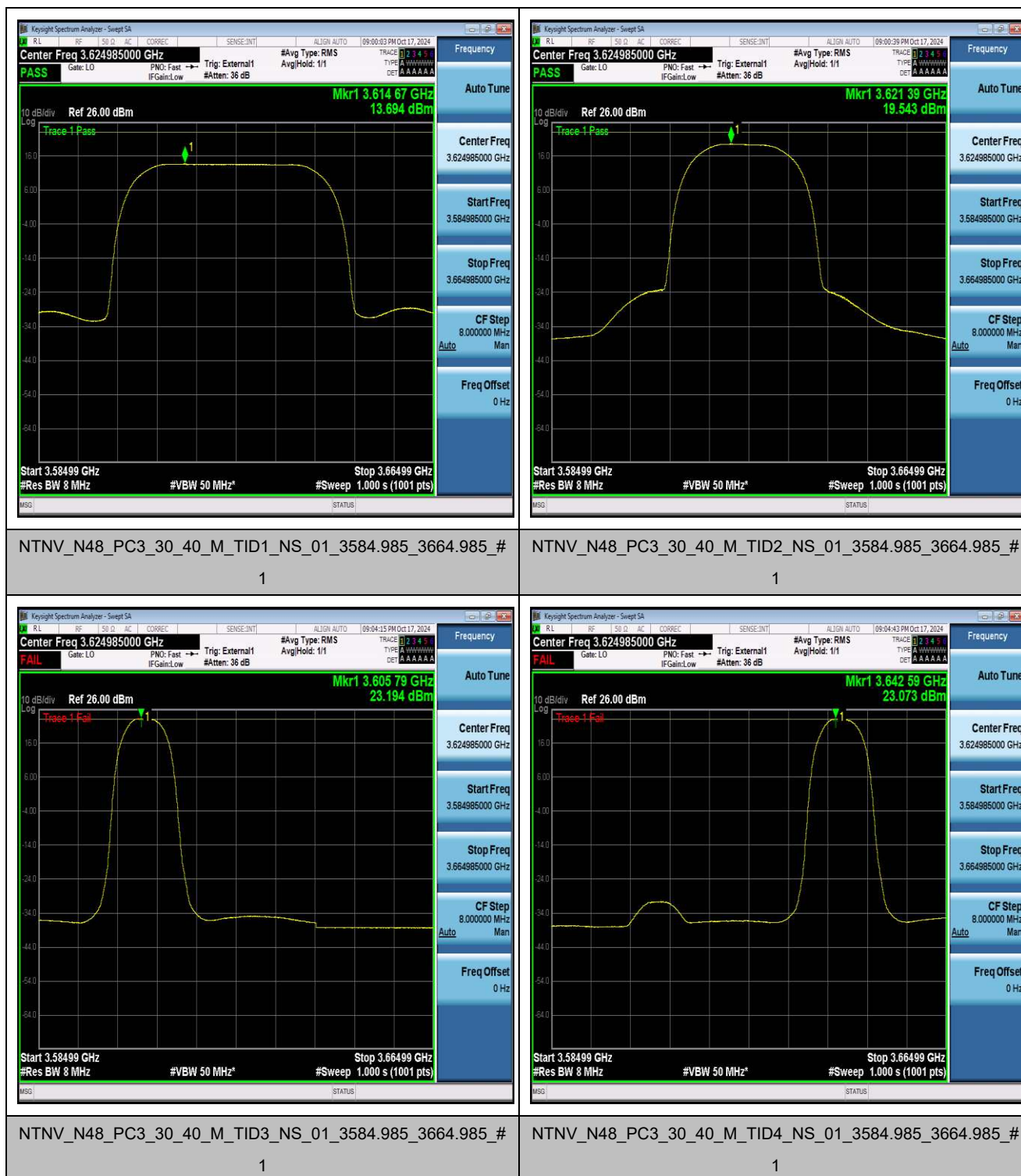
1

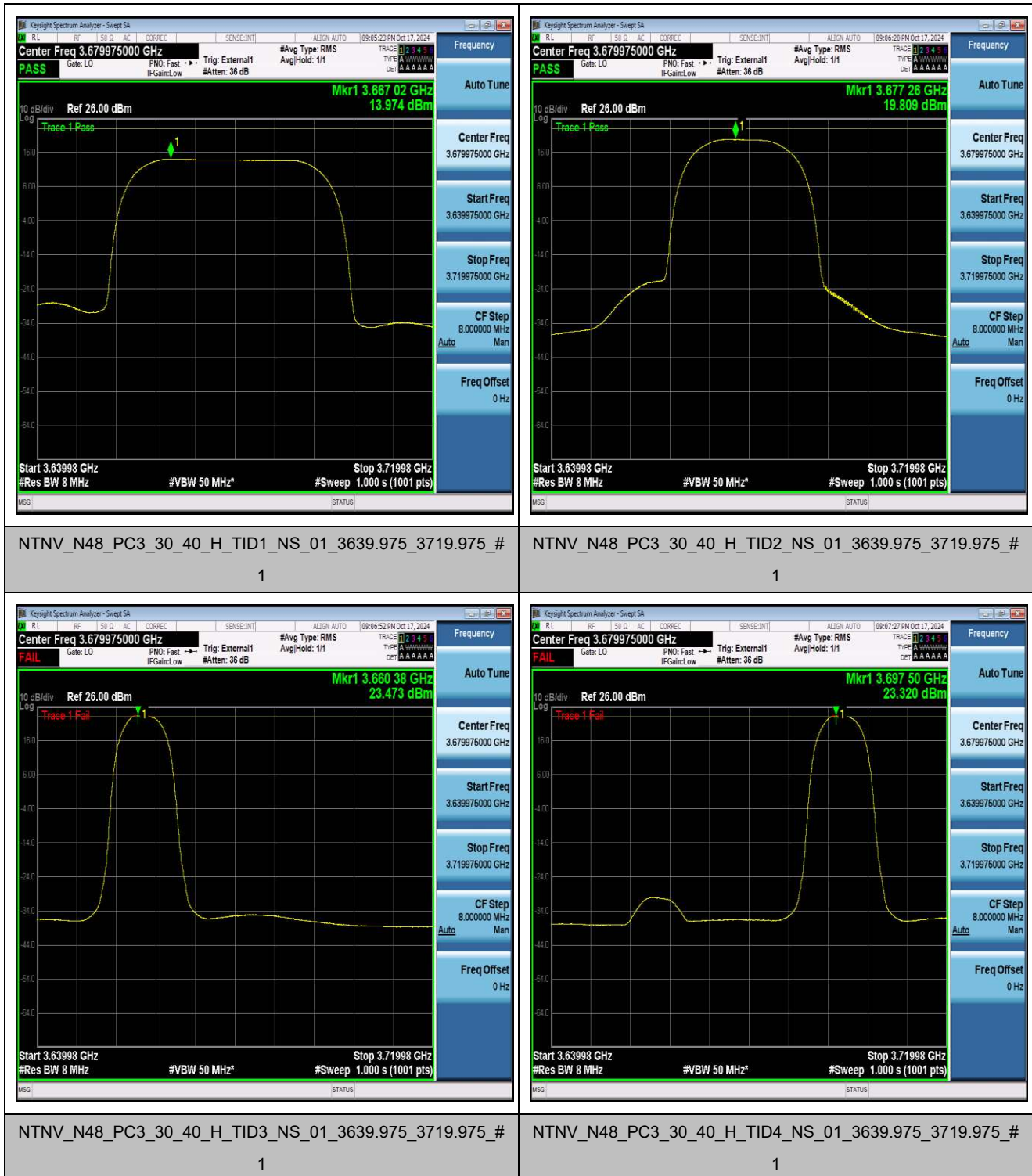


NTNV_N48_PC3_30_30_H_TID4_NS_01_3654.985_3714.985_#

1







PSD for MIMO

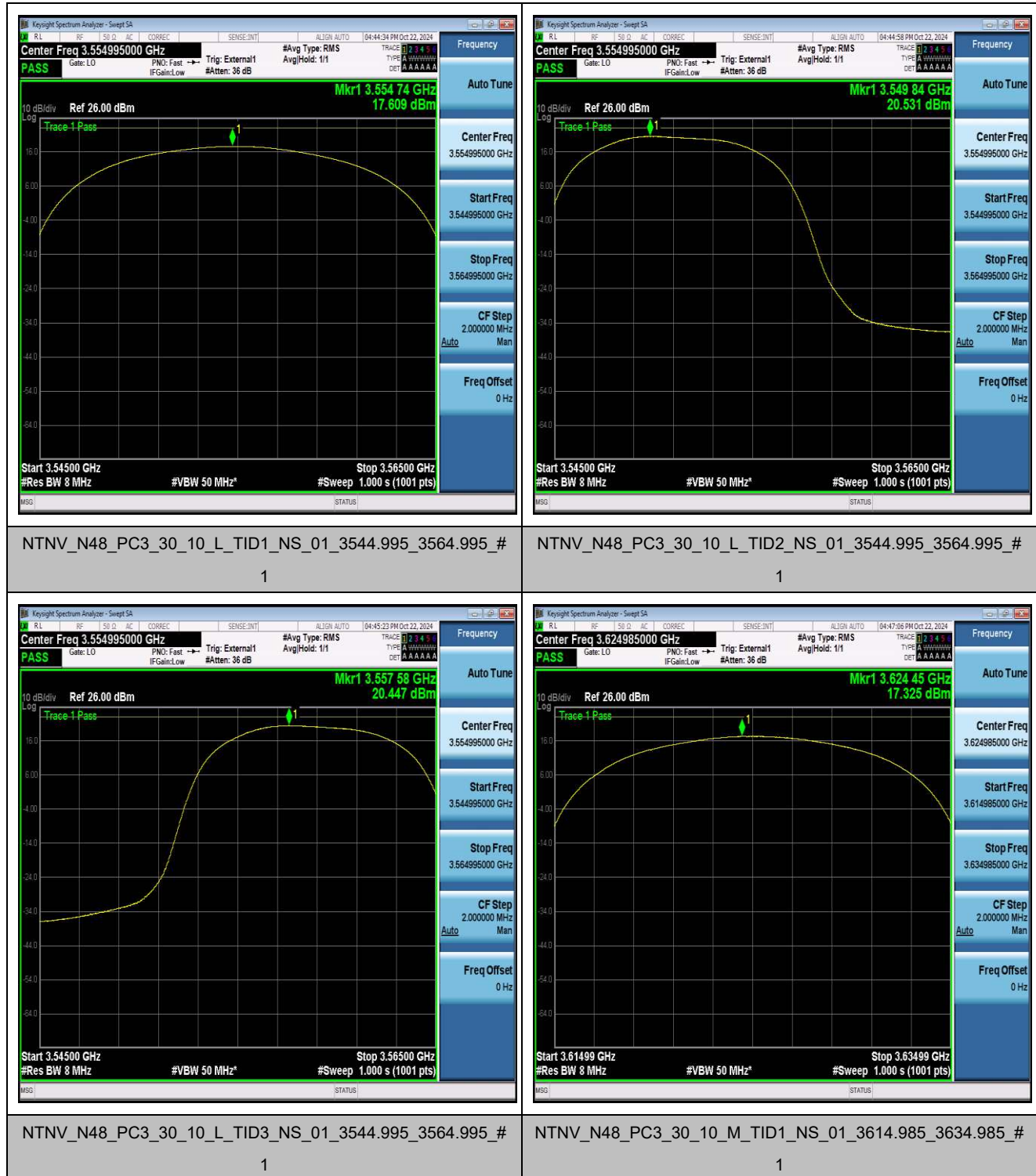
Test Result

Band	SCS	BW	Modulation	CH	RB Config	Ant	Result (dBm/ 10MHz)	Ant	Result (dBm/ 10MHz)	Total Result (dBm/ 10MHz)	Total EIRP (dBm/ 10MHz)	Limit (dBm/ 10MHz)	Verdict
N48	30	10	CP-QPSK	L	Outer_Full	Ant1	17.61	Ant8	17.26	20.45	14.32	23	PASS
N48	30	10	CP-QPSK	L	Inner_1RB_Left	Ant1	20.53	Ant8	20.11	23.34	17.21	23	PASS
N48	30	10	CP-QPSK	L	Inner_1RB_Right	Ant1	20.45	Ant8	20.18	23.33	17.2	23	PASS
N48	30	10	CP-QPSK	M	Outer_Full	Ant1	17.32	Ant8	17.08	20.21	14.08	23	PASS
N48	30	10	CP-QPSK	M	Inner_1RB_Left	Ant1	19.97	Ant8	19.92	22.96	16.83	23	PASS
N48	30	10	CP-QPSK	M	Inner_1RB_Right	Ant1	20.04	Ant8	19.95	23.01	16.88	23	PASS
N48	30	10	CP-QPSK	H	Outer_Full	Ant1	17.81	Ant8	16.78	20.34	14.21	23	PASS
N48	30	10	CP-QPSK	H	Inner_1RB_Left	Ant1	20.34	Ant8	19.59	22.99	16.86	23	PASS
N48	30	10	CP-QPSK	H	Inner_1RB_Right	Ant1	20.30	Ant8	19.49	22.92	16.79	23	PASS
N48	30	15	CP-QPSK	L	Outer_Full	Ant1	15.78	Ant8	15.30	18.56	12.43	23	PASS
N48	30	15	CP-QPSK	L	Inner_1RB_Left	Ant1	20.54	Ant8	19.93	23.26	17.13	23	PASS
N48	30	15	CP-QPSK	L	Inner_1RB_Right	Ant1	20.55	Ant8	20.07	23.33	17.2	23	PASS
N48	30	15	CP-QPSK	M	Outer_Full	Ant1	15.56	Ant8	15.33	18.46	12.33	23	PASS
N48	30	15	CP-QPSK	M	Inner_1RB_Left	Ant1	19.88	Ant8	19.71	22.81	16.68	23	PASS
N48	30	15	CP-QPSK	M	Inner_1RB_Right	Ant1	20.01	Ant8	19.72	22.88	16.75	23	PASS
N48	30	15	CP-QPSK	H	Outer_Full	Ant1	15.95	Ant8	15.01	18.52	12.39	23	PASS
N48	30	15	CP-QPSK	H	Inner_1RB_Left	Ant1	20.19	Ant8	19.48	22.86	16.73	23	PASS
N48	30	15	CP-QPSK	H	Inner_1RB_Right	Ant1	20.40	Ant8	19.37	22.93	16.8	23	PASS
N48	30	20	CP-QPSK	L	Outer_Full	Ant1	14.69	Ant8	14.17	17.45	11.32	23	PASS
N48	30	20	CP-QPSK	L	Inner_1RB_Left	Ant1	20.47	Ant8	20.05	23.28	17.15	23	PASS
N48	30	20	CP-QPSK	L	Inner_1RB_Right	Ant1	20.51	Ant8	19.93	23.24	17.11	23	PASS
N48	30	20	CP-QPSK	M	Outer_Full	Ant1	14.33	Ant8	14.01	17.18	11.05	23	PASS
N48	30	20	CP-QPSK	M	Inner_1RB_Left	Ant1	19.98	Ant8	19.76	22.88	16.75	23	PASS
N48	30	20	CP-QPSK	M	Inner_1RB_Right	Ant1	19.92	Ant8	19.47	22.71	16.58	23	PASS
N48	30	20	CP-QPSK	H	Outer_Full	Ant1	14.74	Ant8	13.80	17.31	11.18	23	PASS
N48	30	20	CP-QPSK	H	Inner_1RB_Left	Ant1	20.33	Ant8	19.49	22.94	16.81	23	PASS
N48	30	20	CP-QPSK	H	Inner_1RB_Right	Ant1	20.25	Ant8	19.19	22.76	16.63	23	PASS
N48	30	30	CP-QPSK	L	Outer_Full	Ant1	12.96	Ant8	12.41	15.70	9.57	23	PASS
N48	30	30	CP-QPSK	L	Inner_1RB_Left	Ant1	20.56	Ant8	20.31	23.45	17.32	23	PASS
N48	30	30	CP-QPSK	L	Inner_1RB_Right	Ant1	20.40	Ant8	20.15	23.29	17.16	23	PASS
N48	30	30	CP-QPSK	M	Outer_Full	Ant1	12.57	Ant8	12.23	15.41	9.28	23	PASS
N48	30	30	CP-QPSK	M	Inner_1RB_Left	Ant1	20.10	Ant8	19.71	22.92	16.79	23	PASS
N48	30	30	CP-QPSK	M	Inner_1RB_Right	Ant1	20.02	Ant8	19.83	22.94	16.81	23	PASS
N48	30	30	CP-QPSK	H	Outer_Full	Ant1	13.03	Ant8	12.13	15.61	9.48	23	PASS
N48	30	30	CP-QPSK	H	Inner_1RB_Left	Ant1	20.47	Ant8	19.70	23.11	16.98	23	PASS
N48	30	30	CP-QPSK	H	Inner_1RB_Right	Ant1	20.24	Ant8	19.54	22.91	16.78	23	PASS
N48	30	40	CP-QPSK	L	Outer_Full	Ant1	10.58	Ant8	10.04	13.33	7.2	23	PASS
N48	30	40	CP-QPSK	L	Inner_1RB_Left	Ant1	19.97	Ant8	19.67	22.83	16.7	23	PASS

N48	30	40	CP-QPSK	L	Inner_1RB_Right	Ant1	19.86	Ant8	19.42	22.66	16.53	23	PASS
N48	30	40	CP-QPSK	M	Outer_Full	Ant1	10.53	Ant8	9.90	13.24	7.11	23	PASS
N48	30	40	CP-QPSK	M	Inner_1RB_Left	Ant1	19.88	Ant8	19.54	22.72	16.59	23	PASS
N48	30	40	CP-QPSK	M	Inner_1RB_Right	Ant1	19.93	Ant8	19.44	22.70	16.57	23	PASS
N48	30	40	CP-QPSK	H	Outer_Full	Ant1	10.89	Ant8	10.01	13.48	7.35	23	PASS
N48	30	40	CP-QPSK	H	Inner_1RB_Left	Ant1	20.35	Ant8	19.50	22.96	16.83	23	PASS
N48	30	40	CP-QPSK	H	Inner_1RB_Right	Ant1	19.93	Ant8	19.10	22.55	16.42	23	PASS

Test Graphs

Ant1





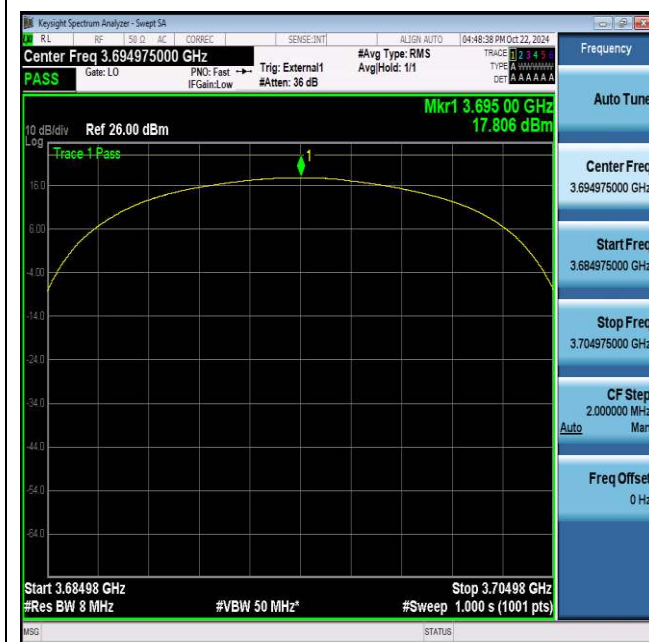
NTNV_N48_PC3_30_10_M_TID2_NS_01_3614.985_3634.985_#

1



NTNV_N48_PC3_30_10_M_TID3_NS_01_3614.985_3634.985_#

1



NTNV_N48_PC3_30_10_H_TID1_NS_01_3684.975_3704.975_#

1



NTNV_N48_PC3_30_10_H_TID2_NS_01_3684.975_3704.975_#

1



NTNV_N48_PC3_30_10_H_TID3_NS_01_3684.975_3704.975_#

1



NTNV_N48_PC3_30_15_L_TID1_NS_01_3542.515_3572.515_#

1



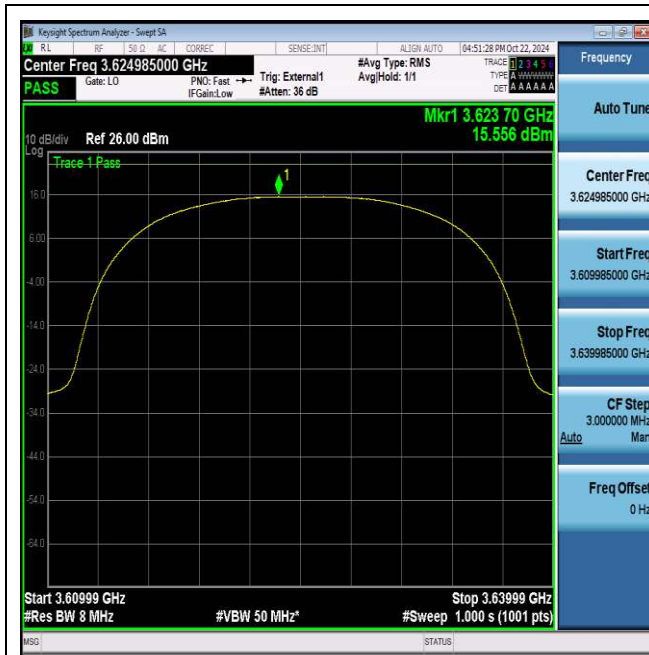
NTNV_N48_PC3_30_15_L_TID2_NS_01_3542.515_3572.515_#

1



NTNV_N48_PC3_30_15_L_TID3_NS_01_3542.515_3572.515_#

1



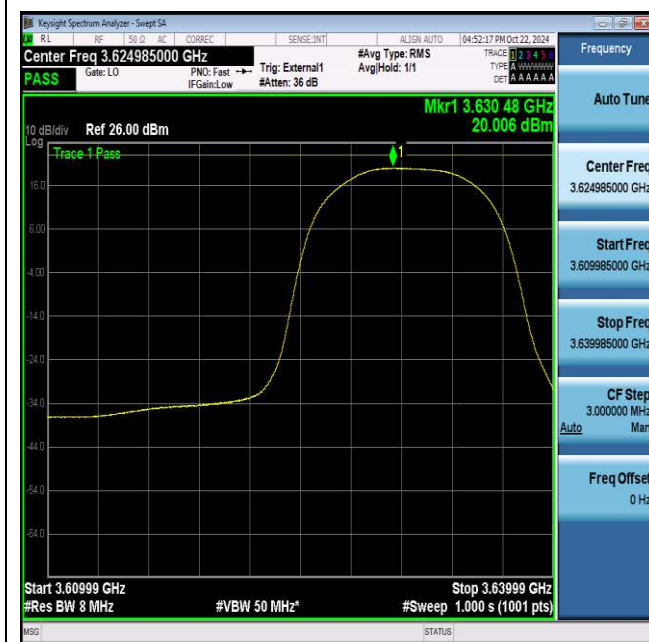
NTNV_N48_PC3_30_15_M_TID1_NS_01_3609.985_3639.985_#

1



NTNV_N48_PC3_30_15_M_TID2_NS_01_3609.985_3639.985_#

1



NTNV_N48_PC3_30_15_M_TID3_NS_01_3609.985_3639.985_#

1



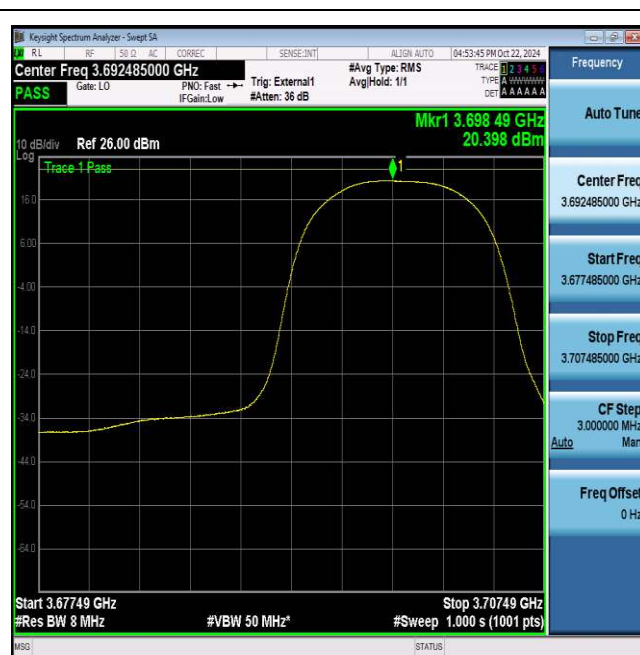
NTNV_N48_PC3_30_15_H_TID1_NS_01_3677.485_3707.485_#

1



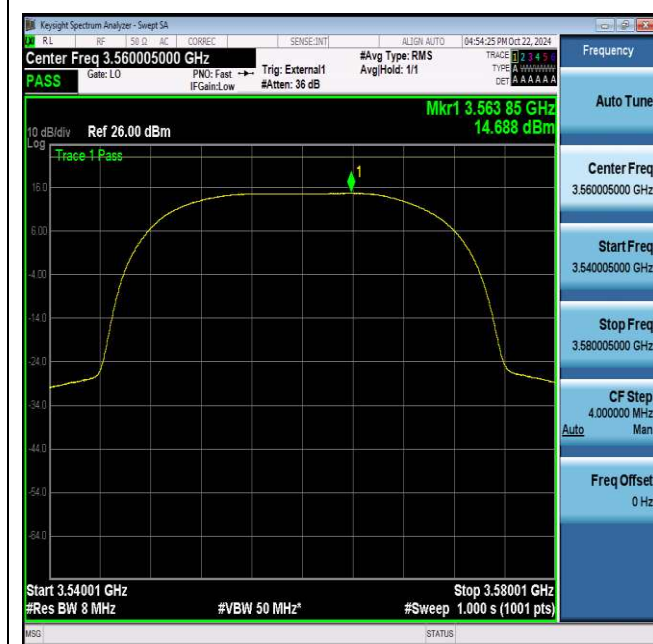
NTNV_N48_PC3_30_15_H_TID2_NS_01_3677.485_3707.485_#

1



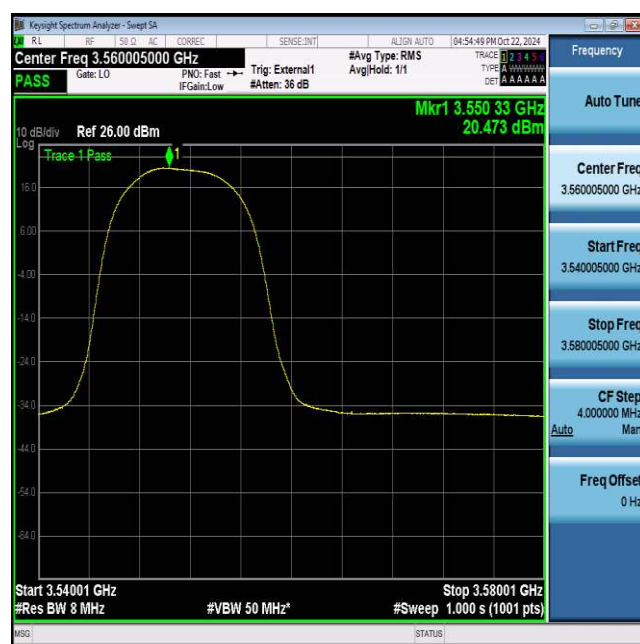
NTNV_N48_PC3_30_15_H_TID3_NS_01_3677.485_3707.485_#

1



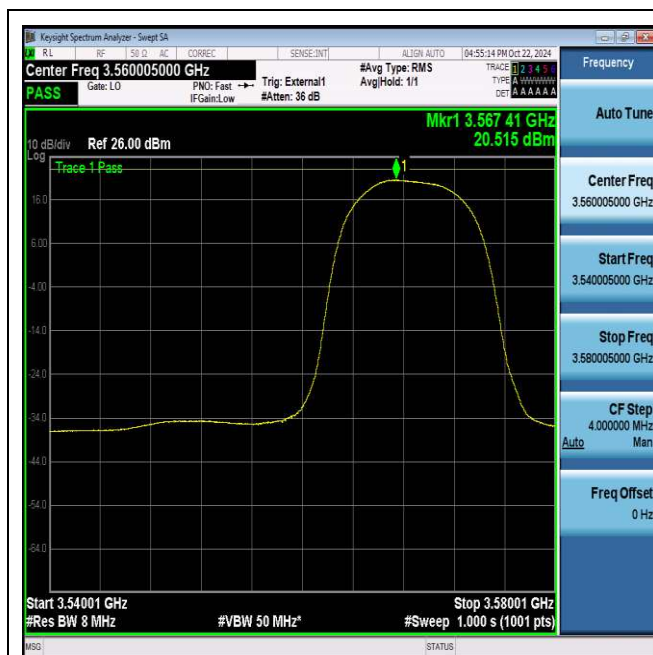
NTNV_N48_PC3_30_20_L_TID1_NS_01_3540.005_3580.005_#

1



NTNV_N48_PC3_30_20_L_TID2_NS_01_3540.005_3580.005_#

1



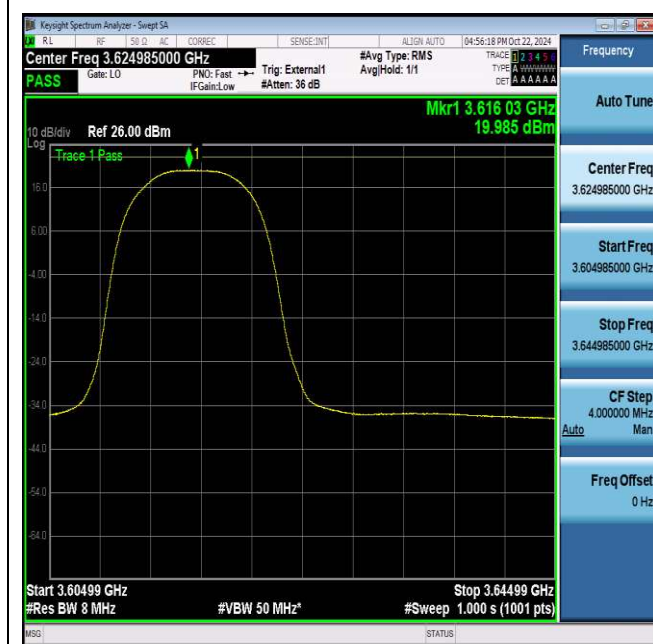
NTNV_N48_PC3_30_20_L_TID3_NS_01_3540.005_3580.005_#

1



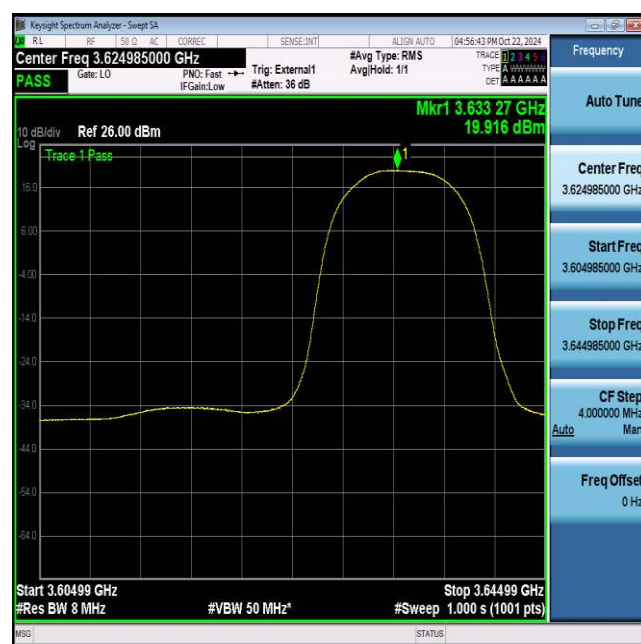
NTNV_N48_PC3_30_20_M_TID1_NS_01_3604.985_3644.985_#

1



NTNV_N48_PC3_30_20_M_TID2_NS_01_3604.985_3644.985_#

1



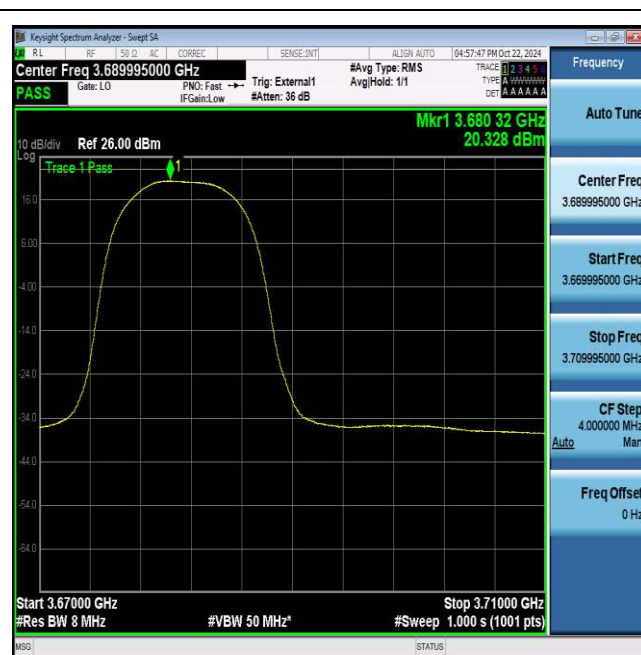
NTNV_N48_PC3_30_20_M_TID3_NS_01_3604.985_3644.985_#

1



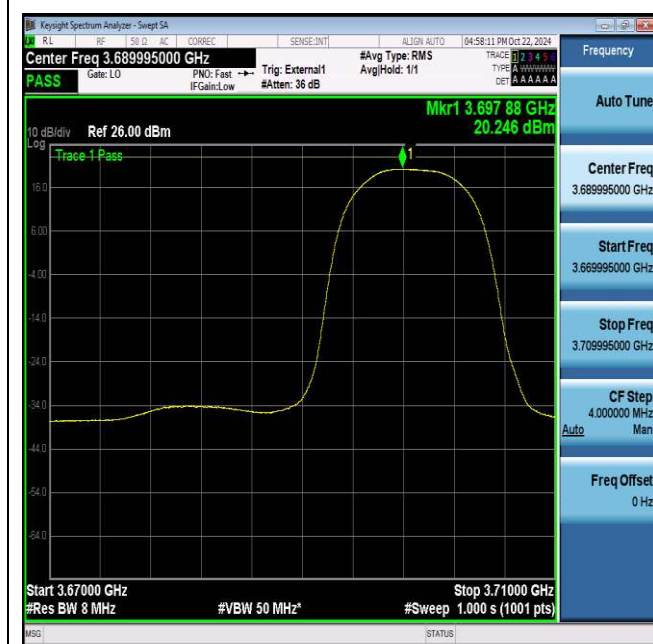
NTNV_N48_PC3_30_20_H_TID1_NS_01_3669.995_3709.995_#

1



NTNV_N48_PC3_30_20_H_TID2_NS_01_3669.995_3709.995_#

1



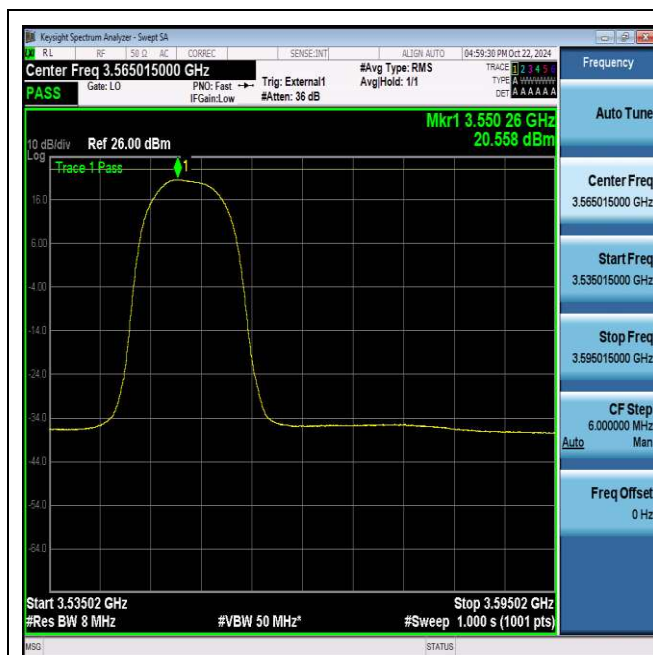
NTNV_N48_PC3_30_20_H_TID3_NS_01_3669.995_3709.995_#

1



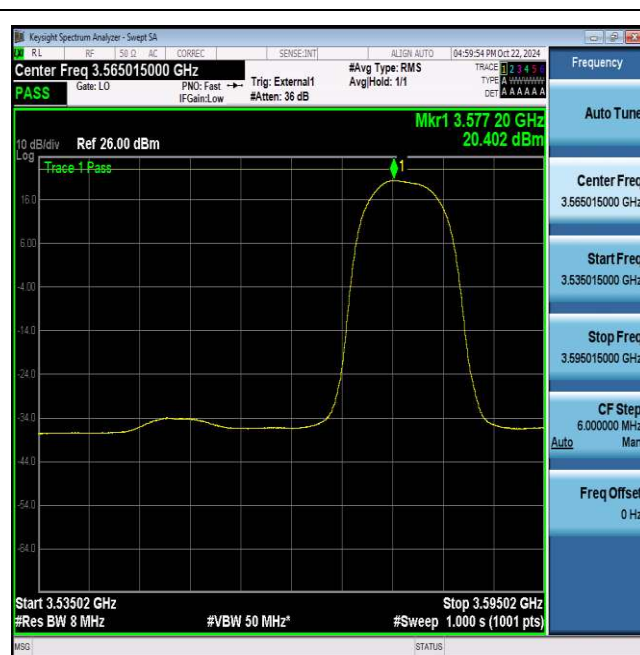
NTNV_N48_PC3_30_30_L_TID1_NS_01_3535.015_3595.015_#

1



NTNV_N48_PC3_30_30_L_TID2_NS_01_3535.015_3595.015_#

1



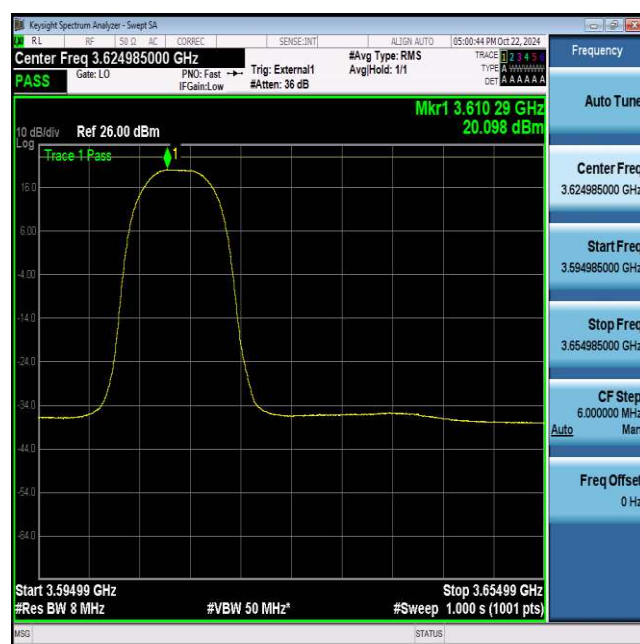
NTNV_N48_PC3_30_30_L_TID3_NS_01_3535.015_3595.015_#

1



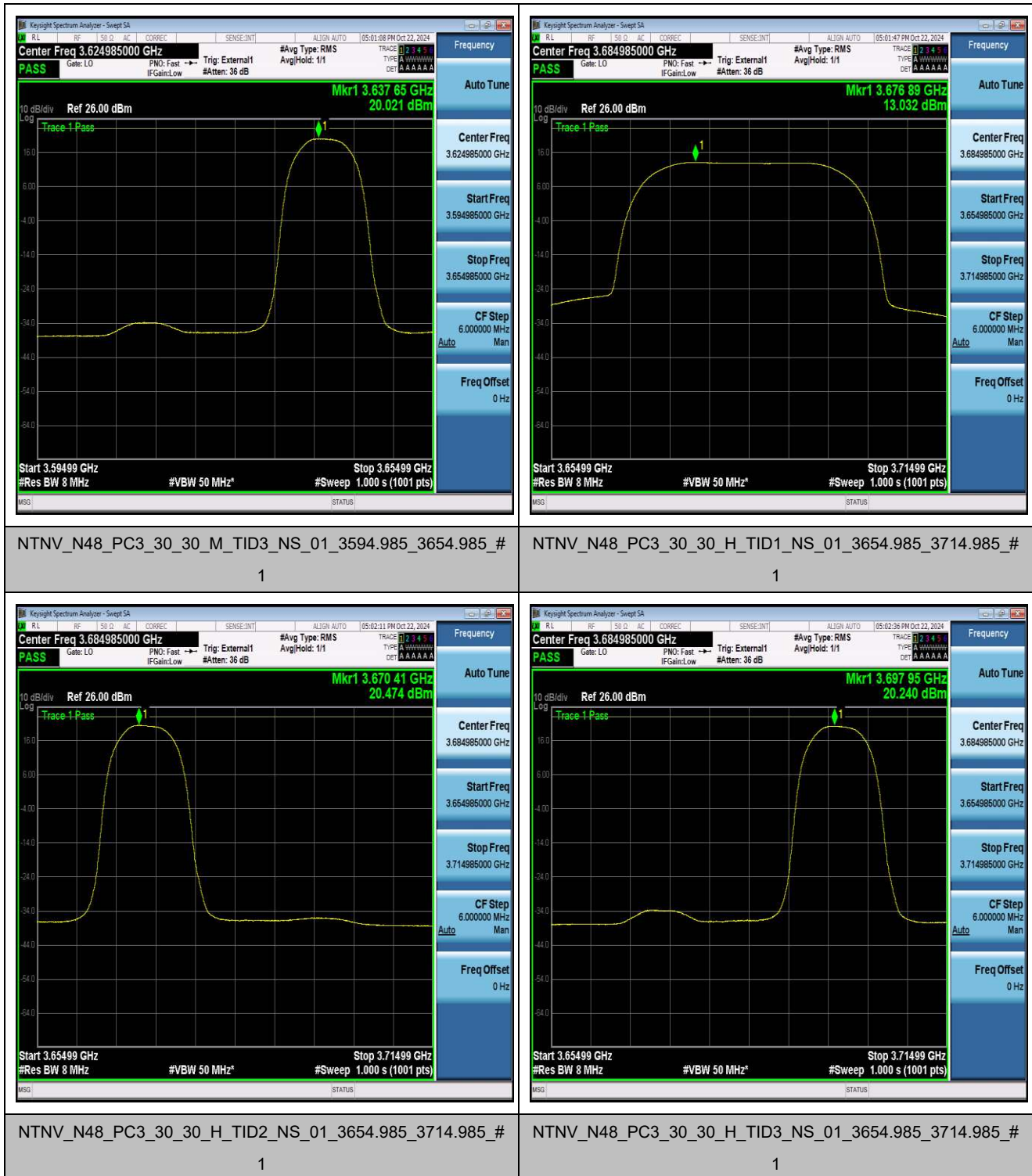
NTNV_N48_PC3_30_30_M_TID1_NS_01_3594.985_3654.985_#

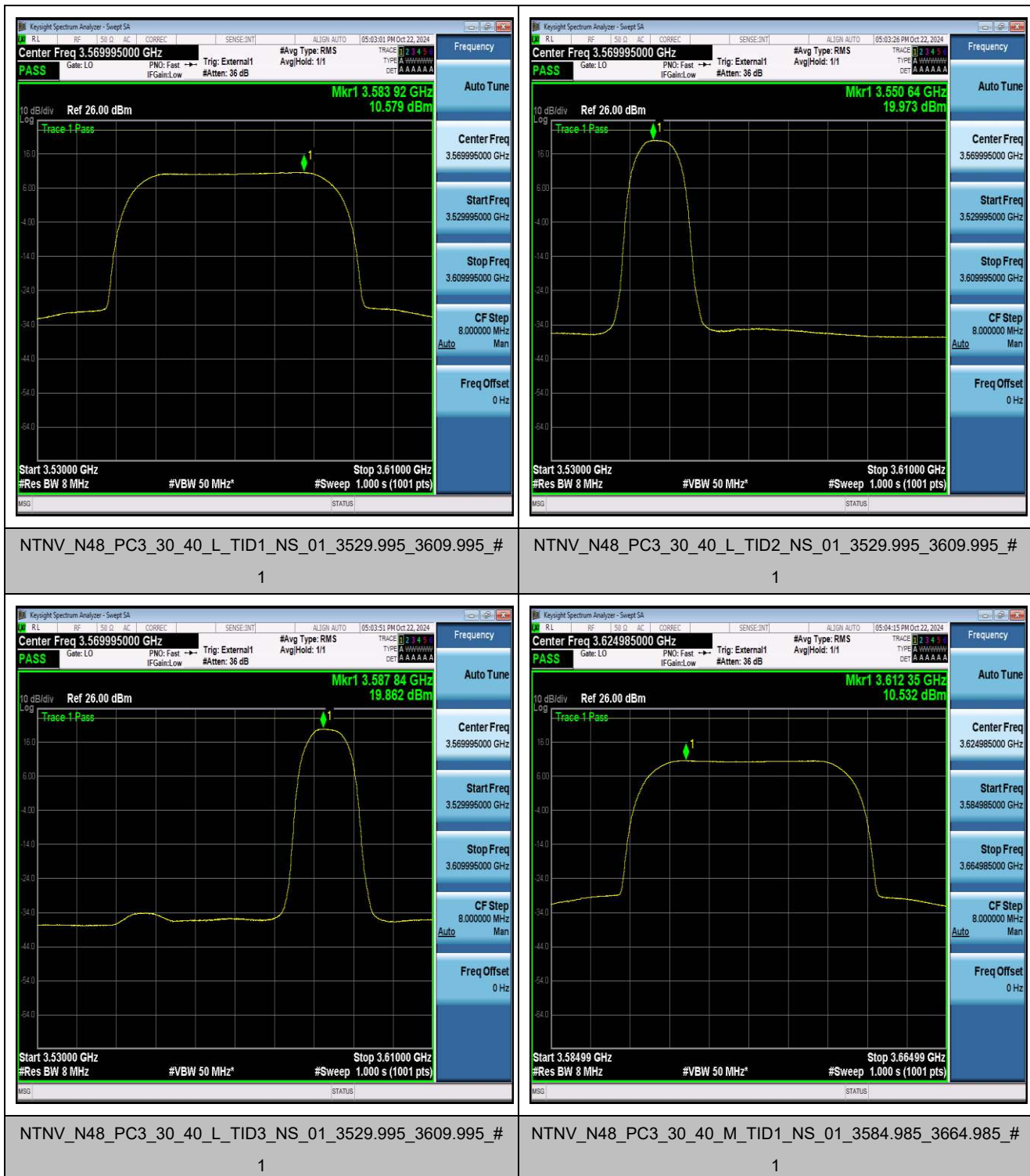
1

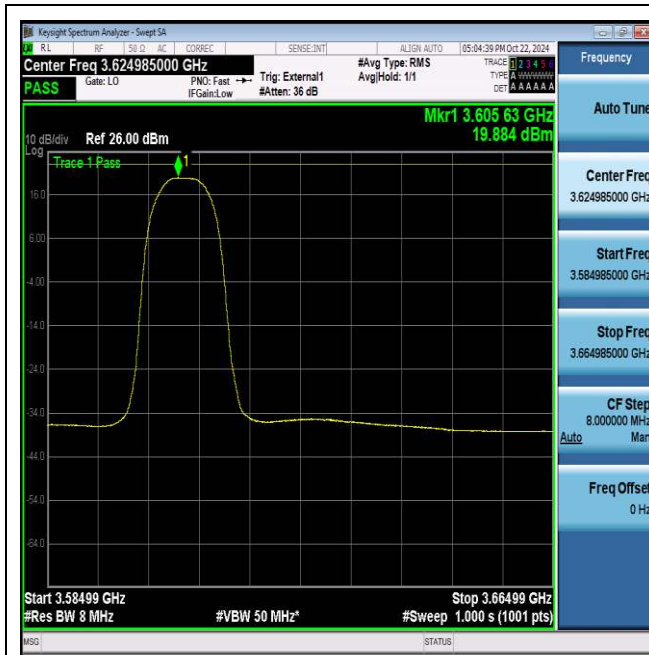


NTNV_N48_PC3_30_30_M_TID2_NS_01_3594.985_3654.985_#

1

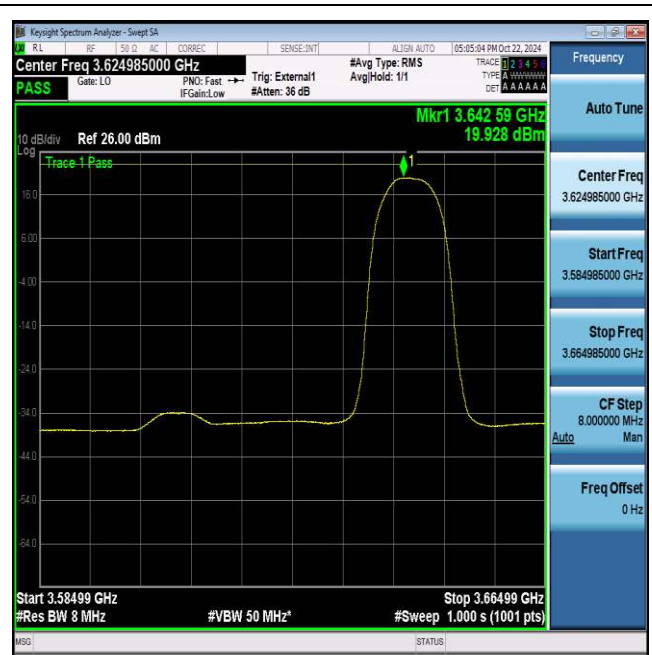






NTNV_N48_PC3_30_40_M_TID2_NS_01_3584.985_3664.985_#

1



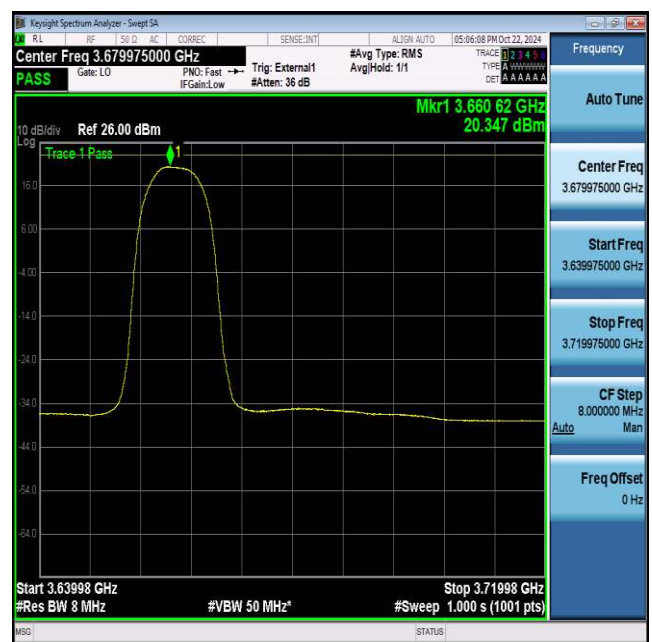
NTNV_N48_PC3_30_40_M_TID3_NS_01_3584.985_3664.985_#

1



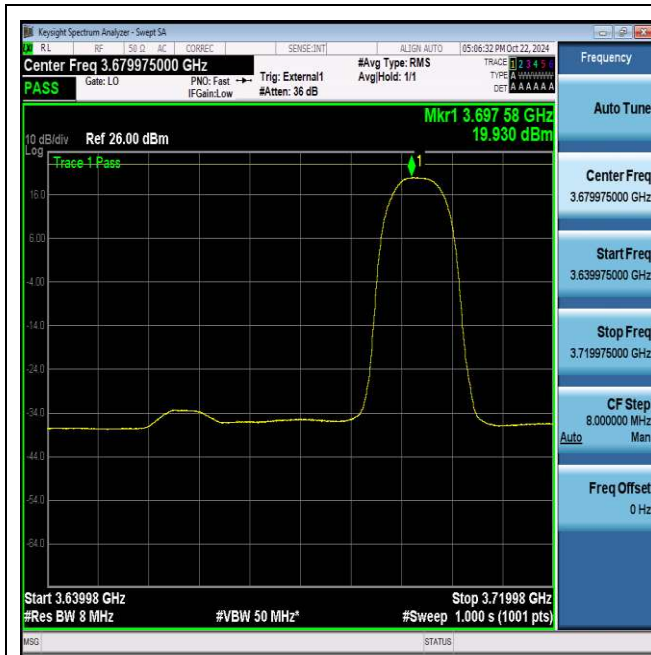
NTNV_N48_PC3_30_40_H_TID1_NS_01_3639.975_3719.975_#

1



NTNV_N48_PC3_30_40_H_TID2_NS_01_3639.975_3719.975_#

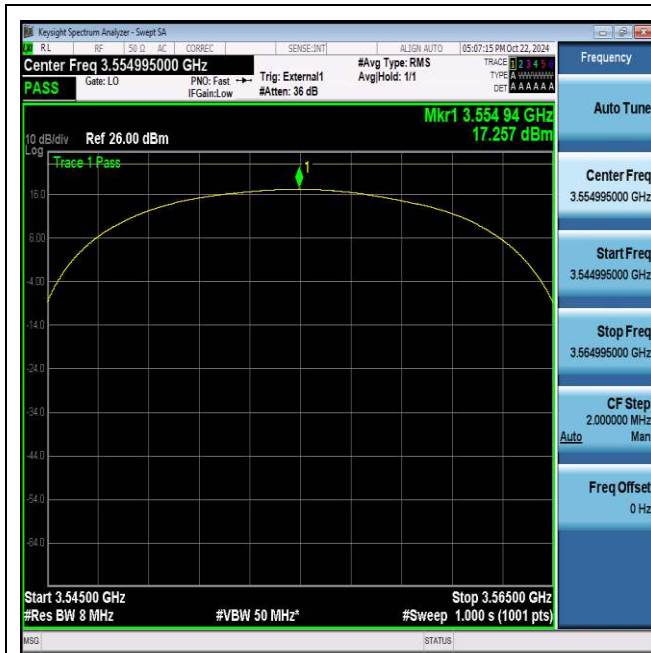
1



NTNV_N48_PC3_30_40_H_TID3_NS_01_3639.975_3719.975_#

1

Ant2



NTNV_N48_PC3_30_10_L_TID1_NS_01_3544.995_3564.995_#

1



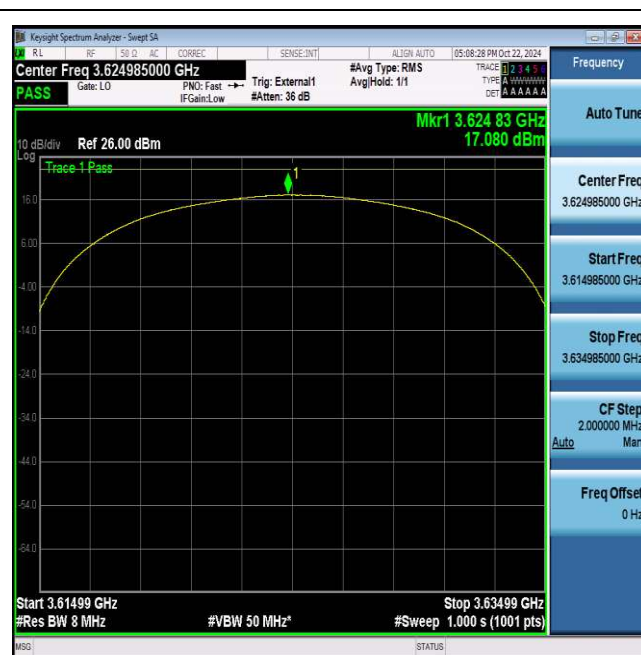
NTNV_N48_PC3_30_10_L_TID2_NS_01_3544.995_3564.995_#

1



NTNV_N48_PC3_30_10_L_TID3_NS_01_3544.995_3564.995_#

1



NTNV_N48_PC3_30_10_M_TID1_NS_01_3614.985_3634.985_#

1



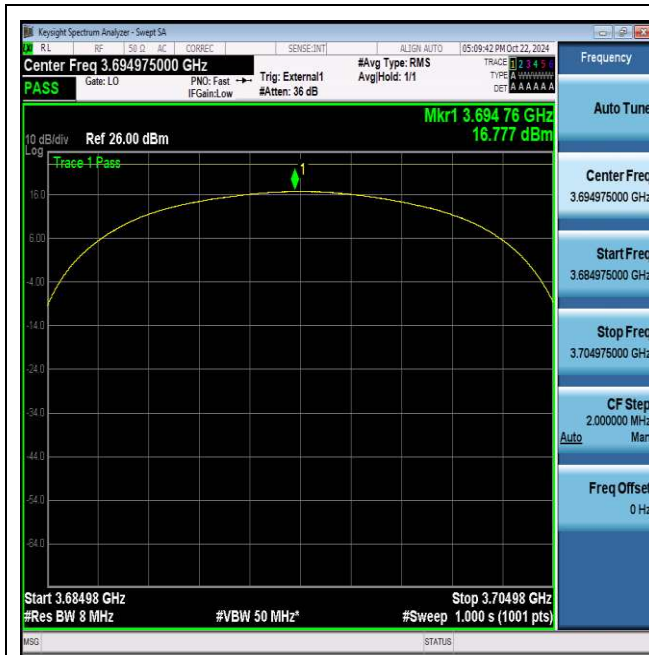
NTNV_N48_PC3_30_10_M_TID2_NS_01_3614.985_3634.985_#

1



NTNV_N48_PC3_30_10_M_TID3_NS_01_3614.985_3634.985_#

1



NTNV_N48_PC3_30_10_H_TID1_NS_01_3684.975_3704.975_#

1



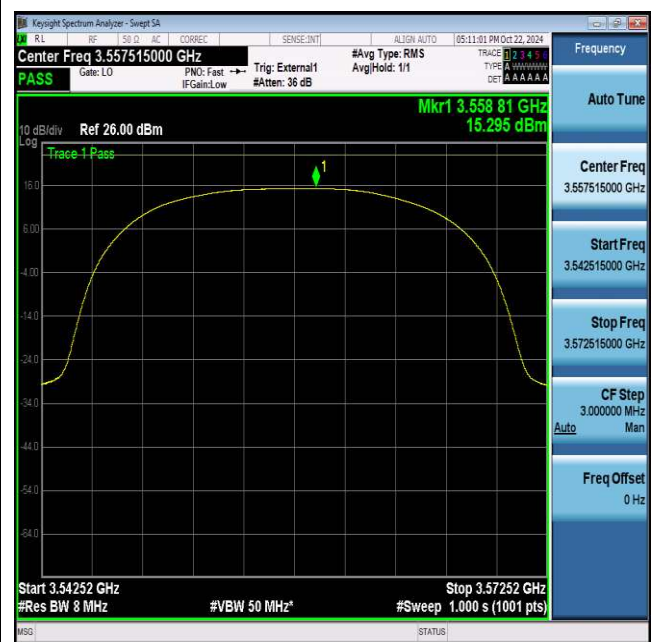
NTNV_N48_PC3_30_10_H_TID2_NS_01_3684.975_3704.975_#

1



NTNV_N48_PC3_30_10_H_TID3_NS_01_3684.975_3704.975_#

1



NTNV_N48_PC3_30_15_L_TID1_NS_01_3542.515_3572.515_#

1



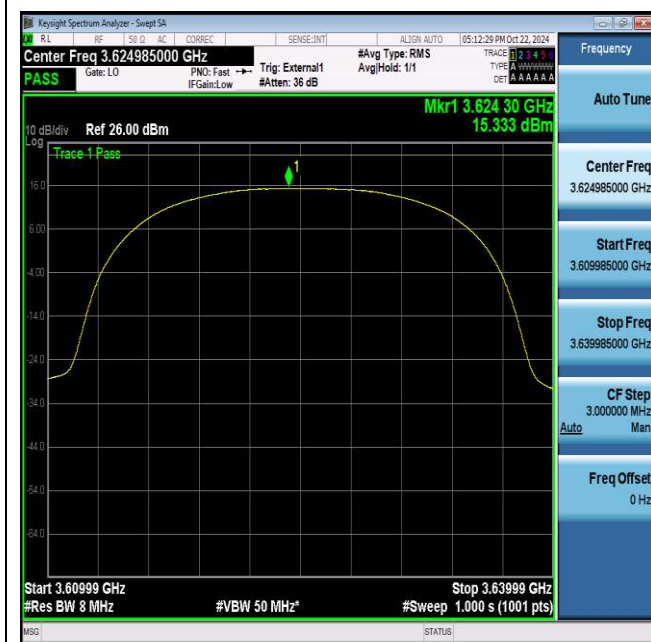
NTNV_N48_PC3_30_15_L_TID2_NS_01_3542.515_3572.515_#

1



NTNV_N48_PC3_30_15_L_TID3_NS_01_3542.515_3572.515_#

1



NTNV_N48_PC3_30_15_M_TID1_NS_01_3609.985_3639.985_#

1



NTNV_N48_PC3_30_15_M_TID2_NS_01_3609.985_3639.985_#

1



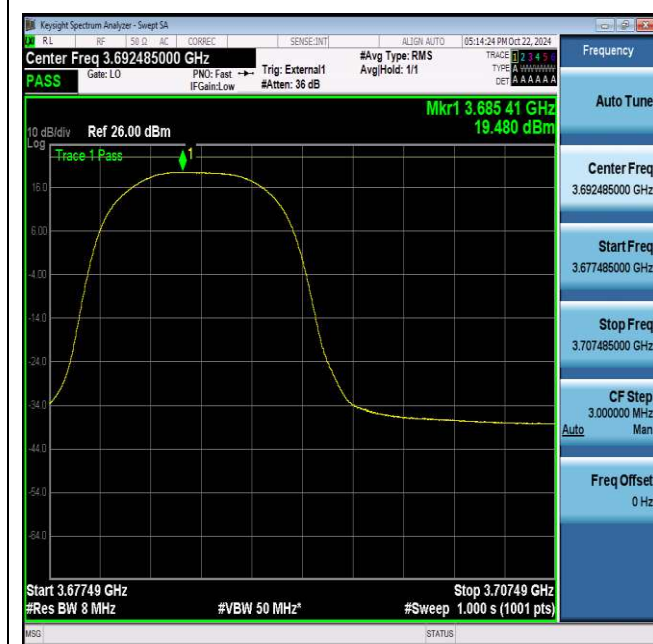
NTNV_N48_PC3_30_15_M_TID3_NS_01_3609.985_3639.985_#

1



NTNV_N48_PC3_30_15_H_TID1_NS_01_3677.485_3707.485_#

1



NTNV_N48_PC3_30_15_H_TID2_NS_01_3677.485_3707.485_#

1



NTNV_N48_PC3_30_15_H_TID3_NS_01_3677.485_3707.485_#

1



NTNV_N48_PC3_30_20_L_TID1_NS_01_3540.005_3580.005_#

1



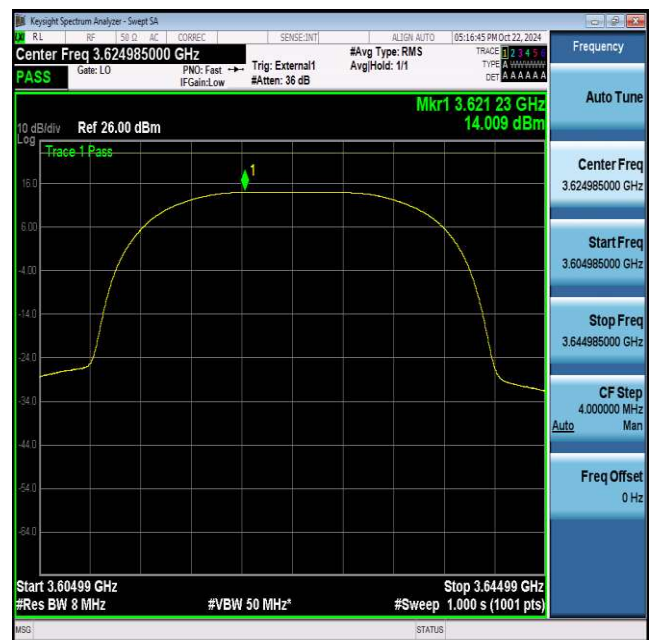
NTNV_N48_PC3_30_20_L_TID2_NS_01_3540.005_3580.005_#

1



NTNV_N48_PC3_30_20_L_TID3_NS_01_3540.005_3580.005_#

1



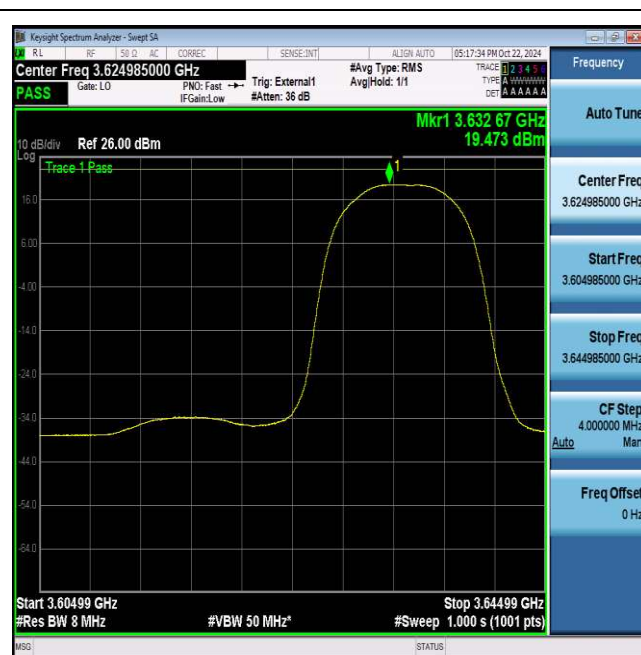
NTNV_N48_PC3_30_20_M_TID1_NS_01_3604.985_3644.985_#

1



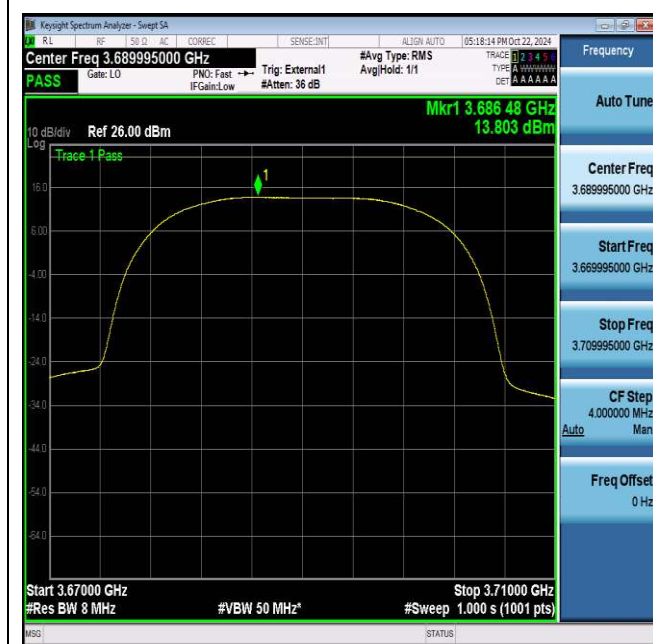
NTNV_N48_PC3_30_20_M_TID2_NS_01_3604.985_3644.985_#

1



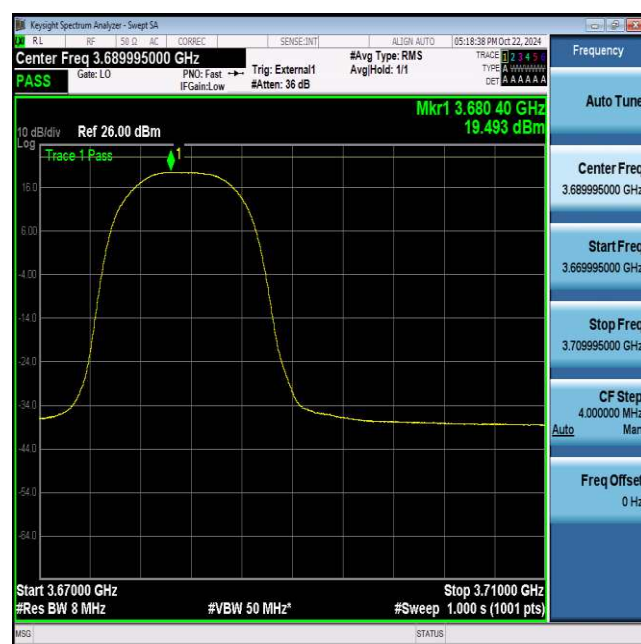
NTNV_N48_PC3_30_20_M_TID3_NS_01_3604.985_3644.985_#

1



NTNV_N48_PC3_30_20_H_TID1_NS_01_3669.995_3709.995_#

1



NTNV_N48_PC3_30_20_H_TID2_NS_01_3669.995_3709.995_#

1

