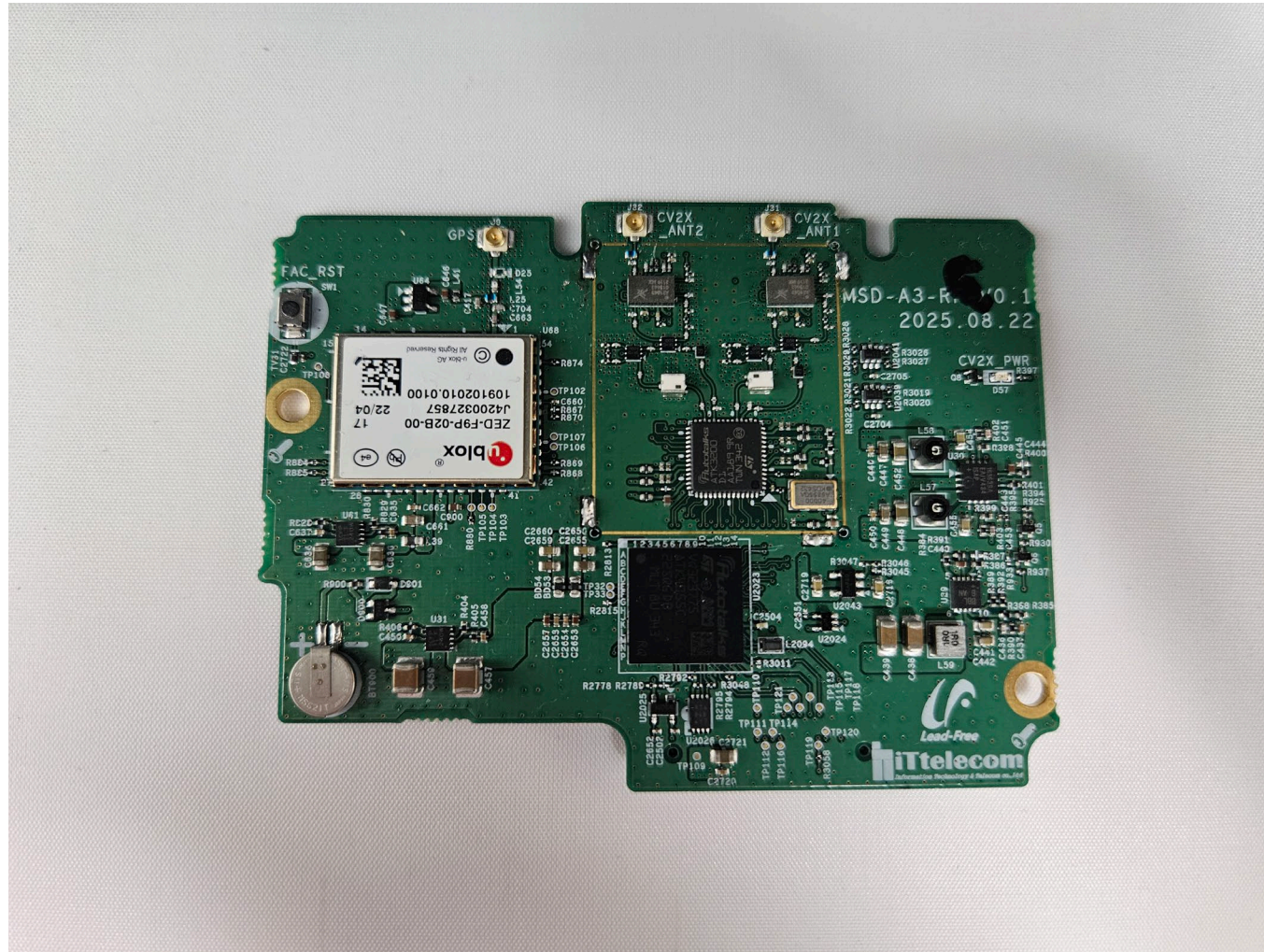


Appendix 3. Photographs of EUT [Internal View]

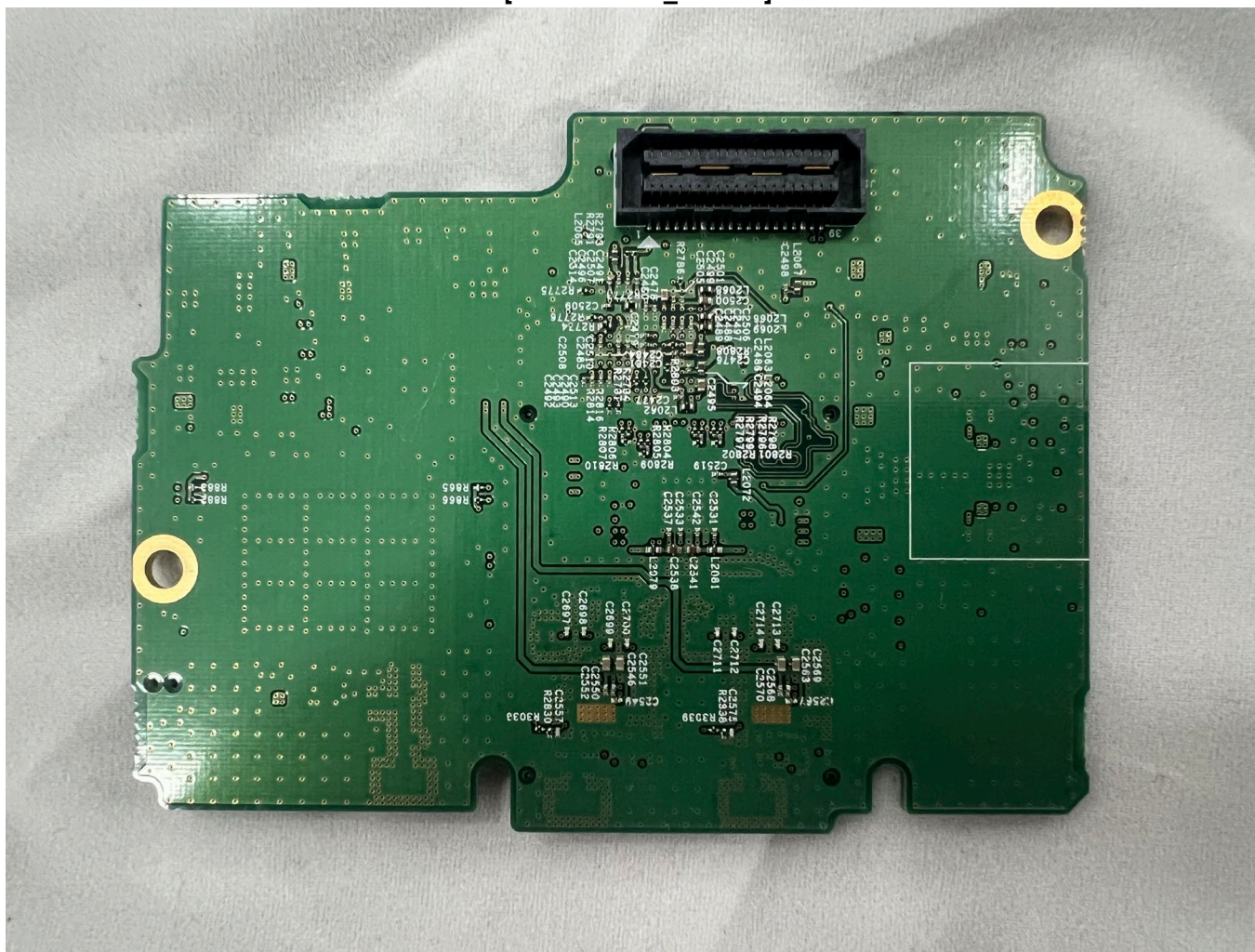
[Inside]



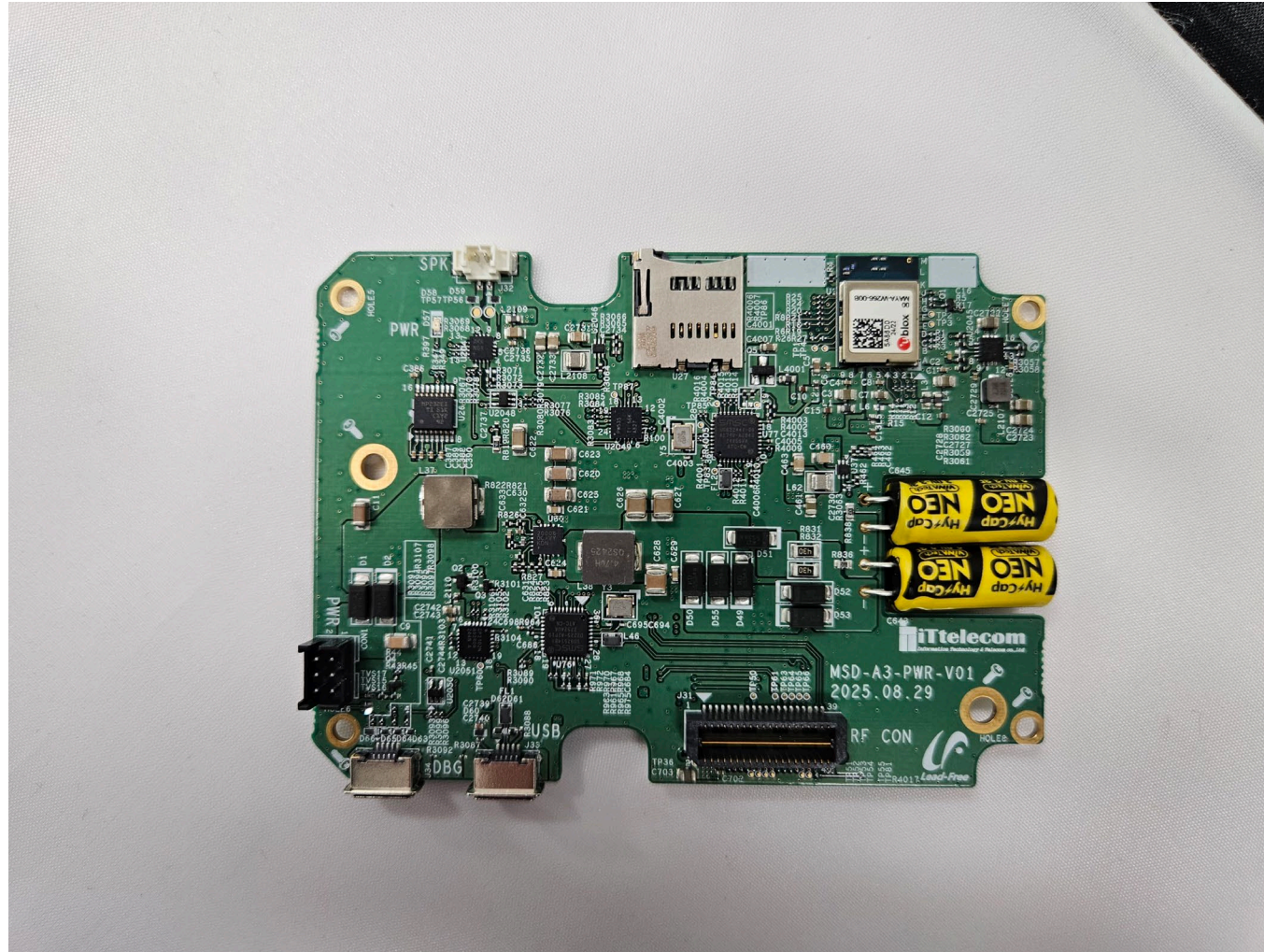
[PCB Board 1_Top]



[PCB Board 1_Bottom]



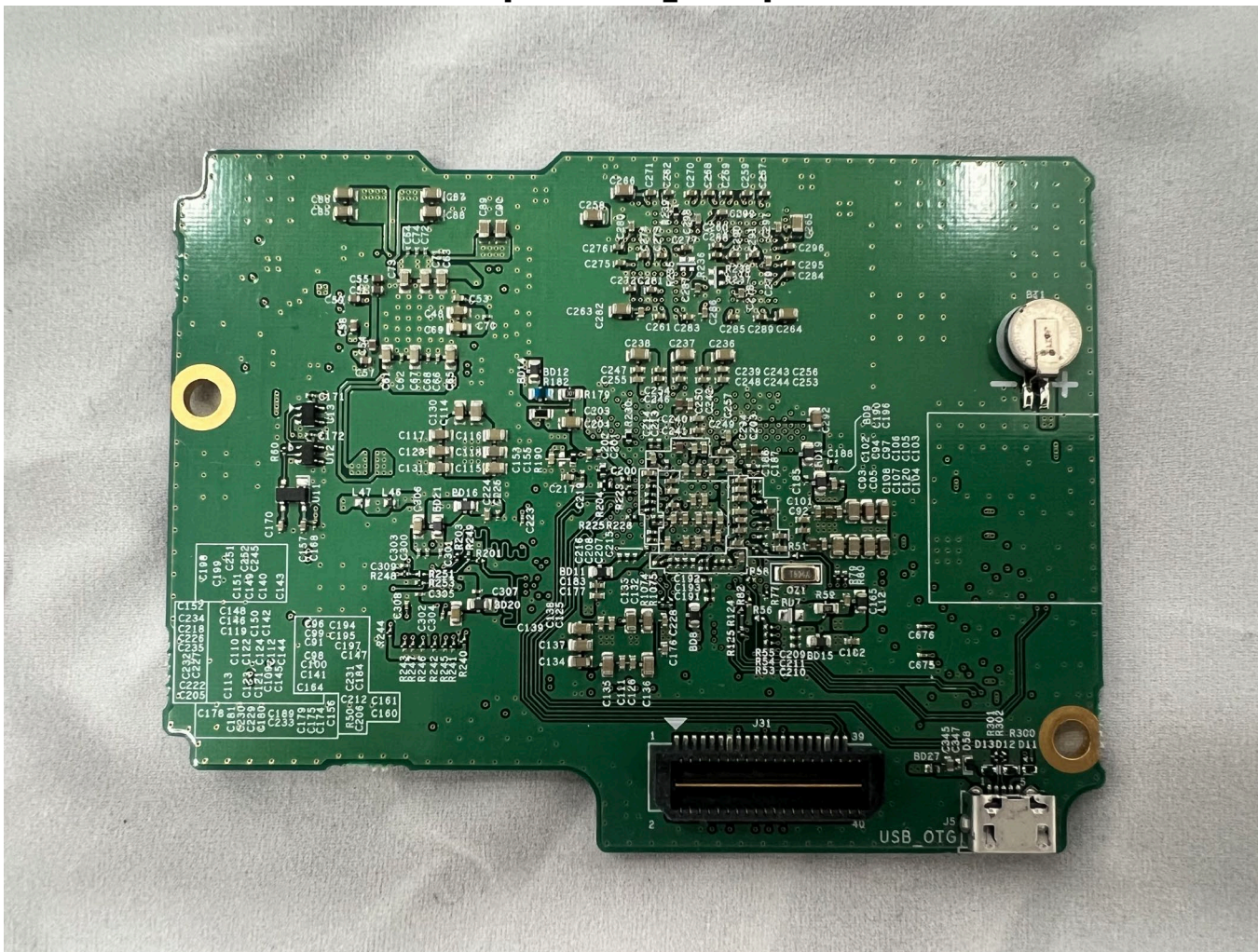
[PCB Board 2_Top]



[PCB Board 3_Top]



[PCB Board 3_Bottom]



[Chipset Photo1]



[Chipset Photo2]

