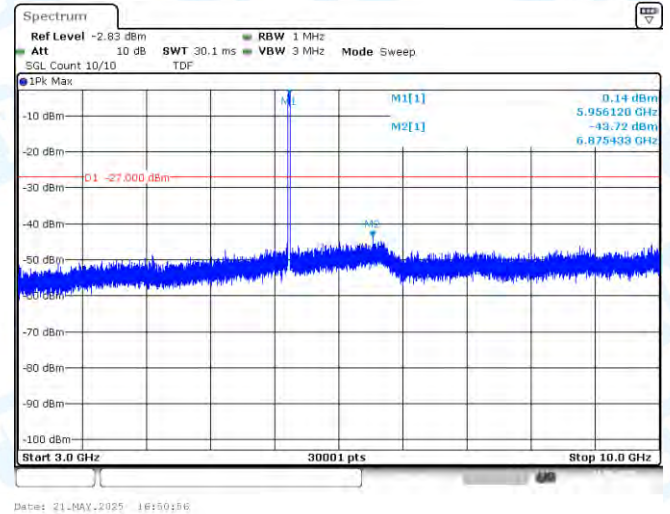
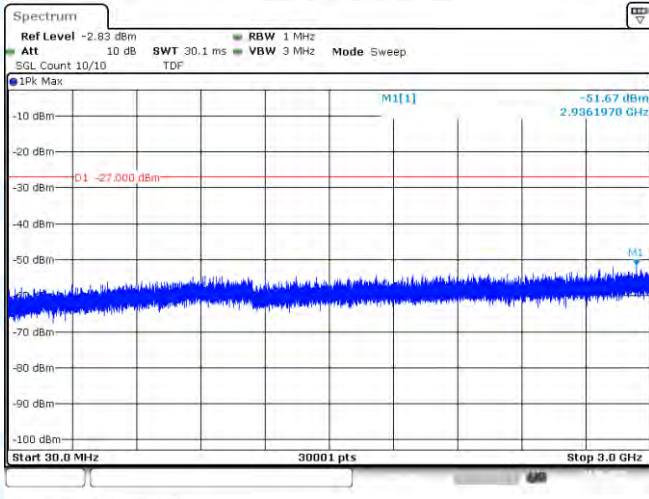


11AX40SISO	Ant1	5965	30~3000	2871.06	-51.79	≤-27	PASS
11AX40SISO	Ant1	5965	3000~10000	6600.33	-44.79	≤-27	PASS
11AX40SISO	Ant1	5965	10000~40000	39652.5	-36.29	≤-27	PASS
11AX40SISO	Ant1	6165	30~3000	2860.96	-52.06	≤-27	PASS
11AX40SISO	Ant1	6165	3000~10000	6968.53	-44.19	≤-27	PASS
11AX40SISO	Ant1	6165	10000~40000	39680.5	-35.77	≤-27	PASS
11AX40SISO	Ant1	6405	30~3000	2485.66	-52.73	≤-27	PASS
11AX40SISO	Ant1	6405	3000~10000	6822.23	-44.55	≤-27	PASS
11AX40SISO	Ant1	6405	10000~40000	39832.5	-36.61	≤-27	PASS
11AX40SISO	Ant1	6445	30~3000	2987.08	-51.8	≤-27	PASS
11AX40SISO	Ant1	6445	3000~10000	6938.67	-44.32	≤-27	PASS
11AX40SISO	Ant1	6445	10000~40000	39793.5	-35.96	≤-27	PASS
11AX40SISO	Ant1	6485	30~3000	2807.2	-52.44	≤-27	PASS
11AX40SISO	Ant1	6485	3000~10000	6985.8	-44.51	≤-27	PASS
11AX40SISO	Ant1	6485	10000~40000	39966.5	-35.89	≤-27	PASS
11AX40SISO	Ant1	6525	30~3000	2904.72	-52.27	≤-27	PASS
11AX40SISO	Ant1	6525	3000~10000	6906.93	-43.68	≤-27	PASS
11AX40SISO	Ant1	6525	10000~40000	39972.5	-36.5	≤-27	PASS
11AX40SISO	Ant1	6565	30~3000	2908.38	-51.63	≤-27	PASS
11AX40SISO	Ant1	6565	3000~10000	6734.73	-44.75	≤-27	PASS
11AX40SISO	Ant1	6565	10000~40000	39989.5	-36.14	≤-27	PASS
11AX40SISO	Ant1	6685	30~3000	2625.45	-52.47	≤-27	PASS
11AX40SISO	Ant1	6685	3000~10000	6850.7	-44.11	≤-27	PASS
11AX40SISO	Ant1	6685	10000~40000	39996.5	-35.87	≤-27	PASS
11AX40SISO	Ant1	6845	30~3000	2878.09	-52.17	≤-27	PASS
11AX40SISO	Ant1	6845	3000~10000	5133.83	-43.73	≤-27	PASS
11AX40SISO	Ant1	6845	10000~40000	39992.5	-34.82	≤-27	PASS
11AX40SISO	Ant1	6885	30~3000	2737.41	-51.38	≤-27	PASS
11AX40SISO	Ant1	6885	3000~10000	6742.9	-45.15	≤-27	PASS
11AX40SISO	Ant1	6885	10000~40000	39702.5	-36.26	≤-27	PASS
11AX40SISO	Ant1	6925	30~3000	2819.88	-52.65	≤-27	PASS
11AX40SISO	Ant1	6925	3000~10000	6654	-44.51	≤-27	PASS
11AX40SISO	Ant1	6925	10000~40000	39257.5	-36.37	≤-27	PASS
11AX40SISO	Ant1	6965	30~3000	2929.66	-51.6	≤-27	PASS
11AX40SISO	Ant1	6965	3000~10000	6753.4	-44.1	≤-27	PASS
11AX40SISO	Ant1	6965	10000~40000	37960.6	-36.16	≤-27	PASS
11AX40SISO	Ant1	7085	30~3000	2974.31	-52.42	≤-27	PASS
11AX40SISO	Ant1	7085	3000~10000	6788.17	-44.09	≤-27	PASS
11AX40SISO	Ant1	7085	10000~40000	39651.5	-35.93	≤-27	PASS
11AX80SISO	Ant1	5985	30~3000	2707.32	-52.36	≤-27	PASS
11AX80SISO	Ant1	5985	3000~10000	6943.33	-44.35	≤-27	PASS
11AX80SISO	Ant1	5985	10000~40000	39619.5	-36.42	≤-27	PASS
11AX80SISO	Ant1	6145	30~3000	2410.92	-49.72	≤-27	PASS
11AX80SISO	Ant1	6145	3000~10000	4608.83	-44.41	≤-27	PASS

11AX80SISO	Ant1	6145	10000~40000	38963.5	-49.36	≤-27	PASS
11AX80SISO	Ant1	6385	30~3000	719.86	-44.47	≤-27	PASS
11AX80SISO	Ant1	6385	3000~10000	6781.4	-44.67	≤-27	PASS
11AX80SISO	Ant1	6385	10000~40000	39330.5	-49.13	≤-27	PASS
11AX80SISO	Ant1	6465	30~3000	2888.68	-52.45	≤-27	PASS
11AX80SISO	Ant1	6465	3000~10000	6842.53	-44.34	≤-27	PASS
11AX80SISO	Ant1	6465	10000~40000	39997.5	-35.92	≤-27	PASS
11AX80SISO	Ant1	6545	30~3000	2937.29	-50.44	≤-27	PASS
11AX80SISO	Ant1	6545	3000~10000	6973.2	-43.55	≤-27	PASS
11AX80SISO	Ant1	6545	10000~40000	39646.5	-50	≤-27	PASS
11AX80SISO	Ant1	6625	30~3000	2413.4	-51.01	≤-27	PASS
11AX80SISO	Ant1	6625	3000~10000	6945.43	-45.07	≤-27	PASS
11AX80SISO	Ant1	6625	10000~40000	39719.5	-36.2	≤-27	PASS
11AX80SISO	Ant1	6705	30~3000	2964.41	-49.91	≤-27	PASS
11AX80SISO	Ant1	6705	3000~10000	5790.9	-45.2	≤-27	PASS
11AX80SISO	Ant1	6705	10000~40000	39354.5	-49.73	≤-27	PASS
11AX80SISO	Ant1	6785	30~3000	909.54	-50.21	≤-27	PASS
11AX80SISO	Ant1	6785	3000~10000	6389.17	-45.87	≤-27	PASS
11AX80SISO	Ant1	6785	10000~40000	38961.5	-49.2	≤-27	PASS
11AX80SISO	Ant1	6865	30~3000	2880.26	-52.01	≤-27	PASS
11AX80SISO	Ant1	6865	3000~10000	5952.37	-42.66	≤-27	PASS
11AX80SISO	Ant1	6865	10000~40000	39717.5	-36.17	≤-27	PASS
11AX80SISO	Ant1	6945	30~3000	2012.76	-49.56	≤-27	PASS
11AX80SISO	Ant1	6945	3000~10000	6638.37	-45.05	≤-27	PASS
11AX80SISO	Ant1	6945	10000~40000	39963.5	-49.44	≤-27	PASS
11AX80SISO	Ant1	7025	30~3000	354.66	-49.9	≤-27	PASS
11AX80SISO	Ant1	7025	3000~10000	6335.27	-44.84	≤-27	PASS
11AX80SISO	Ant1	7025	10000~40000	39660.5	-49.19	≤-27	PASS

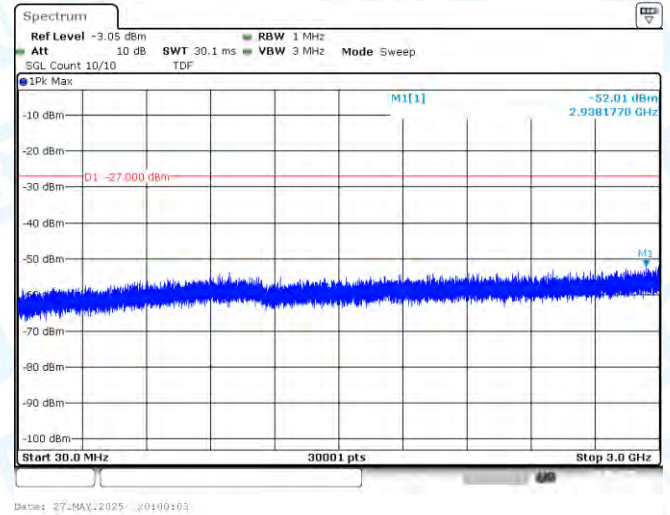
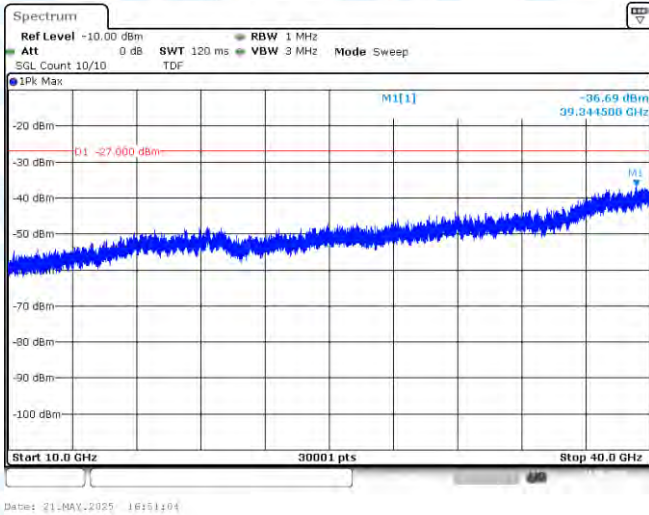
Note: RF Conducted test Offset= cable loss+ Attenuator(10dB).

7.2 Test Graphs



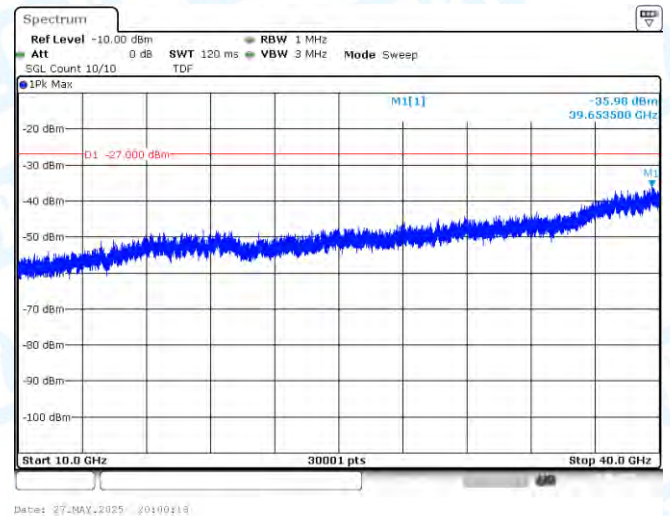
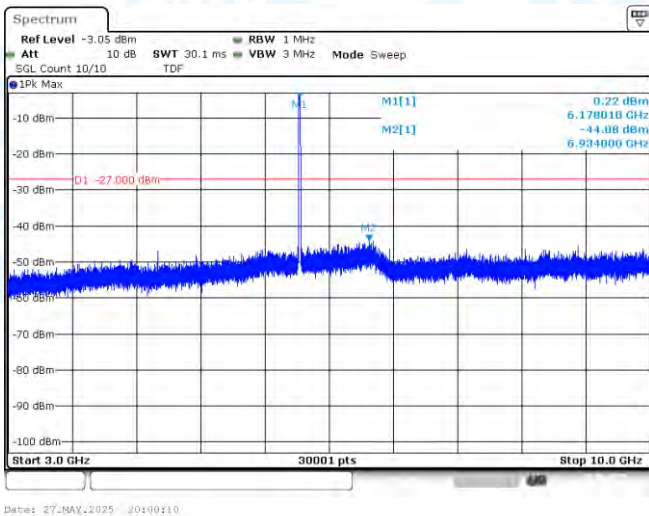
11AX20SISO-Ant1-5955-30~3000-PASS

11AX20SISO-Ant1-5955-3000~10000-PASS



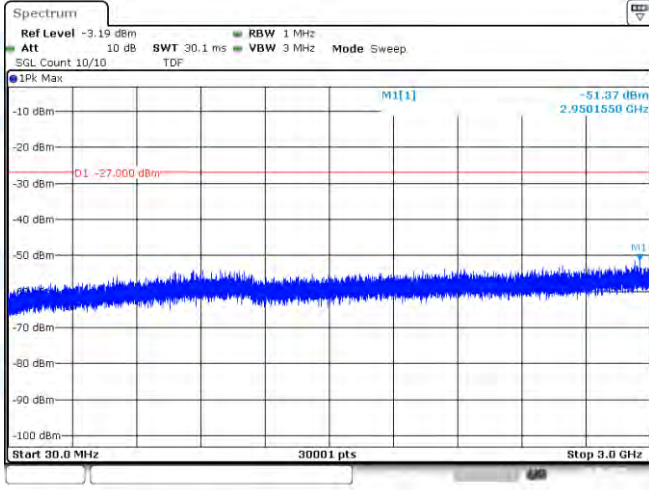
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11AX20SISO-Ant1-6175-30~3000-PASS



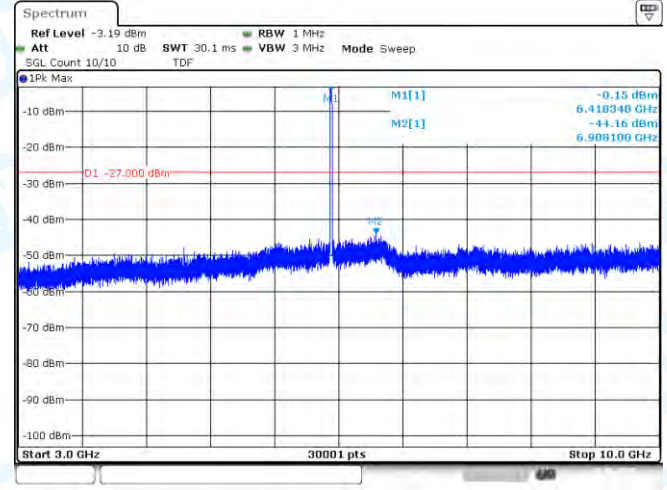
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11AX20SISO-Ant1-6175-10000~40000-PASS



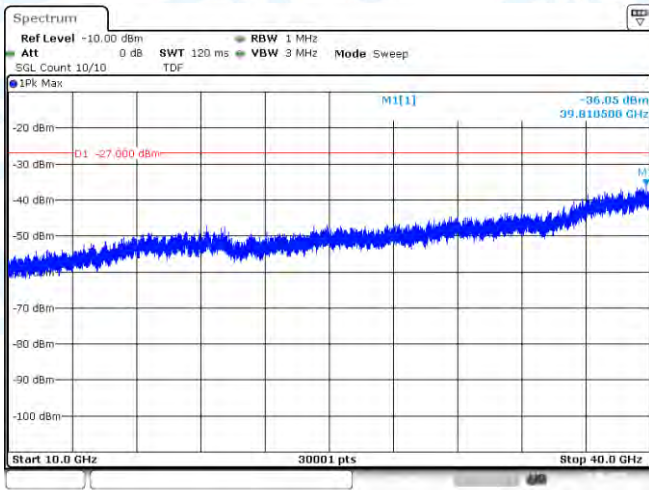
Date: 27-MAY-2025 10:02:15

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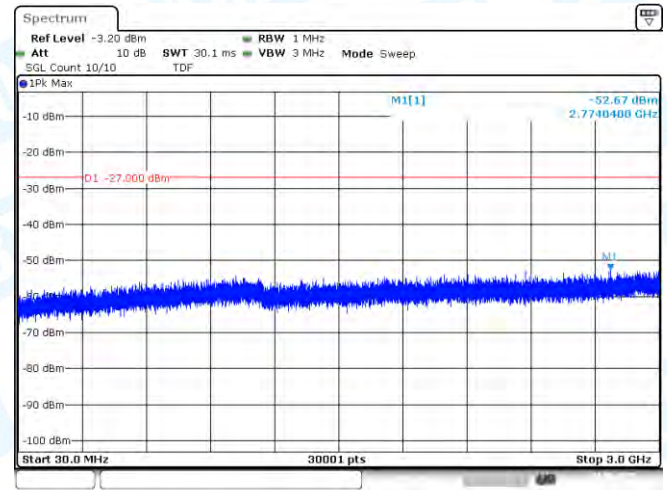
Date: 27-MAY-2025 10:02:22

11AX20SISO-Ant1-6415-3000~10000-PASS



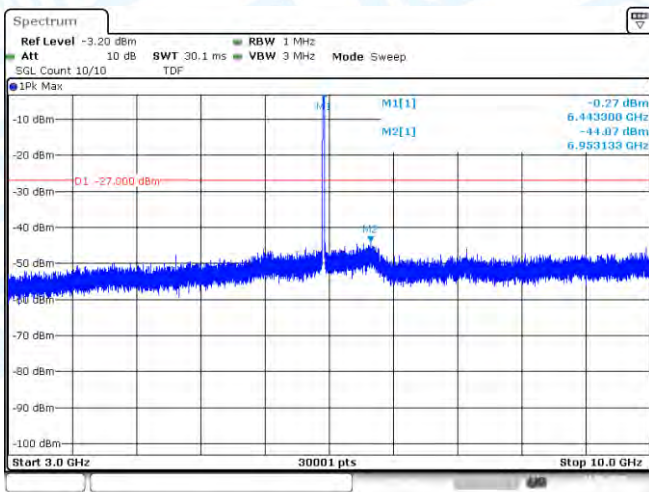
Date: 27-MAY-2025 10:02:30

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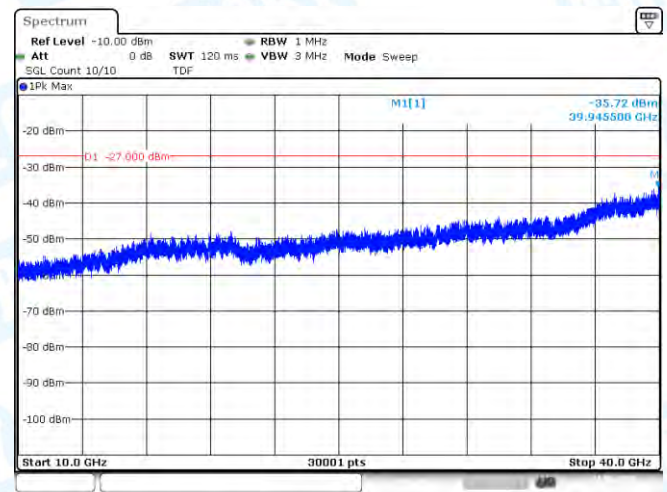
Date: 21-MAY-2025 16:57:37

11AX20SISO-Ant1-6435-30~3000-PASS



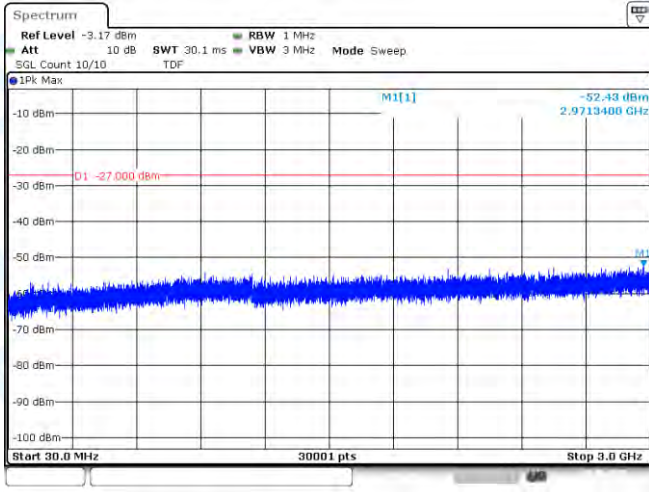
Date: 21-MAY-2025 16:57:34

11AX20SISO-Ant1-6435-3000~10000-PASS



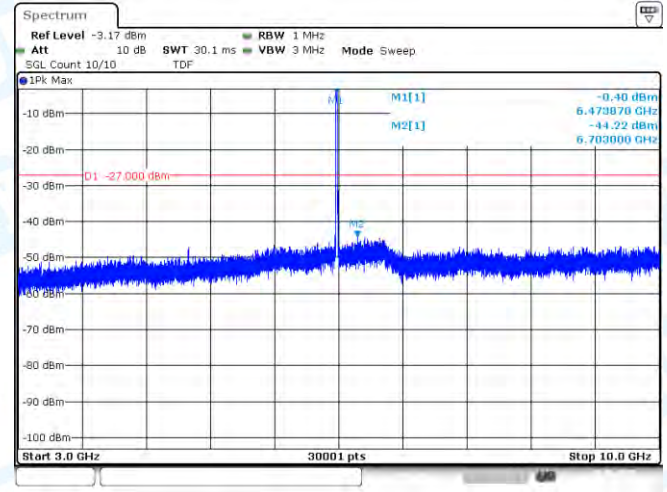
Date: 21-MAY-2025 16:57:43

11AX20SISO-Ant1-6435-10000~40000-PASS



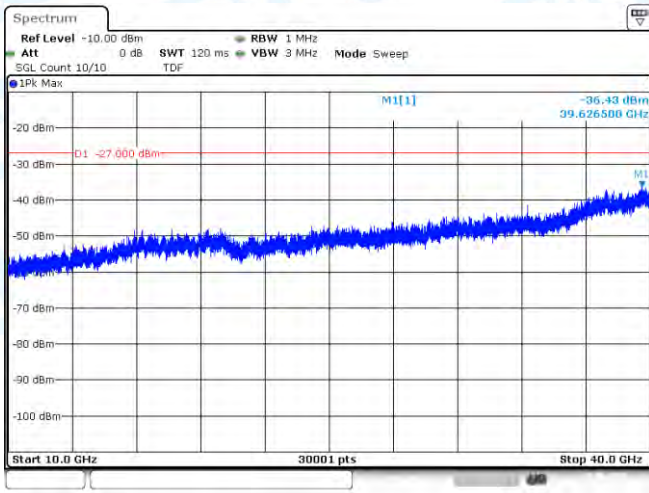
Date: 27-MAY-2025 20:07:33

11AX20SISO-Ant1-6475-30~3000-PASS



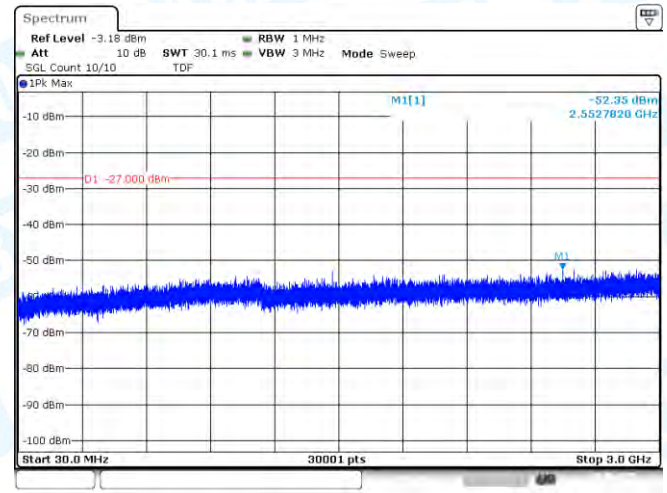
Date: 27-MAY-2025 20:07:40

11AX20SISO-Ant1-6475-3000~10000-PASS



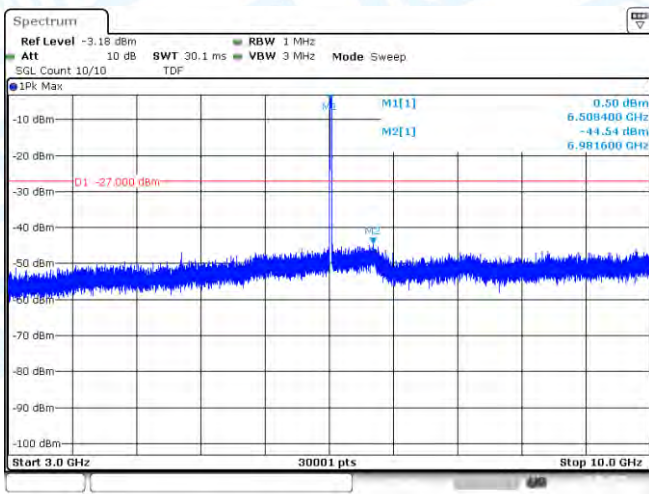
Date: 27-MAY-2025 20:07:56

11AX20SISO-Ant1-6475-10000~40000-PASS



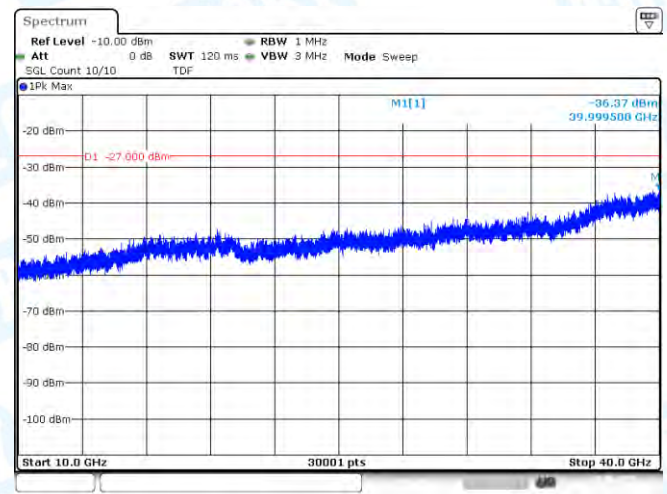
Date: 27-MAY-2025 20:10:54

11AX20SISO-Ant1-6515-30~3000-PASS



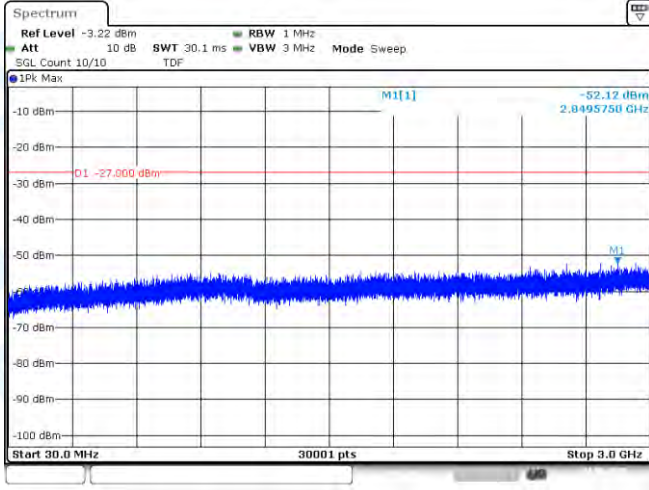
Date: 27-MAY-2025 20:11:01

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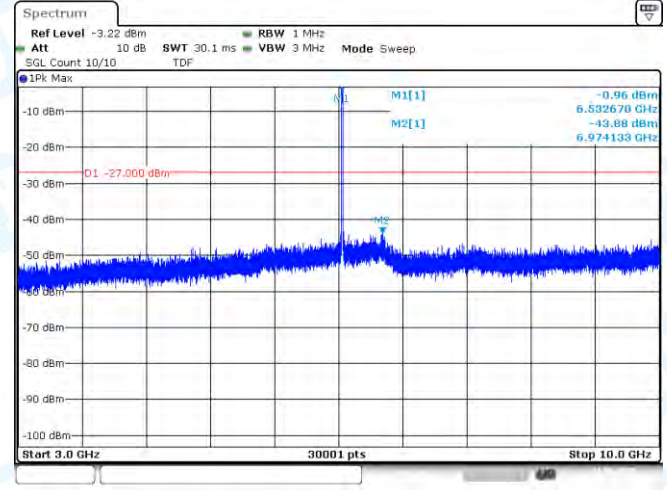
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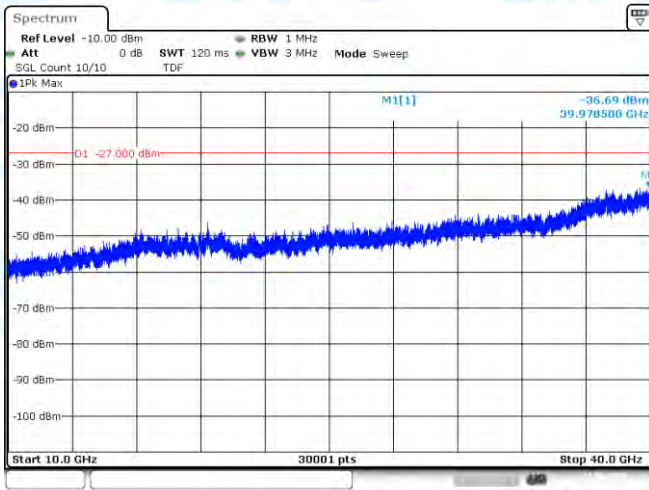
Date: 21-MAY-2025 16:59:10

11AX20SISO-Ant1-6535-30~3000-PASS



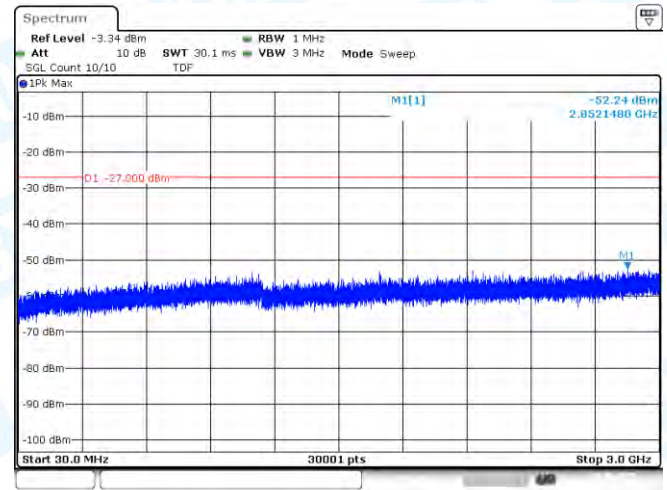
Date: 21-MAY-2025 16:59:17

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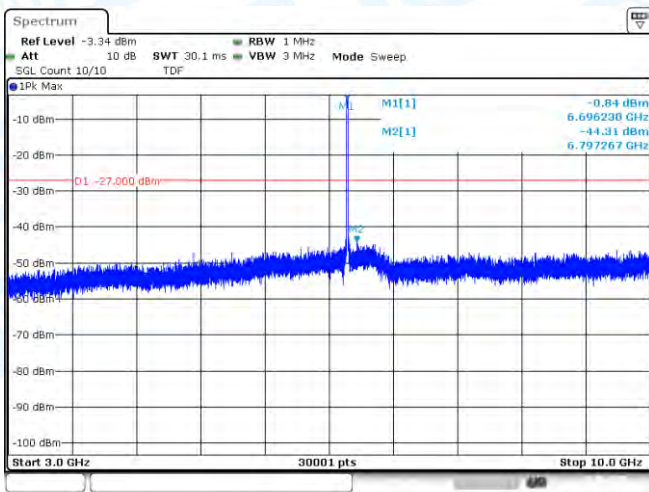
Date: 21-MAY-2025 16:59:25

11AX20SISO-Ant1-6535-10000~40000-PASS



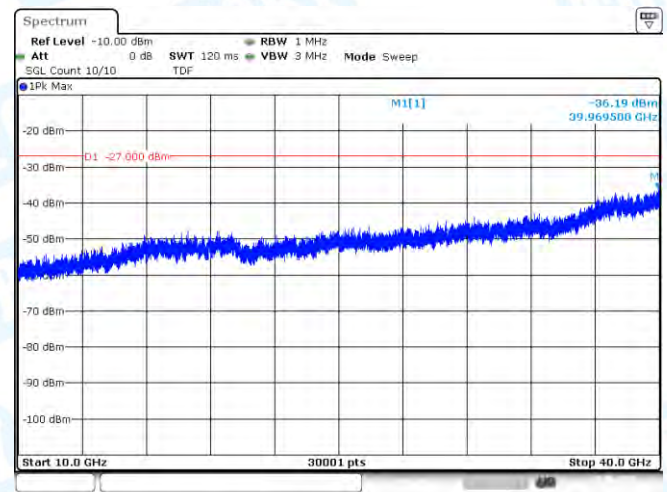
Date: 27-MAY-2025 20:18:25

11AX20SISO-Ant1-6695-30~3000-PASS



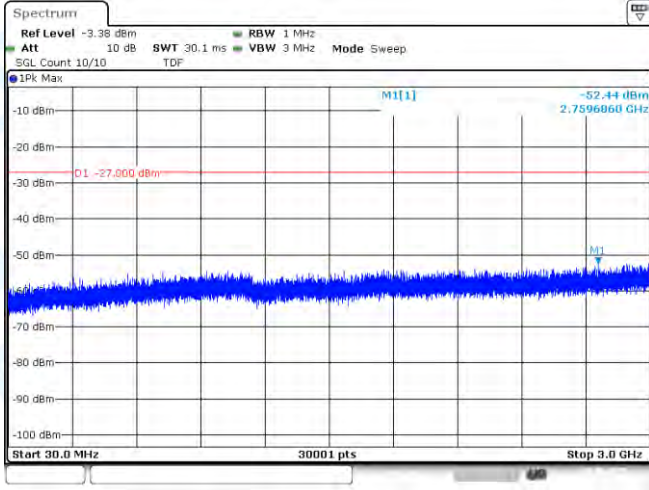
Date: 27-MAY-2025 20:18:32

11AX20SISO-Ant1-6695-3000~10000-PASS



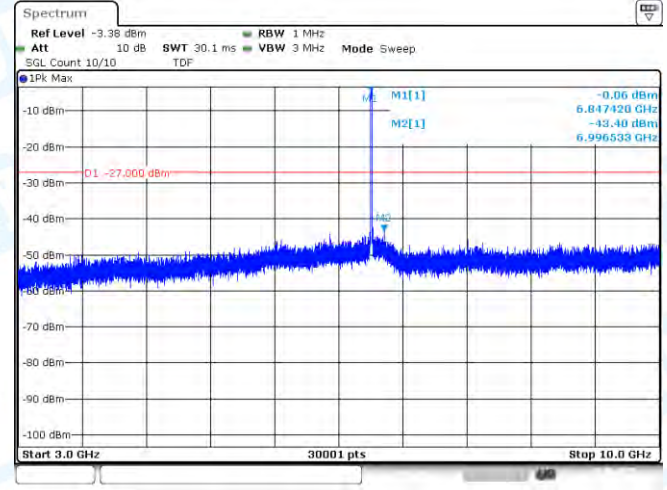
Date: 27-MAY-2025 20:18:40

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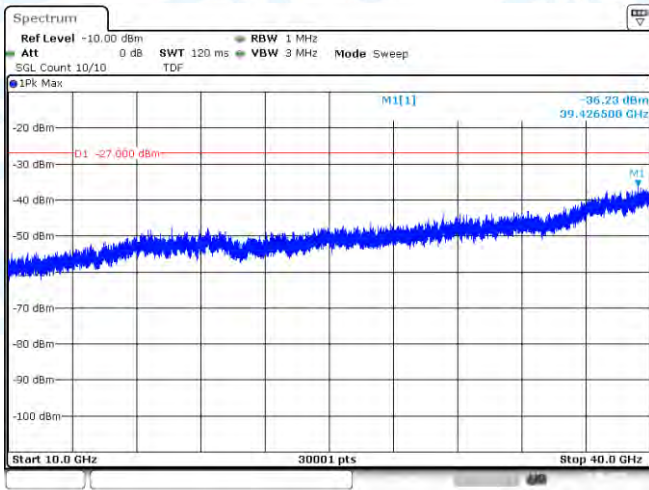
Date: 27-MAY-2025 20:19:52

11AX20SISO-Ant1-6855-30~3000-PASS



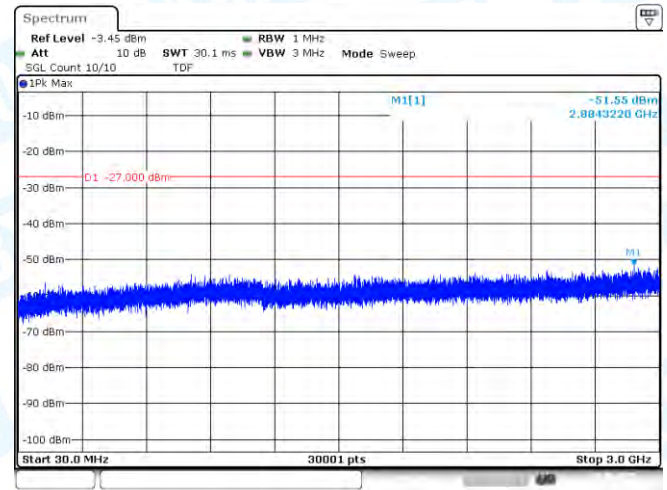
Date: 27-MAY-2025 20:19:59

11AX20SISO-Ant1-6855-3000~10000-PASS



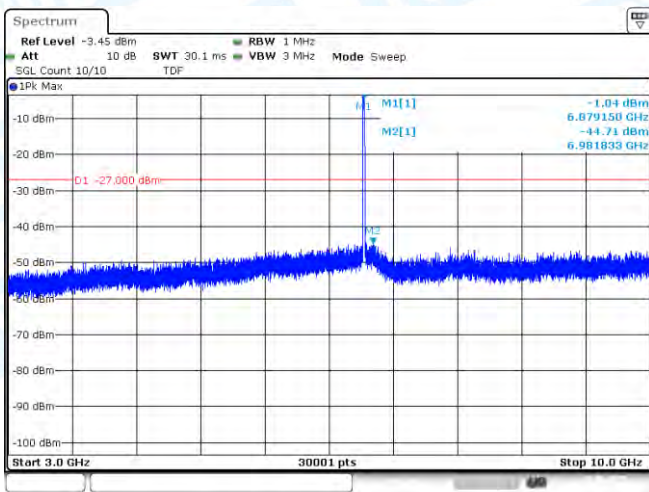
Date: 27-MAY-2025 20:20:06

11AX20SISO-Ant1-6855-10000~40000-PASS



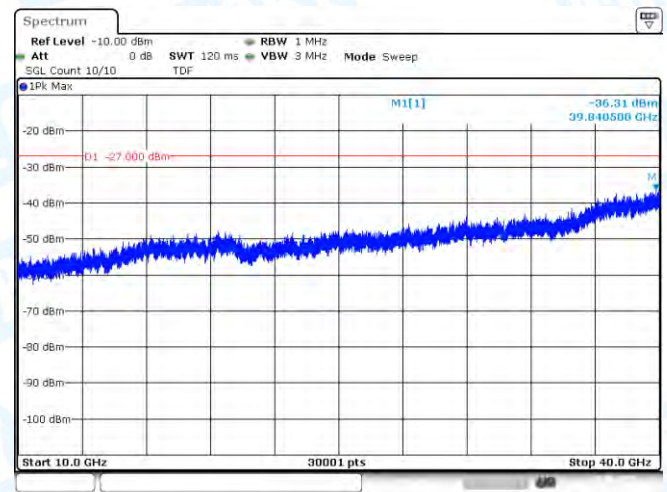
Date: 27-MAY-2025 20:23:20

11AX20SISO-Ant1-6875-30~3000-PASS



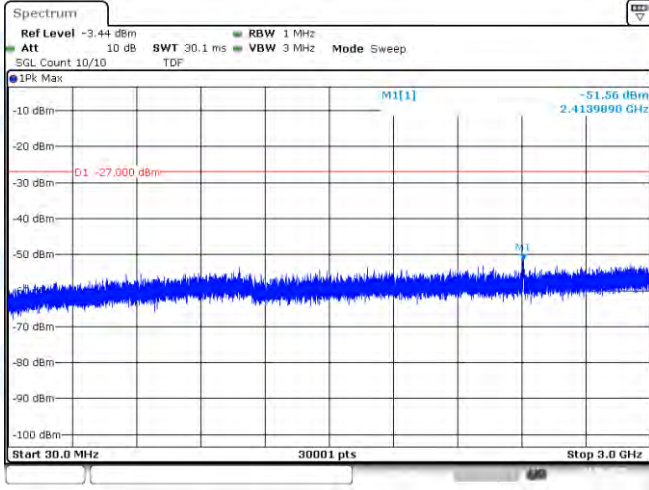
Date: 27-MAY-2025 20:23:27

11AX20SISO-Ant1-6875-3000~10000-PASS



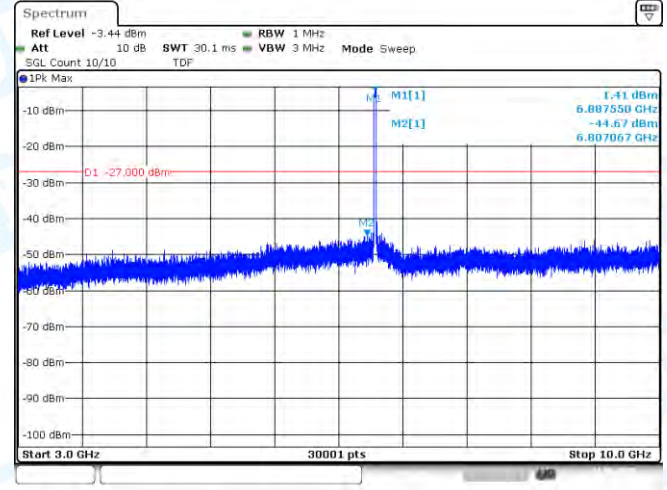
Date: 27-MAY-2025 20:23:35

11AX20SISO-Ant1-6875-10000~40000-PASS



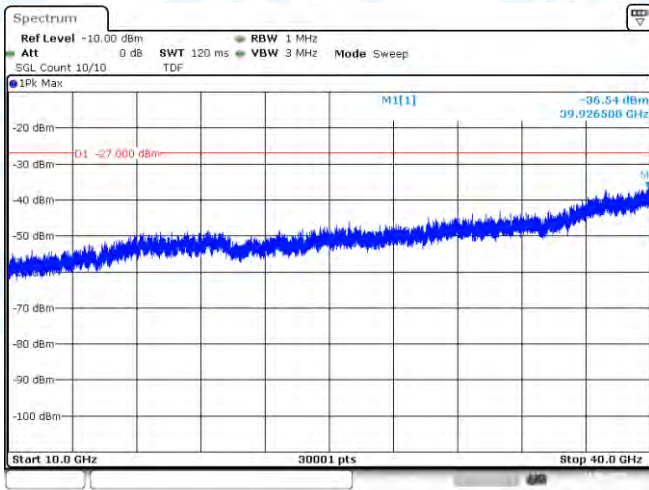
Date: 21-MAY-2025 17:01:05

11AX20SISO-Ant1-6895-30~3000-PASS



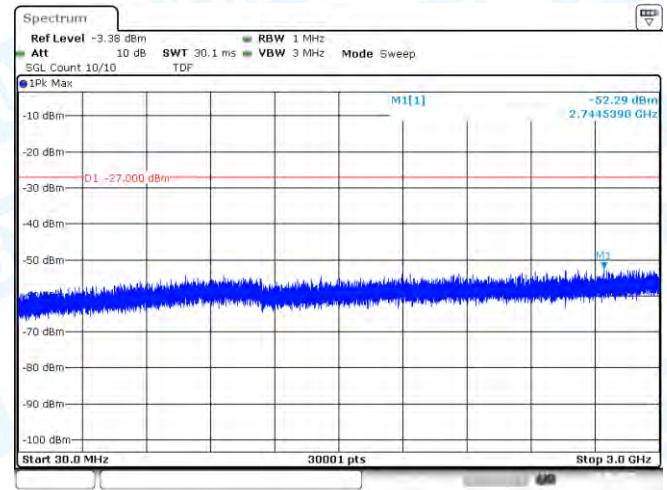
Date: 21-MAY-2025 17:01:12

11AX20SISO-Ant1-6895-3000~10000-PASS



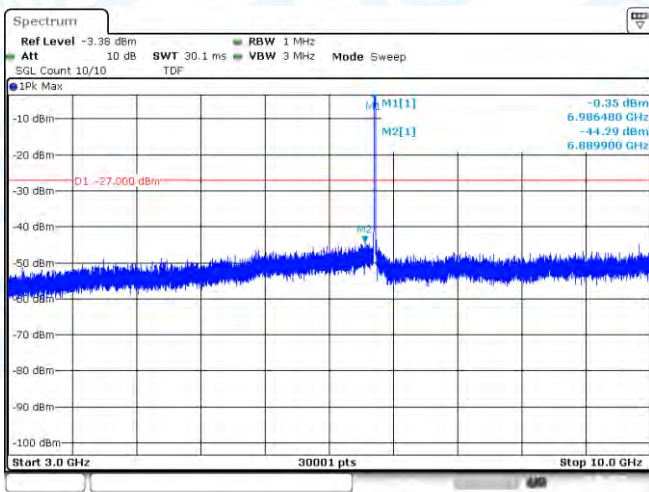
Date: 21-MAY-2025 17:01:20

11AX20SISO-Ant1-6895-10000~40000-PASS



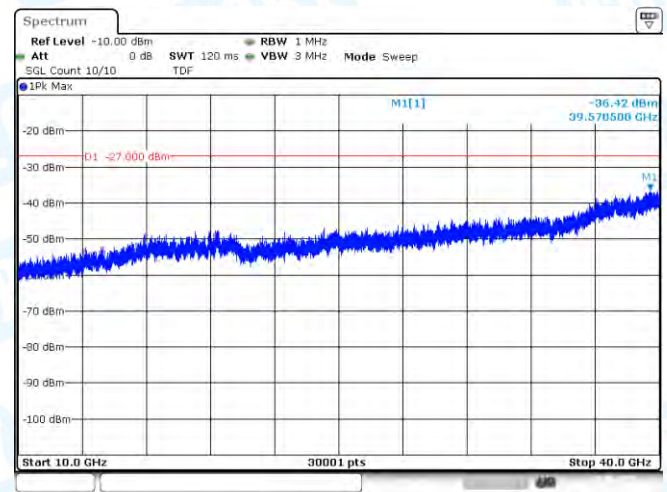
Date: 27-MAY-2025 20:38:08

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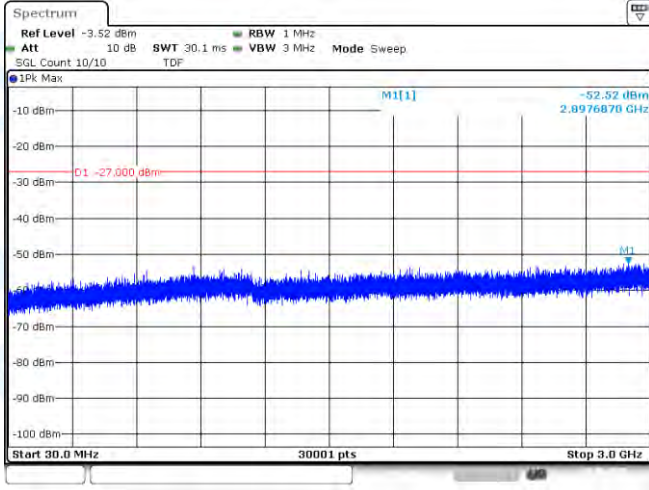
Date: 27-MAY-2025 20:32:15

11AX20SISO-Ant1-6995-3000~10000-PASS



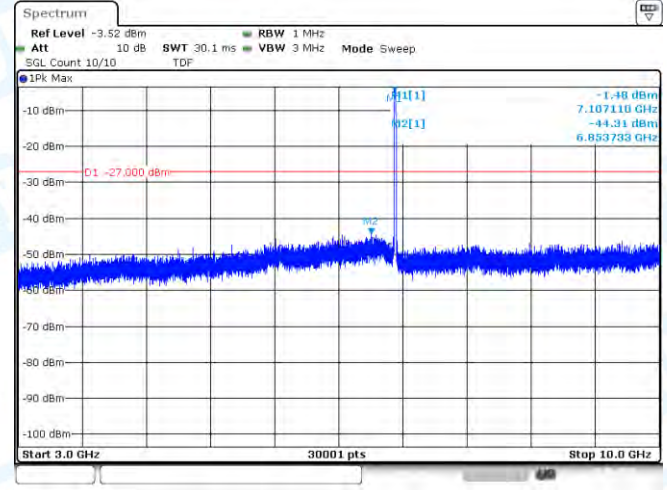
Date: 27-MAY-2025 20:38:33

11AX20SISO-Ant1-6995-10000~40000-PASS



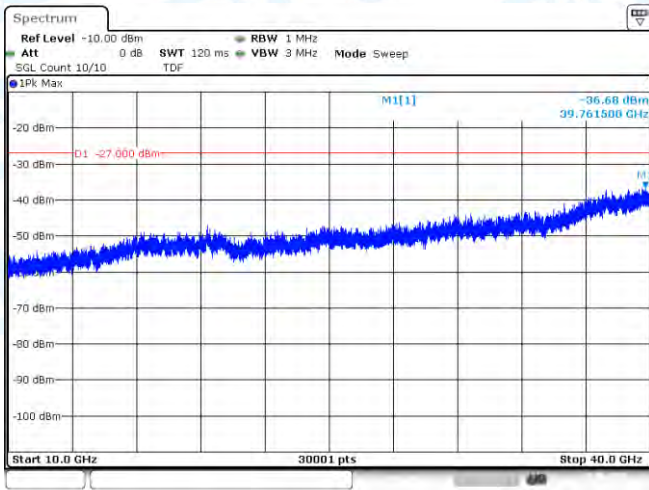
Date: 27-MAY-2025 10:39:04

11AX20SISO-Ant1-7115-30~3000-PASS



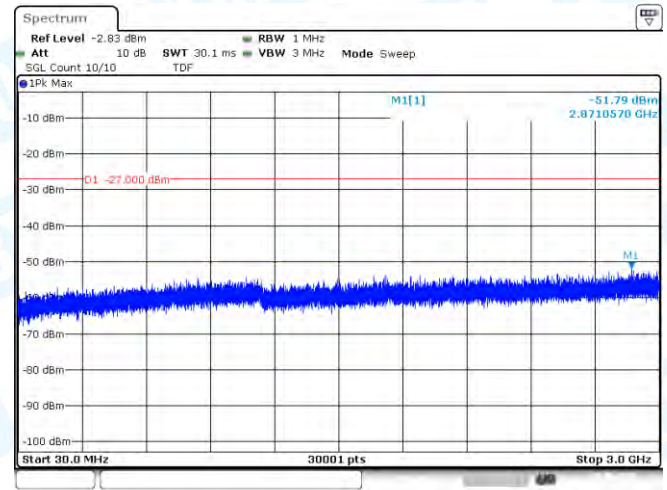
Date: 27-MAY-2025 10:39:11

11AX20SISO-Ant1-7115-3000~10000-PASS



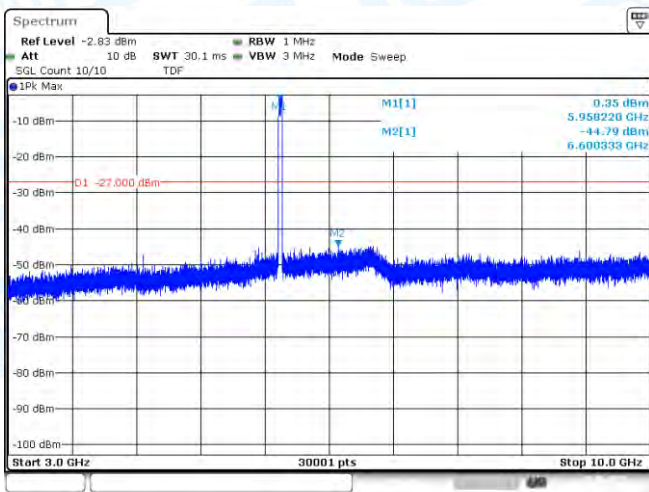
Date: 27-MAY-2025 10:39:19

11AX20SISO-Ant1-7115-10000~40000-PASS



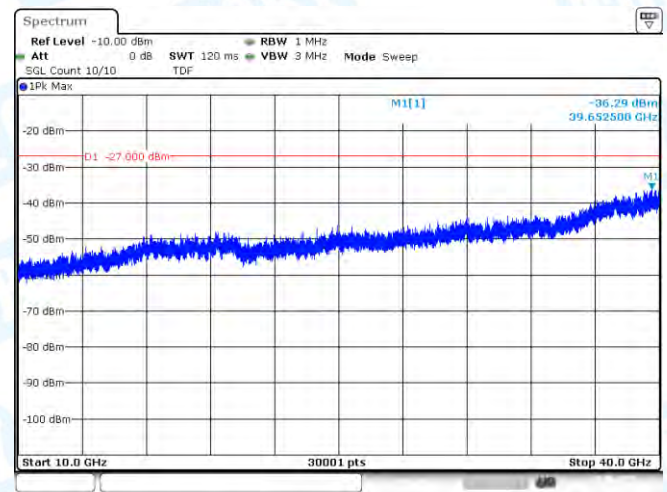
Date: 27-MAY-2025 10:39:31

11AX40SISO-Ant1-5965-30~3000-PASS



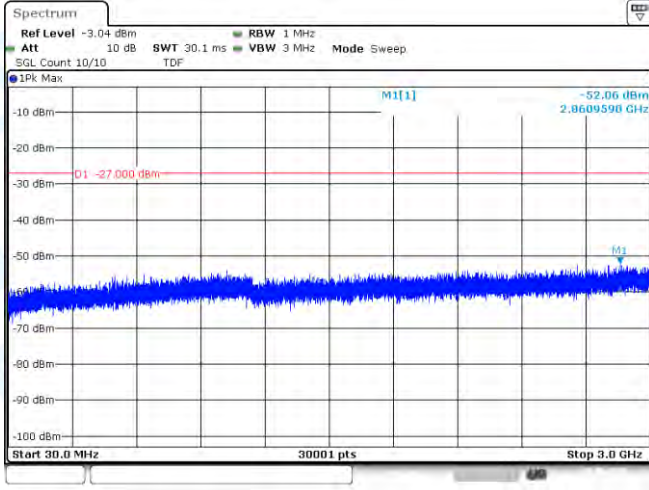
Date: 27-MAY-2025 10:39:38

11AX40SISO-Ant1-5965-3000~10000-PASS



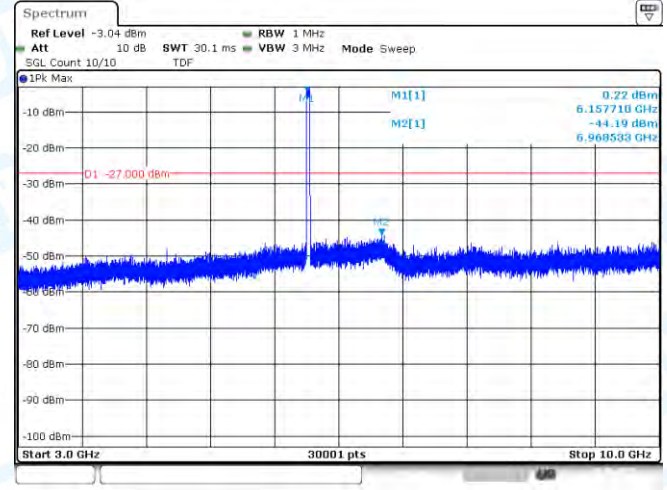
Date: 27-MAY-2025 10:39:46

11AX40SISO-Ant1-5965-10000~40000-PASS



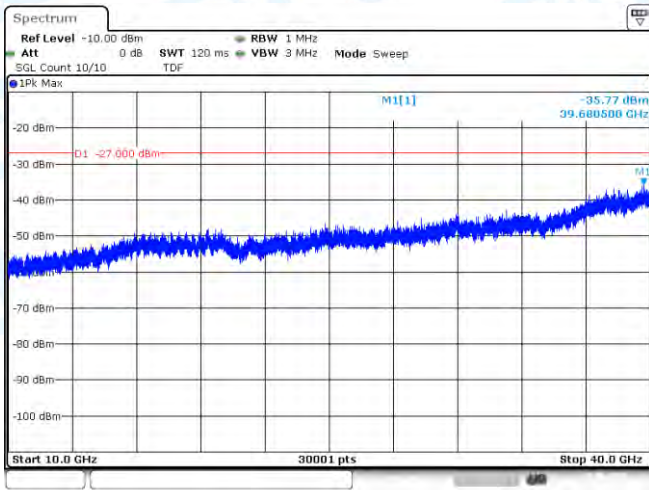
Date: 27-MAY-2025 20:57:46

11AX40SISO-Ant1-6165-30~3000-PASS



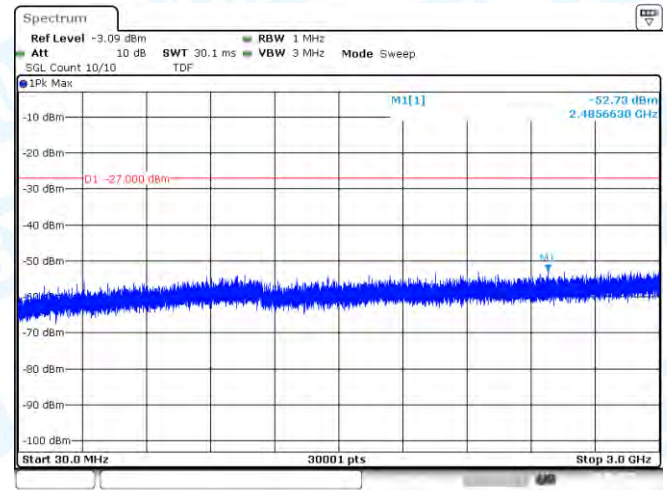
Date: 27-MAY-2025 20:57:53

11AX40SISO-Ant1-6165-3000~10000-PASS



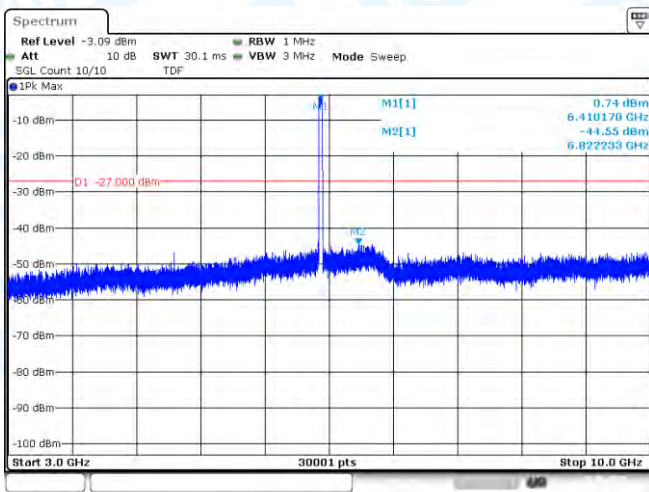
Date: 27-MAY-2025 20:58:01

11AX40SISO-Ant1-6165-10000~40000-PASS



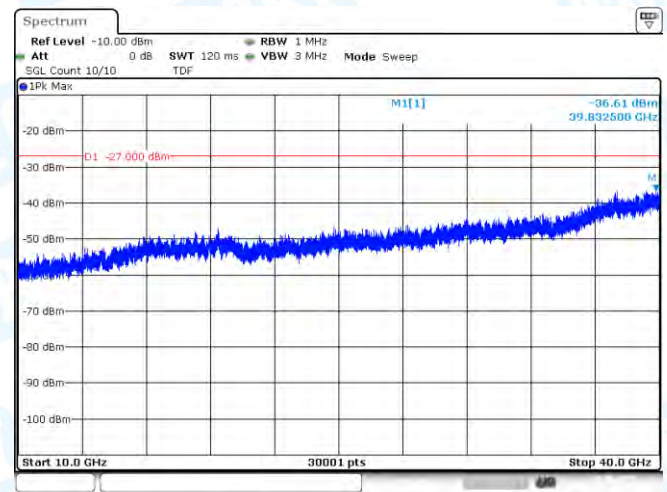
Date: 27-MAY-2025 21:06:25

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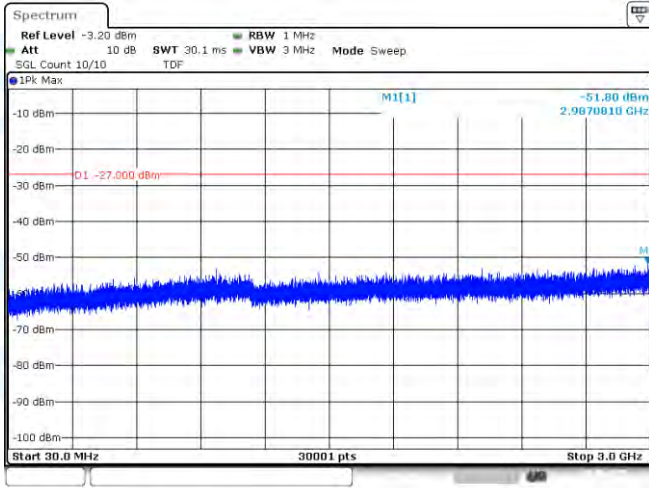
Date: 27-MAY-2025 21:06:31

11AX40SISO-Ant1-6405-3000~10000-PASS



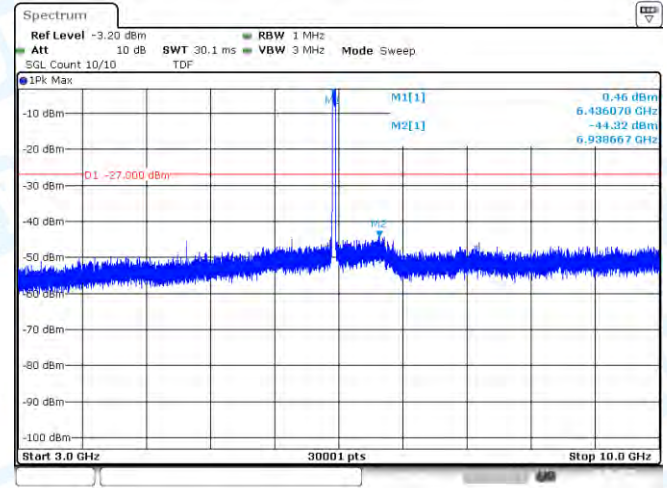
Date: 27-MAY-2025 21:06:39

11AX40SISO-Ant1-6405-10000~40000-PASS



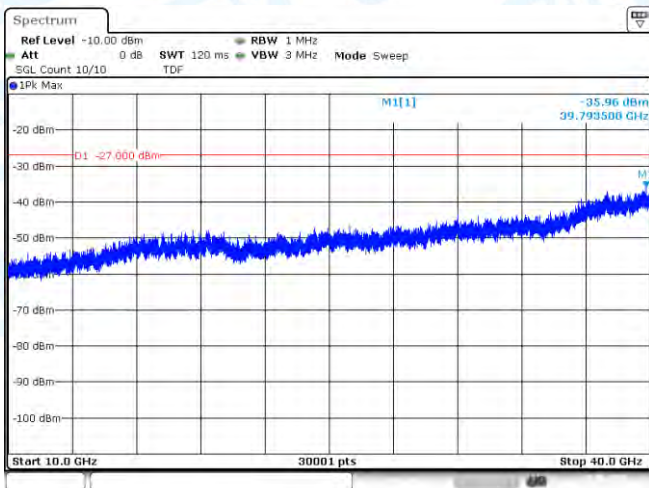
Date: 27-MAY-2025 21:11:25

11AX40SISO-Ant1-6445-30~3000-PASS



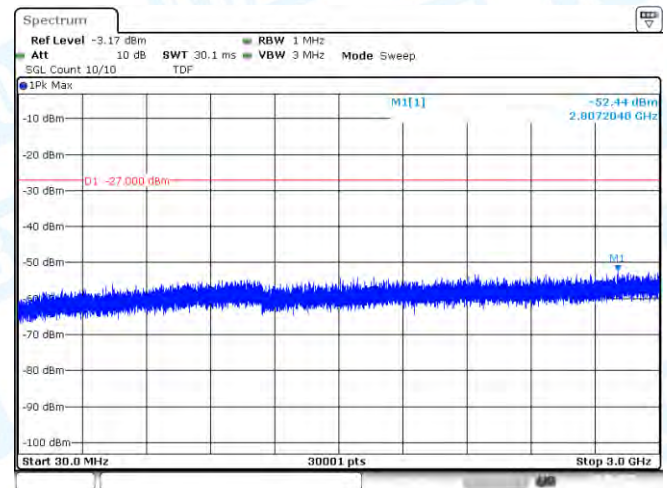
Date: 27-MAY-2025 21:11:32

11AX40SISO-Ant1-6445-3000~10000-PASS



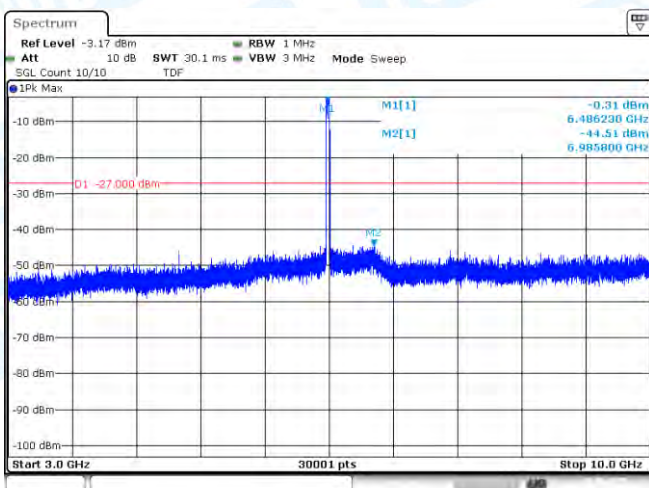
Date: 27-MAY-2025 21:13:40

11AX40SISO-Ant1-6445-10000~40000-PASS



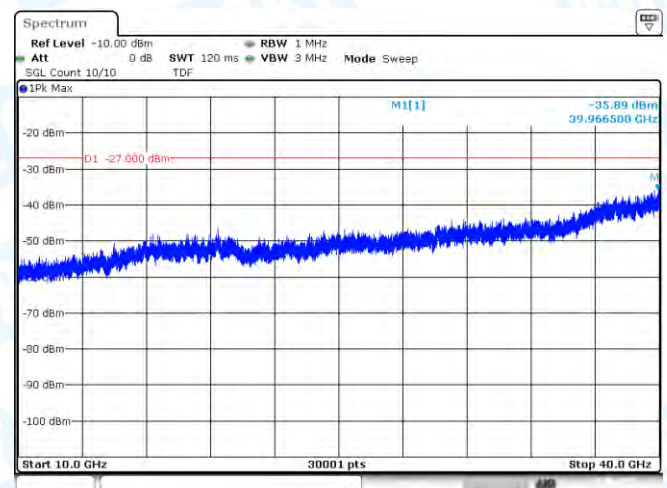
Date: 27-MAY-2025 21:13:47

11AX40SISO-Ant1-6485-30~3000-PASS



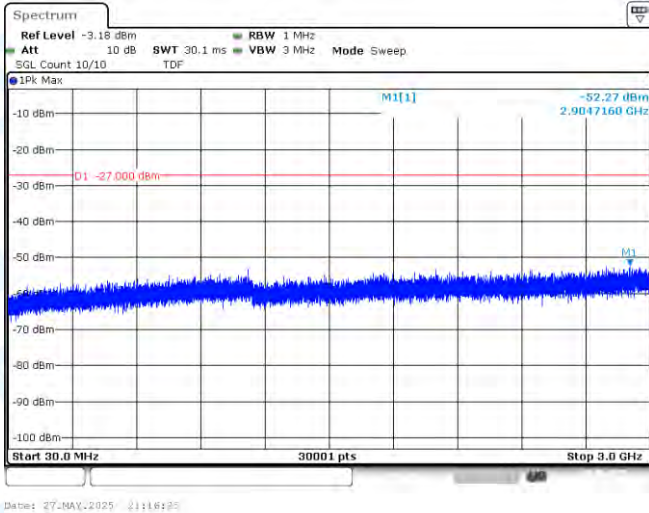
Date: 27-MAY-2025 21:13:54

11AX40SISO-Ant1-6485-3000~10000-PASS

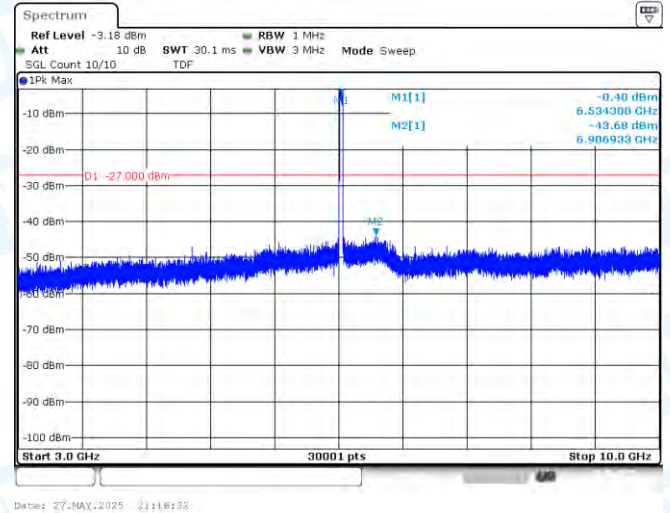


Date: 27-MAY-2025 21:14:02

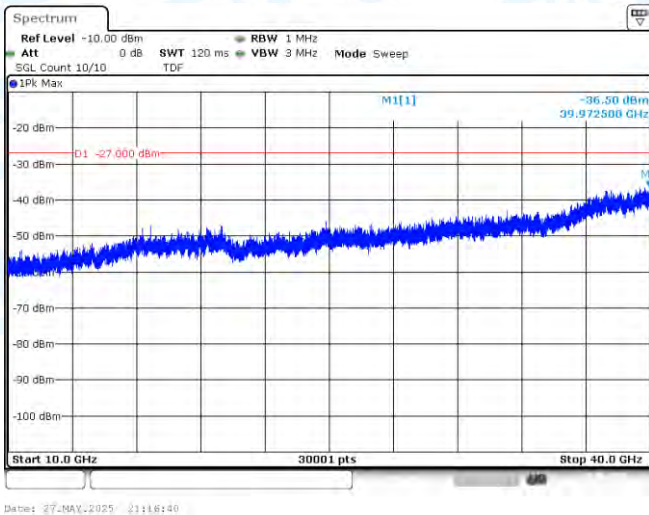
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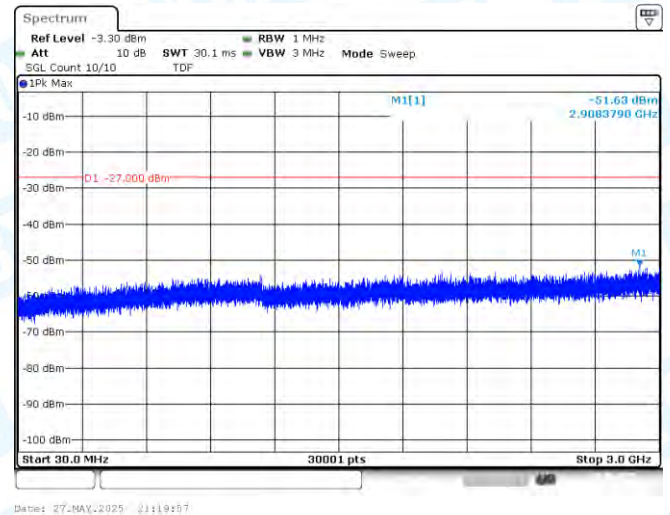
11AX40SISO-Ant1-6525-30~3000-PASS



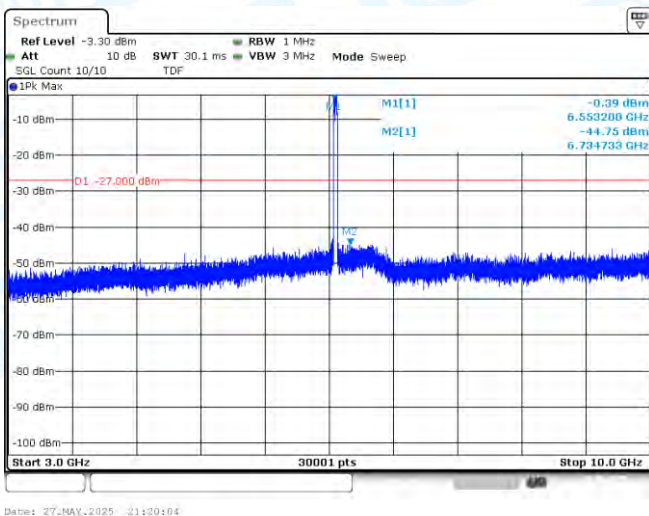
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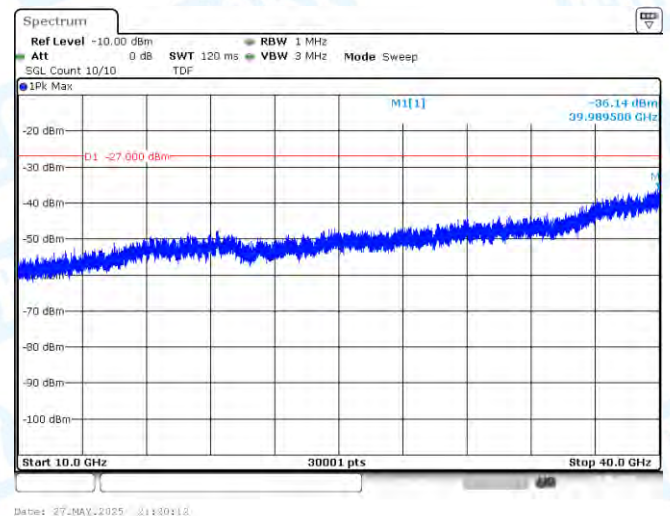
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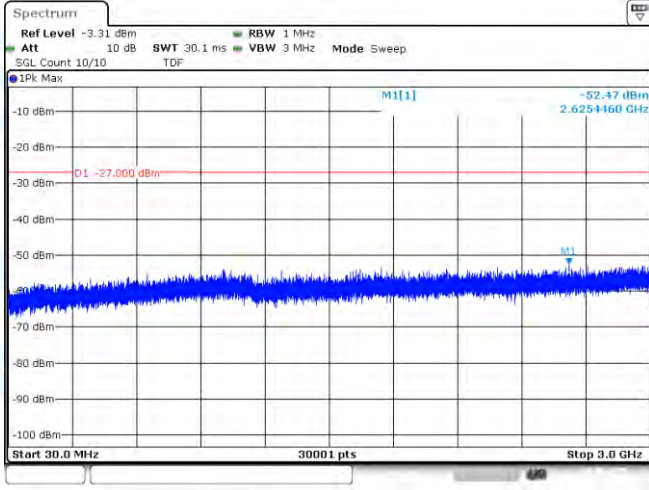
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11AX40SISO-Ant1-6565-3000~10000-PASS

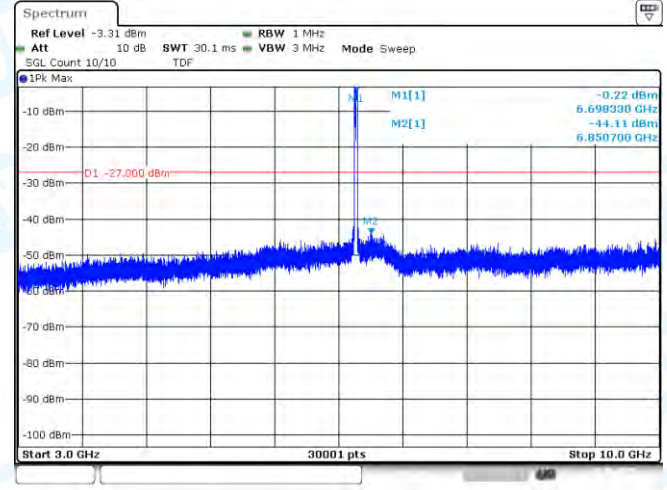


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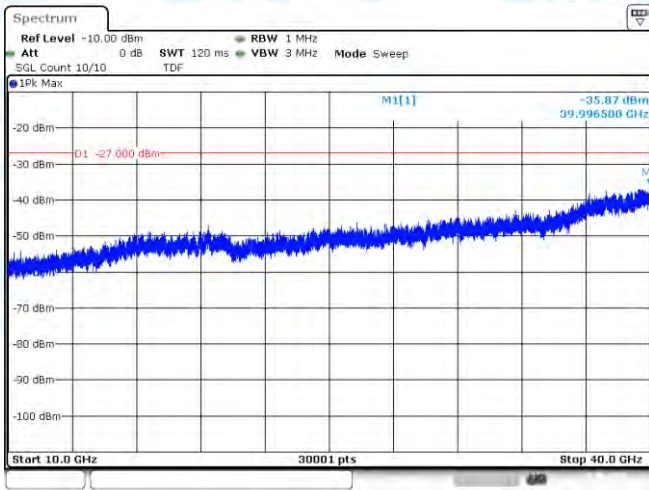
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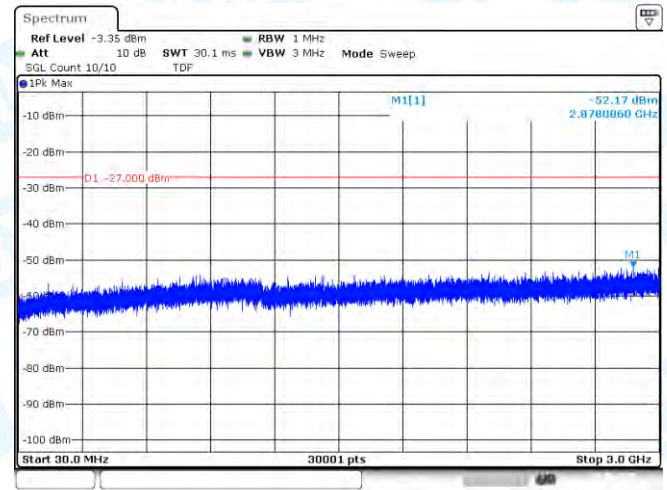
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11AX40SISO-Ant1-6685-3000~10000-PASS



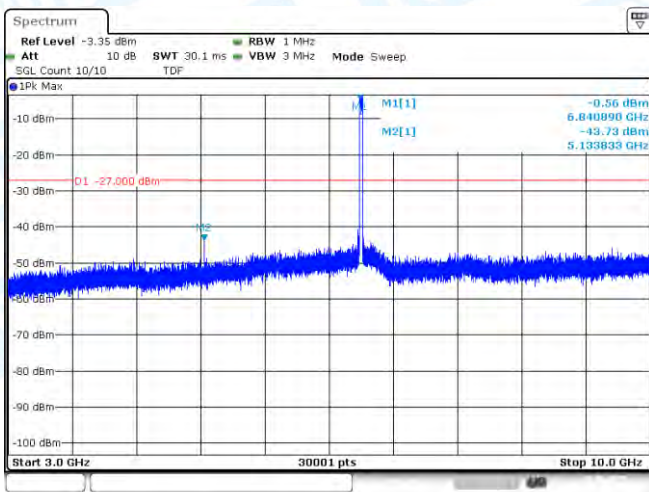
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11AX40SISO-Ant1-6685-10000~40000-PASS



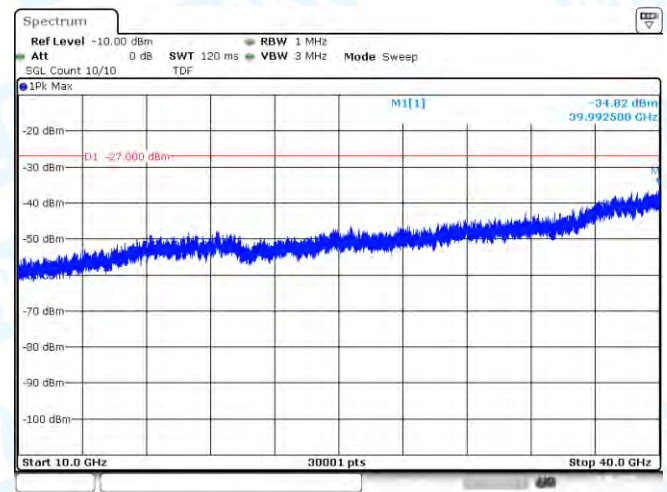
Date: 27-MAY-2025 21:29:16

11AX40SISO-Ant1-6845-30~3000-PASS



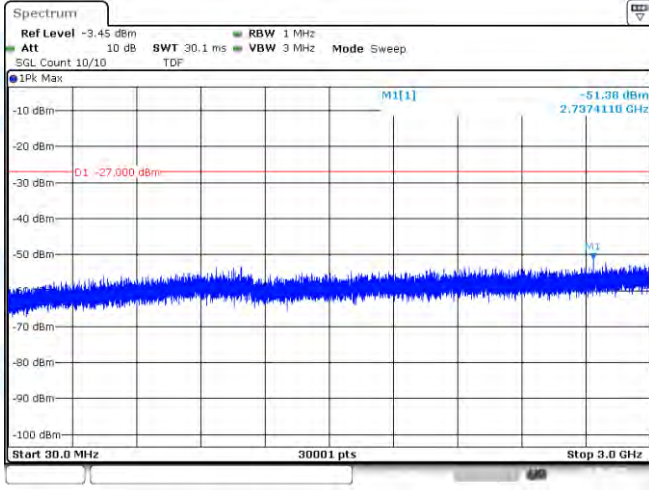
Date: 27-MAY-2025 21:29:23

11AX40SISO-Ant1-6845-3000~10000-PASS



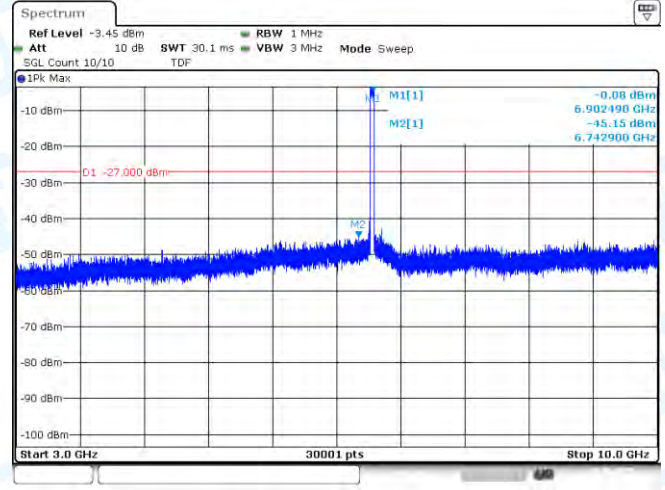
Date: 27-MAY-2025 21:29:30

11AX40SISO-Ant1-6845-10000~40000-PASS



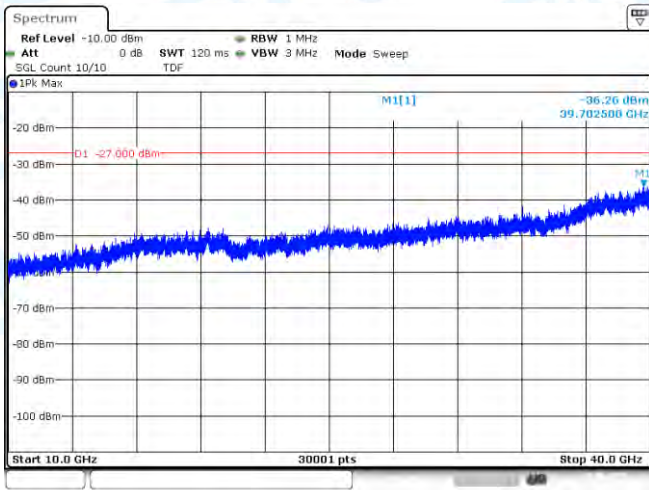
Date: 27-MAY-2025 11:31:39

11AX40SISO-Ant1-6885-30~3000-PASS



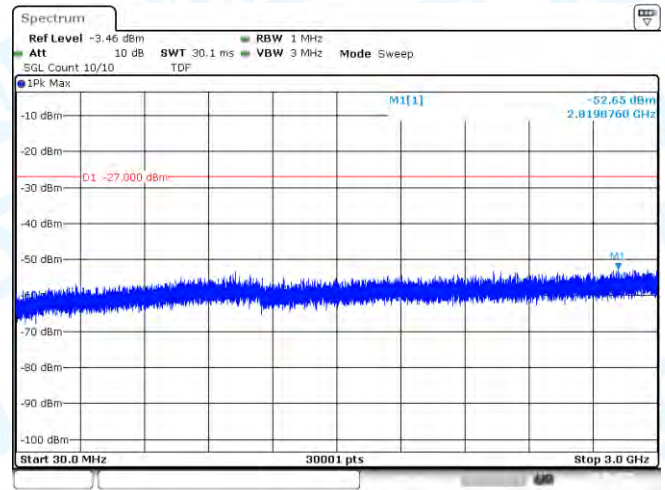
Date: 27-MAY-2025 11:31:46

11AX40SISO-Ant1-6885-3000~10000-PASS



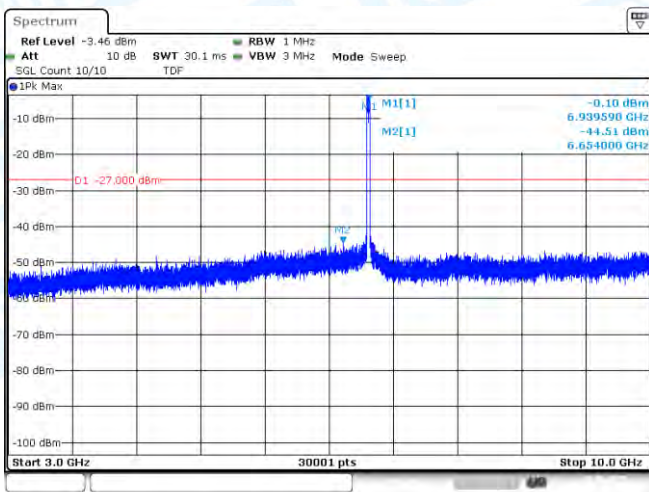
Date: 27-MAY-2025 11:31:53

11AX40SISO-Ant1-6885-10000~40000-PASS



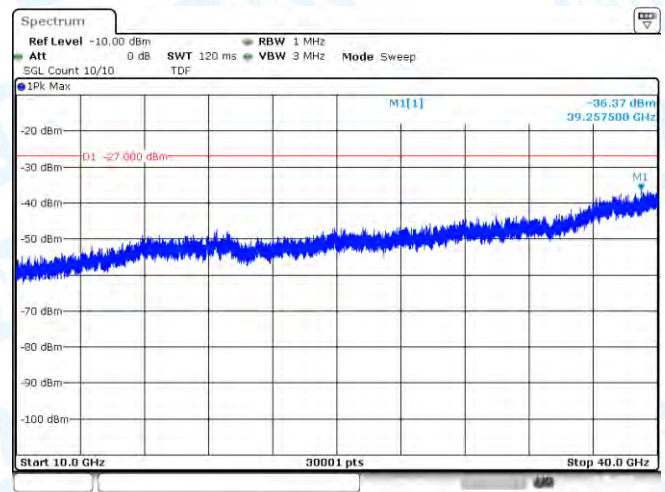
Date: 27-MAY-2025 11:37:50

11AX40SISO-Ant1-6925-30~3000-PASS



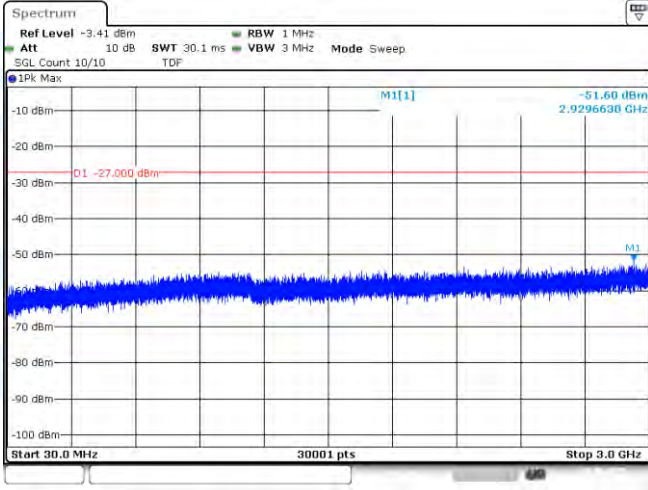
Date: 27-MAY-2025 11:37:07

11AX40SISO-Ant1-6925-3000~10000-PASS



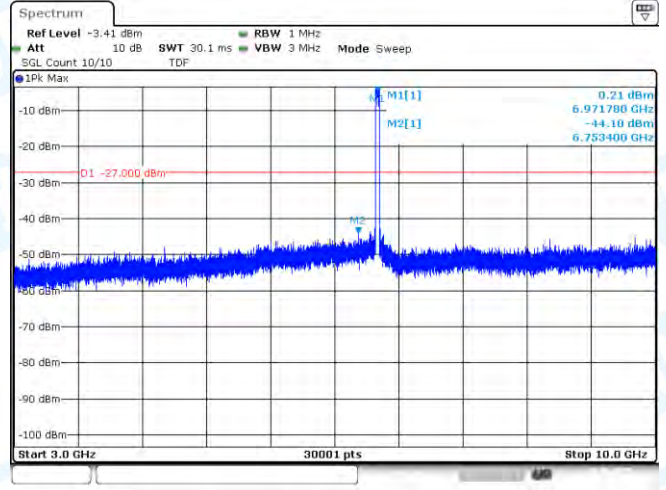
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11AX40SISO-Ant1-6925-10000~40000-PASS



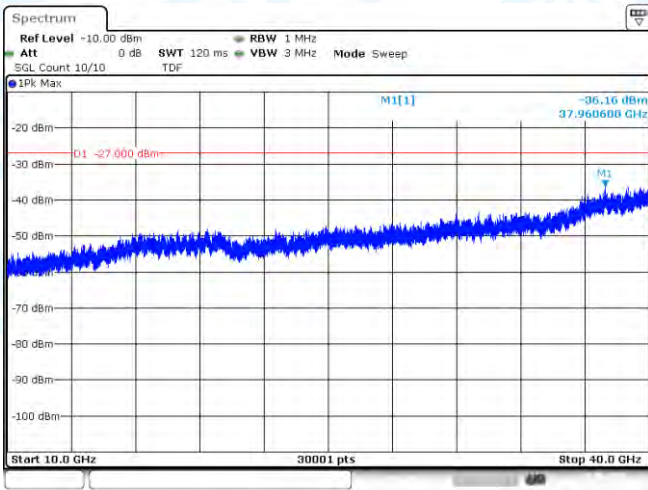
Date: 27-MAY-2025 21:41:02

11AX40SISO-Ant1-6965-30~3000-PASS



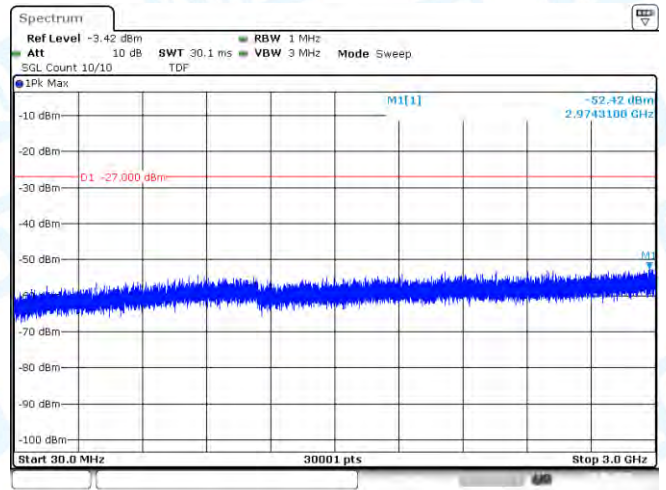
Date: 27-MAY-2025 21:41:08

11AX40SISO-Ant1-6965-3000~10000-PASS



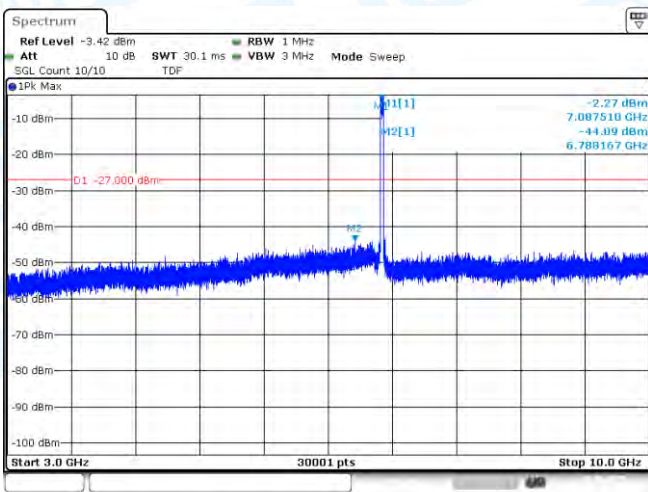
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11AX40SISO-Ant1-6965-10000~40000-PASS



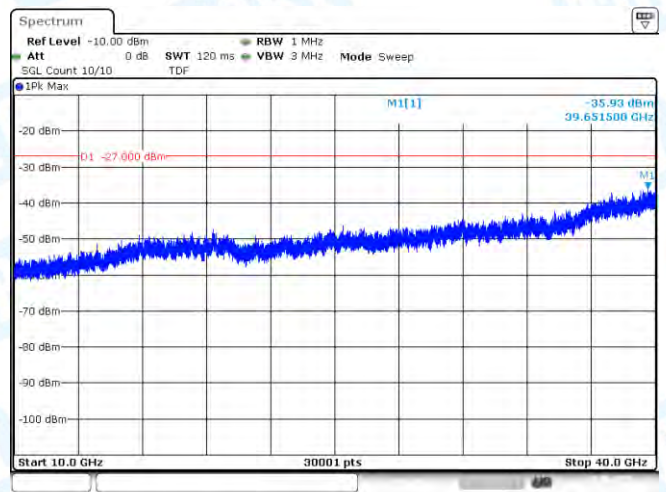
Date: 27-MAY-2025 21:41:21

11AX40SISO-Ant1-7085-30~3000-PASS



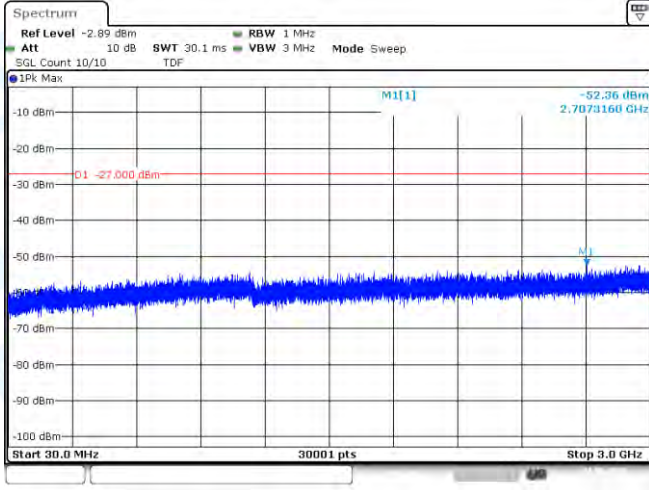
Date: 27-MAY-2025 21:48:27

11AX40SISO-Ant1-7085-3000~10000-PASS



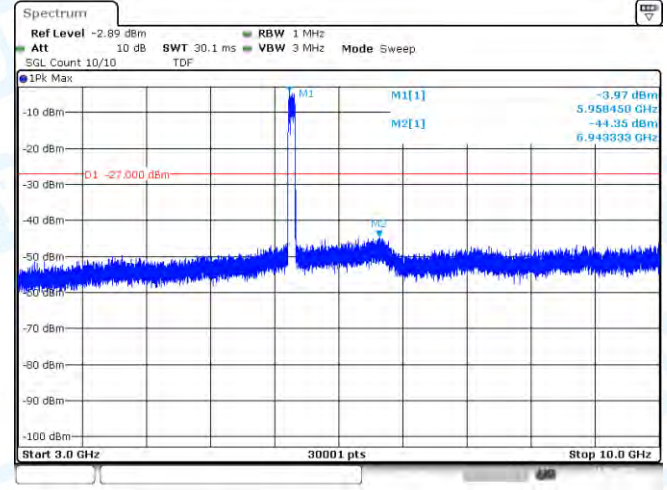
Date: 27-MAY-2025 21:48:35

11AX40SISO-Ant1-7085-10000~40000-PASS



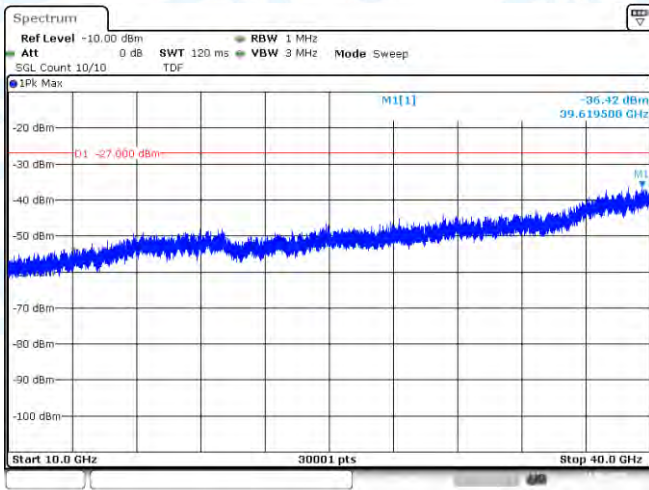
Date: 21-MAY-2025 17:06:13

11AX80SISO-Ant1-5985-30~3000-PASS



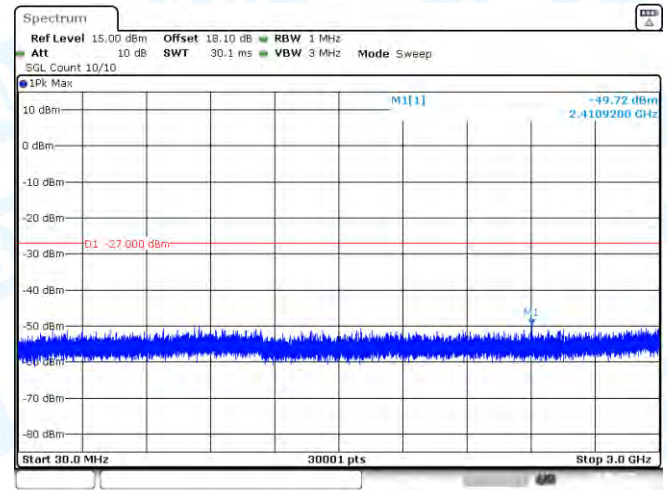
Date: 21-MAY-2025 17:06:20

11AX80SISO-Ant1-5985-3000~10000-PASS



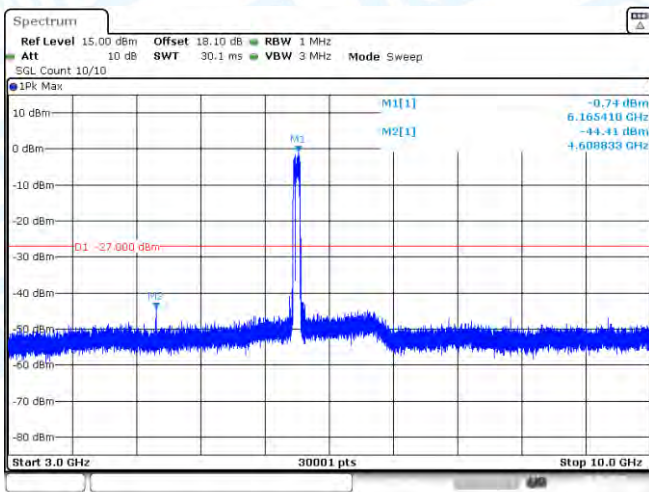
Date: 21-MAY-2025 17:06:37

11AX80SISO-Ant1-5985-10000~40000-PASS



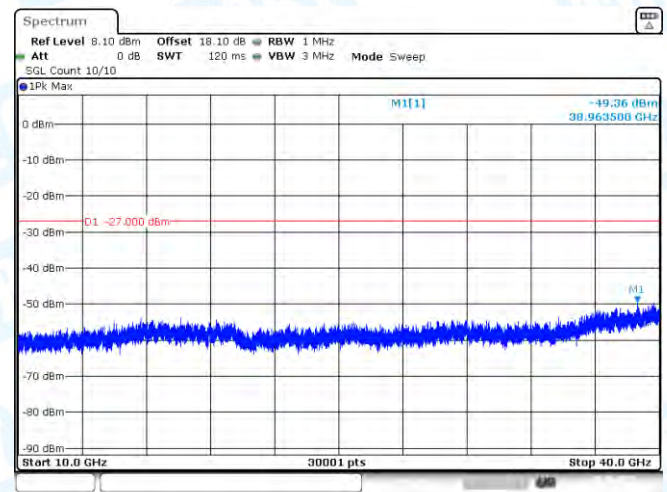
Date: 29-MAY-2025 14:00:36

11AX80SISO-Ant1-6145-30~3000-PASS



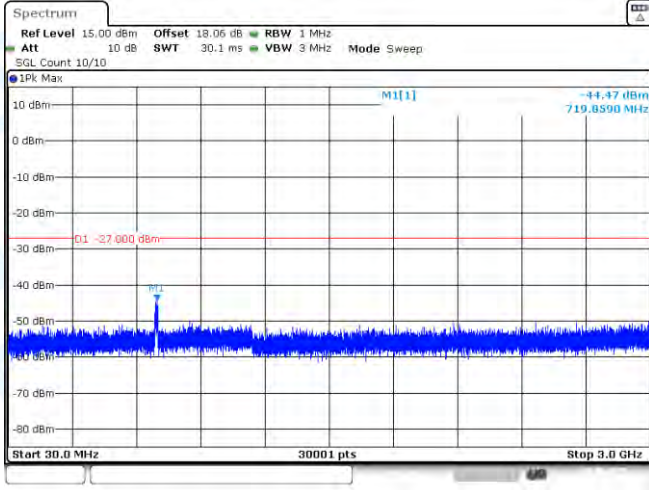
Date: 29-MAY-2025 14:00:43

11AX80SISO-Ant1-6145-3000~10000-PASS



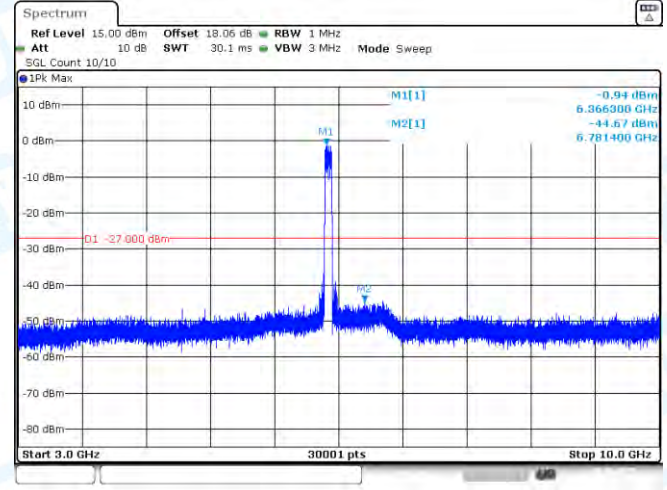
Date: 29-MAY-2025 14:00:55

11AX80SISO-Ant1-6145-10000~40000-PASS



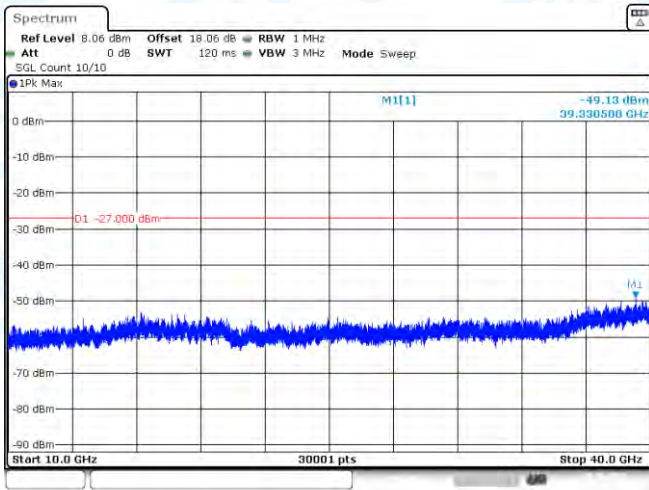
Date: 29-MAY-2025 14:03:27

11AX80SISO-Ant1-6385-30~3000-PASS



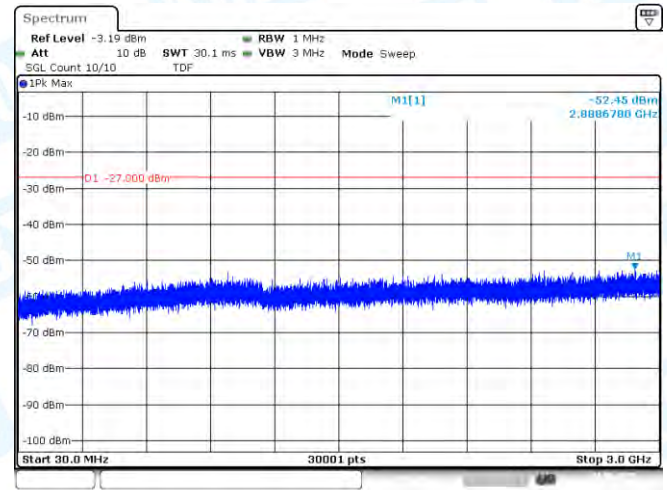
Date: 29-MAY-2025 14:03:34

11AX80SISO-Ant1-6385-3000~10000-PASS



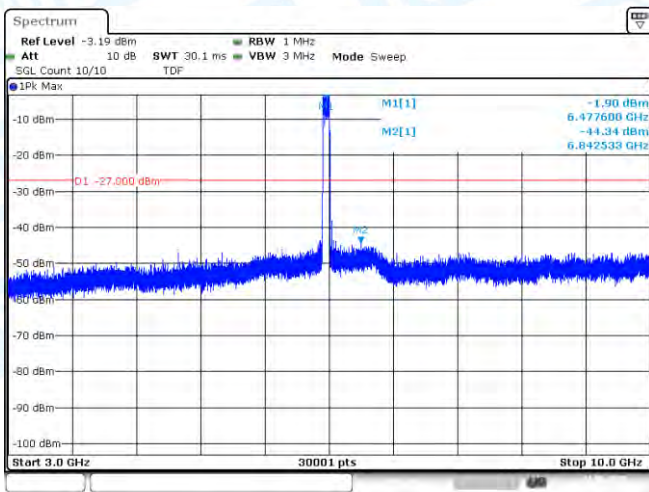
Date: 29-MAY-2025 14:03:41

11AX80SISO-Ant1-6385-10000~40000-PASS



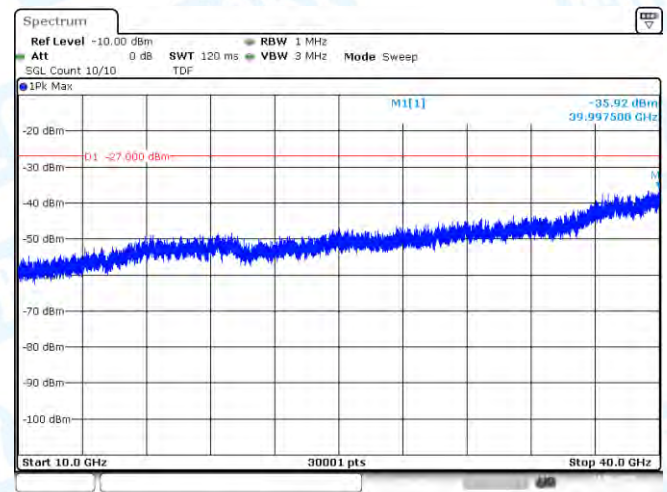
Date: 21-MAY-2025 17:18:38

11AX80SISO-Ant1-6465-30~3000-PASS



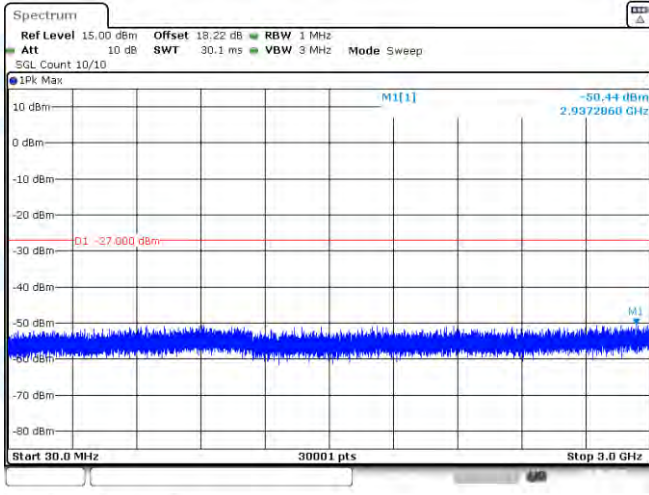
Date: 21-MAY-2025 17:18:45

11AX80SISO-Ant1-6465-3000~10000-PASS



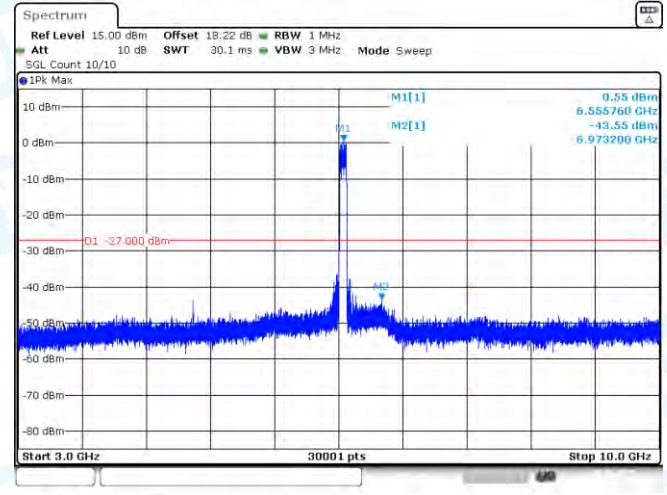
Date: 21-MAY-2025 17:18:55

11AX80SISO-Ant1-6465-10000~40000-PASS



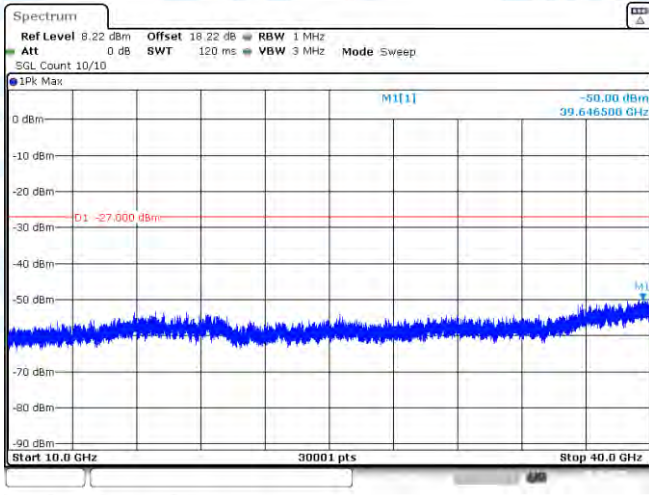
Date: 29-MAY-2025 14:09:48

11AX80SISO-Ant1-6545-30~3000-PASS



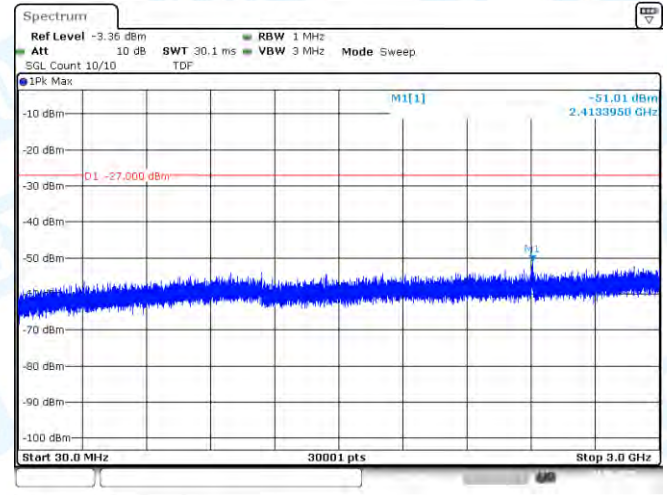
Date: 29-MAY-2025 14:09:55

11AX80SISO-Ant1-6545-3000~10000-PASS



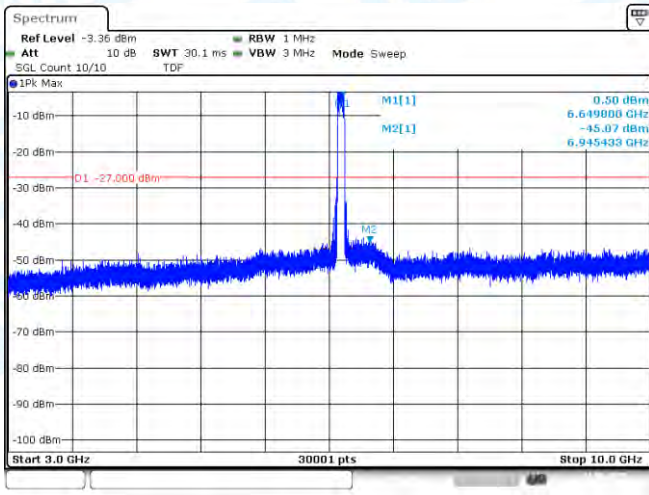
Date: 29-MAY-2025 14:10:03

11AX80SISO-Ant1-6545-10000~40000-PASS



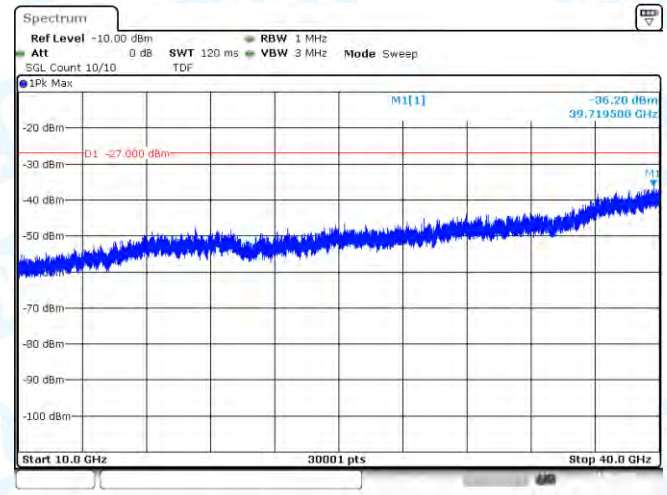
Date: 21-MAY-2025 17:16:50

11AX80SISO-Ant1-6625-30~3000-PASS



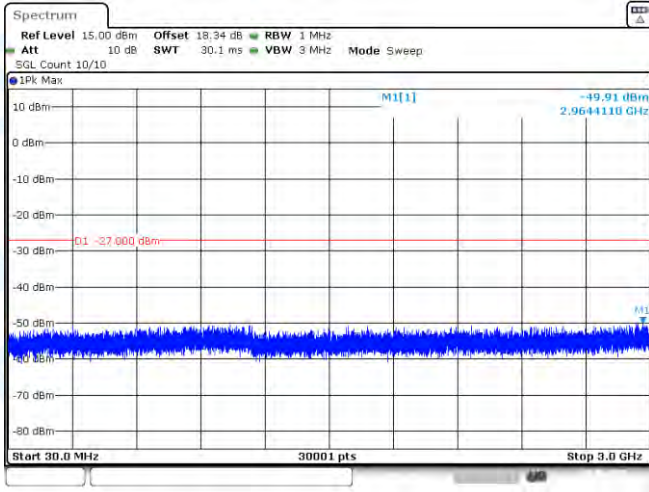
Date: 21-MAY-2025 17:16:57

11AX80SISO-Ant1-6625-3000~10000-PASS



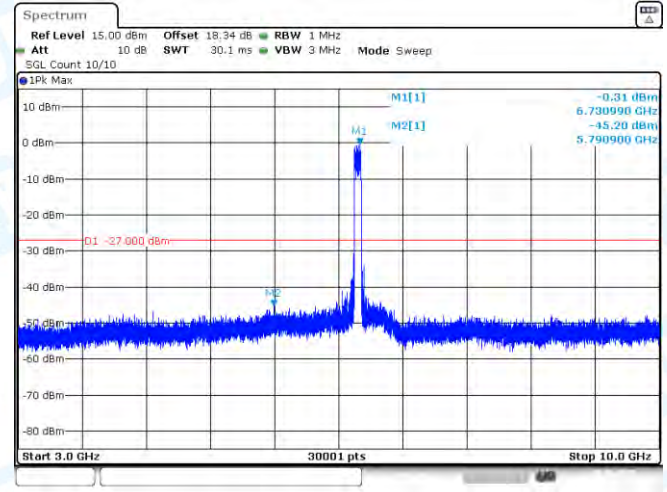
Date: 21-MAY-2025 17:17:04

11AX80SISO-Ant1-6625-10000~40000-PASS



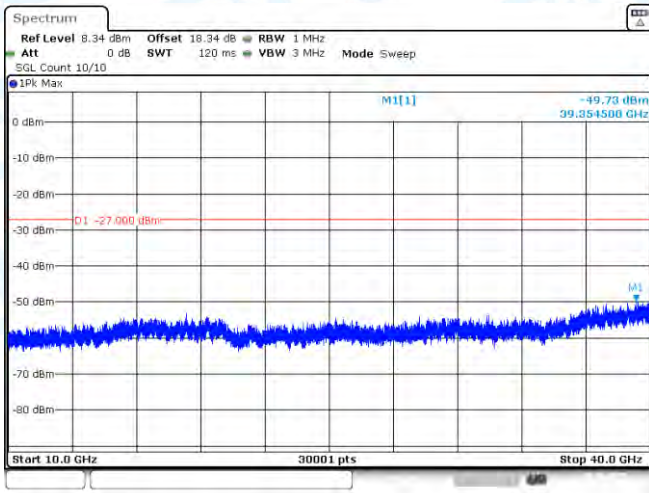
Date: 29-MAY-2025 14:14:12

11AX80SISO-Ant1-6705-30~3000-PASS



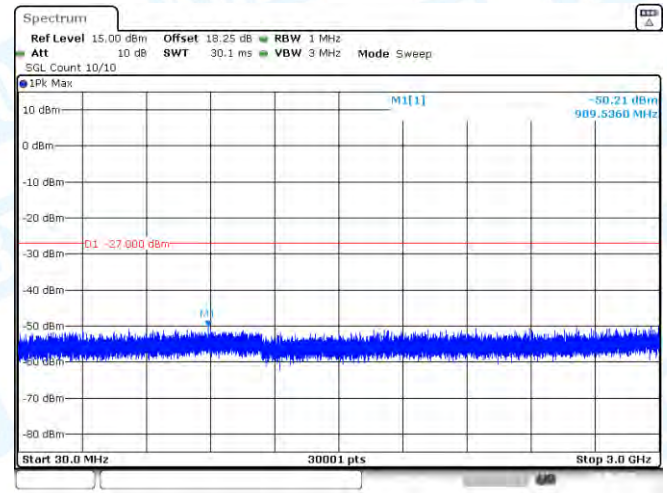
Date: 29-MAY-2025 14:14:18

11AX80SISO-Ant1-6705-3000~10000-PASS



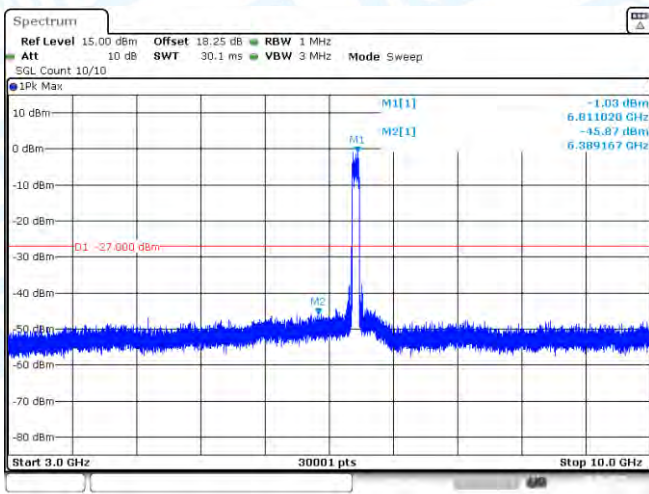
Date: 29-MAY-2025 14:14:26

11AX80SISO-Ant1-6705-10000~40000-PASS



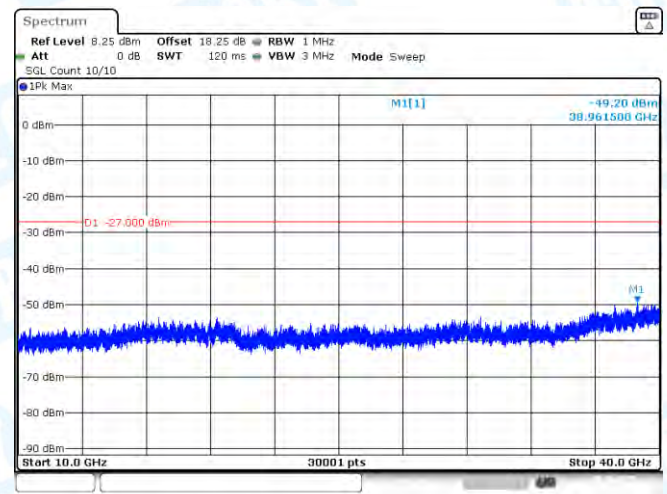
Date: 29-MAY-2025 14:18:46

11AX80SISO-Ant1-6785-30~3000-PASS



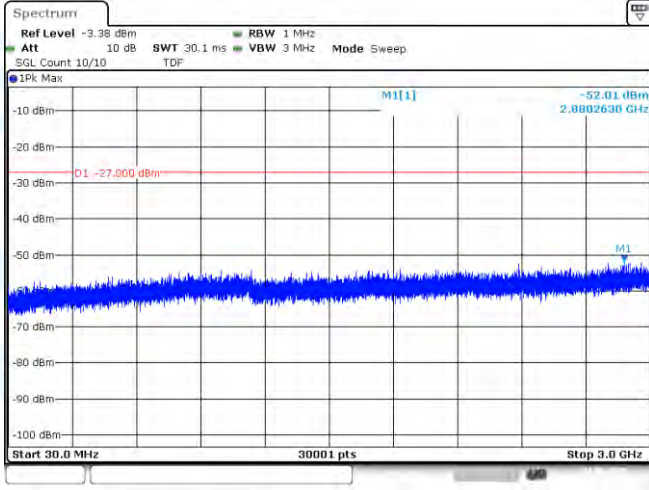
Date: 29-MAY-2025 14:18:56

11AX80SISO-Ant1-6785-3000~10000-PASS



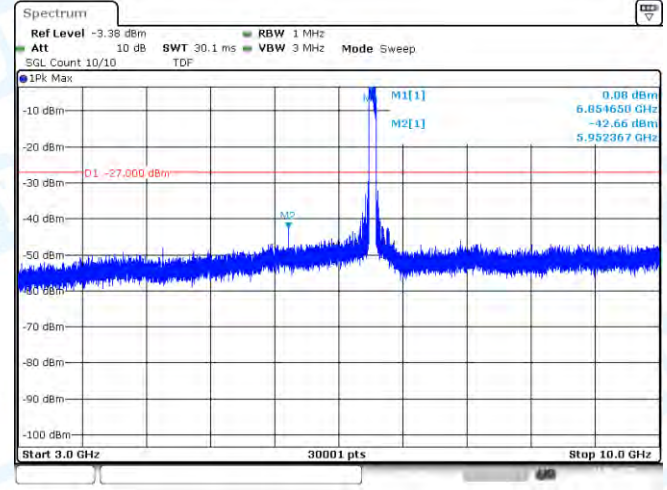
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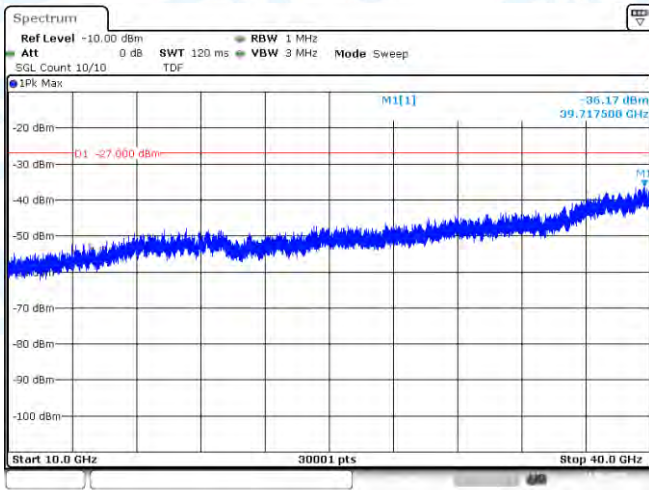
Date: 21-MAY-2025 17:19:48

11AX80SISO-Ant1-6865-30~3000-PASS



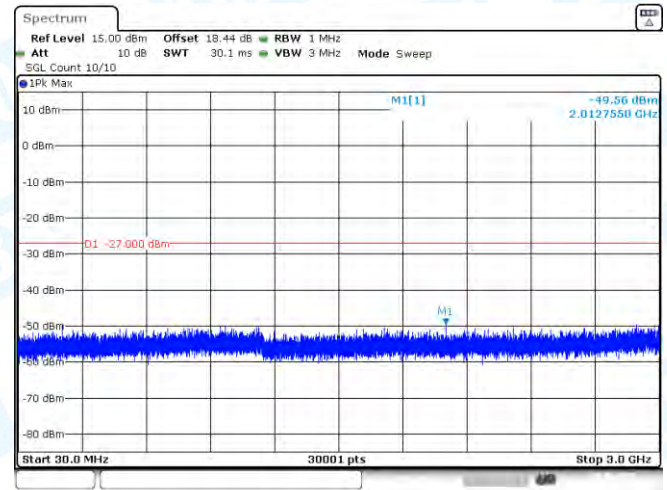
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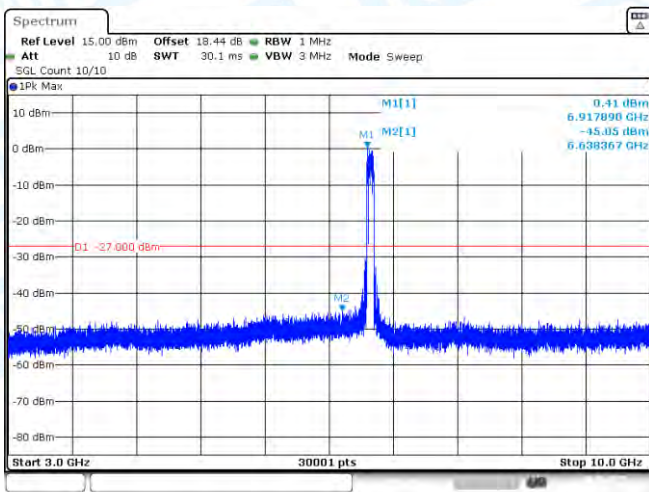
Date: 21-MAY-2025 17:20:03

11AX80SISO-Ant1-6865-10000~40000-PASS



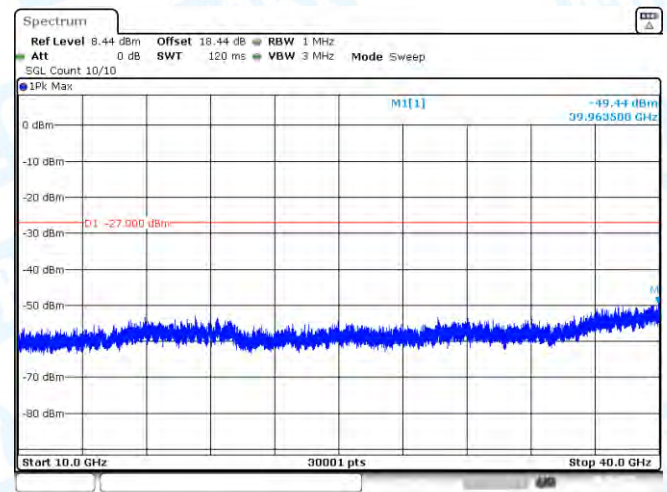
Date: 30-MAY-2025 14:07:33

11AX80SISO-Ant1-6945-30~3000-PASS



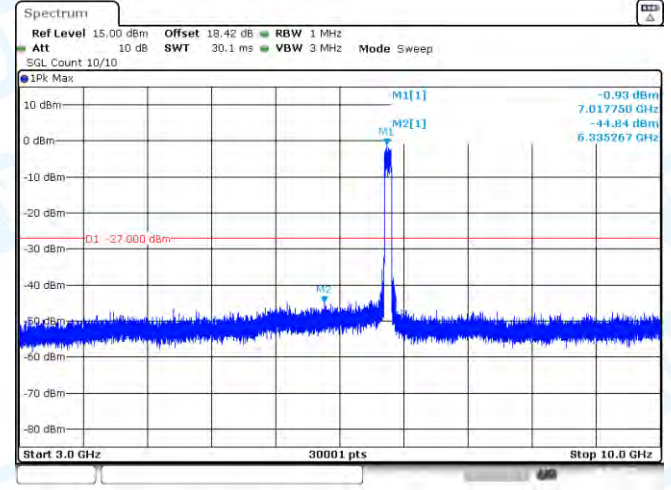
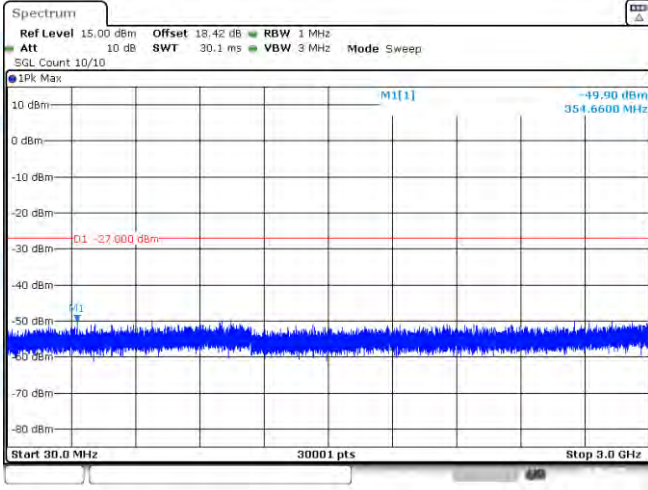
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11AX80SISO-Ant1-6945-3000~10000-PASS



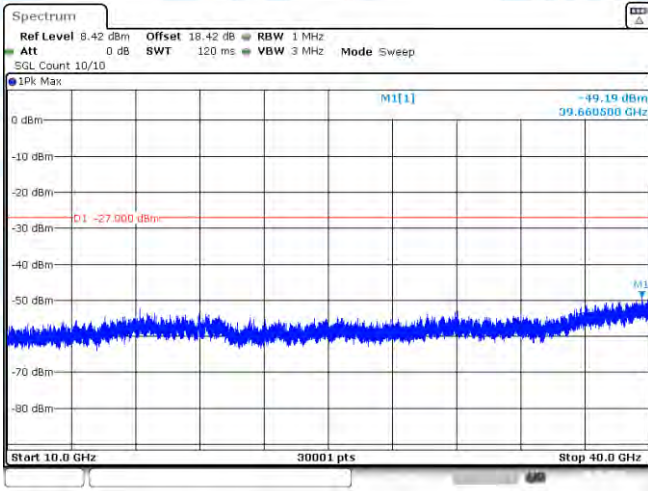
Date: 30-MAY-2025 14:07:38

11AX80SISO-Ant1-6945-10000~40000-PASS



11AX80SISO-Ant1-7025-30~3000-PASS

11AX80SISO-Ant1-7025-3000~10000-PASS



11AX80SISO-Ant1-7025-10000~40000-PASS