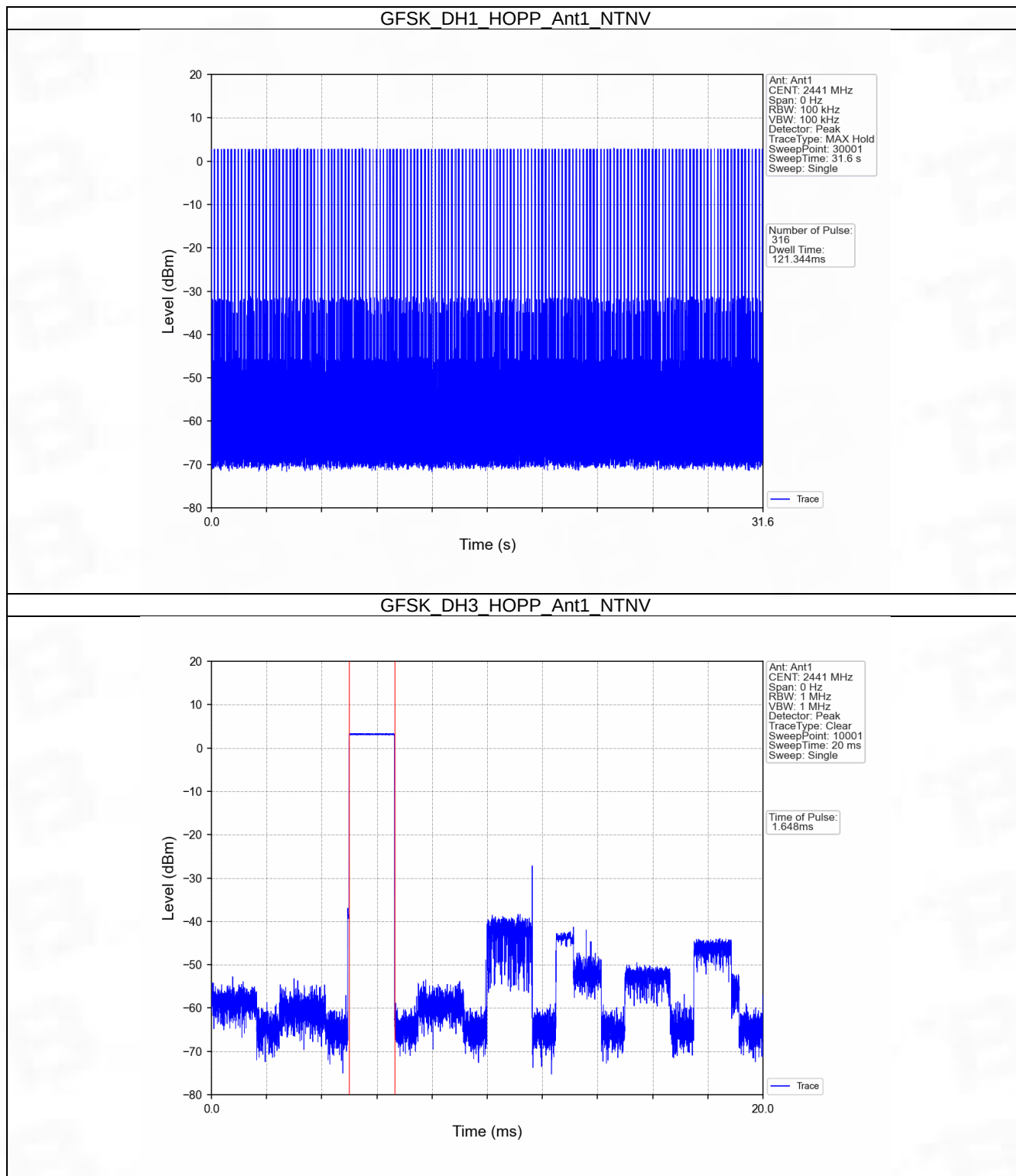
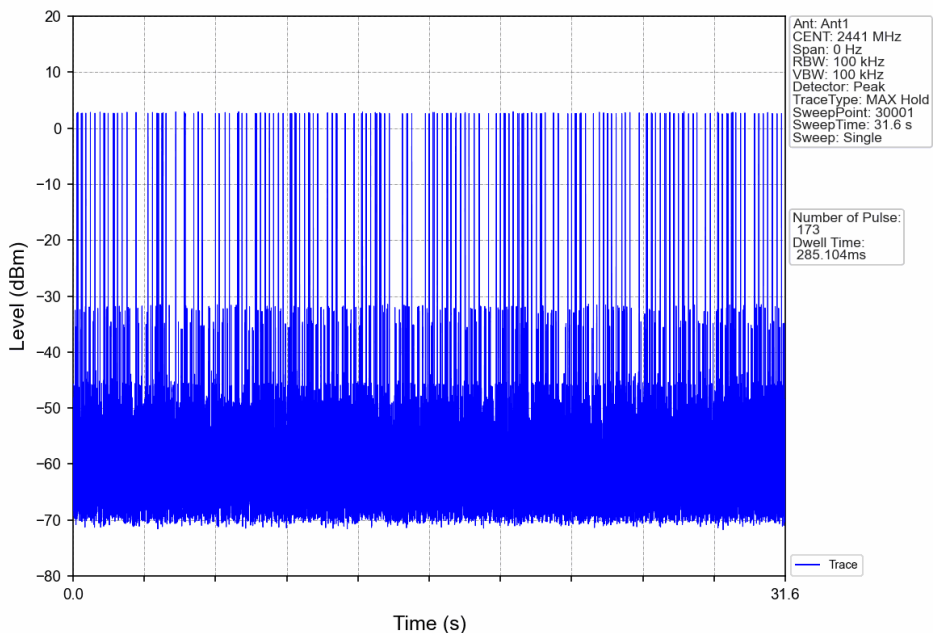
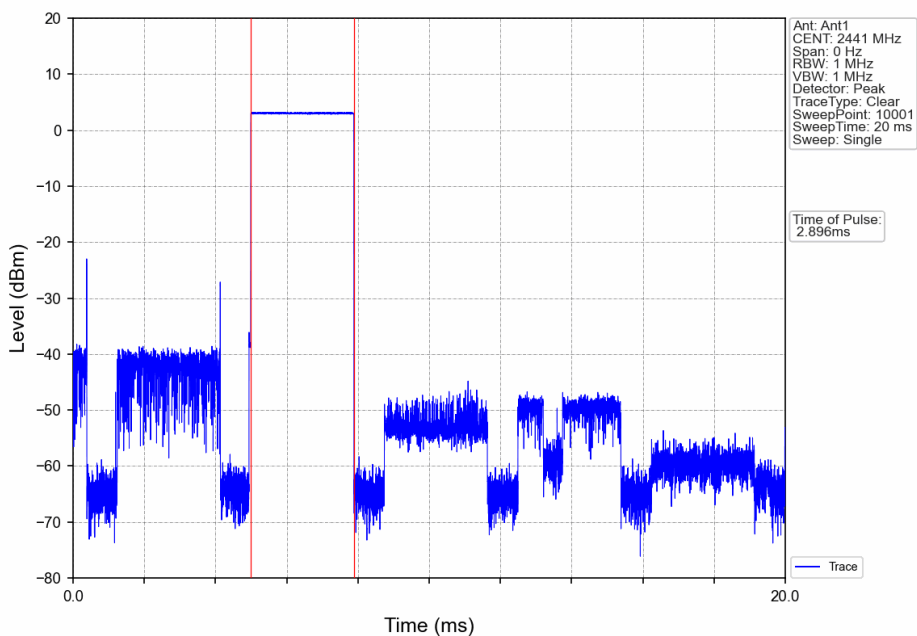


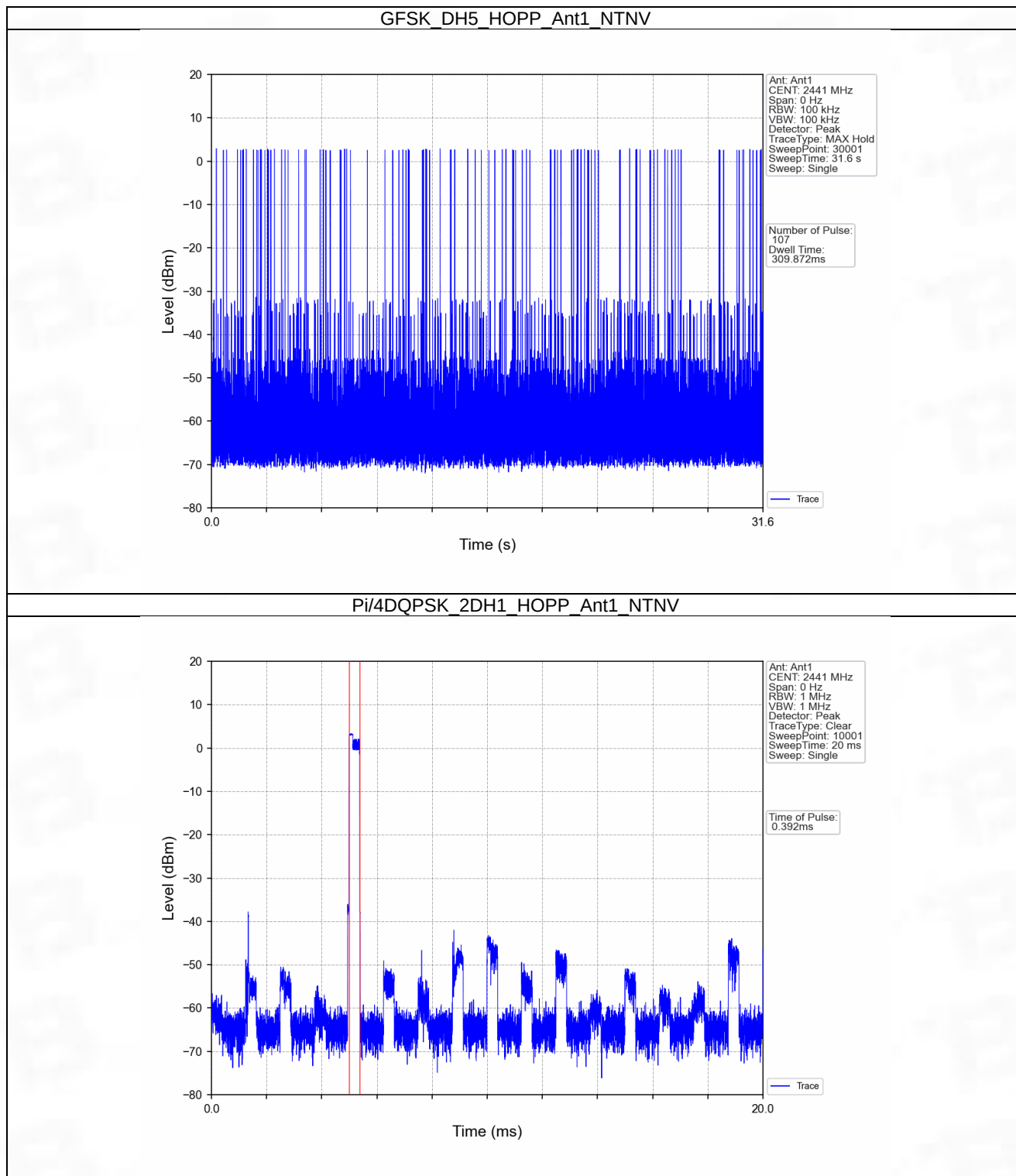


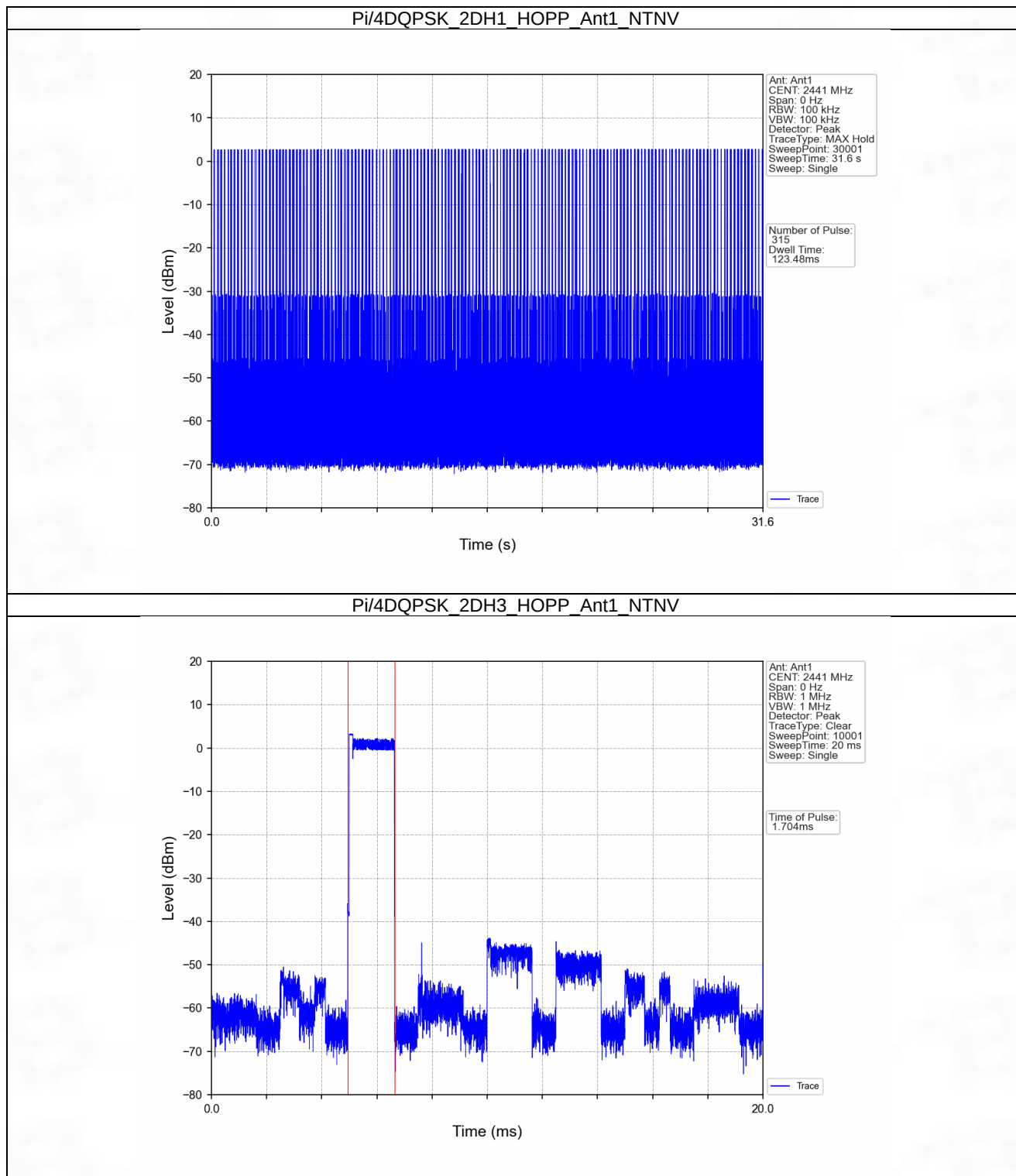
GFSK_DH3_HOPP_Ant1_NTNV

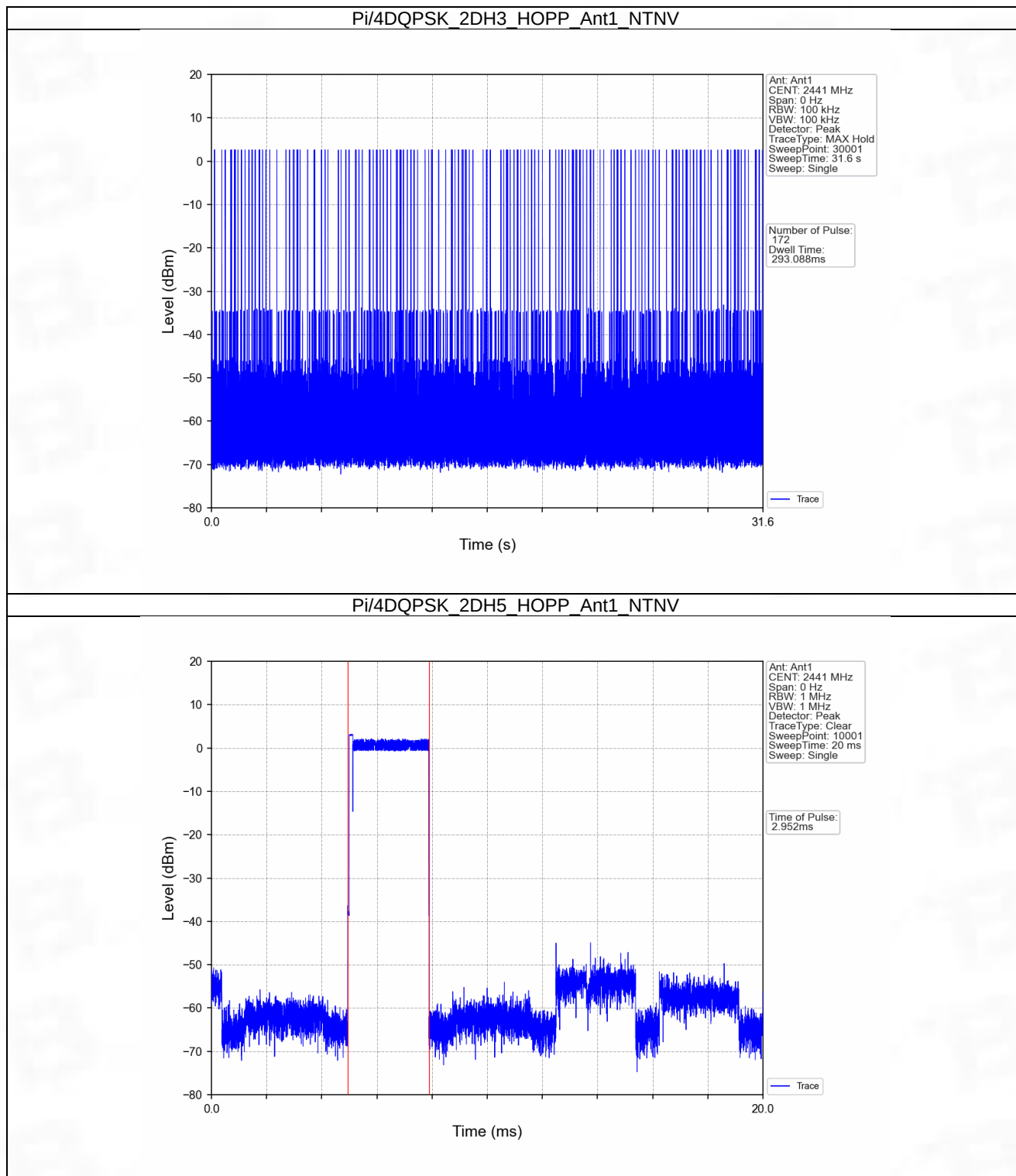


GFSK_DH5_HOPP_Ant1_NTNV

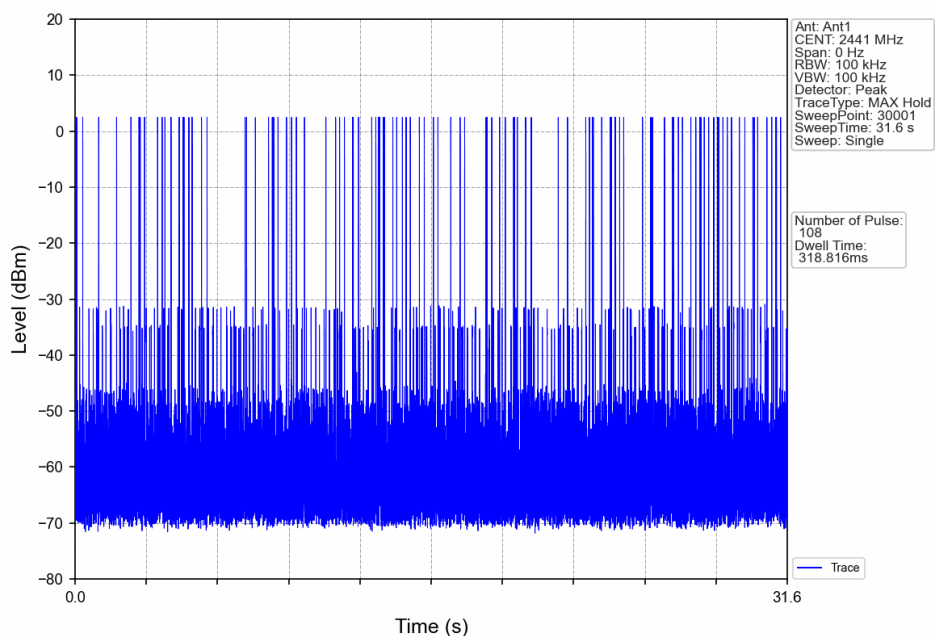




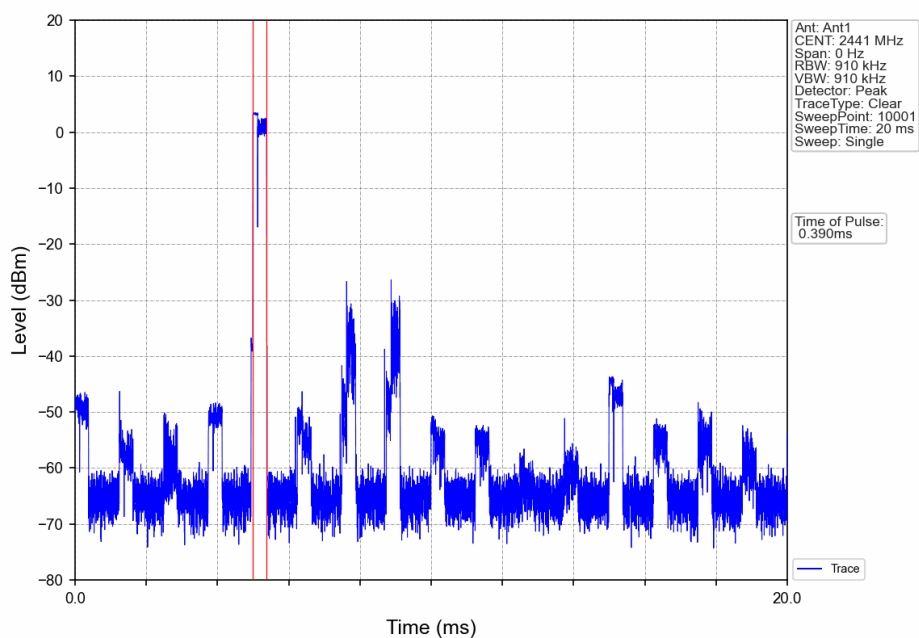




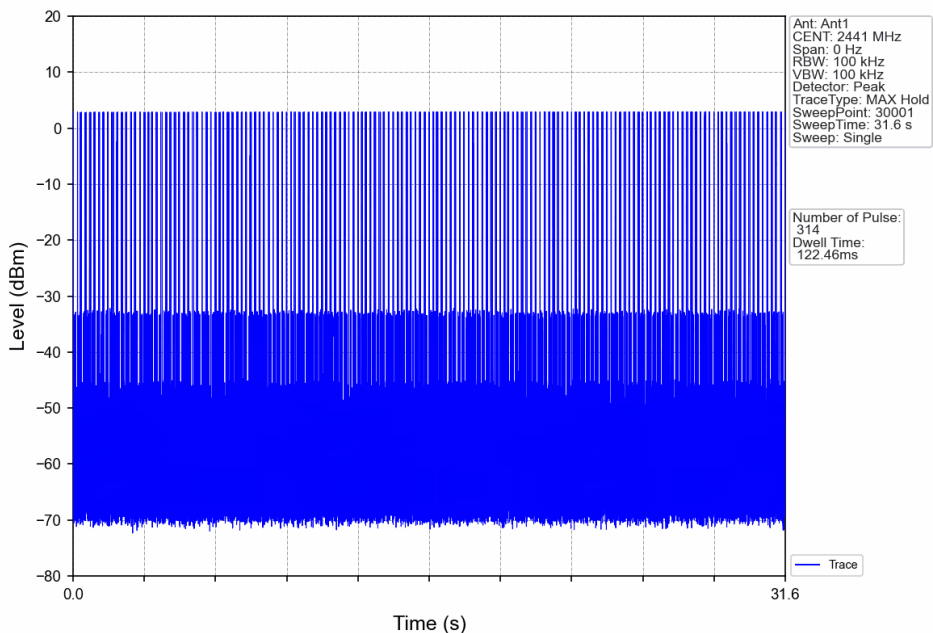
Pi/4DQPSK_2DH5_HOPP_Ant1_NTNV



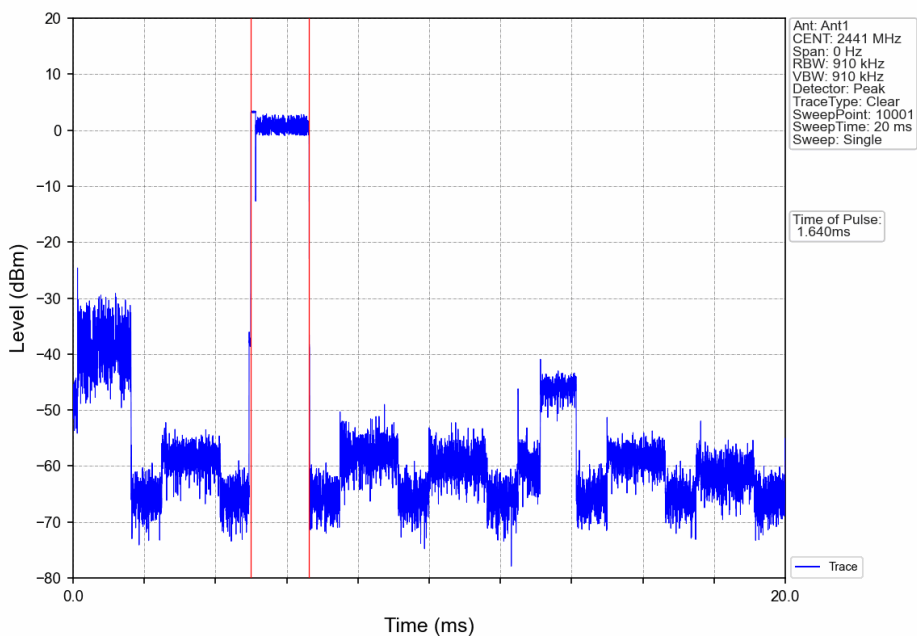
8DPSK_3DH1_HOPP_Ant1_NTNV



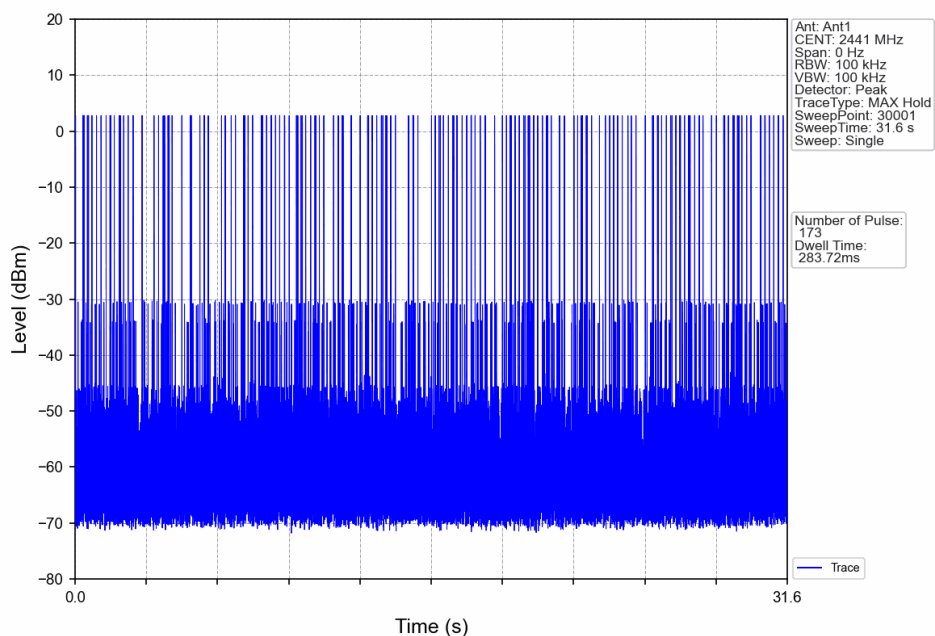
8DPSK_3DH1_HOPP_Ant1_NTNV



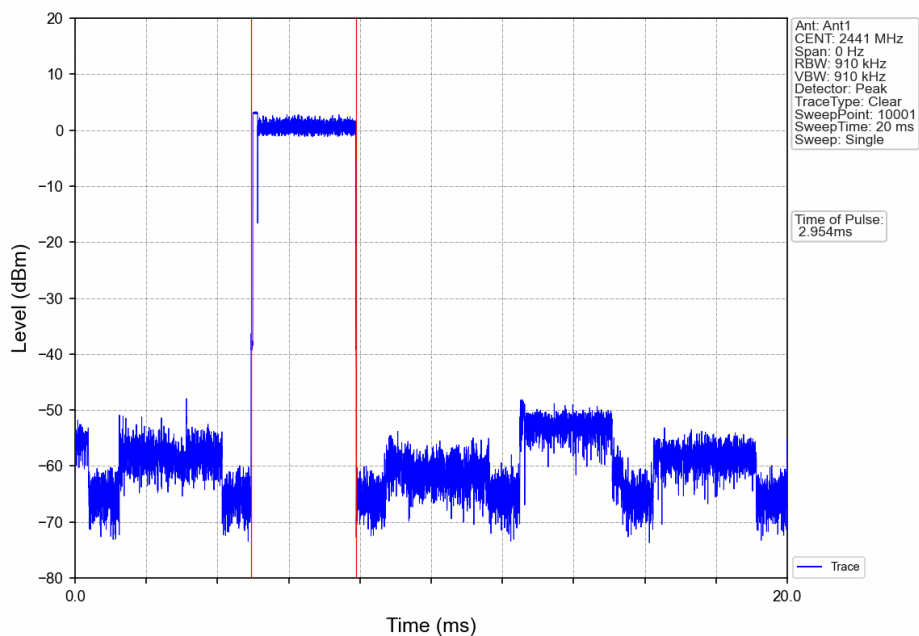
8DPSK_3DH3_HOPP_Ant1_NTNV

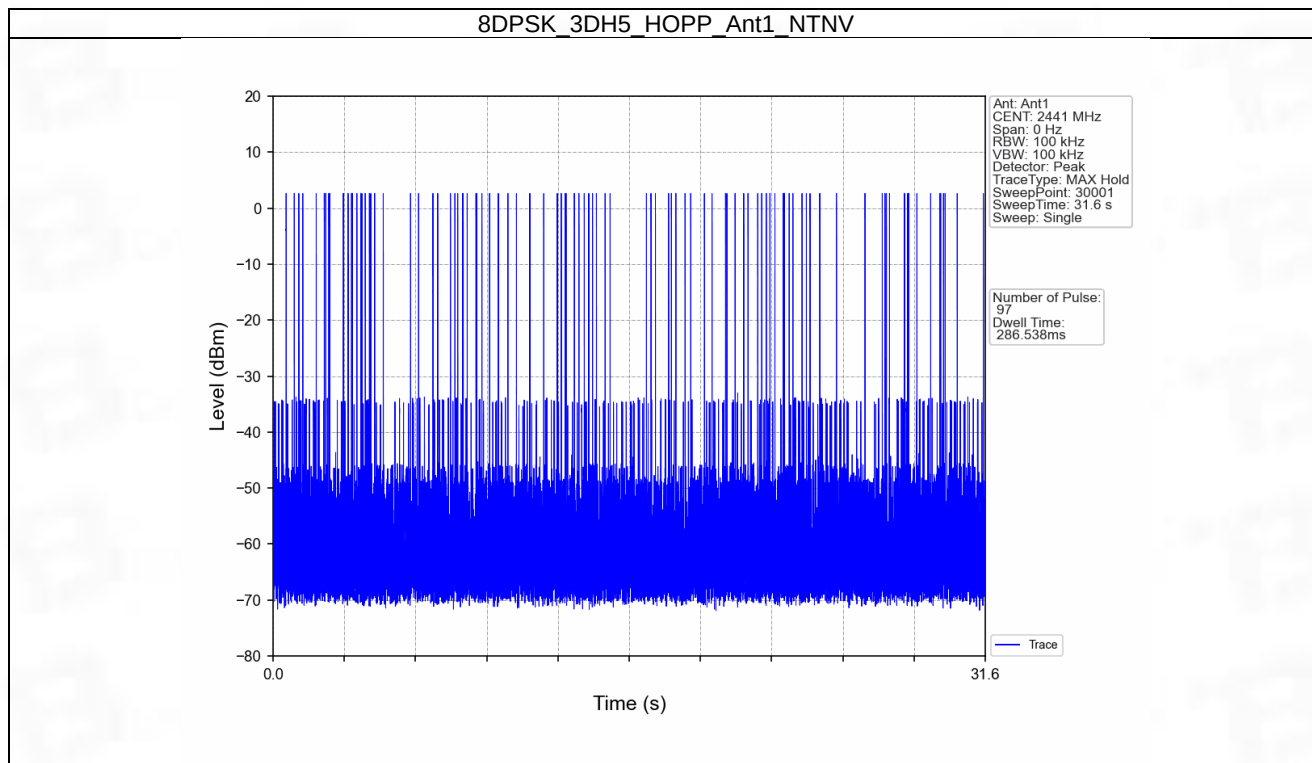


8DPSK_3DH3_HOPP_Ant1_NTNV



8DPSK_3DH5_HOPP_Ant1_NTNV





6. Unwanted Emissions In Non-restricted Frequency Bands

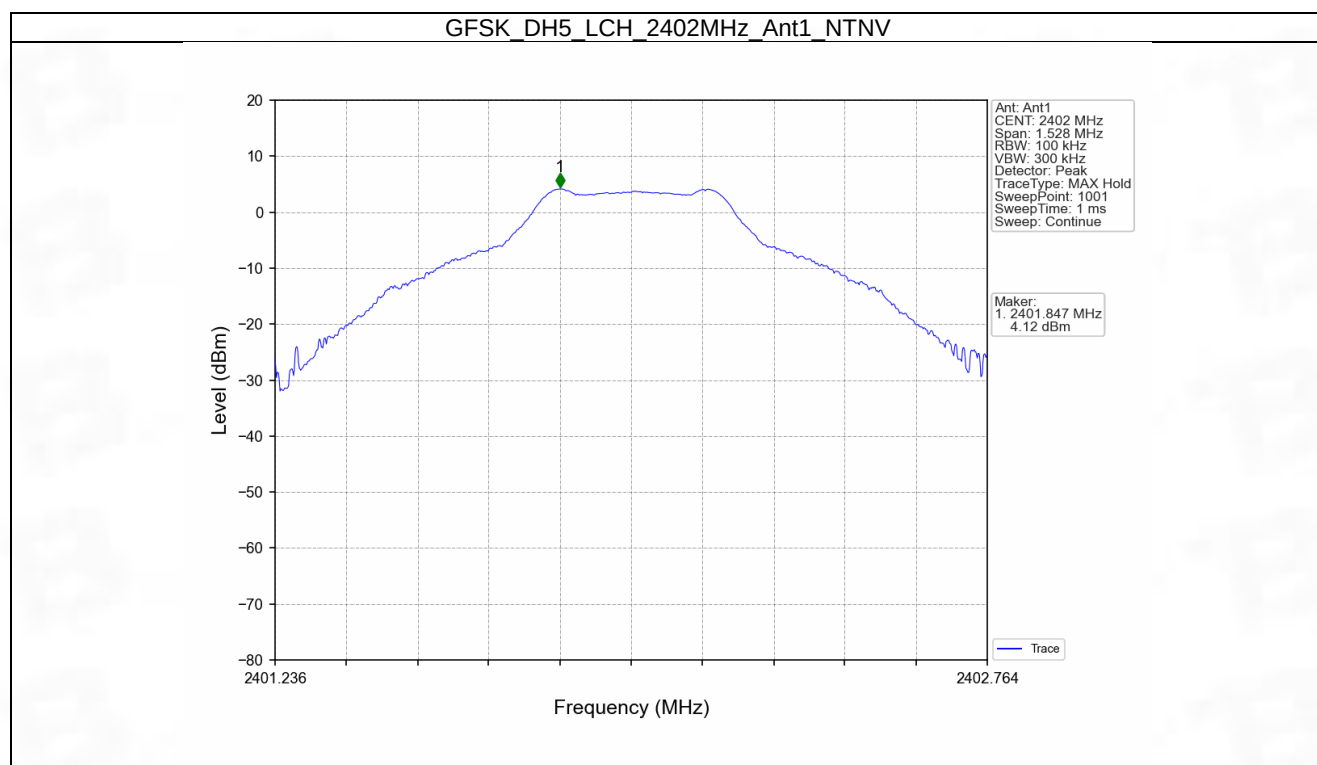
6.1 Ref

6.1.1 Test Result

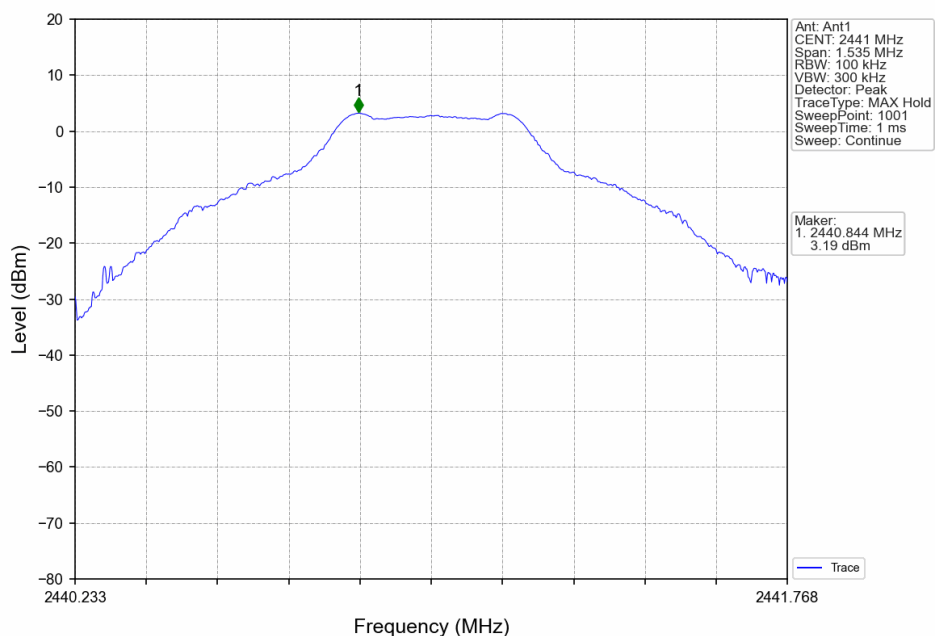
Mode	TX Type	Frequency (MHz)	Packet Type	ANT	Level of Reference (dBm)
GFSK	SISO	2402	DH5	1	4.12
		2441	DH5	1	3.19
		2480	DH5	1	2.09
Pi/4DQPSK	SISO	2402	2DH5	1	4.12
		2441	2DH5	1	3.14
		2480	2DH5	1	2.11
8DPSK	SISO	2402	3DH5	1	4.13
		2441	3DH5	1	3.17
		2480	3DH5	1	2.11

Note1: Refer to FCC Part 15.247 (d) and ANSI C63.10-2013, the channel contains the maximum PSD level was used to establish the reference level.

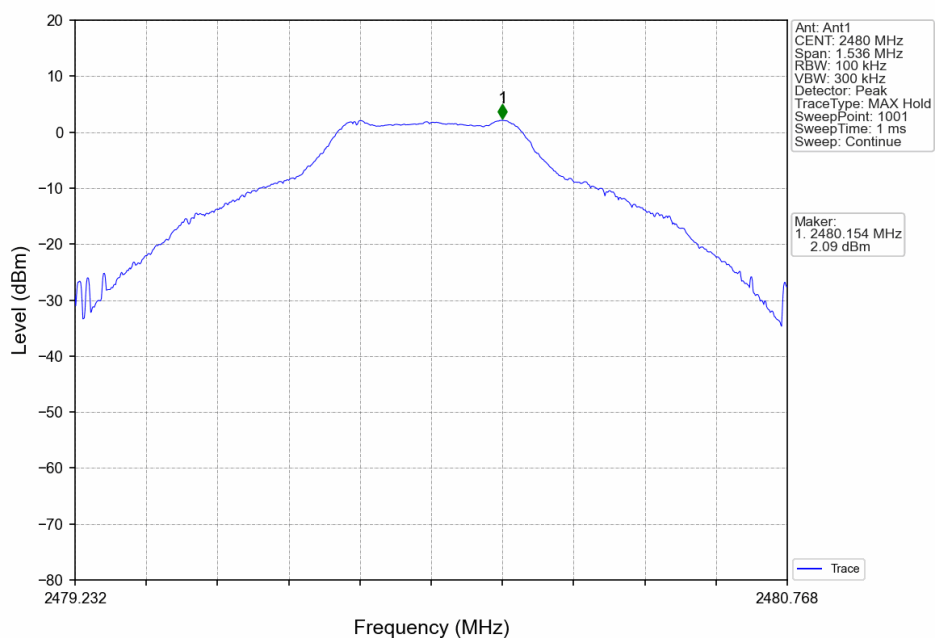
6.1.2 Test Graph



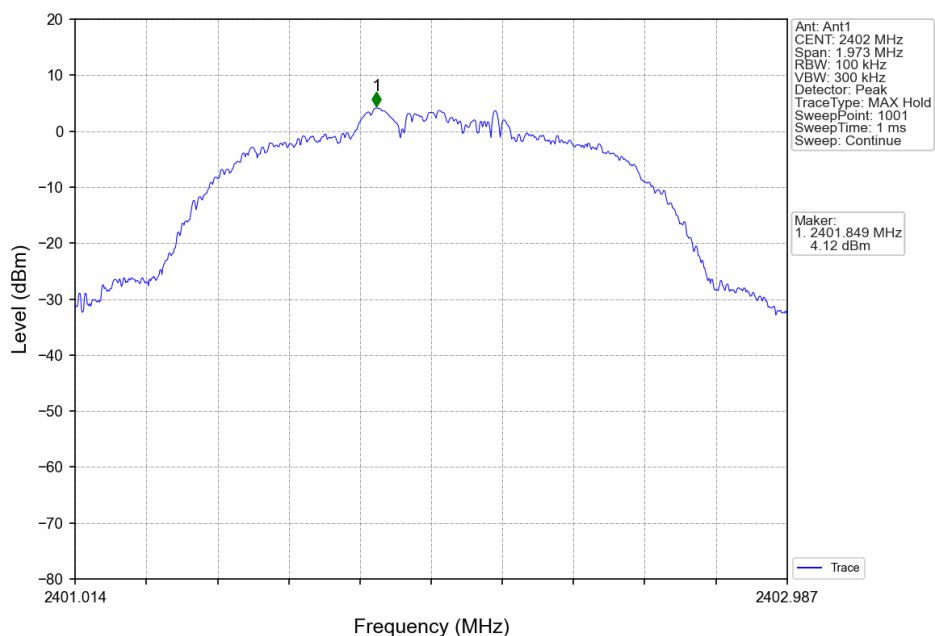
GFSK_DH5_MCH_2441MHz_Ant1_NTNV



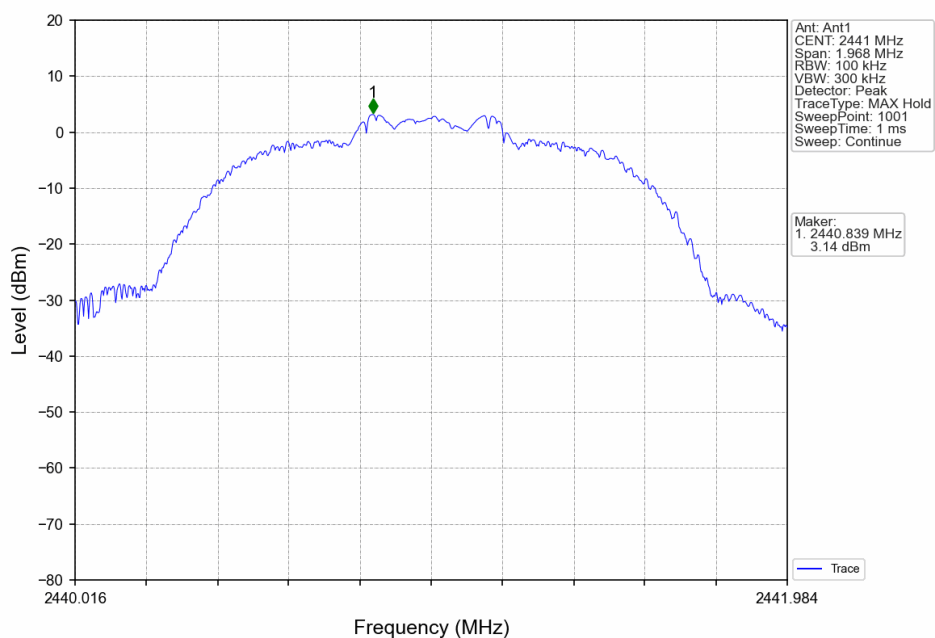
GFSK_DH5_HCH_2480MHz_Ant1_NTNV



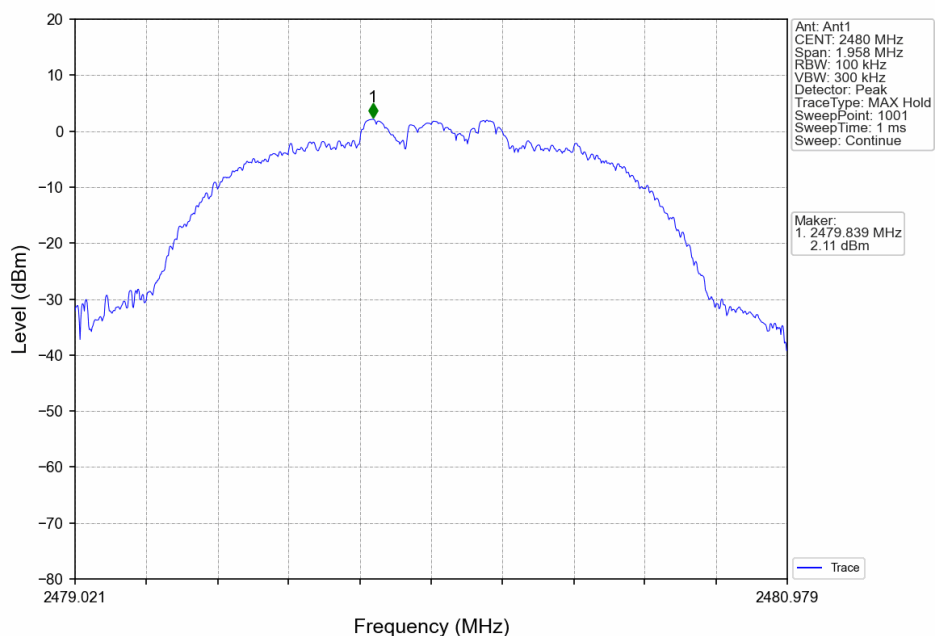
Pi/4DQPSK_2DH5_LCH_2402MHz_Ant1_NTNV



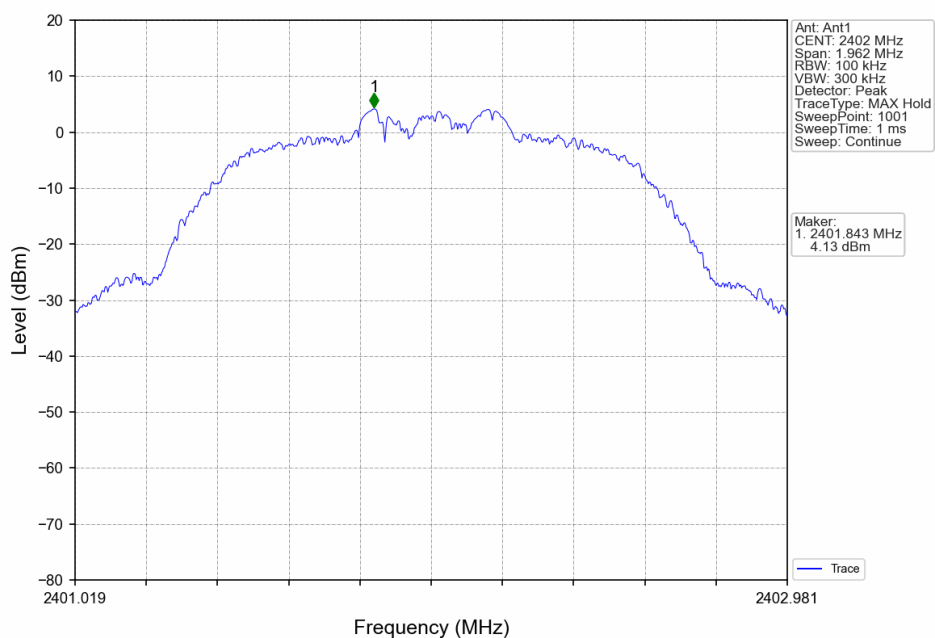
Pi/4DQPSK_2DH5_MCH_2441MHz_Ant1_NTNV



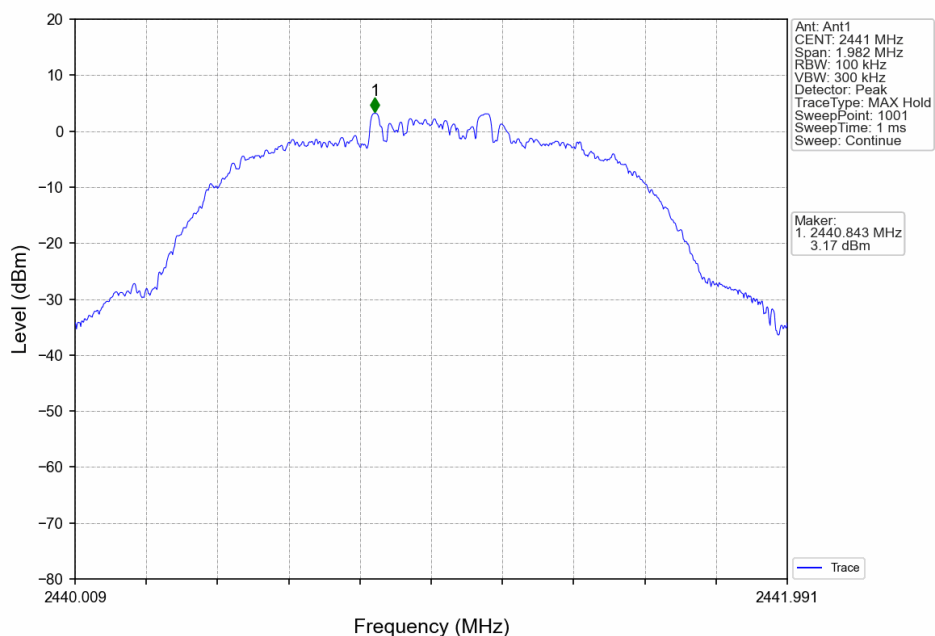
Pi/4DQPSK_2DH5_HCH_2480MHz_Ant1_NTNV



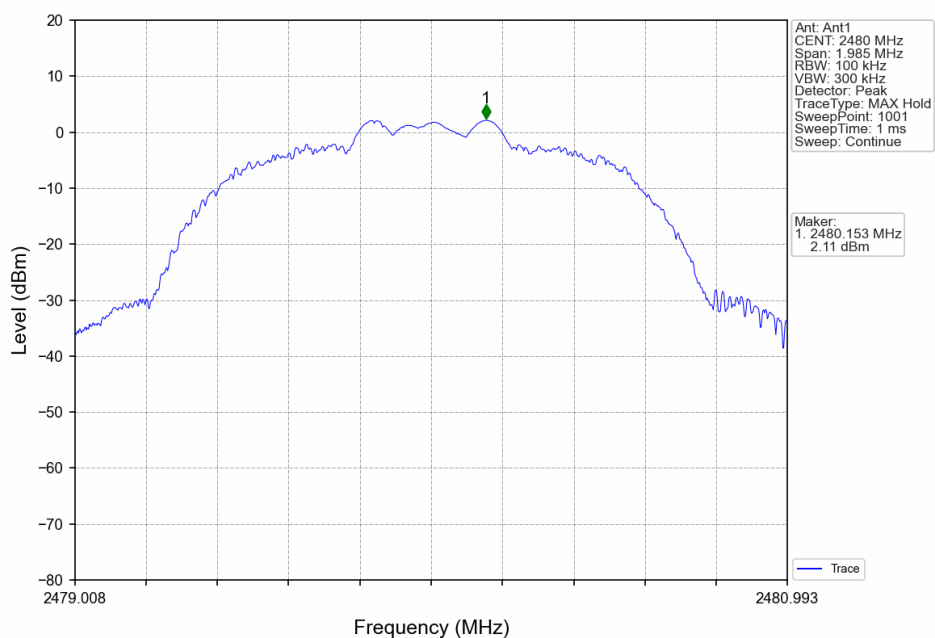
8DPSK_3DH5_LCH_2402MHz_Ant1_NTNV



8DPSK_3DH5_MCH_2441MHz_Ant1_NTNV



8DPSK_3DH5_HCH_2480MHz_Ant1_NTNV



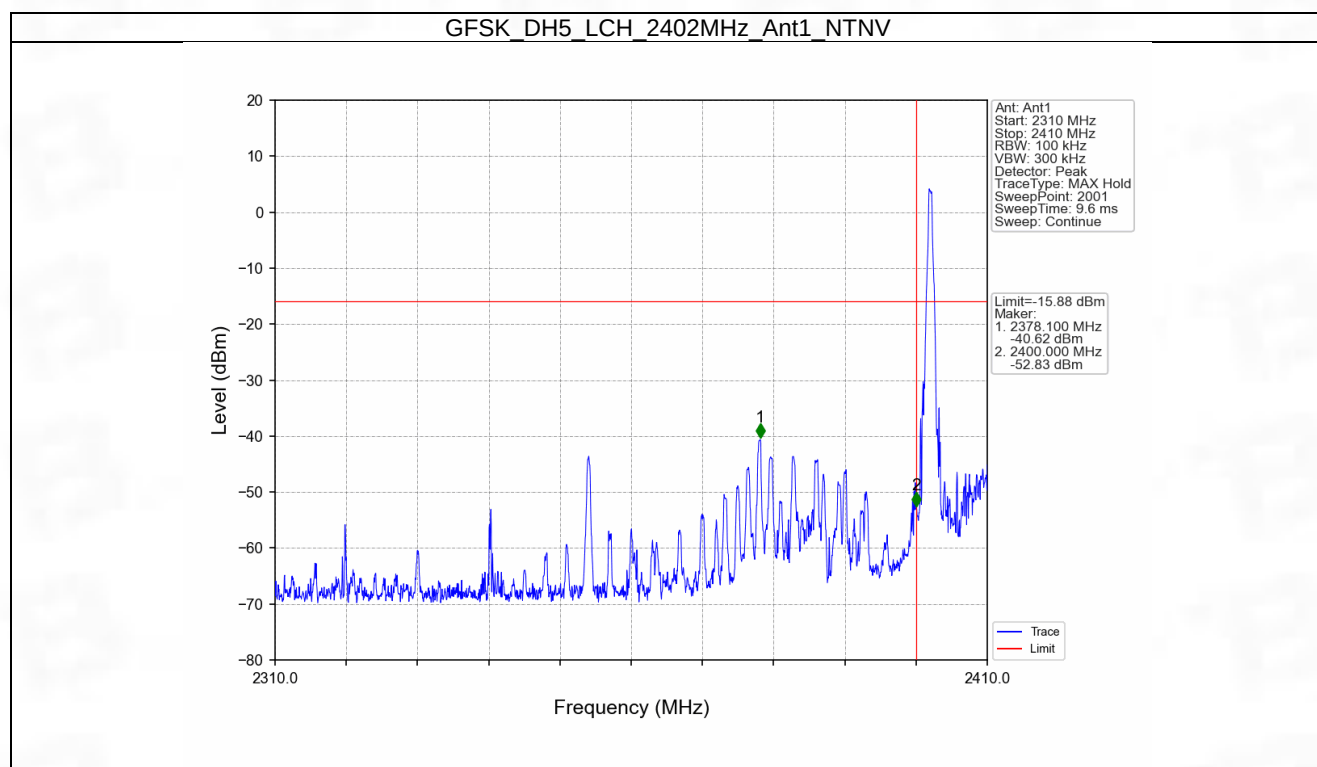
6.2 CSE

6.2.1 Test Result

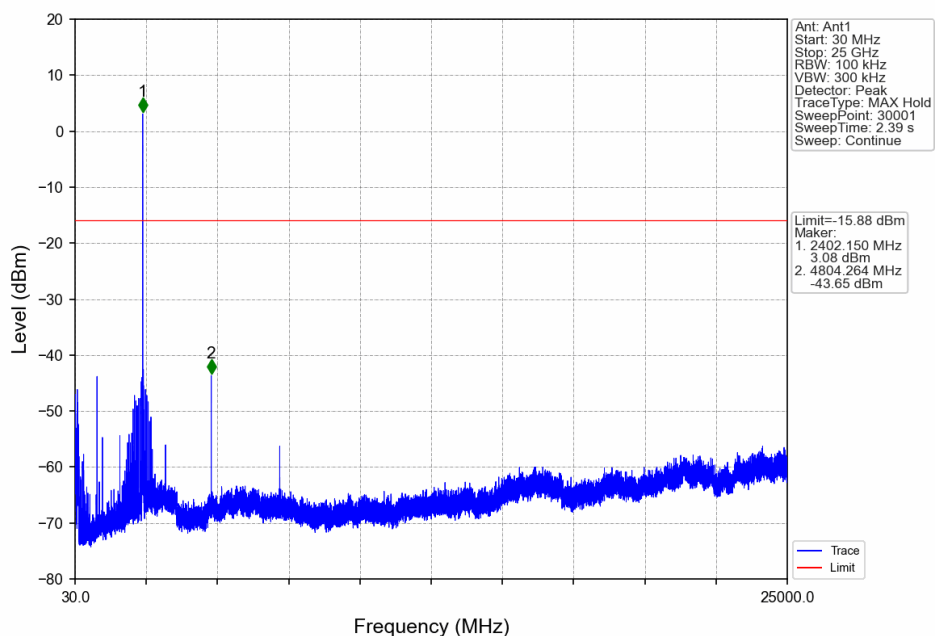
Mode	TX Type	Frequency (MHz)	Packet Type	ANT	Level of Reference (dBm)	Limit (dBm)	Verdict
GFSK	SISO	2402	DH5	1	4.12	-15.88	Pass
		2441	DH5	1	4.12	-15.88	Pass
		2480	DH5	1	4.12	-15.88	Pass
		HOPP	DH5	1	4.12	-15.88	Pass
Pi/4DQPSK	SISO	2402	2DH5	1	4.12	-15.88	Pass
		2441	2DH5	1	4.12	-15.88	Pass
		2480	2DH5	1	4.12	-15.88	Pass
		HOPP	2DH5	1	4.12	-15.88	Pass
8DPSK	SISO	2402	3DH5	1	4.13	-15.87	Pass
		2441	3DH5	1	4.13	-15.87	Pass
		2480	3DH5	1	4.13	-15.87	Pass
		HOPP	3DH5	1	4.13	-15.87	Pass

Note1: Refer to FCC Part 15.247 (d) and ANSI C63.10-2013, the channel contains the maximum PSD level was used to establish the reference level.

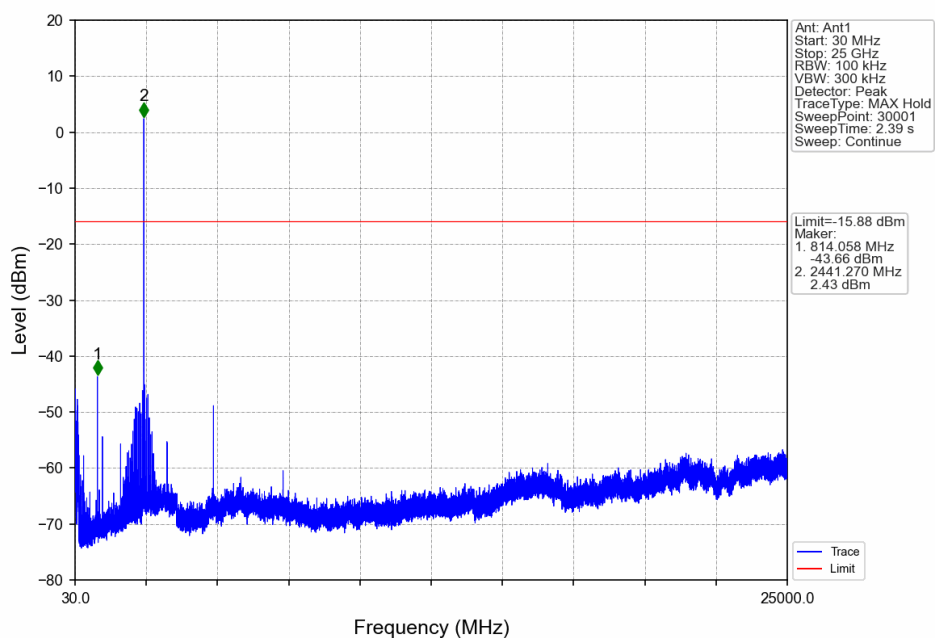
6.2.2 Test Graph



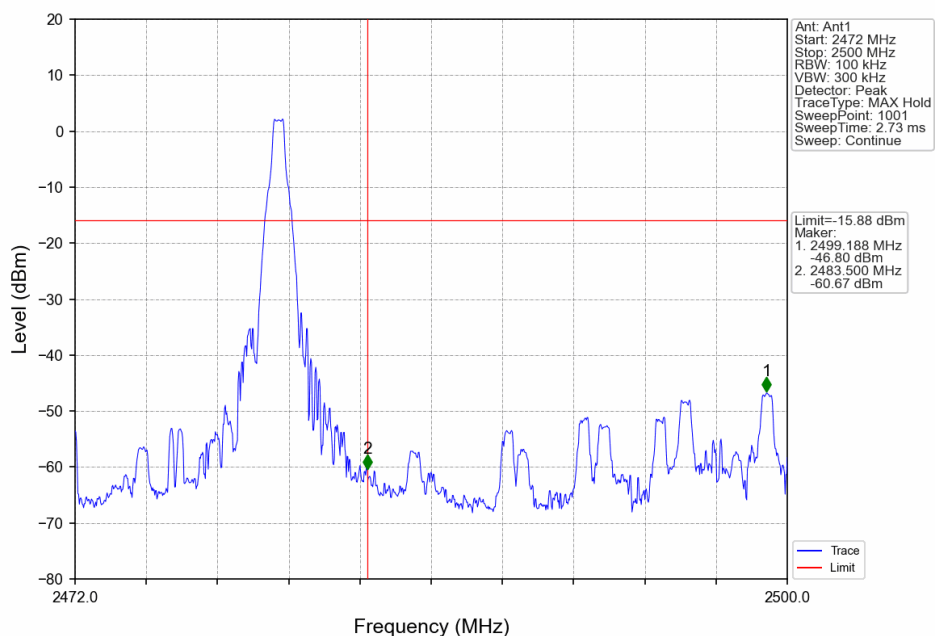
GFSK_DH5_LCH_2402MHz_Ant1_NTNV



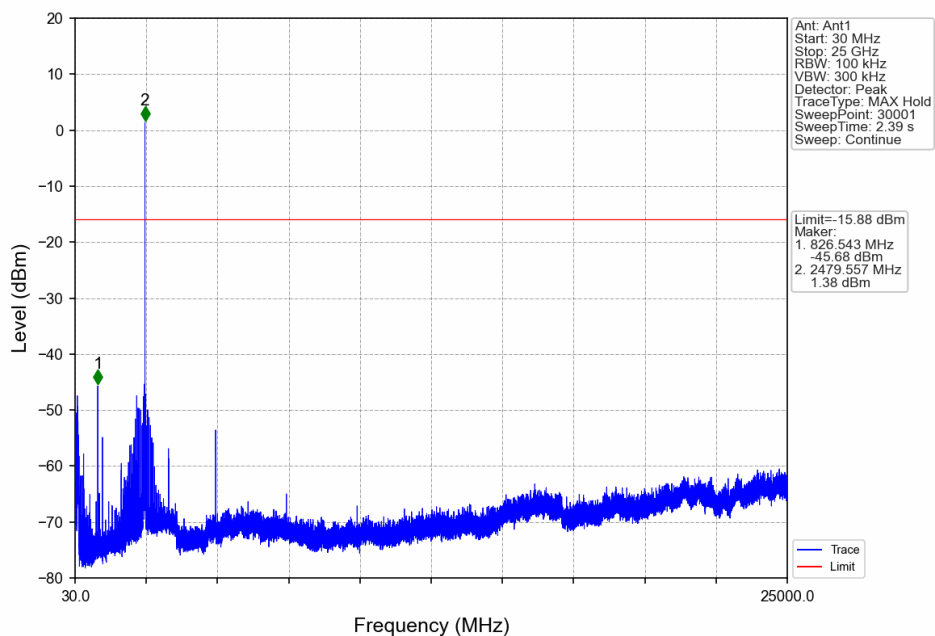
GFSK_DH5_MCH_2441MHz_Ant1_NTNV

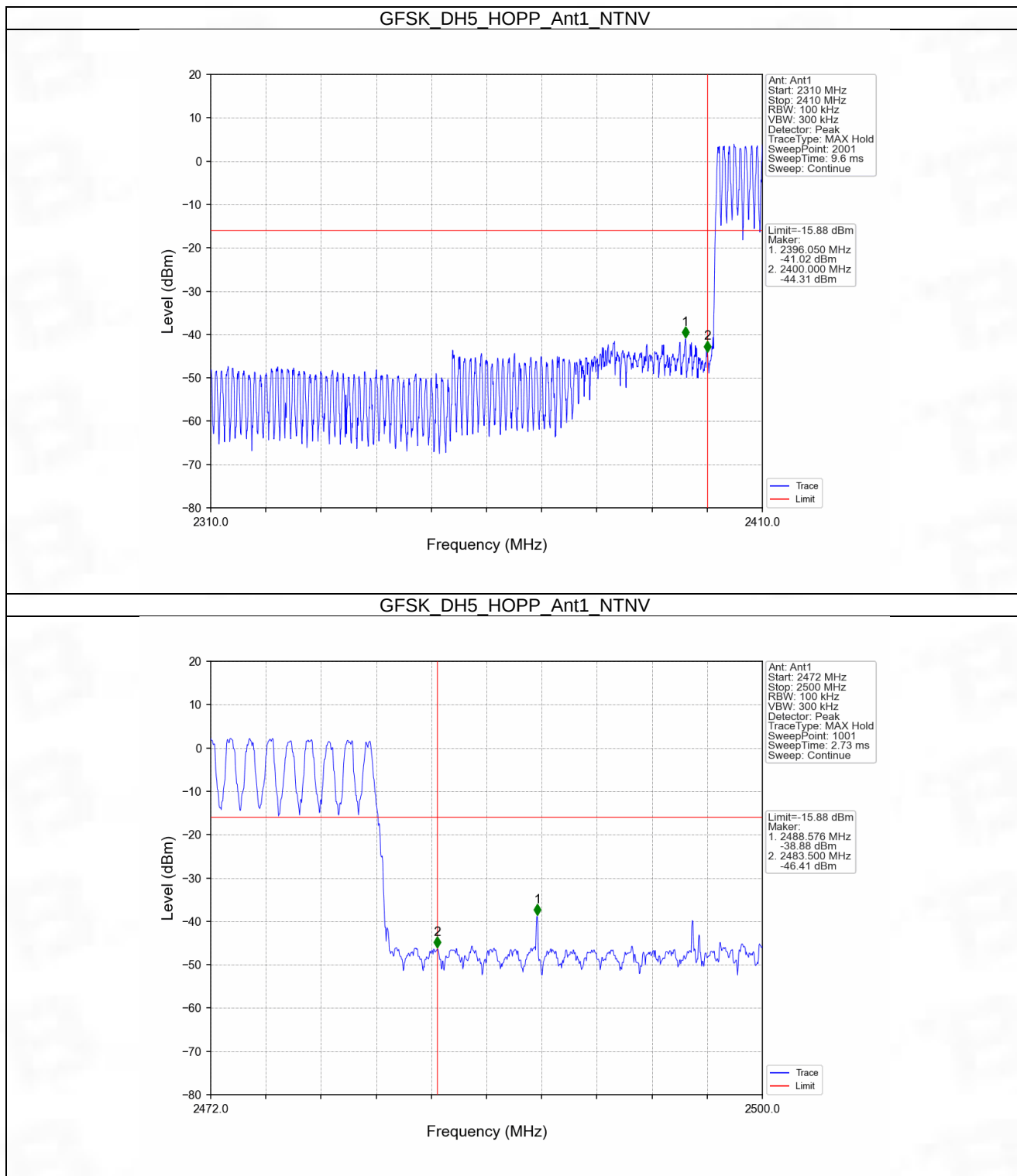


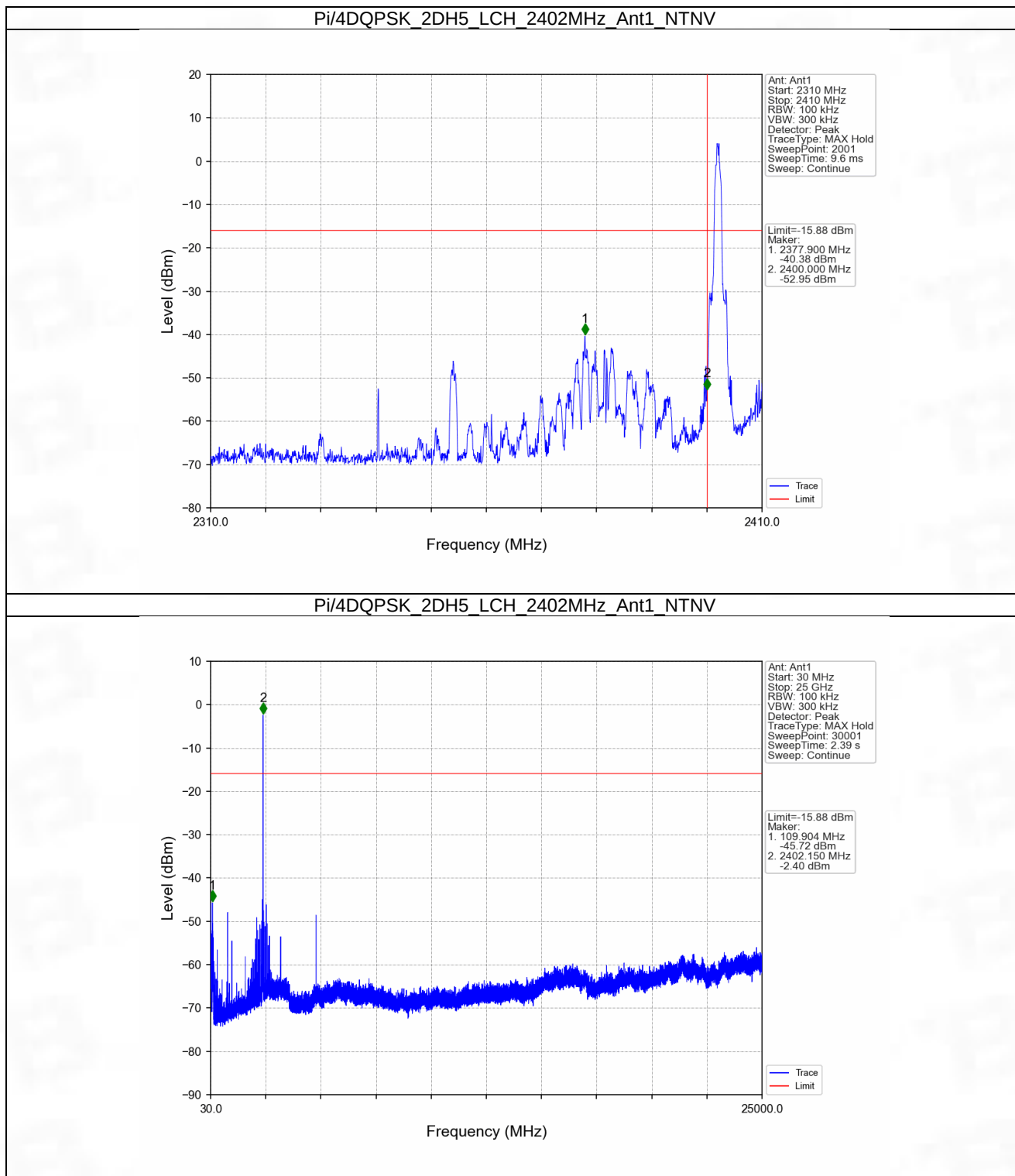
GFSK_DH5_HCH_2480MHz_Ant1_NTNV



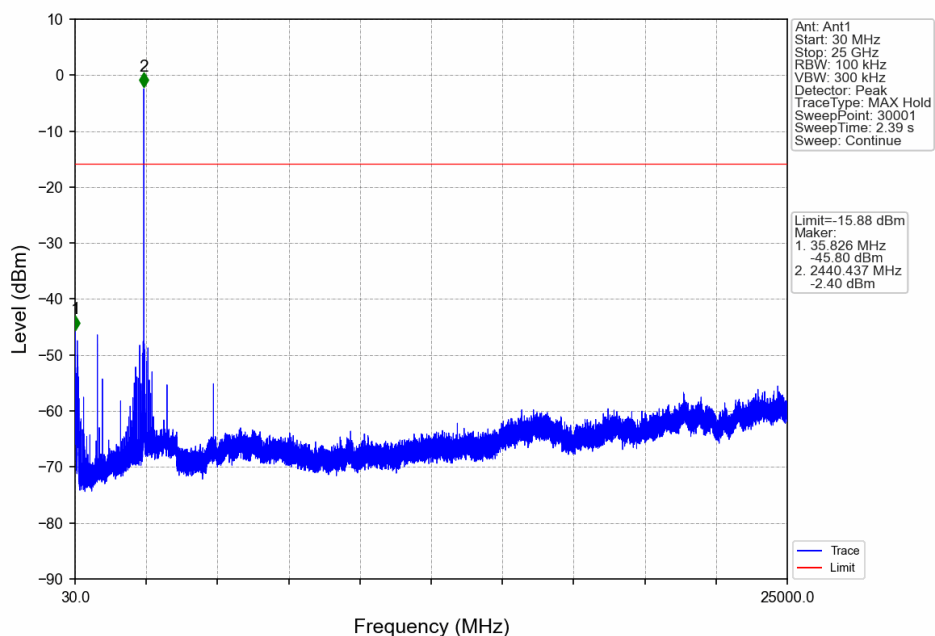
GFSK_DH5_HCH_2480MHz_Ant1_NTNV



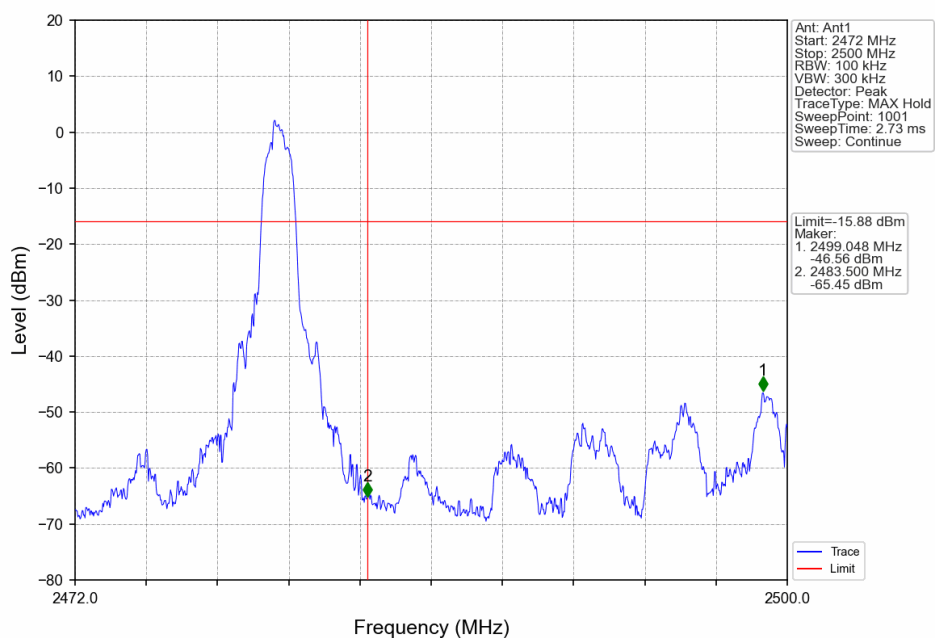


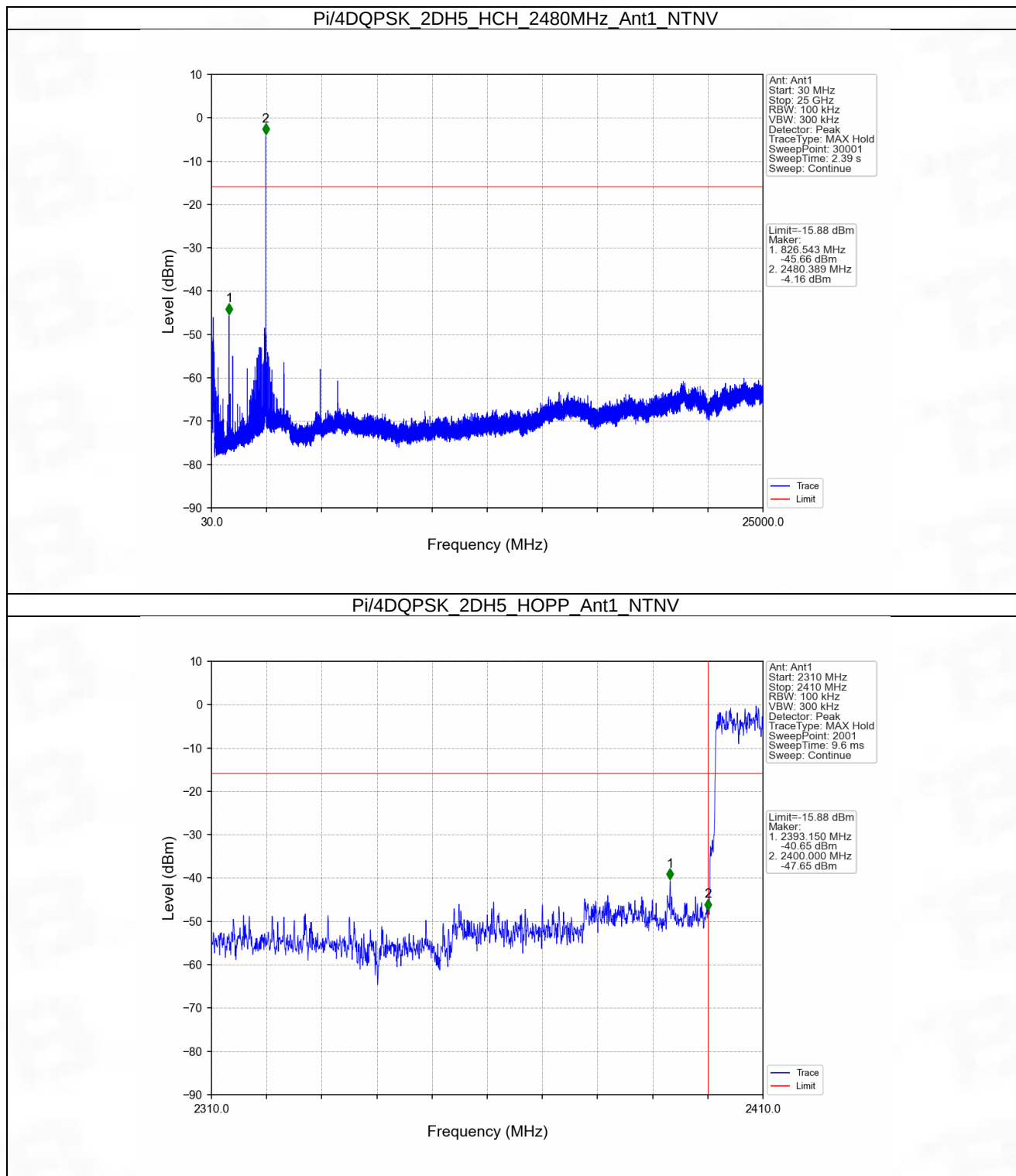


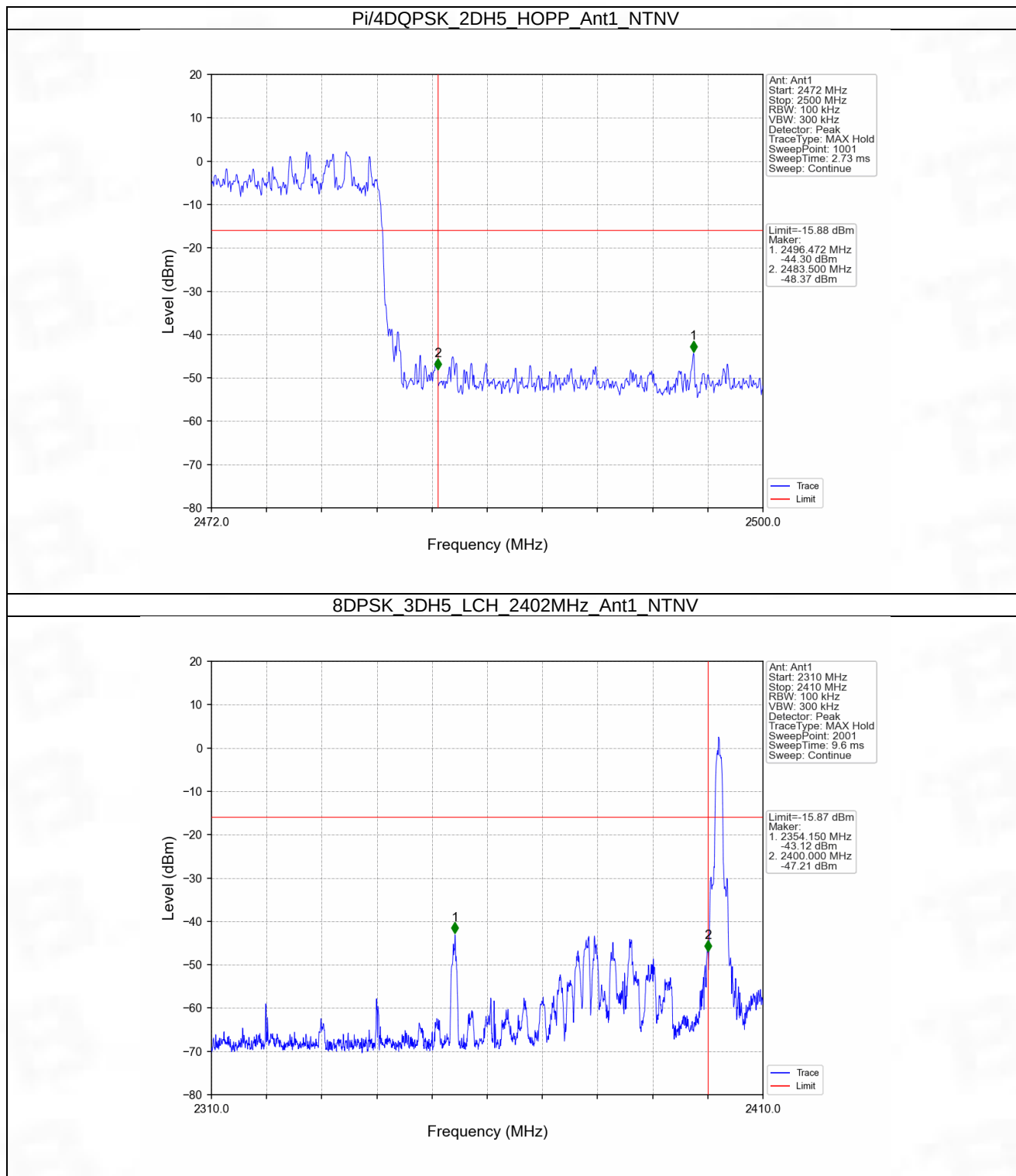
Pi/4DQPSK_2DH5_MCH_2441MHz_Ant1_NTNV



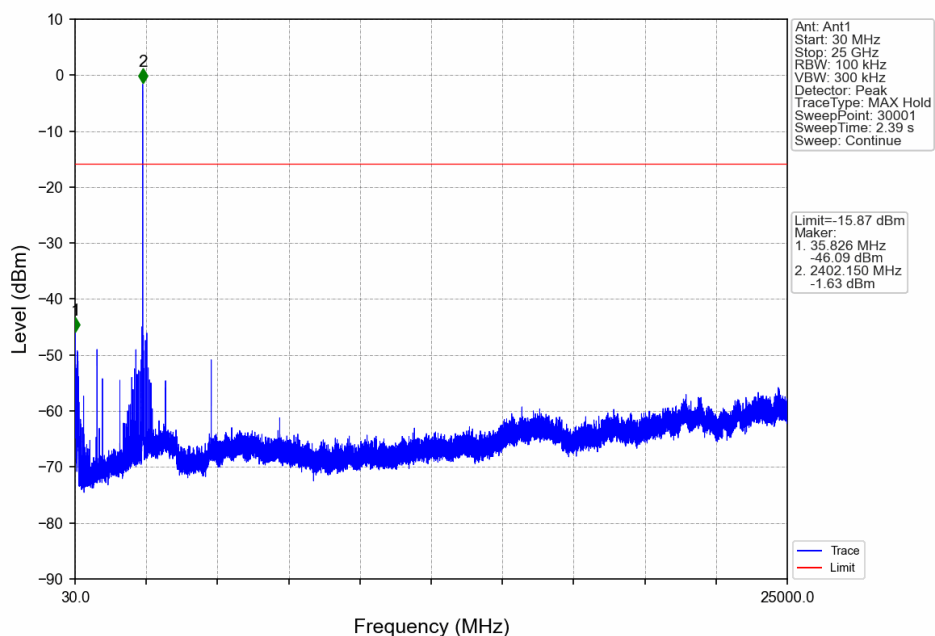
Pi/4DQPSK_2DH5_HCH_2480MHz_Ant1_NTNV



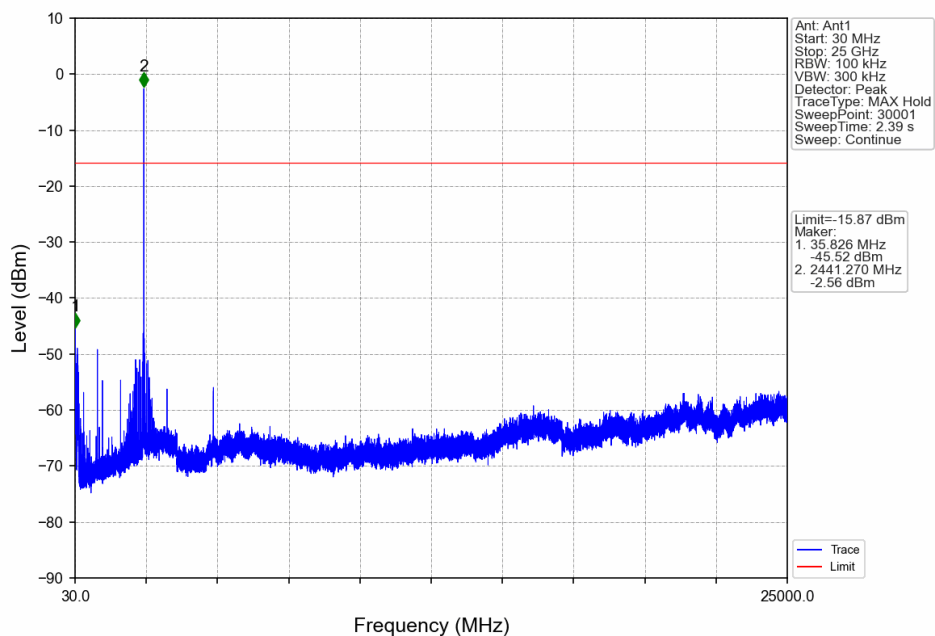




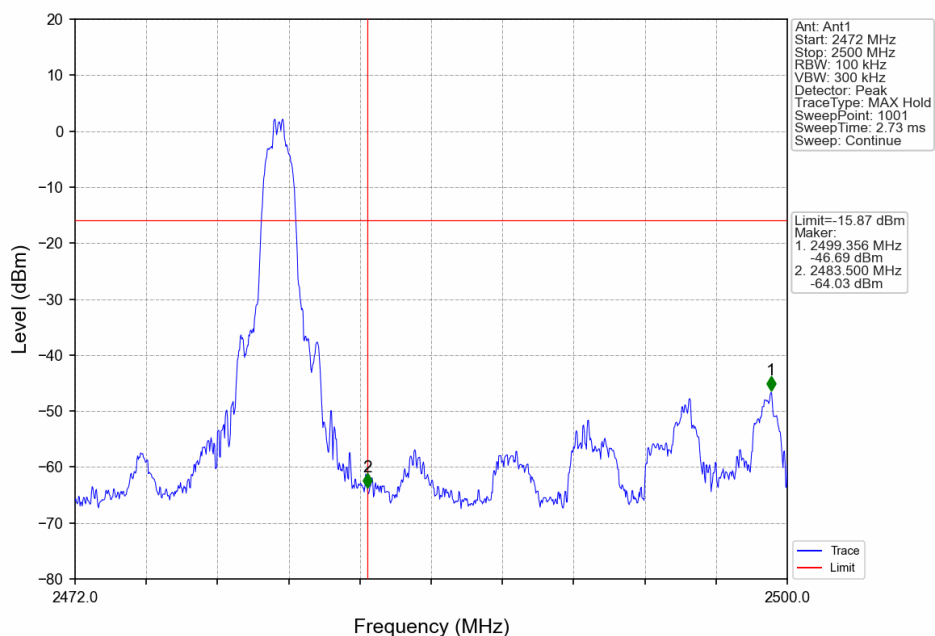
8DPSK_3DH5_LCH_2402MHz_Ant1_NTNV



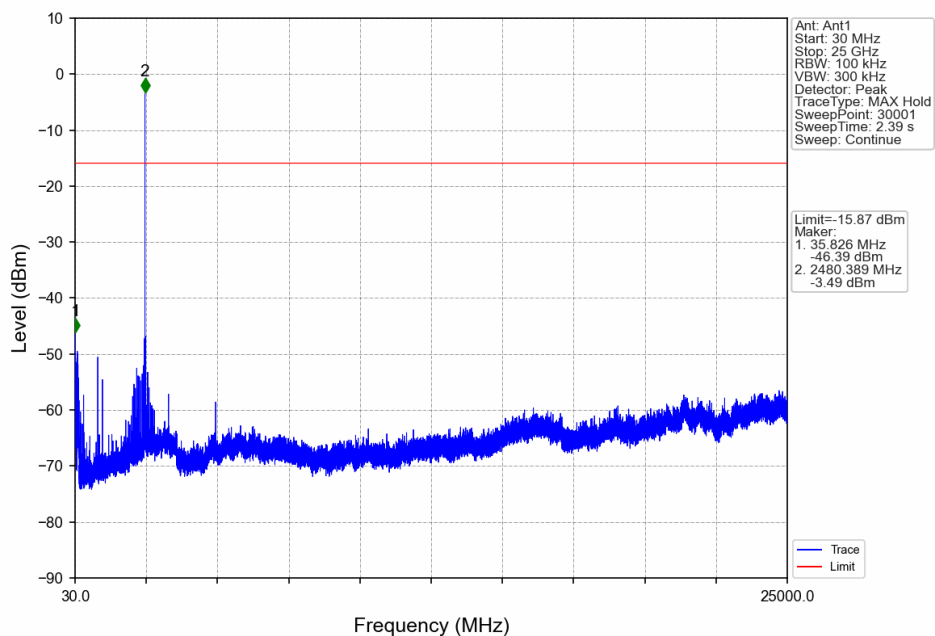
8DPSK_3DH5_MCH_2441MHz_Ant1_NTNV

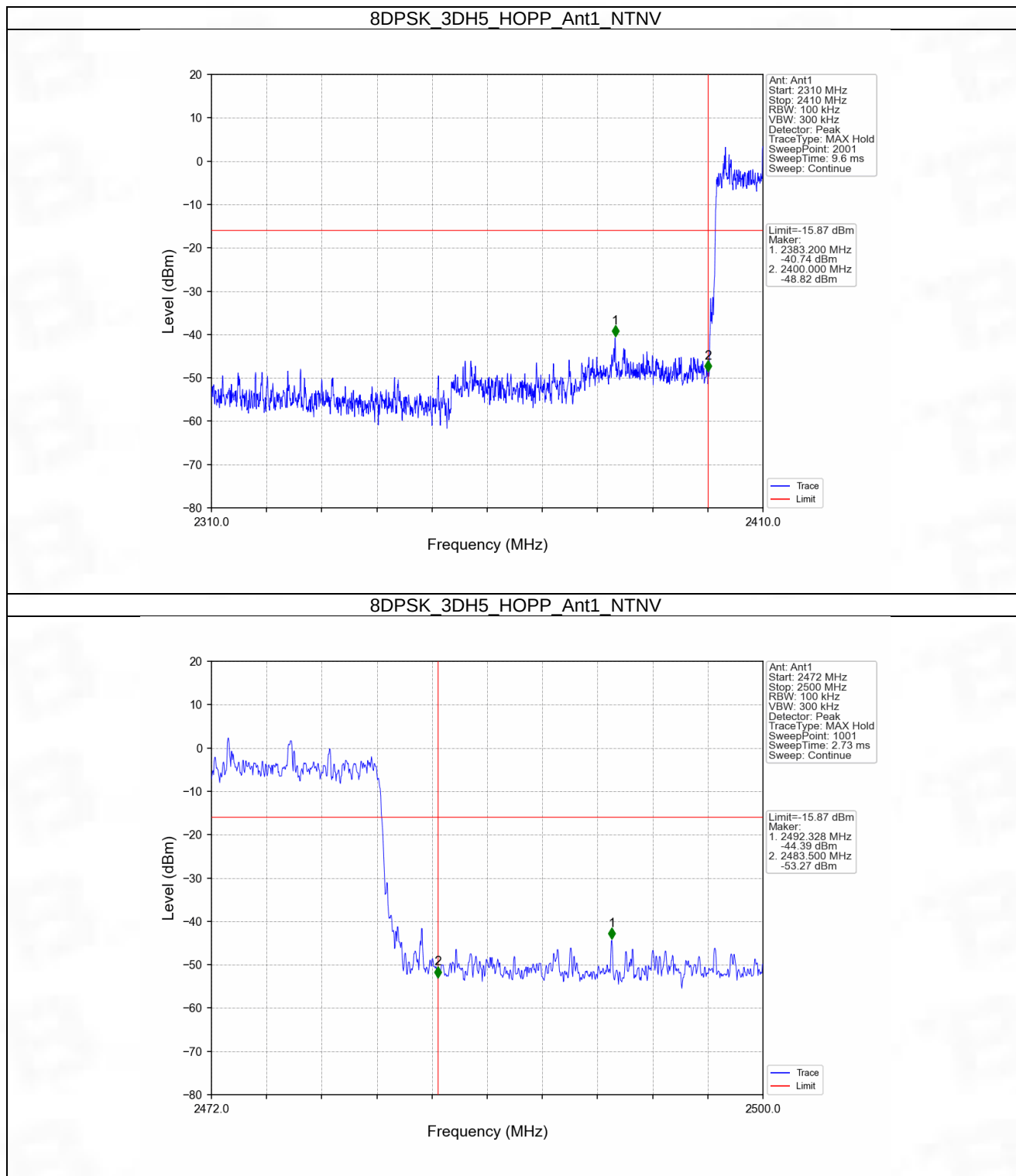


8DPSK_3DH5_HCH_2480MHz_Ant1_NTNV



8DPSK_3DH5_HCH_2480MHz_Ant1_NTNV





7. Form731

7.1 Form731

7.1.1 Test Result

Lower Freq (MHz)	High Freq (MHz)	MAX Power (W)	MAX Power (dBm)
2402	2480	0.0030	4.81



Test Report Number: BTF-SZ230301R-007



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-- END OF REPORT --