October 26, 1970

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Signal processing with digital filters 78
Ten ways to use d-a converters 91
Coping with feedthrough in emitter-coupled logic 98

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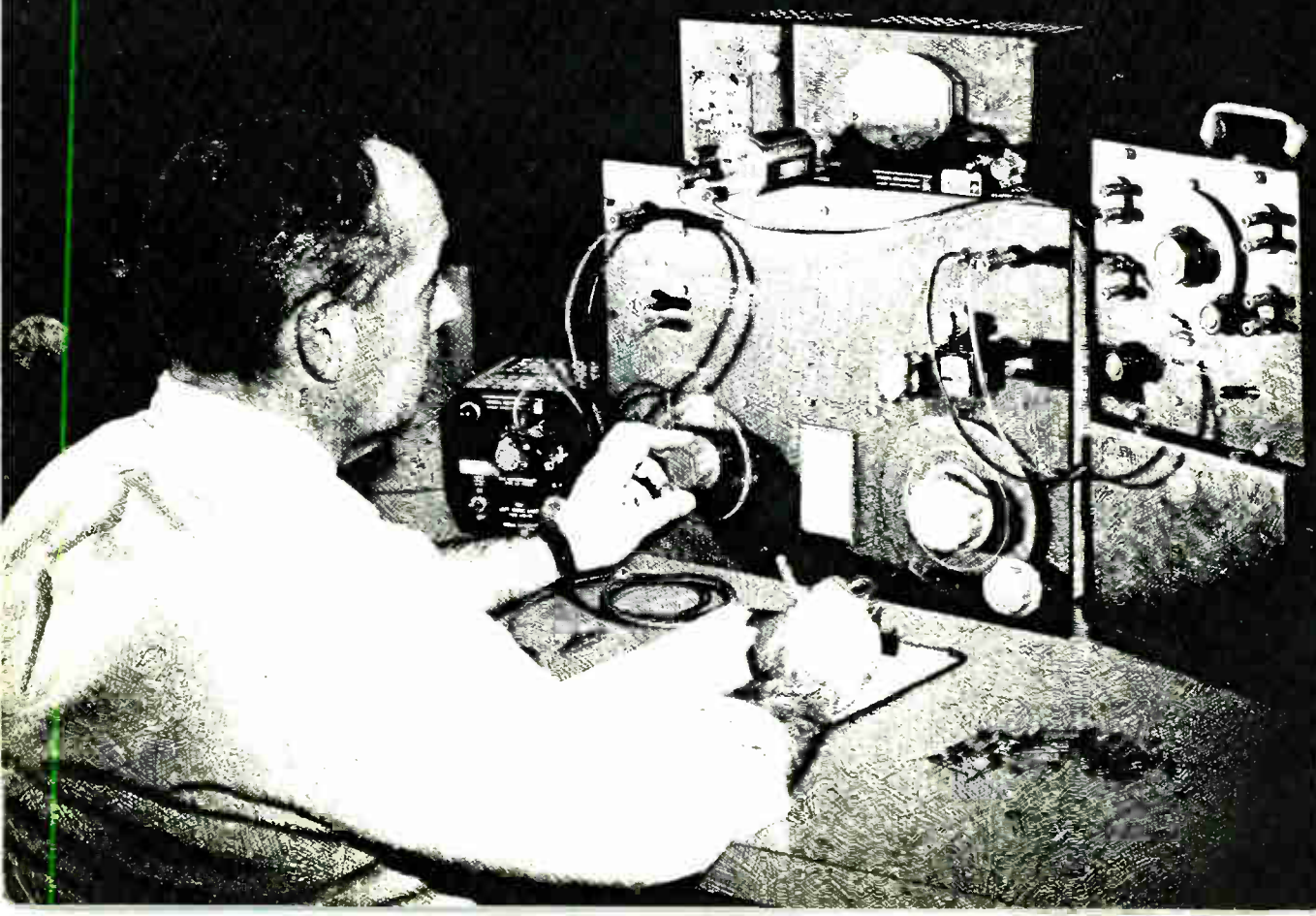

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06005

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DATA ACQUISITION SYSTEMS

Circle 2 on reader service card

Electronics

Volume No. 43, Number 22 October 26, 1970

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About this issue

All the pitched battles were in Cambodia when Arthur Erikson, managing editor, international, was making his reporting rounds in Saigon and Da Nang for the Vietnam report [page 70]. But the Viet Cong managed to demonstrate for Erikson that it can still deliver isolated wallops anywhere in South Vietnam, even at big U.S. bases.

"The first time," Erikson writes, "it was mildly diverting. I was at Da Nang talking old times with a Los Angeles Timesman I'd known in Europe. Suddenly we heard a far-off tat-tat-tat of machine-guns, and the pulump of artillery.

"He'd been at Da Nang several days and the change of background noise didn't seem to faze him. But I went outside to see if there was anything to see. There was. Salvos of star shells lit up a spit of land across the bay, where the Viet Cong apparently had probed somebody's perimeter.

"It was a bizarre scene, a vignette of the bizarre war in Vietnam. The press compound is right on the bay and the Army shows a movie outdoors there. The star shells kept blossoming in the sky for about an hour, but the press people paid no attention while the show was on. A half-mile away, people were getting shot at.

"The second time, they were much less blase. Later that evening the half-dozen reporters there had drifted into the quarters of the resident Marine Corps combat artist, where a party was under way. A Marine lieutenant had launched into an emotional account of how his platoon had lost several men in ambushes. Then a solid whump shook the compound. I hit the deck a millisecond after the Marine lieutenant, but after all he's had more training at that sort of thing.

"The next day we learned the Viet Cong had fired a salvo of rockets, presumably at the air base about a mile-and-a-half away. None had come close to the target, the Army insisted, but one of the strays had smacked in about 200 yards from the compound."

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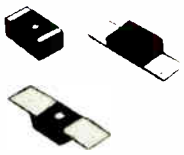

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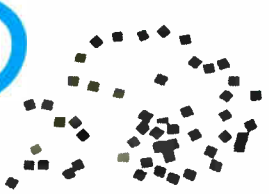

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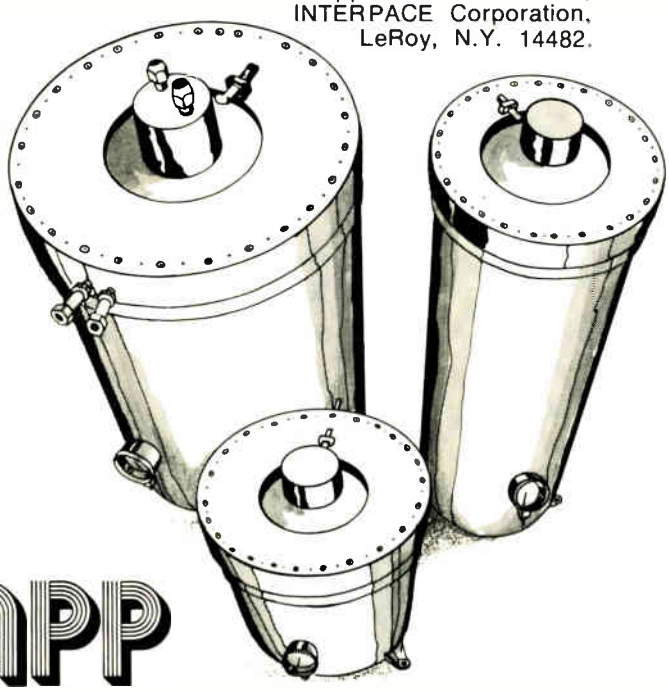

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Readers comment

Making contact

To the Editor: I read with interest the August 31 edition which carried Stephen Scrupski's article "Point-to-point wiring gains new popularity among DIP users" [p. 56]. I agree that wire-wrapped DIP sockets have much to be gained in prototype and one-shot applications, but the article belabors the technical advantage of wire-wrap as an interconnection media. There are few doubts concerning the reliability of wire-wrap; a look at the ratings applied to wrapped vs. soldered connections for MTBF calculations establishes that. The real reliability question concerns the 14 or more-DIP leads making contact with the socket. These are almost always dissimilar metals, the socket usually gold plated, the DIP a variety depending upon manufacturer, type, time, and a host of other variables. This is the area that concerns us in the use of DIPs in sockets, and is the key to their use. Perhaps it's difficult to get data on and it was deleted.

Roger C. Cady
PDP-11 engineering manager
Digital Equipment Corp.
Maynard, Mass.

Automatic answering

To the Editor: In the article on the Bell System's data sets ["Modem race," Aug. 31, p. 42], the following statement is made: "[The 113B] is designed to replace the 103E station for the user who doesn't require features such as call-out and automatic answering."

While it is true that call originating capability is not presently included, automatic answering is a built-in feature of the 113B.

Lewis H. Mammel
Bell Laboratories

Wrong side

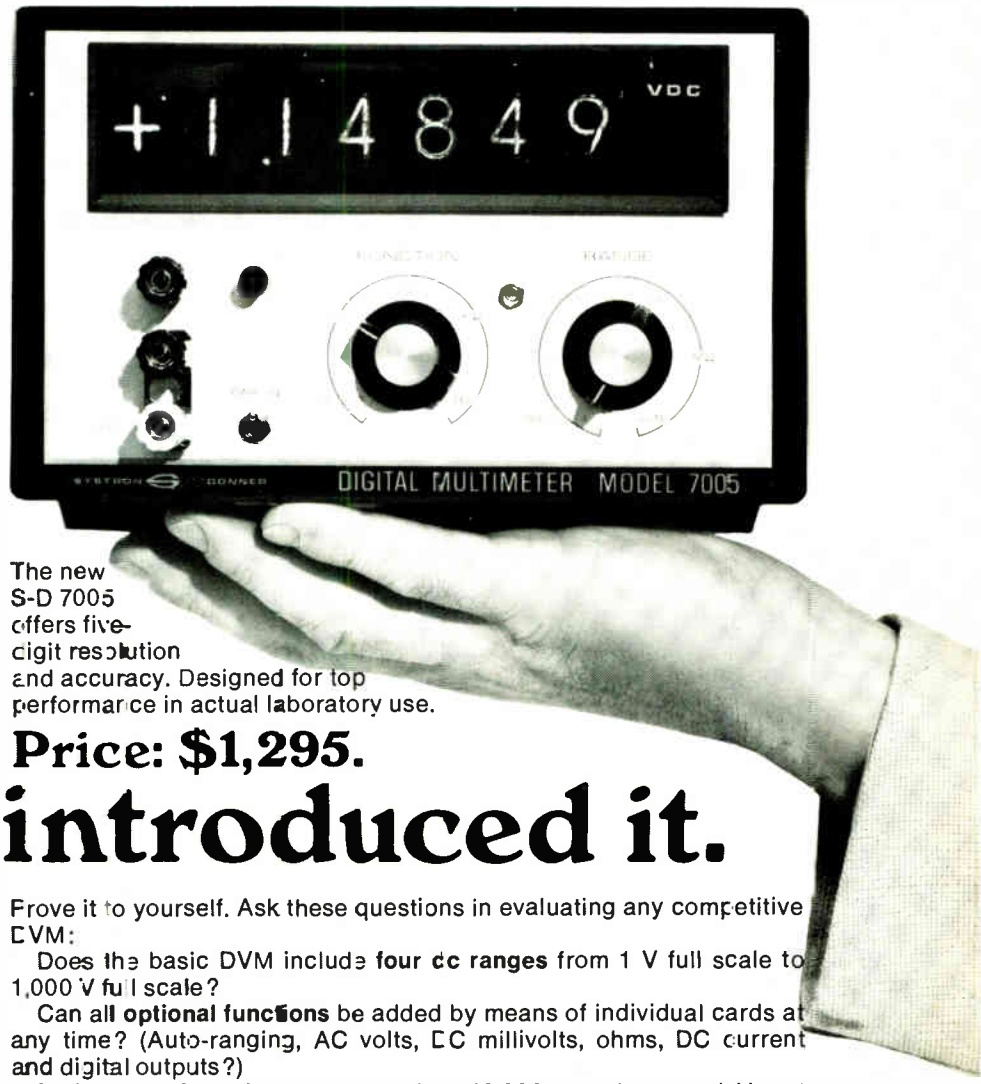
To the Editor: In the Designer's Casebook I authored [Sept. 28, p. 78] I noted that in the circuit diagram, the connection of the −15 V to resistor R_1 was put on the wrong side of R_1.

Robert P. Patterson
University of Minnesota

Circle 6 on reader service card

Circle 7 on reader service card→

Which 5-digit multimeter is your best buy?



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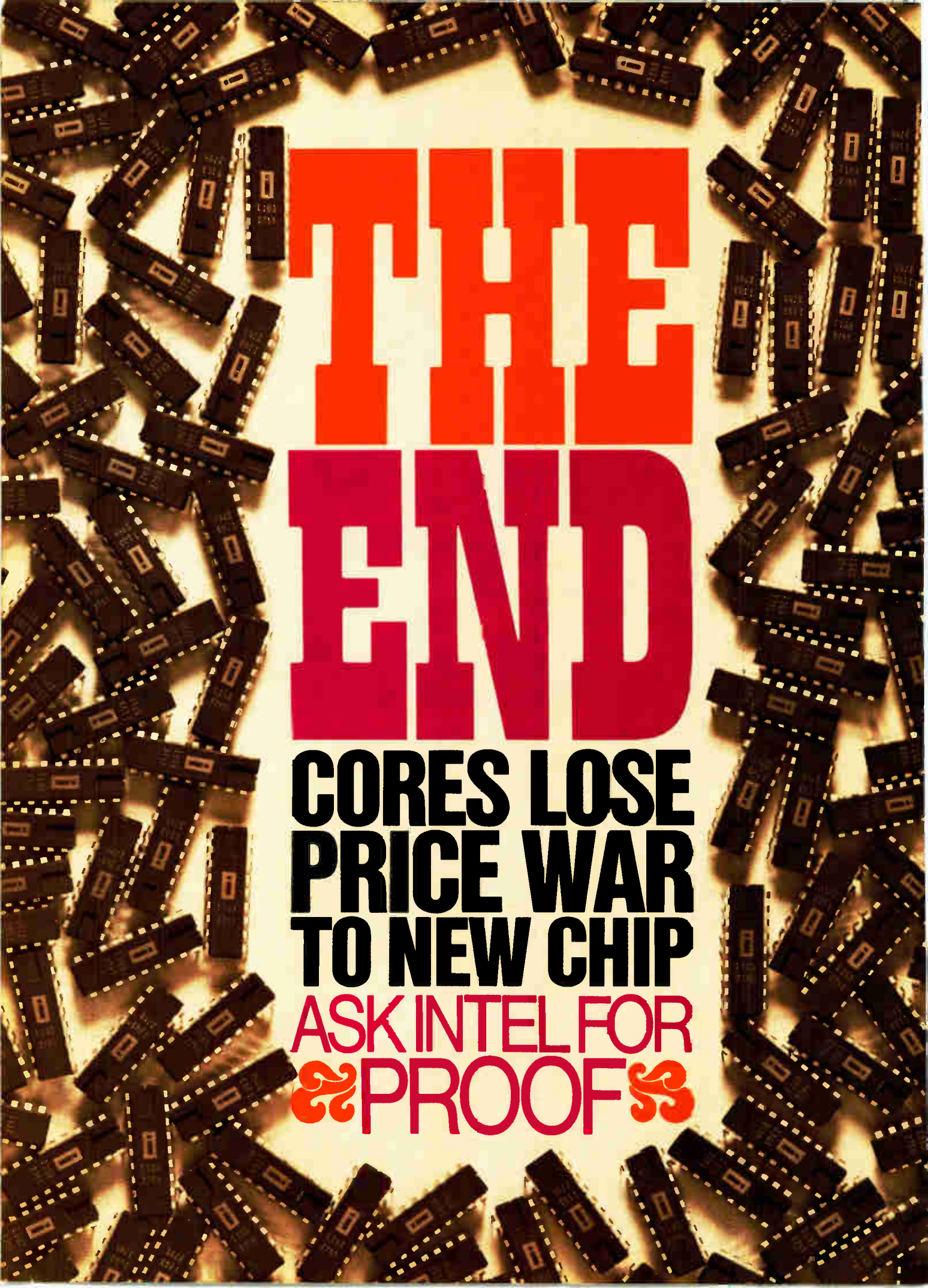
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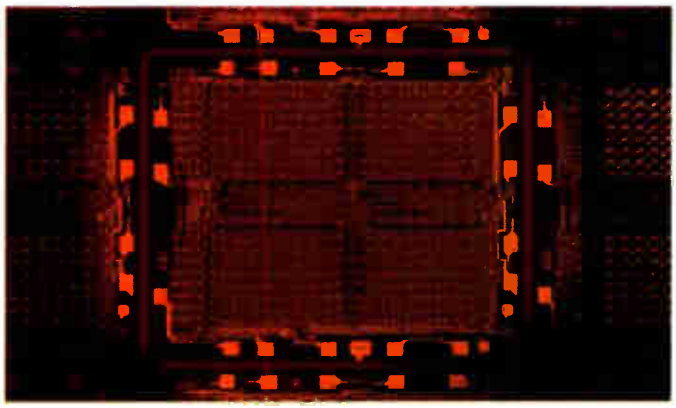

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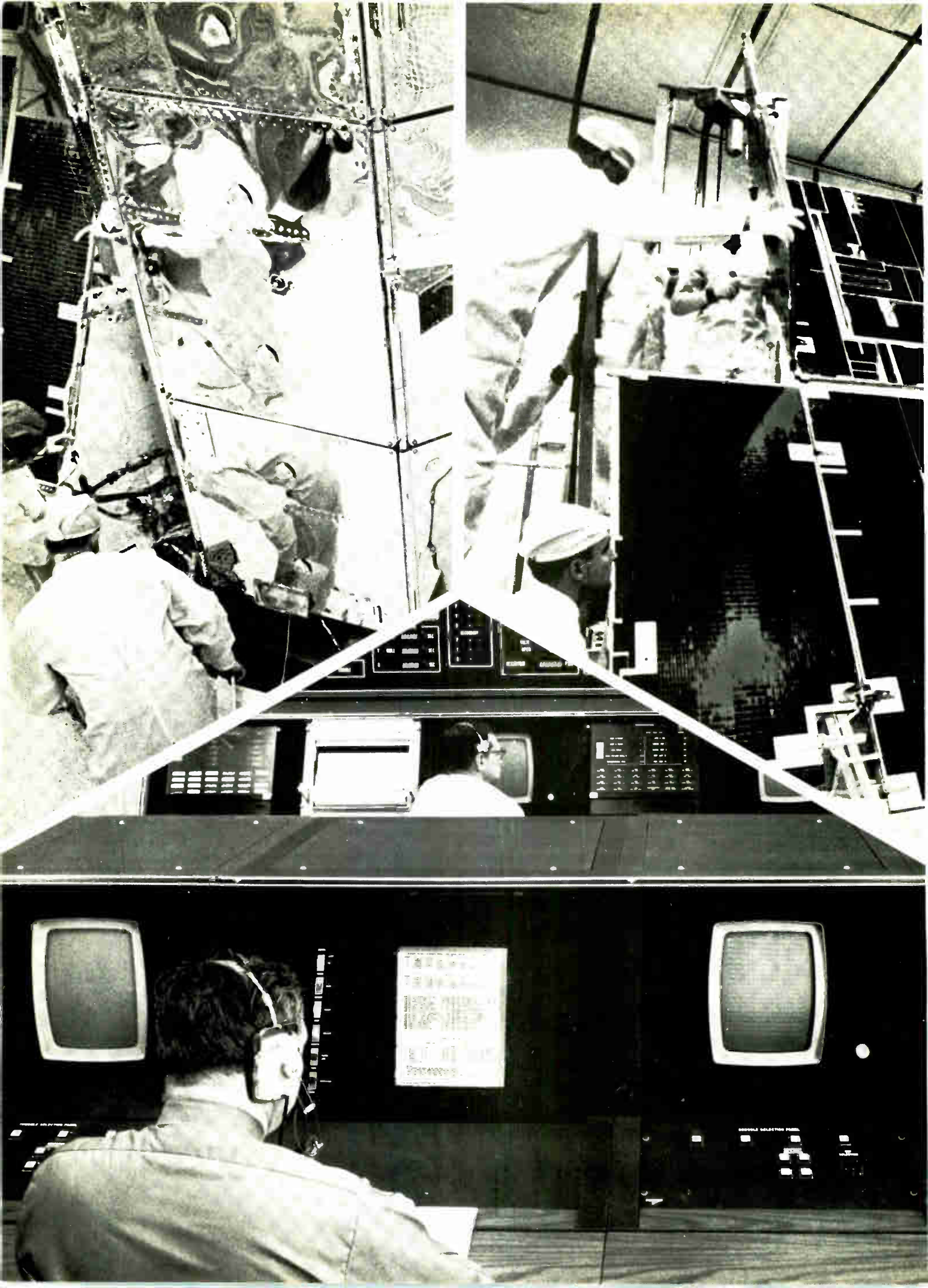

When the Goddard Space Flight Center wrote their specs for a hardcopy printer, they wound up with a choice of one.

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Bright picture

The gamut of display device technology—from the workhorse cathode ray tube to the latest in photochromics—will be run through at the IEEE Conference on Display Devices to be held Dec. 2 and 3 at the United Engineering Center in New York. Two invited speakers will present their views on general interest subjects. Stephen W. Miller of Stanford Research Institute will gaze into the crystal ball to predict "Display Requirements for Future Man/Machine Systems," and Sydney Feinlieb of Arthur D. Little Inc. will examine the present and future market potential for display devices in a talk called "The Business of Displays."

Rapidly developing—and to some extent, competing—display devices will be surveyed in two other invited papers. D.G. Thomas of Bell Laboratories, in "Light-Emitting Diodes," will evaluate and compare the various approaches (such as GaAsP, GaP, and GaAs with phosphor coating) to fabricating these semiconductor devices. H.G. Slottow of Owens-Illinois Inc. will speak on a controversial glow-discharge display technique in "The Plasma Display Panel—Principles and Prospects." In another invited paper, "The Cathode Ray Tube—a Review of Current Technology and Future Trends," Peter Seats of Thomas Electronics Inc. will show that the familiar device is far from obsolescent.

Others papers will delve into subjects ranging from materials for display devices ("Phosphors for Current-Sensitive Single-Gun Polychromatic Cathode Ray Tubes," by F.J. Avella of General Telephone & Electronics Laboratories, and "Orientation and Thickness Dependence of Contrast and Brightness in $Gd_2(MoO_4)_3$ Light Valves," by A.R. Kmetz of Texas Instruments), through theoretical considerations ("Energy Transfer in a Fiber-Optic Ultraviolet Cathode Ray Tube," by J.J. Pucilowski and E.B. Schlam of the U.S. Army Electronics Command).

For further information contact Thomas Henion, Palisades Institute, 201 Varick Street, New York, N.Y. 10014.

Calendar

Society of Engineering Science Meeting, George Washington University; Washington, **Nov. 9-11.**

International Congress on Microelectronics, International Electronics Group; Munich Fairgrounds, Germany, **Nov. 9-11.**

Symposium on Man-Machine Systems, IEEE; Langford Hotel, Winter Park, Fla., **Nov. 12-13.**

Symposium on Communications, IEEE; Queen Elizabeth Hotel, Montreal, Canada, **Nov. 12-13.**

Magnetism and Magnetic Materials Conference, IEEE; Diplomat Hotel, Hollywood Beach, Fla., **Nov. 15-19.**

Fall Joint Computer Conference, IEEE; Astro Hall, Houston, **Nov. 17-19.**

Short courses

Electronic Methods of Precision Positioning at Sea, University of California at Los Angeles, University Extension; Boelter Hall, Room 2444, **Nov. 9-13;** $310 fee.

Metal and Ceramic Matrix Composites, University of California at Los Angeles, University Extension; Boelter Hall, Room 4442, **Nov. 9-13;** $310 fee.

Active Filter Design, George Washington University; Washington, **Dec. 14-16;** $215 fee.

Call for papers

Conference on Computers for Analysis and Control in Medical and Biological Research, IEE; University of Sheffield, England, Sept. 7-9, 1971. **Nov. 5** is deadline for submission of synopses to IEE Conference Dept., Manager, Savoy Place, London WC2R OBL.

International Symposium on Electromagnetic Compatibility, IEEE; Bellevue Stratford Hotel, Philadelphia, July 13-15, 1971. **Dec. 14** is deadline for submission of summaries to F. Haber, chairman, Technical Papers Committee, Moore School of Electrical Engineering, University of Pennsylvania, Philadelphia, Pa. 19104.

International Symposium on Remote Sensing of Environment, University of Michigan; Ann Arbor, May 17-21, 1971. **Dec. 15** is deadline for submission of summaries to The Center for Remote Sensing Information and Analysis, Willow Run Laboratories, P.O. Box 618, Ann Arbor, Mich. 48107, Attn: Jerald J. Cook.

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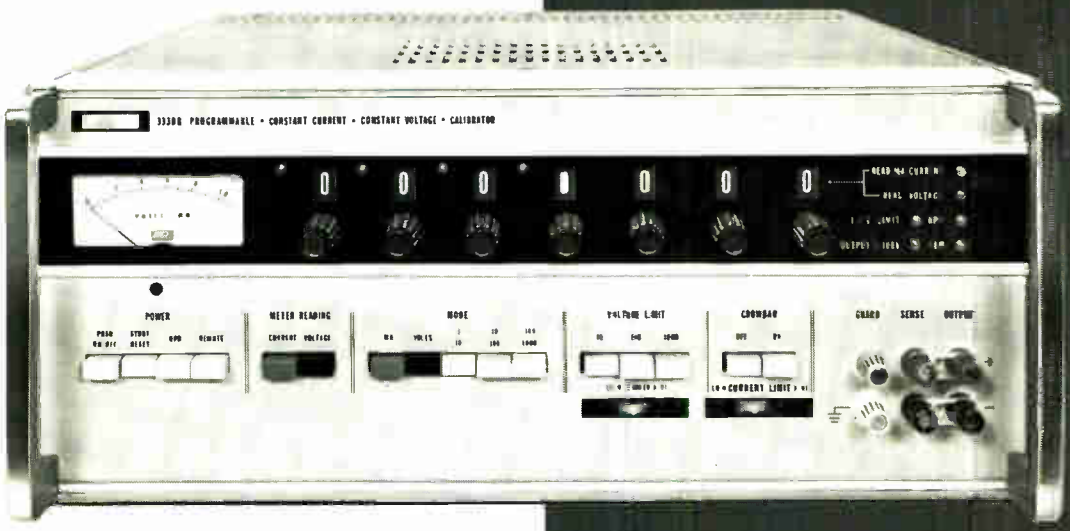
Circle 15 on reader service card→



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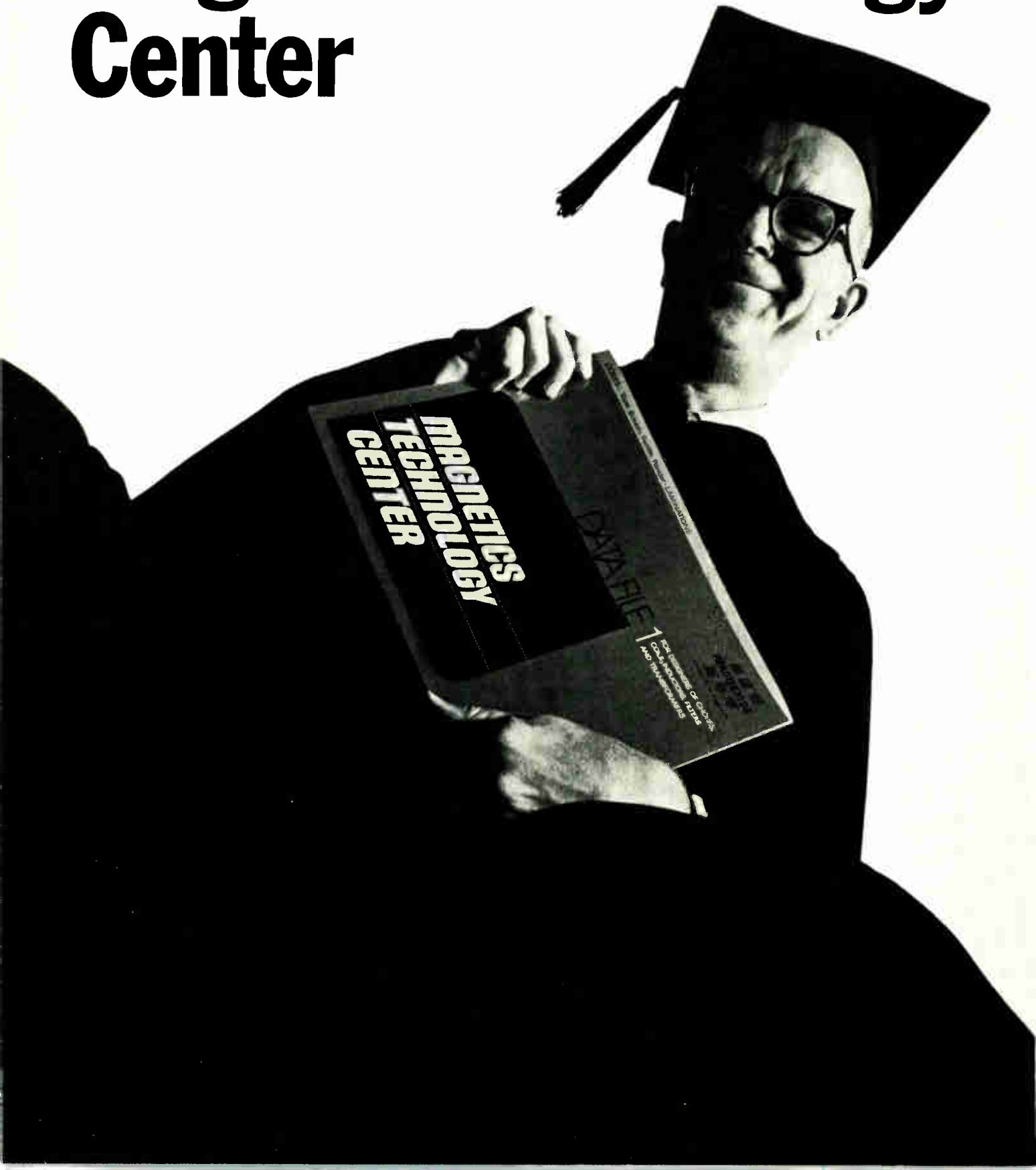

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We don't pretend to be scholars behind ivy-covered walls. We are a group of inquisitive specialists with interests in electronics, electrical engineering, physics, metallurgy and related fields. We work with low and high permeability magnetics, ferrites and photo-chemically machined metals. Some of us have spent over 20 years here at Magnetics developing theories and putting them to practical use.

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MAGNETICS

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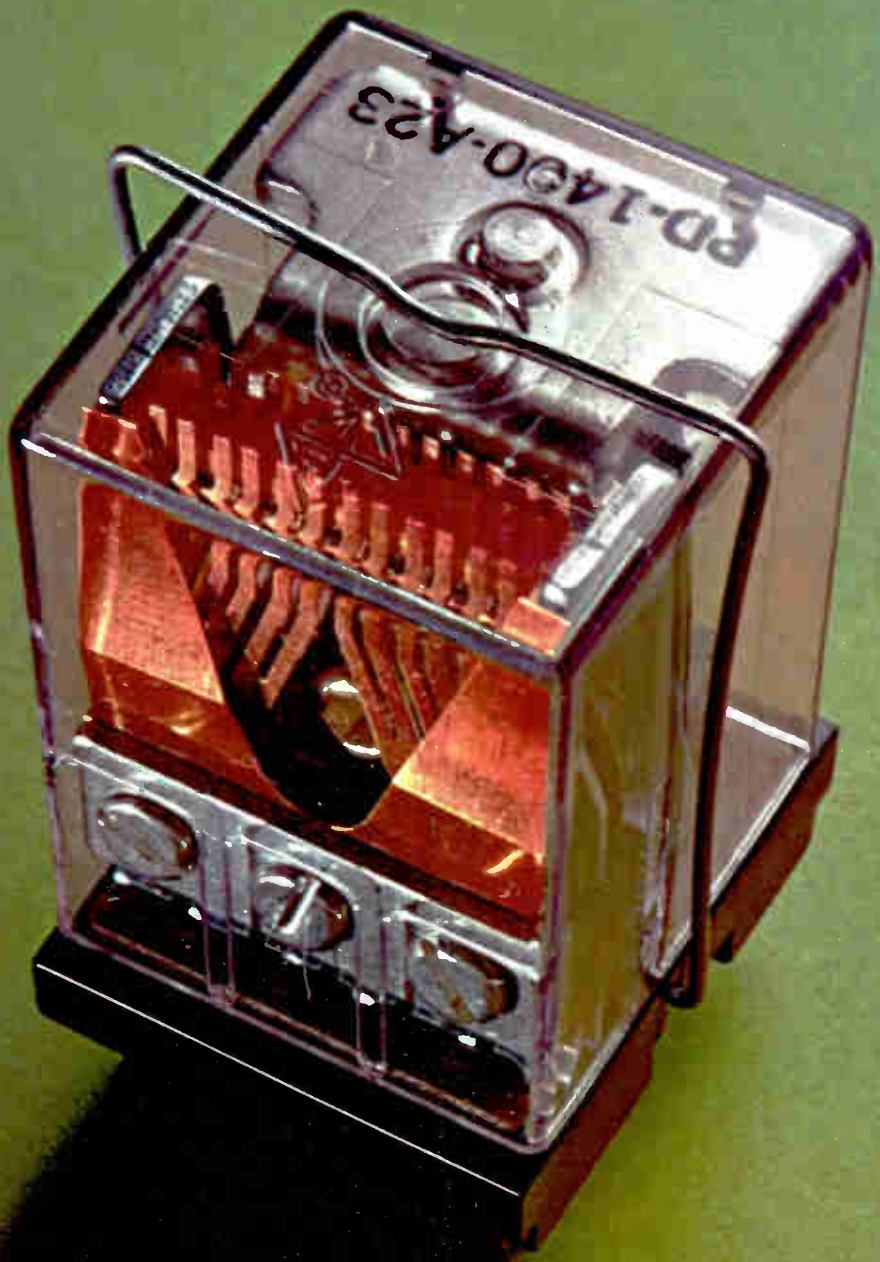
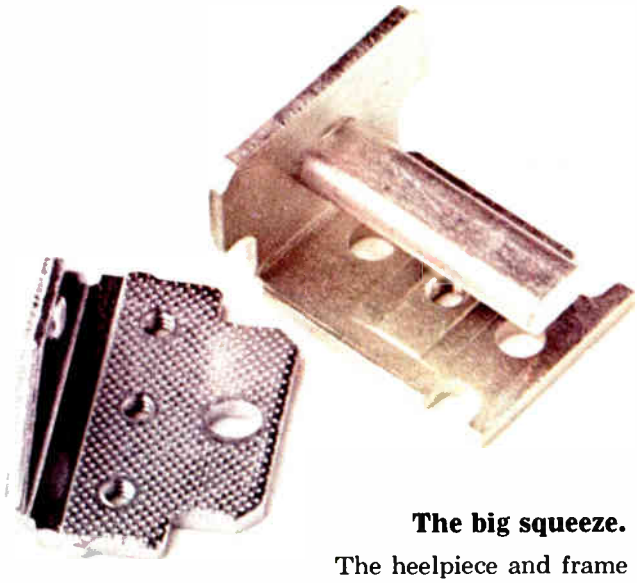
Reliability is 756 little dents and one big one.





The big squeeze.

The heelpiece and frame are the backbone of our Class H relay. The slightest squiggle or shimmy out of either and the whole relay is out of whack.

756 tiny dents on the heelpiece, plus one big one on the frame, make sure this'll never happen.

They're the result of planishing, a big squeeze. Planishing is an extra step we go through in forming the pieces to add strength and stability by relieving surface strain. It also makes the parts extra flat.

This takes the biggest press in the industry and the biggest squeeze. Both exclusively ours.

A different kind of coil.

The heart of a relay is the coil. If ours looks different, it's because we build it around a glass-filled nylon bobbin. It costs us more, but you know how most plastic tends to chip and crack.

Also, moisture and humidity have no effect on glass-filled nylon. No effect means no malfunctions for you to worry about. No current leakage, either.

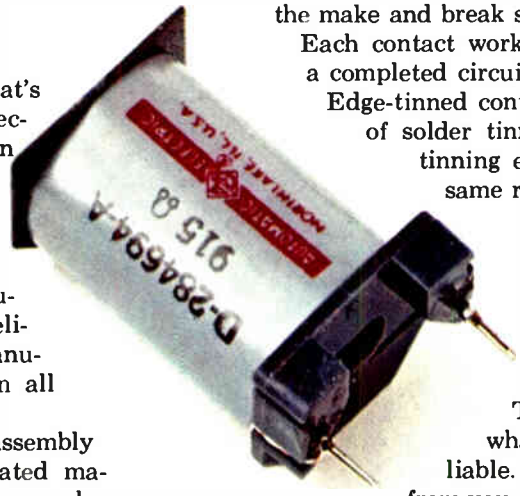
The coil is wound on the bobbin automatically. No chance of human error here.

We didn't forget the solder.

We use a solderless splice. That's because solderless splice connections are sure-fire protection against the coil going open under temperature changes, stress, or electrolysis.

A solderless splice is more expensive to produce, so it's usually found only on the most reliable relays. AE is the only manufacturer to use this method on all of its relays.

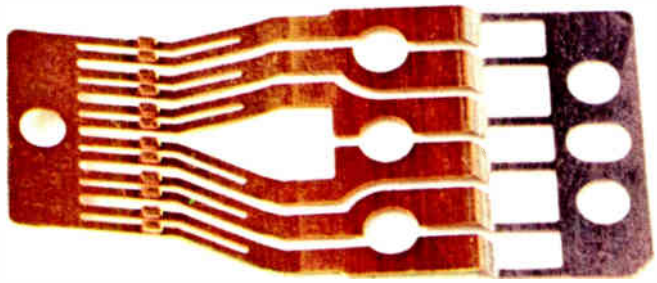
Finally, we wrap the whole assembly with extra-tough, mylar-laminated material. A cover is not really necessary here; but why take chances?

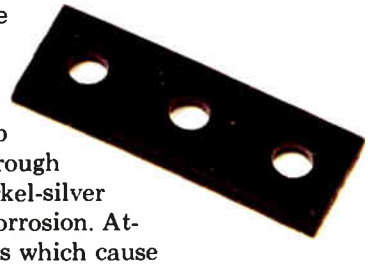


Springs and other things.

We don't take any chances with our contact assembly, either. Even things like the pileup insulators (those little black rectangles) get special attention. We precision mold them. Other manufacturers just punch them out.

It makes a lot of difference. They're stronger, for one thing; and because they're molded, there's no chance of the insulators absorbing even a droplet of harmful moisture. Finally, they'll withstand the high temperatures that knock out punched insulators.





Then there are the contact springs. Ours are phosphor-bronze. Others use nickel-silver. Our lab gave this stuff a thorough check, but found nickel-silver too prone to stress-corrosion. Atmospheric conditions which cause tarnish and ultimately stress corrosion have almost no effect on phosphor-bronze.

Two are better than one.

Our next step was to make sure our contacts give a completed circuit every time. So we bifurcate both the make and break springs.

Each contact works independently to give you a completed circuit every time.

Edge-tinned contact springs save you the job of solder tinning them later. Also, edge-tinning enables you to safely use the same relay with sockets or mounted directly to a printed circuit board. A simple thing, but it takes a big chunk out of the inventory you have to stock.

Etc. Etc. Etc.

There's a lot more to tell about what makes our Class H relay reliable. Now we're waiting to hear from you. Automatic Electric Company, Northlake, Ill. 60164.

AUTOMATIC ELECTRIC

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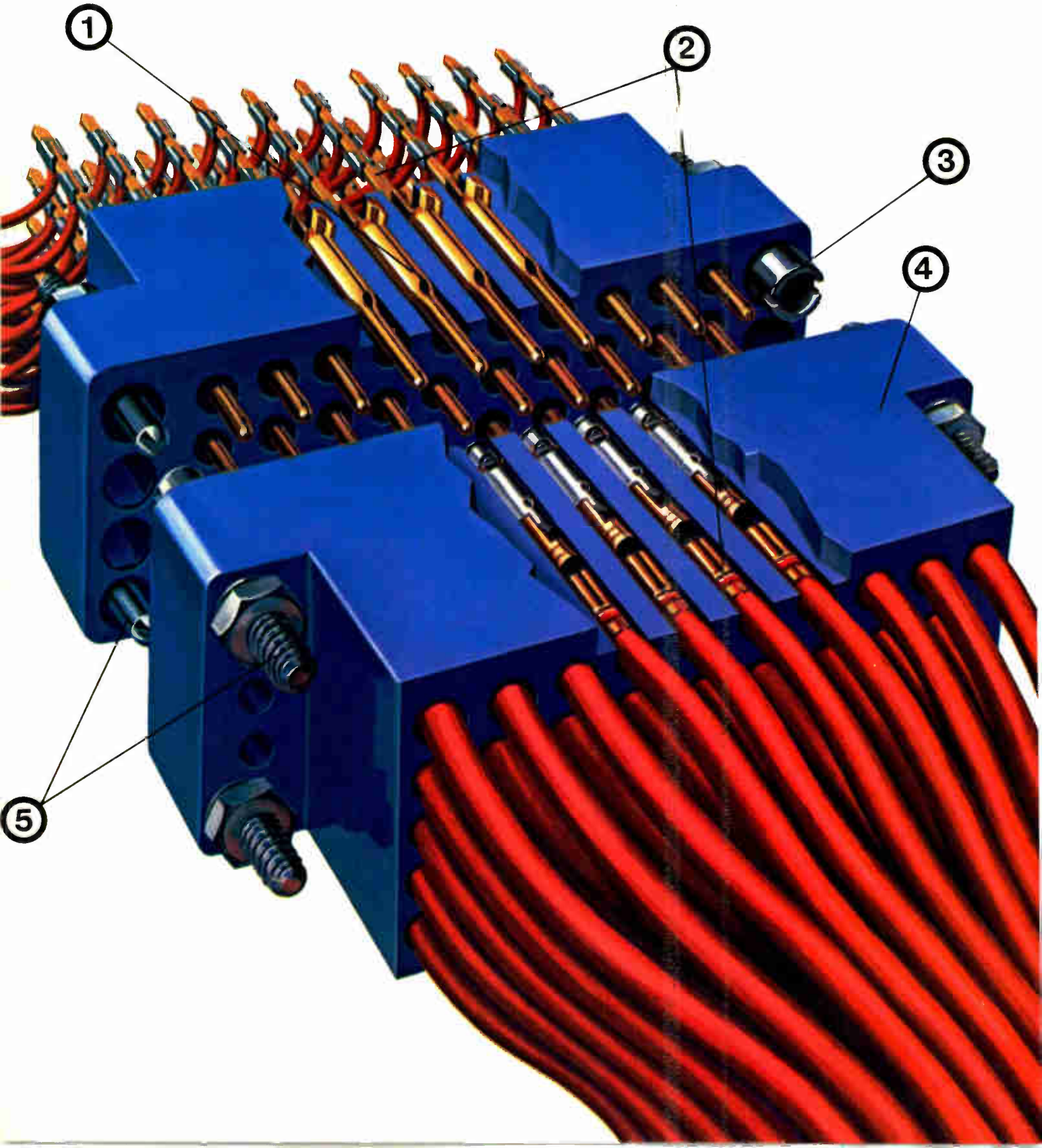
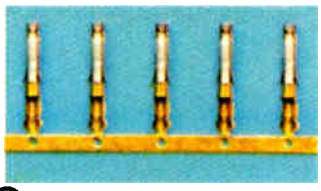
Get nosy



...it pays.

Take a close look at the broad line of AMP "M" Series Connectors. You'll see how everything is designed and made better to give you better performance and higher reliability, at the lowest *installed* cost . . . this is what we call Economation.



① **The right pin and socket contacts for the job.** Formed contacts with special socket spring design to make stronger and more uniform electrical contact—an AMP mated connector exhibits less resistance per contact than equal length of wire. Formed pins and sockets available in contact sizes 20, 18 and 16 accommodating wire ranges #14 to #30 AWG. Solid, screw-machined contacts available for military or aerospace requirements down to #32 AWG wire.

② **AMP contacts** are designed to be manually or automatically crimped on wire, or for soldering or welding techniques. Special contacts are designed for back-bay wiring, with either AMP TERMI-POINT★ point-to-point wiring or wrap type methods.

Single crimp coaxial contacts. AMP COAXICON★ Sub-miniature Coaxial Contacts cover a range from RG174U to RG196U including some twisted pairs. These contacts are terminated with one quick crimping action . . . inner conductor, outer braid and cable support, all simultaneously. VSWR and noise levels are kept at minimum. The finished termination is inspectable via a see-thru port. Coaxicon subminiature contacts fit all "M" Series blocks to allow complete freedom of mixing with standard contacts.

③ **The right contact plating —the right thickness.** With AMP, you can select from various platings engineered to do a specific job best. Available in gold, silver, tin and special alloys. Proper thickness during AMP manufacture is assured by exclusive X-ray emission analysis techniques.

④ **Largest selection of connector blocks.** AMP offers more variety of contact configurations and a choice of quality molded plastics — "M" Series blocks are made from phenolics for all around service, or diallyl phthalates for stability under extreme conditions. They also meet Mil Spec requirements. Available in 6 to 160 positions for standard or coaxial contacts. Mix or match for your particular circuit needs.

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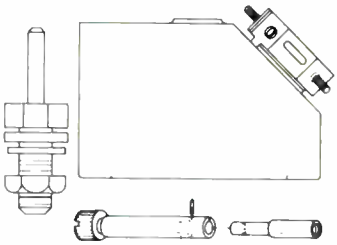


⑤ **Pick from the broadest selection of hardware.** Choose from a complete line of guide pins, sockets, strain-relief clamps. And two piece shields, with either lock-spring, quick-release or jackscrews, and cable clamps up to 1¼" diameter. Internal and external pin hoods are available for pin protection. Jackscrews of all types . . . fixed, turnable, short-short to long-long.

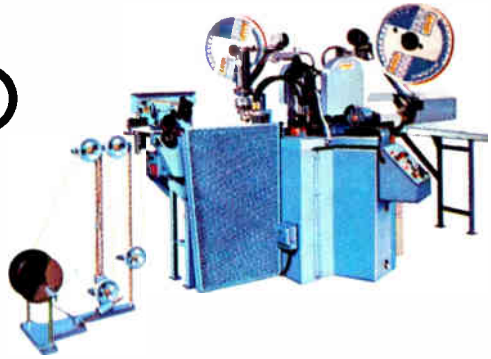
Everything about our "M" Series Connector design permits mating with almost any similar connector available today. So you can use them to retrofit, modify or interface . . . at a cost savings.

⑥ **High-speed crimping or point-to-point wiring machines.** AMP offers the most complete line of automatic application machines for use in your plant that give you capacities of 1,000 to 12,000 terminations per hour. Human error is cut to the absolute minimum. And operator skills are quickly and easily learned — we even train your production personnel.

The unique semi-automatic AMP Stripper / Crimper★ simultaneously strips and terminates unstripped leads or wire harnesses at speeds up to 1,200 pins or sockets per hour.

Our fully-automated AMP-O-MATOR★ Machine does everything . . . cuts wire to exact length, strips, terminates one or both ends. And with the same or two different contacts. Maximum capacity . . . 12,000 terminations per hour.

⑥



The AMP line is thoroughly tested. Tests cover all areas of electrical and mechanical performance and adverse environments — contact resistance, millivolt drop, shock, vibration, heat stability and corrosion resistance. Data covering these and other criteria is available.

Get nosy. Get all the information you need and learn how our "M" Series line pays off in reliability and improved productivity through AMP Economation. Write: **Industrial Division, AMP Incorporated, Harrisburg, Pa. 17105.**

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Who's who in electronics

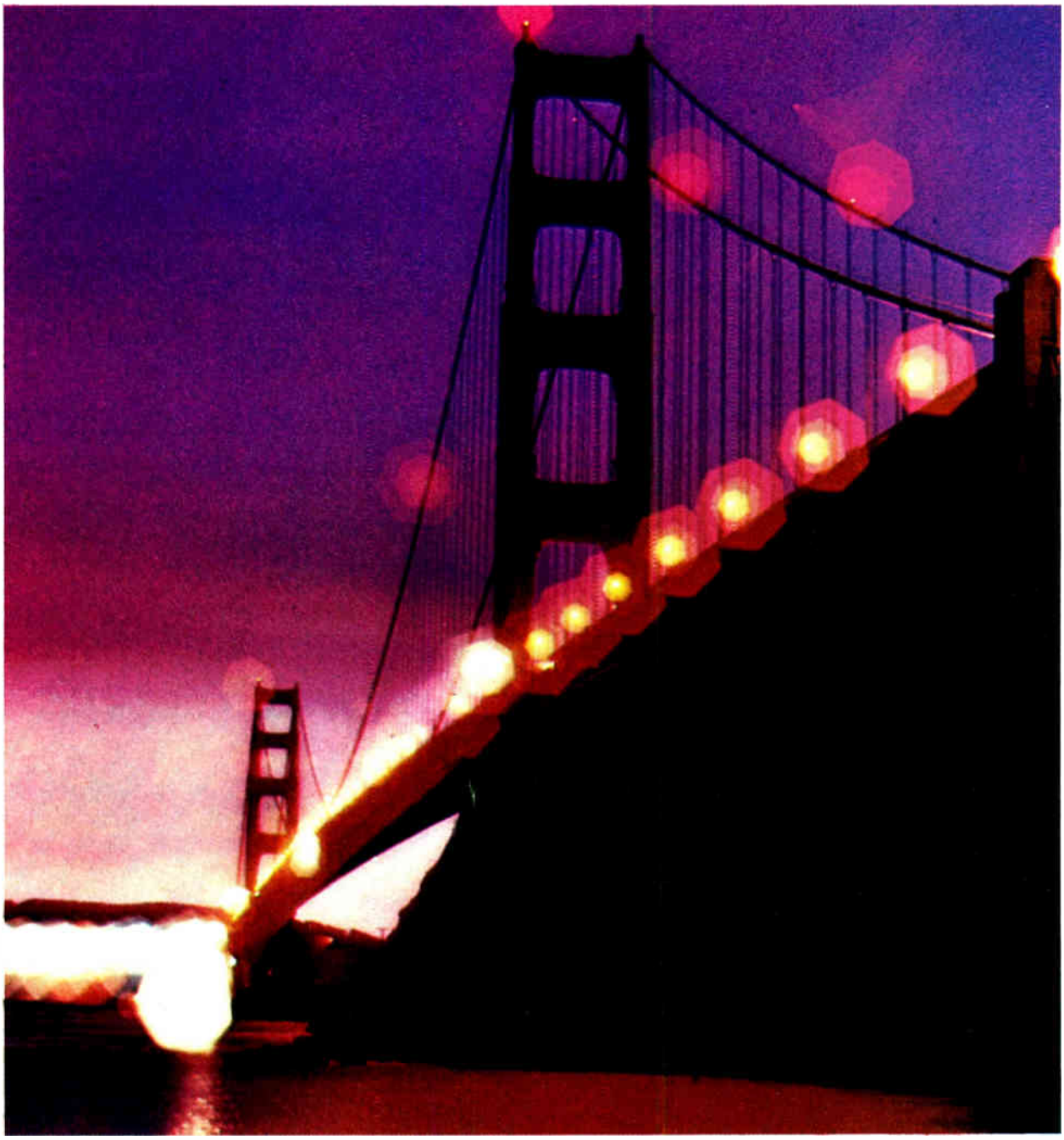
David

"When I came to this job," says Presidential Science Adviser Edward E. David, "people said I'd make it a low-profile operation. Well, all I can say is that I expect to make it high profile." But to most scientists and engineers outside the computer and electronics fields, David himself presents no profile at all—he's virtually a complete unknown.

The word went around, especially through the academic community, that President Nixon was further undercutting basic research by hiring a man who was first an engineer and second an industrial researcher. The university basic research budget may not rise in such classical fields as physics and chemistry, but David makes it clear that he expects to push for strong research and development programs on campus.

That may sound like political sidestepping, but basic science is only one thing on the short, slender science adviser's mind. His job is to make science practical and steer technology on a more problem-directed course.

At Bell Labs, David was known for constantly pushing new ideas into the experimental stage, always with an eye toward applications. He spearheaded Bell's use of computers for designing communications systems. Until his departure



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Space age communication equipment demands a crystal that meets all standards of technical advancement. Crystals that were acceptable some years ago do not meet present day specifications. As a general rule, your crystal must be selected from the best quartz . . . (no throw off cuts). Tight tolerances demand selected angles of cut. The x-ray is important in making this selection. The crystal should be preaged with stress cycling. It should be checked for frequency change vs temperature change. It must be checked for optimum spurious response. It should be calibrated to frequency with the correct oscillator. International Crystals are manufactured to meet today's high accuracy requirements. That's why **we guarantee all** International crystals against defective materials and workmanship for an unlimited time when used in equipment for which they were specifically made.

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in late August he worked to improve the man-computer interface. His working style was to go directly to the scientists under him rather than through the investigator's immediate superior—a habit some section heads found disconcerting. Some felt that at times he was slow to make decisions until all options were examined. At the Office of Science and Technology, David will have less time to ruminate, but at least he'll have a staff that's used to giving instant, considered responses to White House signals. At the moment the OST staff numbers about 20 professionals. It is expected to expand.

What kind of a science adviser will David be? At Bell Labs, where he headed the division of communications principles, he was known as a quick-witted managerial innovator, a sparker of ideas who pushed his colleagues well beyond the most obvious implications.

"Ed grew in every way during his 20 years at Bell," says W.O. Baker, research chief at Bell. "He recognized the balance of judgments that had to be brought into human subsystems. He saw engineering in all its social and economic sides. With those features you don't look for a discoverer of new atomic principles or someone Edisonian, but instead for someone with the skills of synthesis. He represents the new scientist—a bridge between generations."

It was almost as though Baker did the choosing himself, for David's job will be to synthesize the social and economic goals of the country with science and technology. Book after book is being written today about the threat to human life posed by technology. But institutions, including the White House staff, haven't yet learned to cope with change generated by technology. Indeed, they have hardly learned to think about technology as a social force. That will be David's job, whether he thinks about new missile systems or figures a way to coordinate Federal R&D on new housing concepts and medical care delivery.

In fact, while at Bell, David took

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Circle 25 on reader service card→

Room for improvement.

Until now, most rack and panel and I/O connectors came with contacts spaced at .150". Or wider.

Which meant that your dense forest of .100" center connections had to spread out whenever it came to an R/P connector.

Or your standard .125" center automatic-wire-wrapping grid had to lengthen stride to accommodate an I/O connector.

No more.

Our new crimp-and-insert mini-Varilok™ contact fits easily, with no sacrifice of electrical or mechanical integrity, into .100" or .125" center plugs and receptacles.

R/P connectors with mini-Varilok contacts on .100" centers (Series 8026) are less than half the size of their .150" counterparts. They're suitable either for the rigors of military service or the air-conditioned comfort of a computer room. And they have all the options you'll need: 33-contact connectors with plastic covers; 75-or 117-contact connectors with metal covers; cable clamps; jackscrews; polarizing hardware.

For I/O applications you can use the same connectors, mating them with modular receptacles (Series 5540). The receptacles—a keyed connector-center module, contact modules, and polarizing hardware modules—have Varicon™ contacts, ready for wire-wrapping. Your choice of 33, 75, or 117 contacts on .100" centers, 55 or 79 contacts on .125" centers.

When you're ready, you'll find all these connectors, all ready, at your Elco distributor. In the meantime, call, write, wire, or TWX us for your copy of our new 1971 R/P and I/O connector guide. For special assistance, call Joe Roos—Product Manager, Elco Corporation, Willow Grove, Pa. 19090. (215) 659-7000. TWX 510-665-5573.

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part in a project, "Man-Made World," designed to teach high-school seniors to cope with constant technological change. A coordinated textbook and machine course, "Man-Made World" is published by McGraw-Hill and is commercially available.

Universities and the government are the main buyers of Boston-based Adage Inc.'s graphics terminals; they use them for modeling, molecular structuring, intelligence work, and other tasks. But with the recent appointment of Robert M. Beckett as president and director, Adage will be expanding into industrial markets.

Beckett, who held several marketing positions at IBM and was most recently systems manager for its information display systems, approaches his new job from a marketing viewpoint, and says his first goal is "to broaden our base and get into other areas, to uncover applications not existing today."

"Adage should be getting a better piece of the action," he says, and thinks the way to get it is by moving new applications from the exploratory stage to practical use. While universities and the government use terminals for research and nonmanufacturing purposes, Beckett knows that in industry "if the terminal is product oriented, it is of more specific dollar value to the user."

New industrial markets, however, mean "we will have to approach sales differently." The capabilities of Adage's graphics terminals are not yet fully exploited. "The difficulty is in recognizing the match between capability and the customer's problem," says Beckett, and this is a task for Adage's sales force. He plans to give salesmen more support and applications exposure so they can learn to relate problems presented to them to the systems' capabilities.

Although he thinks it's "premature to sound off" about possible future markets, some aerospace and manufacturing firms are beginning to use sophisticated graphics terminals, and Adage will prob-

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Circle 27 on reader service card→

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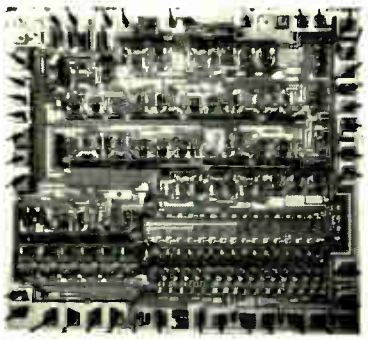

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ably try to get a share of this market. Beckett also thinks Adage terminals can replace other types of displays in existing applications like ticket reservation systems. "Performance at the lower end of a system can be improved by upgrading the device attached to the computer," he says.

In assessing his new job as national sales manager for Philco-Ford's Microelectronics division, Edward A. Youch is more than realistic about the competition he's facing. "For starters," he notes, "my sales force is outnumbered 10 to 1." Not that it scares him: "I've seen those odds overcome before," he asserts. Youch, who has a BSEE as well as a master's in business administration, practically cut his teeth on semiconductor sales and marketing. He spent three years at Fairchild Semiconductor, three at Sprague, and then most recently four at Texas Instruments as marketing manager for the Optoelectronics department.

While the challenges at Philco admittedly are large, the 40-year-old Youch, whose other accomplishments include a brown-belt rating in judo, seems more than equal to the task. High on his priority list is upgrading his sales force and sales reps. This entails "knocking out the lowest guys and bringing in better ones. I'm telling my guys that whatever happens from now on, it's the sales force's responsibility."

If Philco, as claimed, has solved its MOS production problems to the point where wafer yields are up to 65%, then it's truly up to Youch and his staff to make the division a power in MOS, not to mention holding its own in the bipolar market. Youch feels that the era of "sales engineering" has returned—the salesman can no longer just go out and sell a product, he's also got to work with the customer as he had to during the early days of the integrated circuit. "Technological leadership, of course, is vital to success, but only if you help the customer solve his own particular problems," explains Youch.

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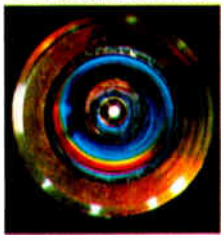

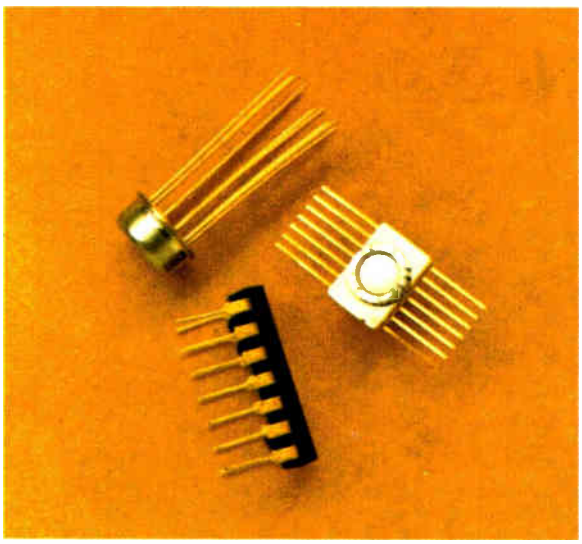

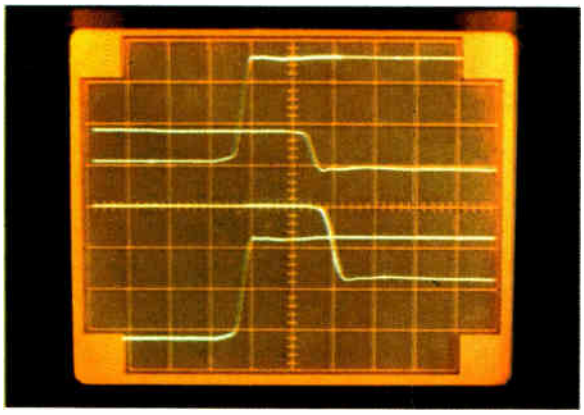

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Electronics Newsletter

October 26, 1970

MOS-bipolar memory due in '71

Sprague Electric Co. and Mostek may announce an MOS-bipolar hybrid memory early in 1971. **Both the MOS storage and bipolar decoding chips would be optimized for speed. Cycle time would be about 100 nanoseconds with TTL decoding.** What's more, even faster versions with ECL decoding chips are being considered.

Sprague spokesmen say their goal is to combine the speed of bipolar devices with the high packing density and low cost of MOS. They hope to enter the market with a memory midway in speed between the ultrafast monolithic ECL units and less-costly, multihundred-nanosecond film or wire assemblies.

N-channel MOS to be twice as fast as p-channel units

Systems designers soon may be working with faster MOS circuits. Intersil Memory Corp. of Cupertino, Calif., is planning a family of n-channel silicon gate MOS devices that will run at 10 megahertz with about 0.6 milliwatt-per-bit power dissipation, **about twice as good as the 5-MHz rates of p-channel MOS.** According to a company official, the family, to be introduced in the first half of 1971, will include static and dynamic random access memories, dynamic shift registers, and read only memories.

The n-channel family will be truly TTL compatible and will require only one 5-volt supply and a 5-V clock, instead of the 15 or 20 volts needed by other MOS circuits.

TI developing new MOS process

Texas Instruments is said to be working on a simplified MOS manufacturing technique **that promises to ease design and production problems, resulting in higher yields.** Called Mark 1, the process would be threshold compatible with TTL. However, unlike Intersil's new line of n-channel MOS, a separate power supply voltage would be needed.

The process uses a dielectric structure during oxidation to prevent oxide contamination of its gate structure. Industry insiders expect TI's first Mark 1 product will be a 1,024-bit random access memory.

Conductive ceramic compensates for temperature changes

A conductive ceramic which can greatly improve the performance of almost any semiconductor device has been developed by the Sprague Electric Co. Called PTCR, for positive temperature coefficient resistance, it would be layered between a semiconductor and its header. **When dc is passed through it, PTCR rises to a controlled temperature—its current-voltage characteristic changes with, and compensates for, changes in temperature.** Thus, says Sprague, it acts as an oven to eliminate temperature-induced drift—something never before available to monolithic IC designers.

Applications of PTCR soon will include a high-accuracy monolithic voltage reference—drift as low as 10 millivolts over a full Mil-Spec temperature range—and operational amplifiers. With a capacitor deposited on top, PTCR would form half of a nearly drift-free RC network. Sprague notes that PTCR should not only improve performance but cut costs as well—less chip real estate would be needed for temperature control.

Electronics Newsletter

IBM to bring out successor to 1800

Watch for IBM's announcement of a successor to its 1800 middle-sized process control computer, perhaps early in 1971. **The new machine would use a combination of technologies already introduced in IBM's System 3 and 370 lines.** Selling price could be as low as $30,000—putting the machine into direct competition with middle-sized systems using Digital Equipment Corp.'s PDP-11 and Data General's Nova line.

At least one potential competitor views the move as more evidence that "IBM is definitely extending its line downward into the faster-growing end of the computer market—minicomputers."

Price to be halved on built-in EVR

As the consumer electronics woods become thicker with home video playback gear, CBS is predicting **that built-in versions of its EVR will add $125 to $150 to the cost of a color television set.** On the other hand, a separate player module that attaches externally to the antenna poles will sell for about $300 by 1972, according to latest CBS estimates. The reason: when built in, EVR will share TV set circuits.

As of now, Motorola is the only set manufacturer licensed to produce and market the player. **However, the firm's consumer division has not yet announced when it will incorporate EVR into its products.**

Process computer controls variables

An analog computer that anticipates changes in process control variables will be announced at the Instrument Society of America convention (Oct. 26-28) by Anacon Inc. of Ashland, Mass. **The model 11 Probability Computer compares real-time values of temperature, pressure, and other variables with their values over the preceding several seconds and with preset limits.** Through a series of integrations, the model 11 computes the probability that the variable will exceed the desired limit.

Fairchild ready with antiskid system

Fairchild Camera and Instrument Co.'s Microwave and Optoelectronics division is entering the automotive electronics field with an antiskid control system. **The system employs a small, solid state doppler radar as a fifth wheel to provide true ground speed.** Other antiskid systems use one of the car's four wheels for a speed reference, and under certain conditions this could provide false information.

A division spokesman says the system has been tested, "and it works under all road conditions." He adds: "It's economical and it can be done now." **In fact, representatives from a Japanese auto manufacturer were investigating the system last week.** Once again, the Japanese may be the catalyst needed to get things going in a consumer area.

IBM, CDC in line for Safeguard computer

If the White House gains Congressional approval for more Safeguard missile sites next year, their command and control computers will be built by IBM or Control Data Corp. rather than Western Electric. **The Safeguard Systems Command has decided that the custom-built Western Electric computers developed prior to the availability of IBM and CDC supercomputers are too expensive for a role in a broad antimissile defense system.** As a result, the command plans to issue requests for proposals for IBM 360/195 or CDC 7600 series machines. The Safeguard sites at Malmstrom Air Force Base, Mont., and Grand Forks Air Force Base, N.D., will continue to use the Western Electric machines.

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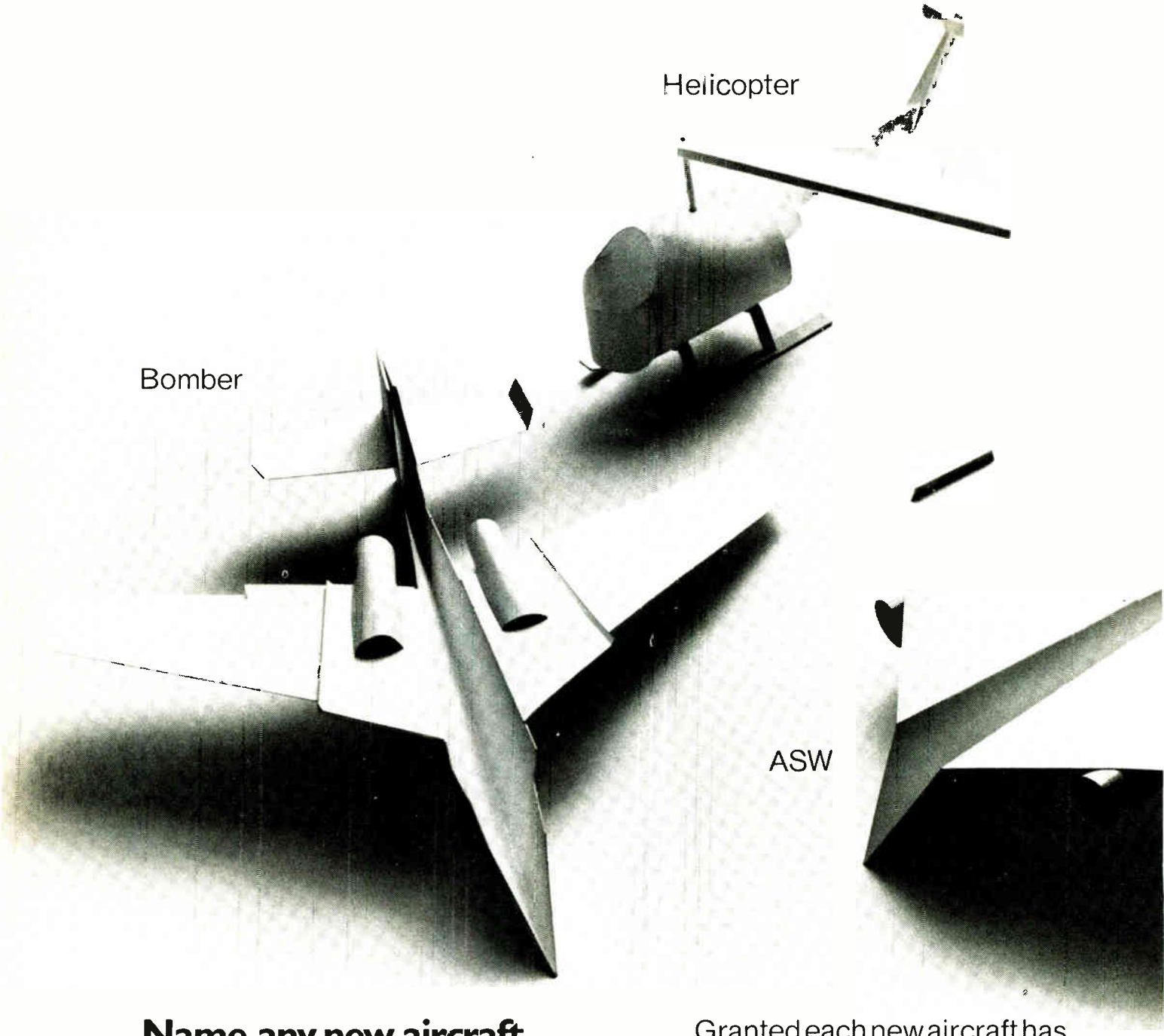

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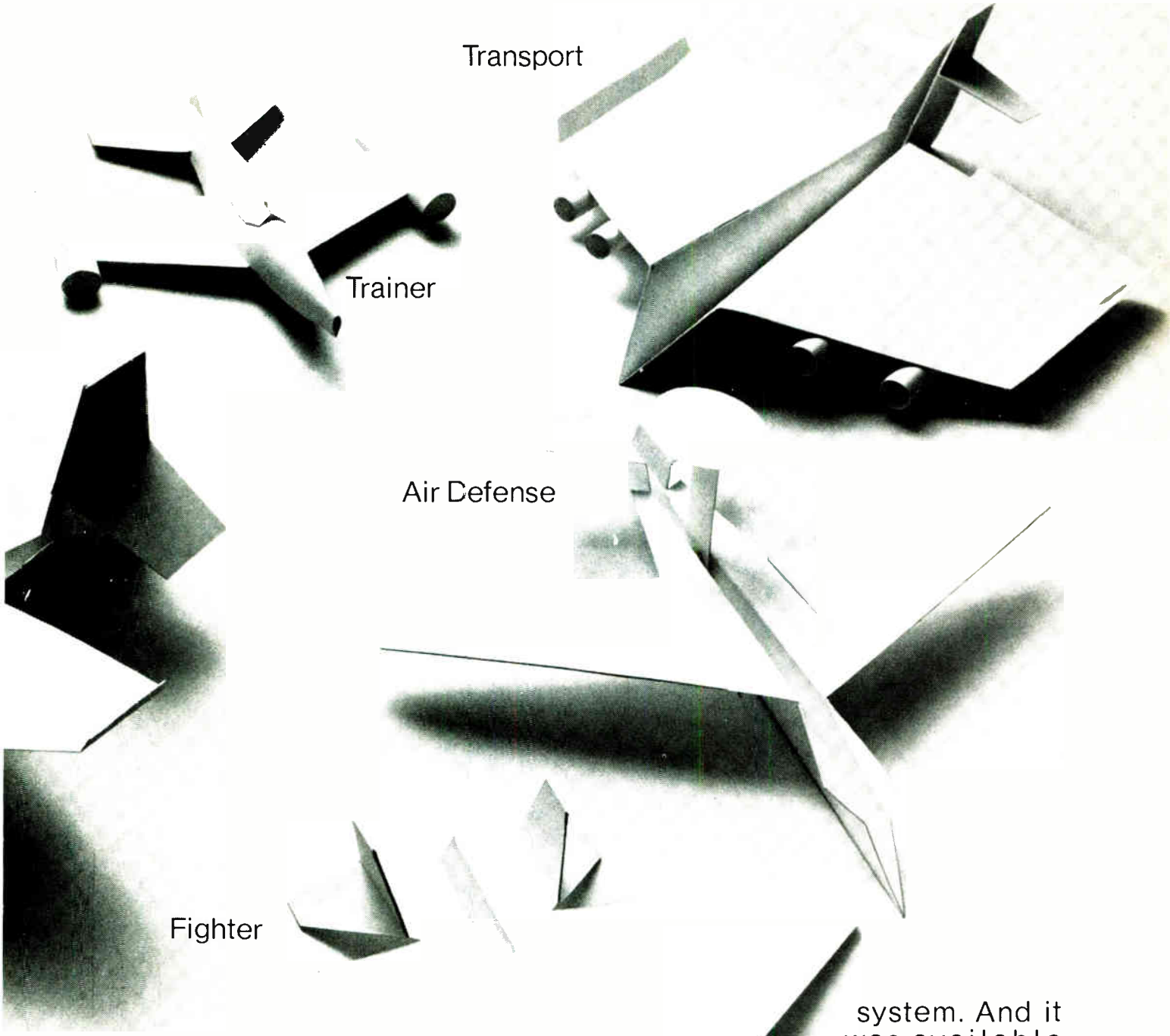

in the United States and overseas with the operationally deployed F-111.

This same system could be economically adapted to the new F-15, as well as to the AWACS and B-1 and other Air Force programs.

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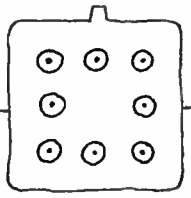

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Electron beam semiconductor set for market

Devices for rf amplification will offer rise times below 1 ns, large bandwidth, and microwave operation

A new electron device that offers the long life of a semiconductor plus the high power amplification of an electron tube is preparing for a market debut. The device, called Ebird (for electron beam ionization of a semiconductor device), has been the subject of work by at least four companies, including RCA. But the Watkins-Johnson Co. of Palo Alto, Calif., is the closest to a product. In fact, says David J. Bates, head of the medium-power tube R&D section, the company will be selling a device by the first of the year.

The new item's electron source is biased negatively with respect to the semiconductor target so that the beam electrons will strike the diode with an energy of 10 kilo-electron-volts. The diode is reversed biased below its avalanche threshold. Without an electron beam, there is almost no target leakage current, but with a beam illuminating the target, target current is proportional to beam current and the device acts as a linear amplifier.

The amplification mechanism is based on formation of multiple electron-hole pairs by a single incident high-energy electron. With pair creation energy of the typical silicon semiconductor lattice at approximately 3.6 eV, each 10-keV incident beam electron will produce thousands of carrier pairs in the diode, yielding a current amplification factor of 2,000 or more.

In essence, the device consists of two metallic electrodes separated by a region of semiconductor material (silicon in the W-J device). It's structured with a very shallow pn junction under the top contact so that all of the semiconductor material between the two electrodes is fully depleted. Thus, no low field regions impede the flow of carriers or add serious parasitic resistances.

Carriers are injected in the beam diode by bombarding the top metal contact with the electron beam. The electrons in the beam penetrate the thin metal contact and enter the semiconductor with considerable energy. This energy is dissipated when electron-hole pairs are formed; this occurs in a region near the edge of the semiconductor region. These carrier pairs are separated by a high electrical field in the semiconductor. One type of carrier moves through the drift region and is collected at the far contact. The other returns very quickly to the bombarded contact.

All Ebird rf amplifiers being developed at Watkins-Johnson utilize deflection modulation of the electron beam and a balanced twin target configuration. Without an rf input signal, the electron beam is deflected from one target to the other, and a positive portion of a sine wave generated is in one diode, the negative portion in the other. This technique allows true class B operation, and maximizes output power capability.

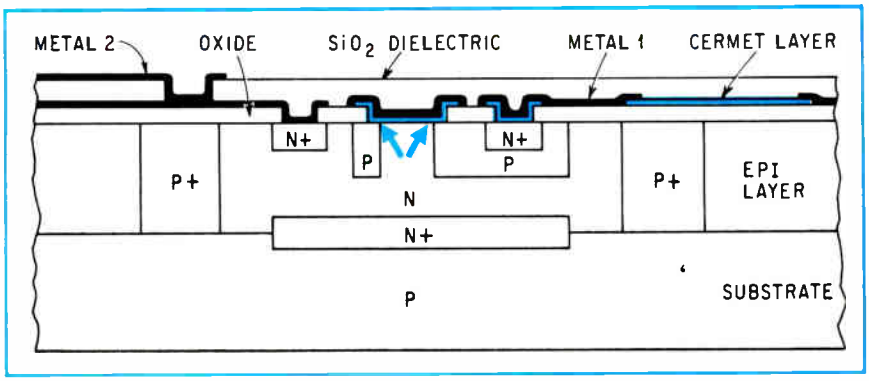
The basic frequency response characteristic of a lumped target Ebird amplifier is that of a low-pass amplifier. Limitations are imposed by the transit time for charge carriers through the diode, and by the diode's time constant. Rise times less than 1 nanosecond are predicted for practical diode geometrics, leading to upper-frequency limits well into the microwave range. With the rf amplifier's low-pass frequency response, a rise time of 1 ns implies an rf bandwidth from 0 to about 350 megahertz.

Integrated electronics

Thin-film cermet solves resistor problems

Diffused silicon resistors leave a lot to be desired. In precision divider networks their ratio tolerances can be as high as 200 parts per million per degree C while temperature coefficients might reach 2,000 or more ppm/°C. These are problems engineers at TRW systems group's microelectronics center have solved in developing a 64-bit large-scale integrated parallel correlator. The device will be used to measure the correlation between two digital words.

TRW engineers have been working on thin-film cermet resistors for about five years. By evaporating chromium silicon monoxide thin



Built in. In cross section of TRW correlator, ohmic contact is to the left of cermet resistor stripe where cermet meets n+ emitter region. Arrows indicate Schottky barrier diodes where cermet meets p region.

films on a monolithic circuit they obtain not only precision resistors, but also Schottky-barrier contacts to the collector region and nonpenetrating ohmic contacts to the emitter region all in one manufacturing step.

This technique produces resistors with ratio tolerances of 5 ppm/°C and an absolute temperature coefficient of 100 ppm/°C. David Breuer, section head in the applications section of the microelectronics center, says the type of contact—whether Schottky-barrier or ohmic—is determined by the impurity level of the silicon under the contact.

TRW's overall goal is to deliver to the Navy by next June a 256-bit correlator using 12,000 devices for a classified communications application. The 3,200-device, 64-bit correlator already has been built. It consists of two shift registers in which each bit position is compared by an exclusive OR circuit, whose output, in turn, controls a digital-to-analog converter for each bit.

In the 256-bit correlator, devices will operate in both a digital and an analog mode. The contract calls for digital propagation delays of less than 10 microseconds and analog accuracies of less than 5% over the full military temperature range.

TRW uses a standard epitaxial collector with a buried layer isolated from the top. The correlator's high-speed requirements dictate shallow transistor diffusions—typically 1 micron deep for the base and 3 microns deep for the emitter—to yield transistors operating at 2 to 3 gigahertz. Before the first metalization layer is applied, TRW evaporates a 300-to-400-angstrom-thick layer of chromium silicon monoxide over the wafer. Etching leaves a cermet resistor stripe pattern and a cermet region over the Schottky-barrier diode areas and over the emitter contacts.

Ohmic contacts between the cermet and silicon are established in those regions that previously received an emitter diffusion. "We want to put the Schottky-barrier diodes in direct parallel to base-collector junction," Breuer explains, "and we do this simply by leaving holes in the base region. We don't put an emitter in that region. The cermet goes directly onto the collector."

The diode is used to prevent the collector from approaching saturation—it would be slowed if it had to be brought out of saturation. The collector potential comes down only far enough to turn on the Schottky-barrier diode. When it does, the base current is shunted out of the active base into the Schottky diode, preventing saturation.

Cermet ohmic contacts also bar the metal penetration into the silicon emitters that occurs with aluminum.

Silicon gate joins C/MOS for wristwatch

Complementary MOS devices offer low power dissipation and low leakage currents, while silicon gate technology yields high-frequency operation and low threshold voltages. What happens if you combine them? Engineers in the central research laboratory at Motorola's Semiconductor Products division have found out by marrying the two technologies in a monolithic circuit. The results aren't laboratory curiosities; they've produced preproduction prototypes of a toggle flip-flop that's designed to function as the frequency divider or counter portion of the circuitry in a $50 electronic wristwatch.

Power consumption in an electronic watch must be low to allow operation on a 1.35-volt mercury battery or a 1.5-V silver oxide "button" for a year or more. Likewise, threshold voltage must be kept below 1.5 V. Ronald Burgess, section manager for process development in the laboratory, says Motorola is aiming at thresholds in the 0.50-to-0.75-V region on both the p- and n-channel transistors in its integrated circuits.

But some other characteristics of the flip-flops in the IC counters are more remarkable. R. Gary Daniels, project electronics engineer, points out that the really unique feature of Motorola's silicon-gate C/MOS flip-flops is operation at 1.5 megahertz from a 1.5-V supply. Moreover, they'll deliver 25 MHz if a 10-volt battery is available. Standby power dissipation at the lower frequency is only 6 nanowatts; power-frequency ratio is 23 nW per kilohertz. Minimum operating supply voltage is 0.9 V.

Daniels says metal-gate C/MOS flip-flops wouldn't provide the high-frequency micropower counter required in an electronic watch. "Without the silicon gate," he notes, "it would take 2 to 3 volts to operate this kind of device." He also points out that the self-alignment feature of silicon gate technology allows high-frequency op-

eration. In conventional metal-gate MOS devices overly large gates assure that the gate will overlap the source and drain. This leads to gate-to-drain capacitance, which, multiplied by the Miller feedback effect typical of these devices, slows the device considerably. The self-aligned silicon gate, however, minimizes capacitance.

Burgess and Daniels believe the most reliable circuit design for such a counter is a cascade of binary flip-flops.

At least 13 of the toggle flip-flops would be required for the counter portion of an electronic watch operating at a crystal frequency of 8,192 hertz; at 131 kHz, 17 flip-flops would be needed.

Cascaded IC flip-flops have been completed, they report, adding that they're processing a 70-mil-square LSI chip that includes not only the cascaded counter but also the oscillator and output buffer for an electronic watch.

The central research laboratory has put together working models of electronic wristwatches (not including the motor drive) using silicon-gate C/MOS devices for the oscillator. A 65.536 kHz quartz crystal has been used with a cascade of 16 flip-flops, each dividing the frequency by 2. Power consumption for the oscillator and counter with a 1.5-V battery is just 4 microwatts; with a 1.35-V battery, it's 3 μW, allowing either battery to operate the system for a year or more.

Burgess and Daniels will detail their work at the International Electron Devices meeting in Washington starting Oct. 28.

Commercial electronics

Photocathode matrix puts copier in new light

A new breed of office copier promising improved speed and simplicity may be possible with the development of a multiphotocathode device by CBS Laboratories.

In its primitive state, the device contains a 1-square-inch photocathode matrix mounted in a vacuum inside a glass envelope which also contains an anode ring. Sealed in the matrix are 62,500 tungsten pins, 0.001 inch in diameter and spaced 0.004 inch apart. Each pin is a photocathode, produced by a technique that selectively deposits photo-emitting material on each pin top.

Fast image conversion is the device's strong point. For example, latent image formation rate with a pin illumination of 100 foot-candles and a sensitivity of 100 microamperes/lumen is 0.01 second. But CBS Labs researchers admit that plenty of follow-up will be required before this technique seriously challenges the photoconductive approach in both resolution and cost areas.

To function inside an office copier the matrix will have to be larger, at least 8.5 x 0.5 inch with 265,525 pins, Robert J. Doyle of CBS will note at the International Electron Devices Conference in Washington beginning Oct. 28. A negatively charged dielectric mounted on a backplate would be located under the pin matrix and anode vacuum device. The image to be copied is projected past the anode to the photosensitive pins with a lens system. Then a 1,000-volt charge is applied—positive pole to the anode, negative to the backplate. At this point photoelectrons from the pins excited by the image bombard the anode, positively charging the dielectric below in the projected pattern. This forms the latent image of positive and negative fields on the dielectric.

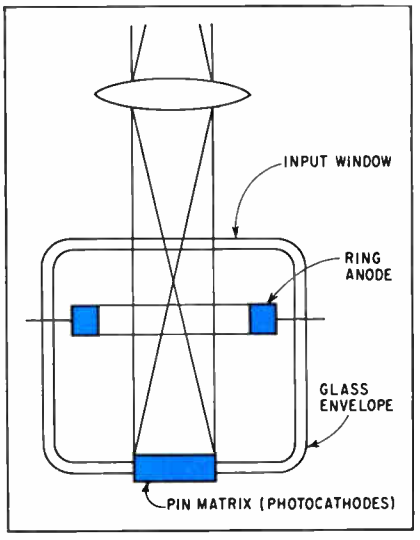
The remainder of the copying procedure is similar to present techniques. Negatively charged toner is spread over the latent image, adhering only to the positive sections. The copy, now visible, can be fixed chemically. Finally, the fixed image may be transferred to standard paper. Treated paper also can be used as the dielectric to omit the transfer step.

One advantage of the photocathode technique, says Doyle, is its ability to get continuous halftones by the pin screen, similar to the dot pattern that makes up photoengravings for newspapers. In addition, because the pins don't directly contact the dielectric, there is no wear. Only one voltage source is necessary, and there's no need to clear the neutral charge on the matrix. Finally, the image converter's light spectrum response depends on the photoemitter deposited on the pins, so it is possible to produce a matrix capable of converting infrared and ultraviolet light as well as the visual spectrum.

On the other hand, production presents a reliability problem. Every pin must be completely sealed in the glass body, a difficult manufacturing task. Moreover, the pins are more efficient if a fraction of the glass is etched away before the optical transmission is applied. This etch step increases the possibility of breaking the seal around the pins. And all of this adds up to high cost.

The 1-square-inch matrix assembly used in the initial experiments cost about $500. A similar device for a copier will measure 4.25

Copy mat. Pin matrix of photocathodes in new image converter is key to fast copying of images focused into glass envelope vacuum past anode ring.



square inches, thus increasing the likelihood of failure. To improve resolution, twin matrices probably will be necessary and three matrices will be required for a color copier. Despite the reliability problem, however, Doyle projects a full-scale production price of just $75.

Technology enters the science class

While most faculties are meeting student demands for curricular relevance by redesigning existing courses, one interdisciplinary group is taking a more radical approach. By teaching high-school students how to use some electronic equipment, the group's curriculum project brings them face to face with modern technology.

The project, which is called "The Man-Made World," hinges on a small analog computer made by AMF. Other equipment includes a specially developed logic circuit board, a card reader and switch demonstrator for the board, a resonant circuit board, a relay demonstrator, and a torque amplifier. The text and lab manual will be published by McGraw-Hill's Webster Division, Manchester, Mo., in April. Total minimum course cost for tests and materials is about $2,400.

An important part of "The Man-Made World" is the study of decision-making. Using modeling, algorithms, and optimization, the student examines the problems of today's social systems to which there are no obvious solutions. An example is the urban housing crisis, created by an economic situation that simultaneously discourages the construction of new houses and causes abandonment of buildings. The student works out systems for solving such problems, and finally is taught the principle around which the whole solution method is built.

The course also helps the student make valid predictions for models, and to communicate with machines effectively. Its emphasis is not so much on how to write programs, however, as on how the computer works and what kind of problems it can solve.

Moreover, in contrast to contemporary science teaching, with its emphasis on conceptual material, "The Man-Made World" presents problems of personal interest to the student, such as pollution, population, health services, computers, and traffic control.

John Truxal, academic vice president of the Polytechnic Institute of Brooklyn and director of the project with Edward E. David [see p. 22], says he hopes the course will educate a generation not to be afraid of technology and its implications. "People exhibit a terrible antipathy to, and distrust of, electronics technology, for example," Truxal said. "They need to understand that a computer is limited by the imagination and creativity of its master."

Materials

GaP enters ballgame against cold cathodes

Gallium phosphide always has been considered a highly efficient—but expensive—light emitter with possible application as a numeric indicator. But now a year-old New Jersey company, Opcoa Inc., says it can produce the crystals in quantity, and will sell a GaP indicator at a price almost as low as that of cold cathode tubes.

Opcoa is using a technique developed at Bell Laboratories to produce GaP crystals in sufficient quantity to make displays economically possible. Opcoa's seven-segment bar indicator will sell for $8 in 1,000-unit quantities, and "for less than half of that in very large quantities," says company president Aaron Kestenbaum. Gas-discharge display tubes, by comparison, are priced in the $3-to-$4 range.

Gallium arsenide phosphide (GaAsP) diodes have been the exclusive source for solid state displays. In the two years GaAsP displays have been available, their price has fallen dramatically, but the per-digit price is still in the $12 area in 1,000-unit lots.

GaP displays are less expensive because the material has a much higher quantum efficiency than GaAsP. Even though the eye is more sensitive to GaAsP's 6,600-angstrom radiation than to GaP's 6,900-Å light, overall ratio of visible light out to power in is still three times higher for GaP, says Richard Ahrons, Opcoa's vice president and engineering chief. As a result, less material is needed for a given job. The raw materials for GaAsP and for GaP cost about the same. Opcoa, under a general licensing agreement with Western Electric, uses Bell's technique for making its GaP diodes. The method demands some relatively expensive equipment, some of which isn't even available—Opcoa had to build its own ovens for the two liquid-phase

Growing GaP

Opcoa follows a process developed at Bell Laboratories to make gallium phosphide diodes. The material is grown by the Czochralski method. For GaP the rotating and pulling process takes place at approximately 1,500°C in a nitrogen atmosphere at 1,000 psi.

The resulting crystal is cone shaped, about 1 inch in diameter, and from 2 to 3 inches high. While growing, the crystal is doped with an n-type material, such as telurium or sulfur. This doped crystal then is sliced into wafers 15 mils thick which are lapped and polished to a 10-mil thickness.

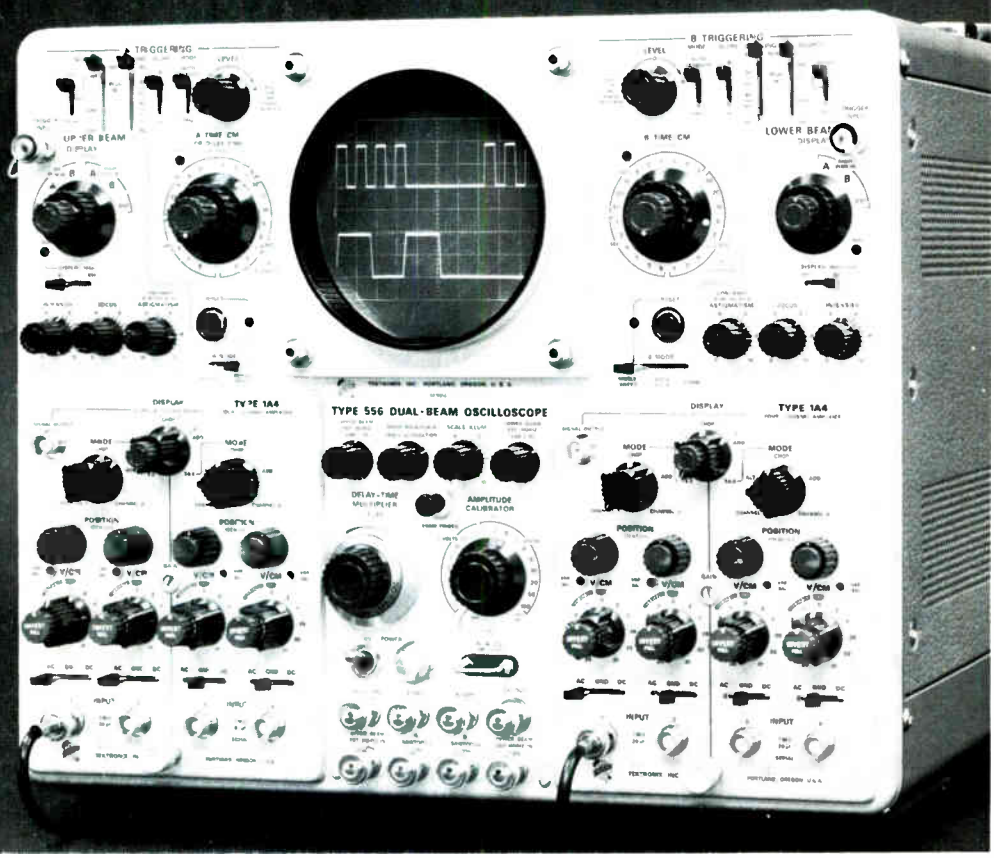
Next, a 2-mil-thick layer of GaP, also doped with an n-type material, is grown on the wafers by a liquid-phase epitaxial process. Then a second 2-mil layer of GaP is grown by the same technique. However, this layer is doped with zinc and oxygen so that a pn junction is formed between the two epitaxial layers. Each wafer is diced into 1,000 diodes 15 mils on a side.

50-MHz dual-beam oscilloscope

556

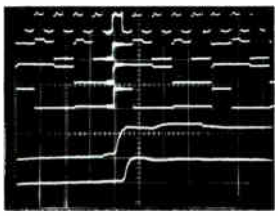
delayed sweep

7-ns risetime



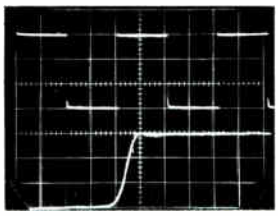
Multi-Trace

The six waveforms are time related digital pulses. The upper four displays are A Sweep (2 μs/cm) with the Type 1A4 Four-Channel Plug-In. The lower two displays are B Sweep Delayed (100 ns/cm) with the Type 1A2 Dual-Trace Plug-In.



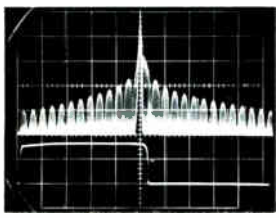
Sampling and Real-Time

The upper beam shows a square wave at 2 μs/cm as applied to a Type 1A2 Dual-Trace Plug-In. The lower beam shows the risetime of the same pulse with the Type 1S1 Sampling Plug-In at 1 ns/cm.



Frequency and Time

The upper beam shows the spectral output of a 200 MHz gated oscillator applied to the Type 1L20 Spectrum Analyzer; calibrated dispersion is 1 MHz/cm. The lower beam shows a real-time display of the 2.5 μs gating pulse.



The Tektronix 556 Dual-Beam Oscilloscope features 50-MHz bandwidth, calibrated sweep delay, 6 x 10 cm scan per beam and dual plug-in flexibility. Using two plug-ins at a time, the 556 offers many display combinations, including: **dual-beam single-shot;** multiple-trace; sampling and real-time; frequency and time; delaying and delayed sweep.

The two independent horizontal deflection systems provide full bandwidth triggering and calibrated sweep speeds from 5 s/cm to 100 ns/cm, extending to 10 ns/cm with the X10 magnifier. The calibrated sweep delay range is from 100 ns to 50 seconds.

The CRT shows two simultaneous single-shot pulse sequences displayed at two different sweep speeds, a measurement that is possible only with a truly dual-beam oscilloscope. The two 1A4 Four-Channel Plug-Ins provide eight channels, each with 7-ns risetime and DC to 50 MHz bandwidth. You can also select from differential plug-ins with bandwidths to 50 MHz, TDR and sampling plug-ins with 90-ps risetime, and spectrum analyzer plug-ins that cover the spectrum from 50 Hz to 40 GHz. The 556 is also available in a rackmount model.

For a demonstration, contact your nearby Tektronix field engineer or write: Tektronix, Inc., P. O. Box 500, Beaverton, Oregon 97005.

556 Dual-Beam Oscilloscope	**$3700**
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epitaxial growth steps. There are no diffusion steps involved. The epitaxial steps, says Ahrons, are relatively easy to control and he characterizes yields as "extremely high."

Opcoa's indicator resembles the GaAsP indicators sold by Hewlett-Packard and Monsanto. However, each segment in the Opcoa device has only one diode, 15 mils by 15 mils, not two. And at 0.334 inch, the Opcoa character itself is a bit higher than those in GaAsP displays. The seven diodes required are mounted on a ceramic base, capped with a faceplate that has seven long rectangular holes. Plastic fills each hole, and reflects the light coming from an energized diode. The reflection spreads the light over the entire segment. A lighted segment appears not as a uniform light source, but as a row of about four or five sources.

Solid state

Resistance probe has gentle touch

The pressure exerted by standard four-point testing probes used on semiconductor fabrication lines isn't uniform, but if it were, it would come to around 1.5 million pounds per square inch—quite a jolt for a silicon wafer. And that, says Solid State Measurements, a new company in Murrysville, Pa., is one of the reasons it expects to find a more than willing market for its automatic spreading resistance probe.

The company itself has been set up by two ex-Westinghouse Electric Corp. employees, Harold F. John, who is president, and Robert G. Mazur, vice president. Their spreading resistance probe was developed at Westinghouse, which has just licensed Solid State Measurements. The first two $17,750 systems will be delivered this month to Bell Laboratories and Canadian Westinghouse, and John and Mazur promise two-month delivery.

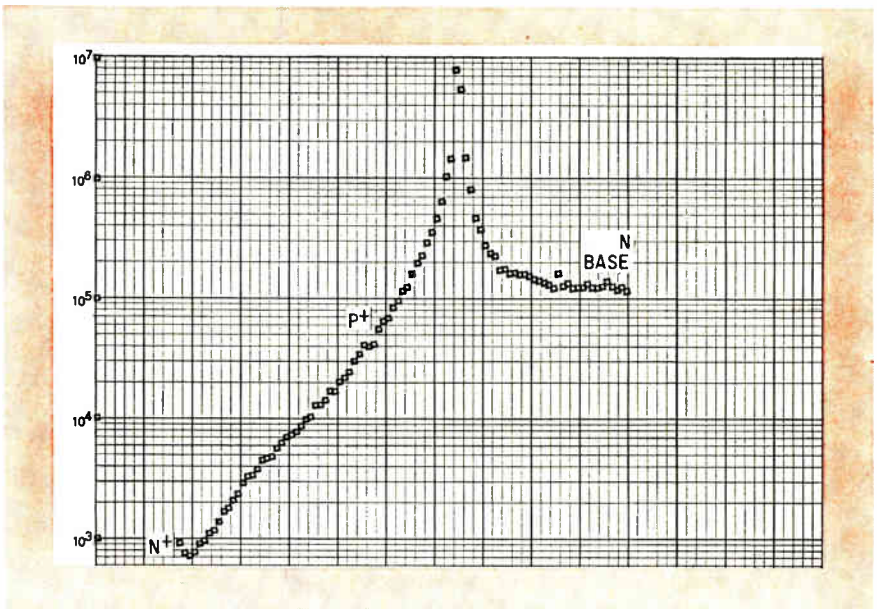
The pressure problem is solved, says Mazur, with a pneumatic control. This means that the pressure is reproducible. What's more, the control provides resiliency so that the probes' load is transferred to the surface of the sample gradually —in a couple of hundred milliseconds. Mazur agrees that other probe testers in use today are doing basically the same thing, but his, he says, is the only one available commercially.

But Solid State Measurements isn't basing its pitch on pressure alone. Another key feature of its test system, says president John, is the probes' smaller radius of curvature than that of other versions. He says that his spreading resistance probe has a radius of 1.2 mils compared to "5 mils or so" for others. And he adds that his tester can do what capacitance testers do plus examine as many as 19 layers with no limit to high or low resistivity or thickness of layers—and that includes integrated circuits.

The system, as it's used at Westinghouse's Semiconductor division, gives a printed diffusion profile that can dramatically increase yields and cut costs when taken early in the fabrication process, before wafers go through their many steps. In fact, at Westinghouse its use has led to the establishment of a so-called diffusion bank in which wafers are stored according to characteristics, enabling the company to promise 54-hour delivery on certain devices.

The tester works by applying a 10 millivolt bias to its probes. Current is measured and plotted on an x-y point plotter. At the same time, a digital voltmeter gives a visual reading. After analog to digital conversion, a digital interface converts the DVM output to ASCII code for punched tape. The data then is fed to a computer for analysis and instructions.

And it comes out here. Diffusion profile taken by automatic spreading resistance probe shows thyristor profile.

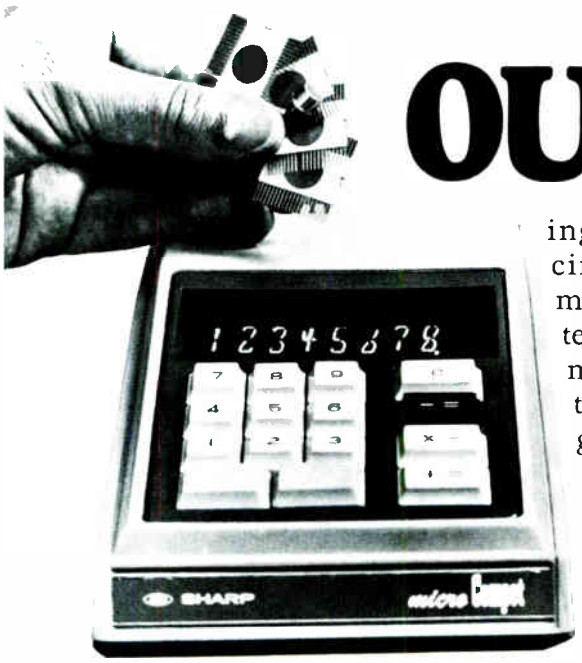


Memories

MNOS in bipolar device could increase speed

MNOS (metal nitride oxide silicon) structures, which are common enough in field-effect memory circuits, may also be usable in bipolar devices, suggests a Fairchild R&D engineer, B.A. McDonald. He says the surface potential dependence on the base and collector currents in a bipolar transistor can be com-

SHARP FIGURES WITH MOS/LSI.



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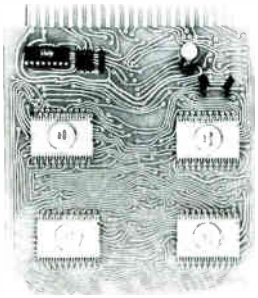

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North American Rockwell Microelectronics Company

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bined with charge storage effects of the MNOS gate to create a programable bipolar transistor. The result may be faster memories.

The idea is that, if an MNOS gate is put over the emitter/base junction of a bipolar transistor, the base surface beneath the gate can be shifted from accumulation to inversion because of the stored charge in the MNOS gate. The inversion layer beneath the gate acts as an efficient emitter, according to McDonald, which results in a "designable increase in collector current at fixed forward emitter base bias." And the technique applies to both npn and pnp devices.

In the case of npn devices, McDonald says that the normal positive interface charge results in the inversion of the base surface. "Under this condition, the base current consists principally of recombination within the field-induced junction. Collector current at fixed V_{be} is at maximum, since the inversion layer, which is electrically tied to the diffused emitter, results in additional minority carrier injection," he explains. Since the diffused emitters in the devices are very shallow, the base q beneath the inversion layer is essentially equal to that beneath the diffused emitter.

Under a positive gate pulse of 25 volts, enough negative charge is stored within the MNOS structure to accumulate at the base surface. This results in minimum base and collector currents since the base current now consists of recombination within the metallurgical junction only, and minority carrier injection takes place from the diffused emitter only.

A negative gate pulse of 25 V returns the device to the inverted mode. The ratio of collector currents between modes was found to be equal to the ratio of the summed areas of both the inversion layer and diffused emitter to the area of the diffused emitter alone.

With pnp devices, the normal positive interface charge results in base surface accumulation. Hence pnp devices require positive gate voltage to invert the base surface.

The principal advantage that this type of device has over conventional MNOS devices is its inherently higher transconductance. While both types of devices rely upon the conductance of the inversion layer for charge transport, the inversion layer in the case of the MNOS bipolar injects over its entire length. This reduces the effective channel resistance for a given read current, so decreasing the read access time.

Lasers

MIT radar uses cw carbon dioxide system

Laser ranging systems have bounced light pulses off the moon for years, but laser radar is still in its infancy. The effort now is to develop continuous-wave laser systems capable of doppler tracking along with a crude imaging capability. The military wants these systems for warhead discrimination and satellite tracking.

Perhaps the most powerful cw laser radar has been running for about 18 months at MIT's Lincoln Laboratory in experiments performed by research scientists T.J. Gilmartin, H.A. Bostick, and L.J. Sullivan. The system uses mixer and master oscillator carbon dioxide lasers. Close tolerances on cavity dimensions result in output frequencies that vary by less than 1 kilohertz over several seconds. And since these signals are mixed to give the doppler output of the radar system during tracking, that subkilohertz figure translates into a velocity measurement error of only 0.012 miles per hour.

The transmitter boosts the master oscillator's 10.6-micron output first in a 100-watt preamplifier, then in a 1,400-W final stage. Mirrors are used to move the beam through azimuth and elevation readings. Transmitter aperture is 48 centimeters and the beam is nearly diffraction limited, its shape is as well controlled as system optics will allow.

The team has tried about six tracking methods, ranging from hand wheels to computer control. One technique scans the beam through a small conical angle, establishing a null in the center of the cone and allowing automatic tracking somewhat like that of monopulse radars. Using this system, the Lincoln Lab team has been able to track an airborne 2.5-centimeter-diameter corner reflector as far away as 30 kilometers with a tracking error of only two inches rms.

Good as that is, the team feels it can be bettered and is experimenting with new conical scan frequencies and detectors to improve the system's signal-to-noise ratio. Gains here should allow the system's mechanical components to respond to faster-moving targets—the laser system alone can register targets moving at radial velocities of about 15,000 mph with the latest available detectors, but the relative speed with which nearby targets could cross the radar's field of view would be too fast for the present beam steering system.

Pulsed experiments are coming up. The final stage CO_2 laser amplifier is capable of 15-kilowatt pulses at 10,000 per second. These experiments will have the incoming and outgoing beams duplexed through a larger 50-centimeter aperture, and with the five-microsecond-long high-power pulses, the lab team is looking for a 20-decibel signal-to-noise ratio improvement with a true monopulse detector array of four copper-doped germanium photo diodes. This not only should increase the system's range capability, but help shave its tracking error to mark nearer the potential of the laser system in general.

Employment

Sinking feeling in the Bay State

While the nation broods over an unemployment rate of 5% to 6%, electronics engineers in Massachusetts may face a job shortage three times as severe. Harold S. Gold-

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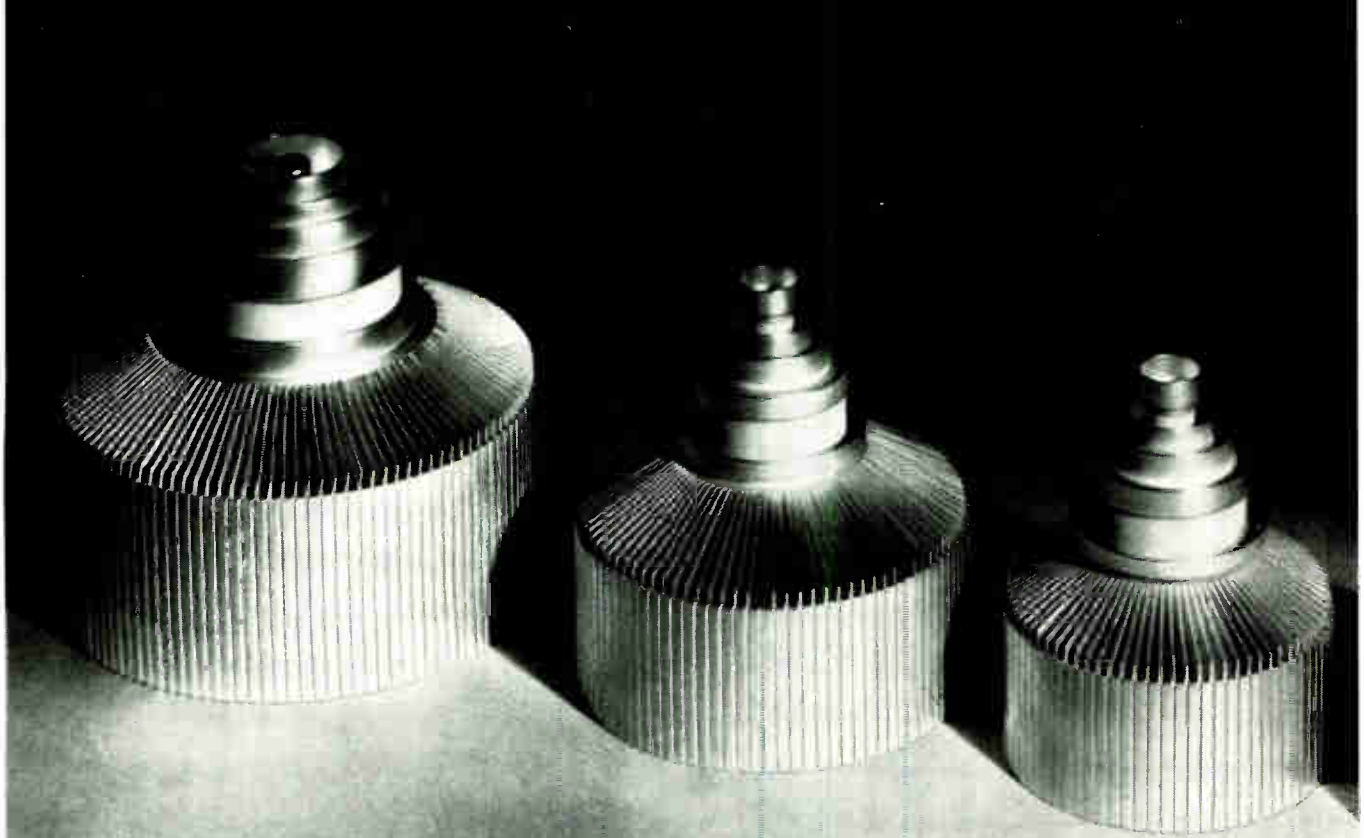

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berg, chairman of the IEEE Boston section and applications vice president at the Applicon Corp., says this estimate may be conservative. "We started with the idea of a 10% unemployment rate, but every added bit of information pushes the figure upward."

One employment agency official scoffed at the suggestion that there were only 2,000 unemployed electronics engineers out of 18,000 in the Boston area. "There are a hell of a lot more than that," he says, adding, "Any day you want only 2,000, I can put them on your doorstep."

Goldberg notes that precise figures are hard to nail down. "The firms in this area making layoffs have been successful in keeping them quiet. They've laid off many people, but a few at a time."

Raytheon is a prime example. Reorganization of its communications and data processing organization, its Equipment division, and particularly its Missile Systems division has proceeded quietly over the past 12 months. But total cuts for the firm, which used to be known as a safe haven for electronics engineers, are estimated to exceed 4,500. No one can guess how many of those laid off were EEs.

Other firms have been more obvious: Itek now employs 1,400 instead of 1,600; Texas Instruments has laid off more than 200 of its 5,000-man staff; Teradyne has released 15% of its employees (about 100 men) and imposed graduated salary cuts as test gear sales slowed; Sprague Electric in North Adams has closed two of four plants and cut salaries by 7% to 12%. What's more, North Adams has made the Department of Labor's list of regions of "substantial unemployment." It's also the first major town in the state to go over the 6% overall unemployment rate.

Most recently, about 1,200 jobs were eliminated with the closing of Sylvania's Semiconductor division [*Electronics*, Oct. 12, p. 46].

A report from a panel headed by Albert J. Kelley, dean of Boston College's School of Management, is filled with gloom. Kelley's state-sponsored study found that almost all defense-oriented companies in the state have had "significant" layoffs over the past six months.

The report notes that these and other layoffs have broken up talented research and development teams. Furthermore, "many of the scientists and engineers in the defense-oriented firms never have been involved with commercial products and markets." Thus, it concludes, such men may be doubly unemployable, because not only do most aerospace firms in the Boston area depend on government R&D funds that are shrinking, but they also seem disinclined to diversify into commercial markets.

Things could get worse in Massachusetts. According to a new study by Arthur D. Little Inc., 25,000 to 30,000 defense-related jobs could be lost by the end of 1972 if spending cuts continue at present rates.

Communications

Time or frequency: that's multiplex question

It's agreed that time division multiplex is best for large key systems with about 50 stations. But when it comes to smaller systems, opinions diverge: Bell Laboratories sticks to time division while the General Telephone and Electronics Labs prefers frequency division.

The newer of the two systems—both of which are still prototypes—is GT&E's. It reduces the number of wires for a six-button set to four; two extra wires are required for each additional six buttons, with the limit at 14 wires and 36 buttons. (Present key systems require 50 wires for six-button sets and 120 wires for 18-button units.) The wires run from the telephone stations to a centrally located Electronic Key Service Unit (EKSU).

One pair of the wires linking the basic key telephone set to the EKSU carries voice and user commands when a button is depressed. The other pair carries these control signals from the EKSU back to each telephone station.

Independent control of the different functions required at the key set is accomplished by frequency division multiplexing of the supervisory signals. Six out-of-band (18 to 28 kilohertz) carriers, one for each key in the set, are transmitted to the station, and serve for both line pickup and local supervision. When a key is depressed, one of the six carriers is coupled to the voice pair and relayed to the EKSU, which picks it up and identifies which key was depressed and what the associated command is. Decisions are made by the circuit logic, which allows the EKSU to respond to the user's commands by actuating a space division crosspoint switch. In response to the signals generated by the line circuit and station logic, a status signal is generated for each line. These status signals indirectly modulate the amplitude of the associated carriers transmitted to each station. And as the amplitude changes, lights on the phone go on and off.

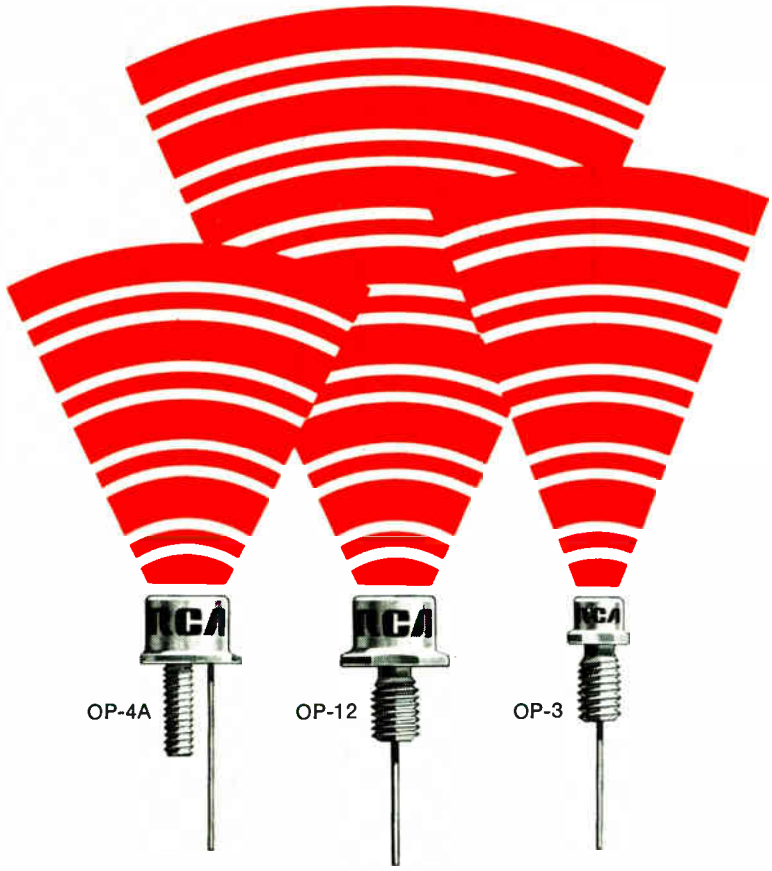
The Bell system, called the Modular Electronic Key Telephone System (MEKTS), is organized on a circuit-per-function basis where each circuit is semi-autonomous. It uses a small fixed sequencer, and employs modular distributed processor facilities as the system size increases. This system requires only six wires per station, no matter how many buttons are required; the limit is 60 buttons and is a function of power. Two wires are used for voice, two transmit data from the station, and two receive data from the station.

Instrumentation

IC wafers probed at 2 GHz and beyond

One thing led to another at the Hewlett-Packard Corp. in Palo Alto, Calif., where a new line of high-frequency counters required development of a new technique

Performance pacers! RCA's new GaAs lasers



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Single-diode lasers
- efficiency—4%
- wavelength—905 nanometers
- V_f at max. drive— 9 V typ.
- pulse duration—0.2 μs (max.)*
- case polarity—negative

Type	Pkg.	Power Output (W) Min.	Power Output (W) Typ.	I_{FM}(A)	No. of Diodes	Source Size (mil)	Duty Factor (%)	(Typ.) Threshold Current A
TA7606	OP-3	1	2	10	—	3 x 0.08	0.1	4
TA7607	OP-3	5	6	20	—	6 x 0.08	0.1	7
TA7608	OP-3	5	6	25	—	6 x 0.08	0.1	7
TA7609	OP-3	10	13	30	—	9 x 0.08	0.1	10
TA7610	OP-3	10	13	40	—	9 x 0.08	0.1	10
TA7925	OP-3	15	21	60	—	16 x 0.08	0.02	20
TA7699	OP-3	15	23	75	—	16 x 0.08	0.02	20
TA7864	OP-12	25	30	75	—	24 x 0.08	0.02	24
TA7763	OP-12	25	30	100	—	24 x 0.08	0.02	30
TA7705	OP-12	40	50	250	—	55 x 0.08	0.005	75
TA7787	OP-12	60	65	250	—	55 x 0.08	0.005	75

*0.1 μs max. for TA7705 and TA7787

Laser Arrays
- wavelength—905 nanometers
- series wired
- drive current—25 A
- case polarity—negative

Type	Pkg.	Power Output (W) Min.	Power Output (W) Typ.	I_{FM}(A)	No. of Diodes	Source Size (mil)	Duty Factor (%)	(Typ.) Threshold Current A
TA7687	OP-4A	25	50	25	10	100 x 0.08	0.02	8
TA7688	OP-4A	35	75	25	15	150 x 0.08	0.02	8
TA7689	OP-4A	50	100	25	20	110 x 40	0.02	8
TA7690	OP-4A	75	150	25	30	160 x 40	0.02	8
TA7691	OP-4A	100	200	25	40	110 x 60	0.02	8
TA7692	OP-4A	150	300	25	60	160 x 60	0.02	8

Laser stacks
- wavelength—905 nanometers
- series wired
- pulse duration—0.2 μs (max.)

TA7764	OP-12	25	30	40	3	10 x 10	0.01	14
TA7765	OP-12	50	60	100	2	25 x 4	0.01	23

Note: new types are shown in bold face.

Circle 49 on reader service card

for testing such monolithic integrated circuits, at frequencies in excess of 2 gigahertz while still at the wafer stage. Crucial to the technique, which will be described in detail at the International Electron Devices Meeting, Oct. 28 in Washington, D.C., is a test transistor that's placed on each chip on a wafer.

H-P engineers wanted a monolithic IC counter capable of 500-megahertz operation as part of the new line that the company plans to introduce in December. No such device was available, so they had to design one. And once it was designed and built, it had to be tested.

The combination of a special high-frequency probe, a sophisticated software package, and an H-P network analyzer measures both large and small signal parameters and accumulates enough data to give a complete picture of the wafer. According to Merrill Brooksby, manager of h-f counter development at H-P's Santa Clara division, the transistor can be completely characterized at frequencies from 100 MHz through 2 GHz while still part of a wafer.

"This allows the process engineer to know immediately whether the wafer is good," says Brooksby, "and also how good it is." All the information is stored on cassettes for future analysis.

The system is programed to step the frequency, current, and voltage applied to the device and to measure a complete set of s-parameters at each point. Processed information is put out on a bar graph plotter in the form of a probability density function and the distribution function of the wafer for any specified parameter. The mean and standard deviation are also indicated on each plot.

Brooksby says that the probing system is also used for checking complete circuits for large signal dynamic characteristics and functional operation. "Digital ICs have been tested at frequencies greater than 700 MHz," he says, "and the results are better than those obtained from the packaged circuits."

For the record

H-P pushing components. Instrument-maker Hewlett-Packard is reaching for the high-volume components business. Having recently developed its version of the high-performance microwave transistor, HP21, [*Electronics*, March 30, p. 33], H-P's going full blast to market—and at prices that should give pause to the big transistor makers. H-P's small-quantity price tag for its F_{max}=15-gigahertz transistor: $15 for the chip, $19 in the stripline package.

Price aside, the HP21's performance is up to its competitors' specs. For example, at a frequency as high as 8 GHz it offers 3-dB gain with a power output of approximately 4 milliwatts, whereas 4-GHz output is 100 mW. Typical noise figures: 3 dB at 1 GHz, 5.5 dB at 8 GHz.

Standard package. In an effort to standardize MOS packaging, nine manufacturers in committee are considering a 22-pin package as a compromise. The configuration, to be presented at the committee's next meeting, consists of a large-cavity, 22-lead ceramic package with 0.400-inch lead spacing.

"The 22-pin package is a give-and-take situation," Ken Moyle of Intersil Memory Corp., chairman of the committee, says. "But if we can settle on a single package, it will be cheaper for all of us. And the package makers, too, can tool up for high-volume production." Other companies represented are Advanced Memory Systems, American Micro Systems, Electronic Arrays, Fairchild Semiconductor, Intel, National Semiconductor, Signetics, and Motorola Semiconductor.

GaAs, glass in sea. Researchers have started to promote gallium arsenide and electronic conducting glass as resistive sea materials. The resistive sea, in vidicon tubes, is the thin film covering the silicon diode array. It prevents current leakage from diode to diode while permitting leakage outward.

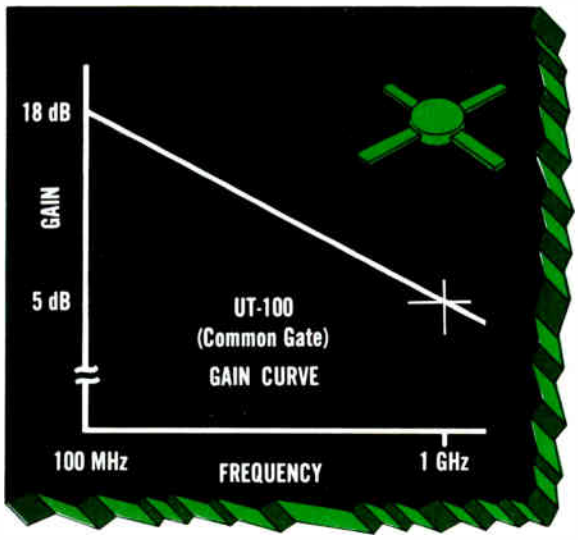
The material currently used for this purpose is antimony trisulfide but both GaAs and electronic glass permit vacuum processing at higher temperatures, yielding better outgassing of the tubes as well as improved performance and longer life.

When GaAs is used, beam acceptance by the target is two to three times better, tube aging is improved, lag (smearing on the screen) is reduced by a third to a half, and picture resolution is improved, according to Bell Laboratories' Hatsuaki Fukui and F.J. Morris. Reduced lag and improved performance are also claimed by R.H. Wilson of General Electric, whose group is advocating a resistive sea of glass that's made electronically conductive by being combined with other substances.

TI computer coming on. Texas Instruments' big computer project (Advanced Scientific Computer, or ASC) is on schedule, according to a source connected with the project. TI has built and purchased enough read/write magnetic heads to assemble one machine at its Austin, Tex., plant. Reports indicate that some minor speed problems have been encountered.

Sale. Sylvania Electric Products has agreed to sell its microwave department to Alpha Industries of Newton, Mass.

Cheerless Yule. Tektronix employees can expect a payless two-week Christmas vacation. The reason, says the company, is that inventory is piling up too fast. At the same time, two Maryland rental firms—Rental Electronics and Rentronix—are trying to sign instrument makers to a trade-in deal. Rental Electronics says it's close to a deal with Monsanto under which that firm will accept Tektronix scopes in trade toward any of its instrument line. Rental adds that it is negotiating a similar deal with Singer-General Precision.



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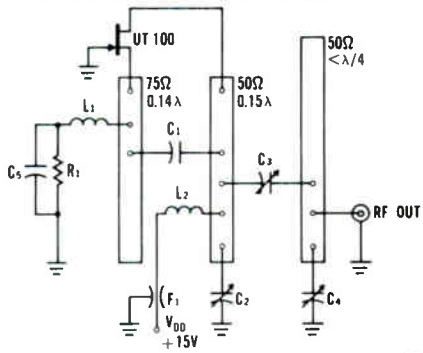

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Circle 51 on reader service card

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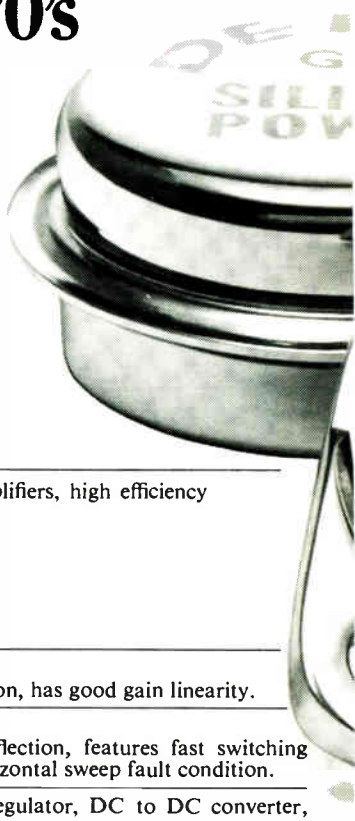

* Circle no.	Transistor	V_{CEX} Voltage	Continuous I_C	V_{CEO} (sus)	Maximum Power Dissipation	Typical Applications
310	DTS 103	80V	15A	60V	125W	Voltage regulators, power amplifiers, high efficiency switching circuits.
311	DTS 104	80V	15A	60V	125W	
312	DTS 105	100V	15A	75V	125W	
313	DTS 106	110V	15A	80V	125W	
314	DTS 107	120V	15A	85V	125W	*I_C Peak = 5A Vertical magnetic CRT deflection, has good gain linearity.
315	DTS 401	400V	2A*	300V		
316	DTS 402	700V	3.5A*	325V		*I_C Peak = 10A Horizontal magnetic CRT deflection, features fast switching time, high reliability under horizontal sweep fault condition.
317	DTS 410	200V	3.5A	200V	80W	
318	DTS 411	300V	3.5A	300V	100W	Voltage regulator, switching regulator, DC to DC converter, class A audio amplifiers.
319	DTS 413	400V	2.0A	325V	75W	*I_C Peak = 10A High V_{CBO} and V_{CEO} ratings make it practical to operate directly from rectifier 117V or 220V AC line.
320	DTS 423	400V	3.5A*	325V	100W	
321	DTS 424	700V	3.5A*	350V	100W	*I_C Peak = 10A High V_{CBO}, V_{CEO}(sus) ratings make them ideal for use in deflection circuits, switching regulators and line operating amplifiers.
322	DTS 425	700V	3.5A	400V	100W	
323	DTS 430	400V	5A	300V	125W	Voltage regulators, power amplifiers, high voltage switching.
324	DTS 431	400V	5A	325V	125W	
325	DTS 701	800V	1A	600V	50W	Vertical magnetic CRT deflection circuits.
326	DTS 702	1200V	3A	750V	50W	Horizontal magnetic CRT deflection circuits operating off-line.
327	DTS 704	1400V	3A	800V	50W	
328	DTS 721	1000V	3A	800V	50W	High voltage DC regulators.
329	DTS 723	1200V	3A	750V	50W	Very high voltage industrial and commercial switching.
330	DTS 801	1000V	2A	700V	100W	Color vertical magnetic CRT deflection circuits.
331	DTS 802	1200V	5A	750V	100W	Color horizontal magnetic CRT deflection circuits.
332	DTS 804	1400V	5A	800V	100W	
333	2N3902†	700V	3.5A*	325V	100W	*I_C Peak = 10A Ideal for switching applications. Can be operated from rectified 117 or 220 volt AC line.
334	2N5157	700V	3.5A*	400V	100W	
335	2N5241	400V	5A	325V	125W	
336	2N2580	400V	10A	325V	150W	For general use in electrical and electronic circuits such as converters, inverters, regulators, etc.
337	2N2581	400V	10A	325V	150W	
271	2N2582	500V	10A	325V	150W	
272	2N2583	500V	10A	325V	150W	
273	2N3079	200V	10A	200V		
274	2N3080	300V	10A	300V		

†Mil. qualified units available.

Transistors are NPN triple diffused.

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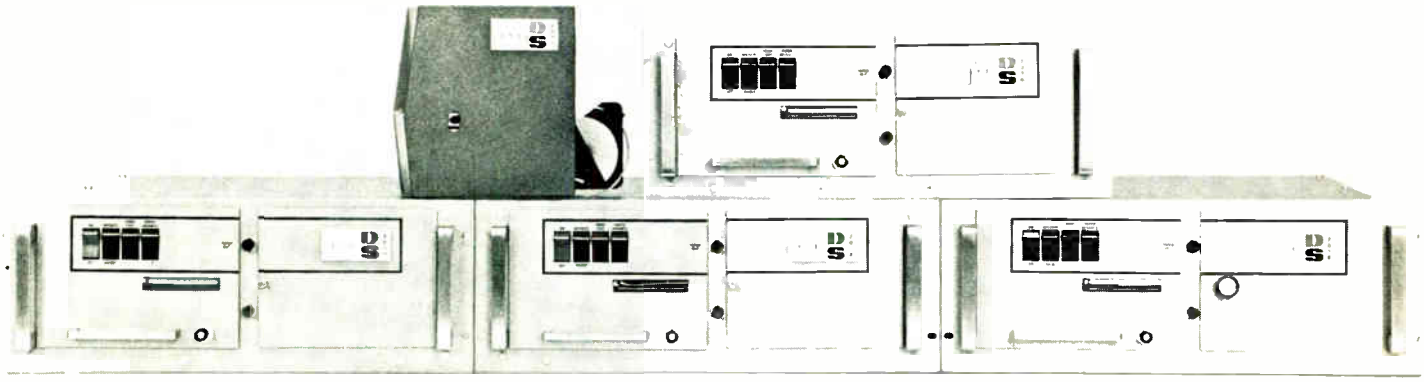

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Circle 57 on reader service card

An Introduction to—

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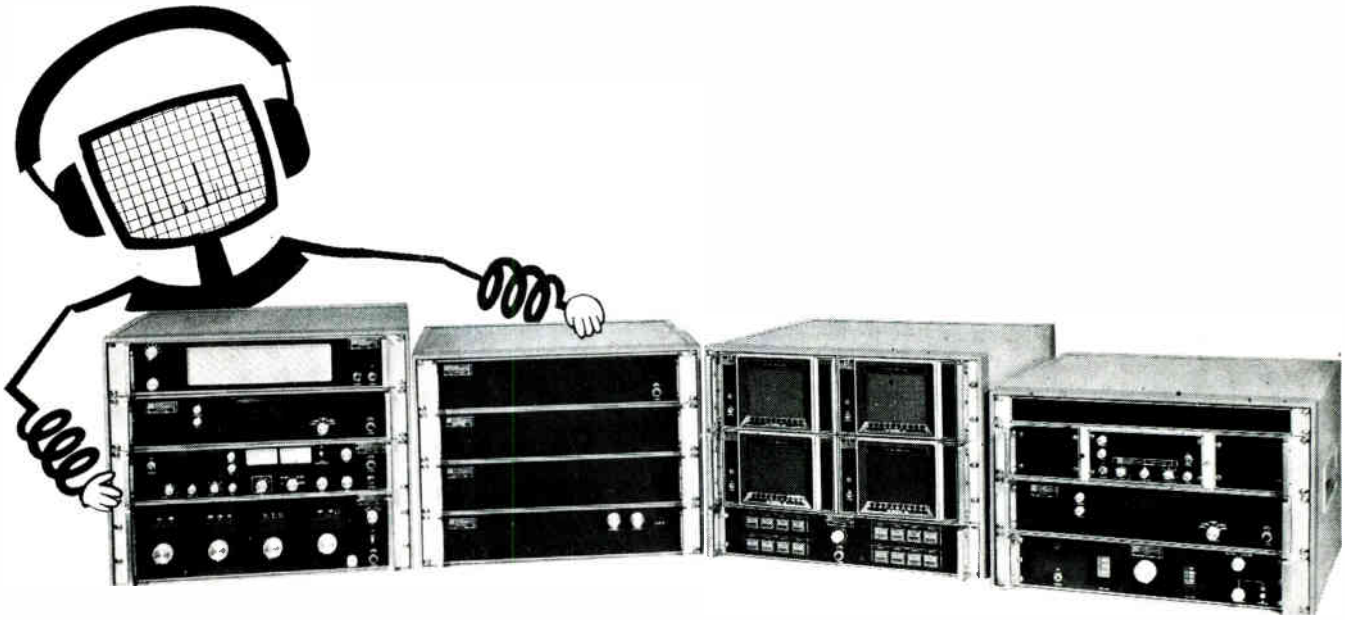

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Washington Newsletter

October 26, 1970

USAF scrambling to counter EMP threat

An accelerated Air Force program is under way to harden strategic weapons against pulses of electromagnetic energy from nuclear explosions outside the atmosphere. Since one or two well-placed bursts could create broadband rf fields that could knock out most Minuteman missile electronics, the Air Force is pouring funds into simulators that will test missile systems' resistance to electromagnetic pulses (EMP). One such simulator, called Ares, has just been completed at the Air Force Weapons Laboratory, Kirtland Air Force Base, N.M. It uses capacitative generators to pulse extra-high-voltage currents through wires suspended by four towers over a concrete pad. **To test electronics buried in silos, the laboratory is asking industry for bids on lighter-than-air or glider-like simulators thousands of feet long that could be towed over silos by helicopters.**

Industry expects no WWMCCS money after House cuts

Even though a final defense appropriation bill will not emerge until Congress reconvenes after the elections, at least two major computer makers have given up hopes of any funding for the controversial World-Wide Military Command and Control System (WWMCCS). **Their expectations were dashed by a preadjournment House cut of $6.4 million in Defense Communications Agency procurement money, coupled with the admonition to the Pentagon that it "should not allow the services any funds to implement the system."** Though the Pentagon wants the $6.4 million restored in a Senate-approved bill, current betting is that the House will prevail and the best that can be done is to get half back. Congress, still awaiting a General Accounting Office report on WWMCCS, **is disturbed at reports that the final cost is likely to run over $500 million.** "At this point, we're not counting on anything for this year," says one competitor, suggesting that "fiscal 1972 will be the big funding year," if it can be salvaged at all.

Navy spells out specs, pricetags for AADC system

Specifications for the Navy's Advanced Airborne Digital Computer are being nailed down. **The Naval Air Systems Command says it will first ask industry to build a simplex processor using three memories:** a 10,000 word-by-32-bit random access module using closed-flux thin film and costing between $3,200 and $16,000 depending on size of procurement; a 70,000-word ferroacoustic bulk storage module costing between $2,240 and $5,400; and a 2,000-word semiconductor or thin film task memory costing between $2,700 and $3,000. **The Navy also is seeking a 2-million-operation-per-second processor module with fixed- and floating-point arithmetic for $1,800 to $3,000.** The largest of the four modules will be the bulk storage unit, which Navair says can weigh no more than 10 pounds and be no larger than 448 cubic inches.

FAA, NASA to define Aerosat roles

Top officials of the Federal Aviation Administration and the National Aeronautics and Space Administration will be closeted for several weeks **to iron out conflicts over the roles that the two agencies will play in developing aeronautical services satellites** [*Electronics*, Sept. 28, p. 52]. The FAA holds the upper hand, since the Office of Management and

Budget is expected to say no to the NASA and European Space Research Organization plan to develop a two-satellite L-band system. According to FAA officials, the most likely solution will be for NASA to research antenna design and a means of decreasing carrier noise for future aeronautical satellites, while the FAA lays plans for a hybrid satellite to be lofted over the Pacific in 1973. **Chances are that three satellites will be orbited, with one moved over the Atlantic in the mid-1970's.**

Remotely piloted vehicles studied by Mitre, Aerospace

Contract-hungry avionics companies are watching to see whether the Air Force Systems Command can generate DOD interest in Mitre Corp. and Aerospace Corp. studies of remotely piloted vehicles. The RPV concept, originated by AFSC and the Rand Corp., is being studied for feasibility by Mitre and Aerospace, as well as by NASA and the Naval Weapons Center. **One use for RPVs would be as air superiority fighters, with one concept calling for launching from a radar-carrying mother ship similar to the one used in the Airborne Warning and Control System.** Unlike the comparatively austere aircraft such as the F-15 now being sought by USAF, the RPV would be loaded with avionics. **However, "the end of the tunnel leading to RPVs is still probably a decade away for industry," says one program source.** Nevertheless, the Air Force is looking to the program as a major post-Vietnam effort.

ULMS cleared; MIT lab gets guidance package

The Navy has funded Massachusetts Institute of Technology's Draper Laboratory to proceed with development of a guidance package for its new Undersea Long-range Missile System. **The Navy will get the full $44 million requested for ULMS research and development in fiscal 1971.** Though the missile effort has been approved, there is still a question about **whether top-level DOD approval can be obtained for the new class of large submarines that will be required to carry the long-range intercontinental missiles** [*Electronics,* Sept. 14, p. 67].

Though the Navy wants the new submarine class for ULMS, some high-ups in DOD management are arguing for a surface ship to launch the missiles, which are seen as an addition to and eventually successor to the USAF Minuteman vehicles. What's more, the fact that the new subs would not have to leave the protection of the continental shelf—where detection is more difficult—to launch ULMS missiles **is being used as argument for holding costs down by limiting their range and depth.**

Second U.S. system planned by Comsat

The Communications Satellite Corp. plans to seek Federal Communications Commission approval to launch a second domestic satellite system **for smaller carriers in addition to its agreement to operate a $210 million, two-satellite system for the American Telephone & Telegraph Co.** Culmination of the Comsat-AT&T agreement comes after more than eight months of negotiations [*Electronics,* Mar. 2, p. 77]. AT&T will be sole user of the all-digital Comsat system, using 24-transponder models that probably will be built by Hughes Aircraft.

Comsat engineers say they have developed a technique for doubling the capacity of the 4-to-6-gigahertz band, thus avoiding the need to go to bandwidths above 10 GHz, where weather can affect performance. Each synchronous satellite would be able to handle 10,800 voice channels, 24 color TV channels, or combinations of these.

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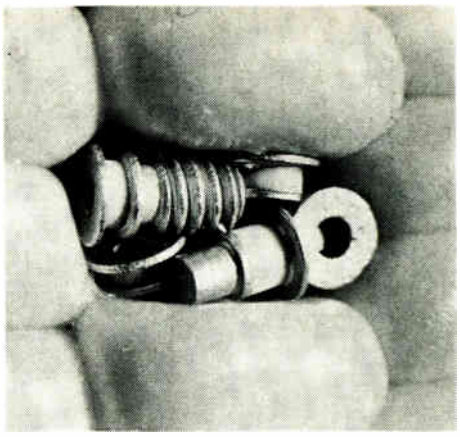

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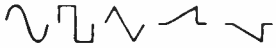

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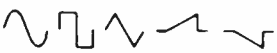

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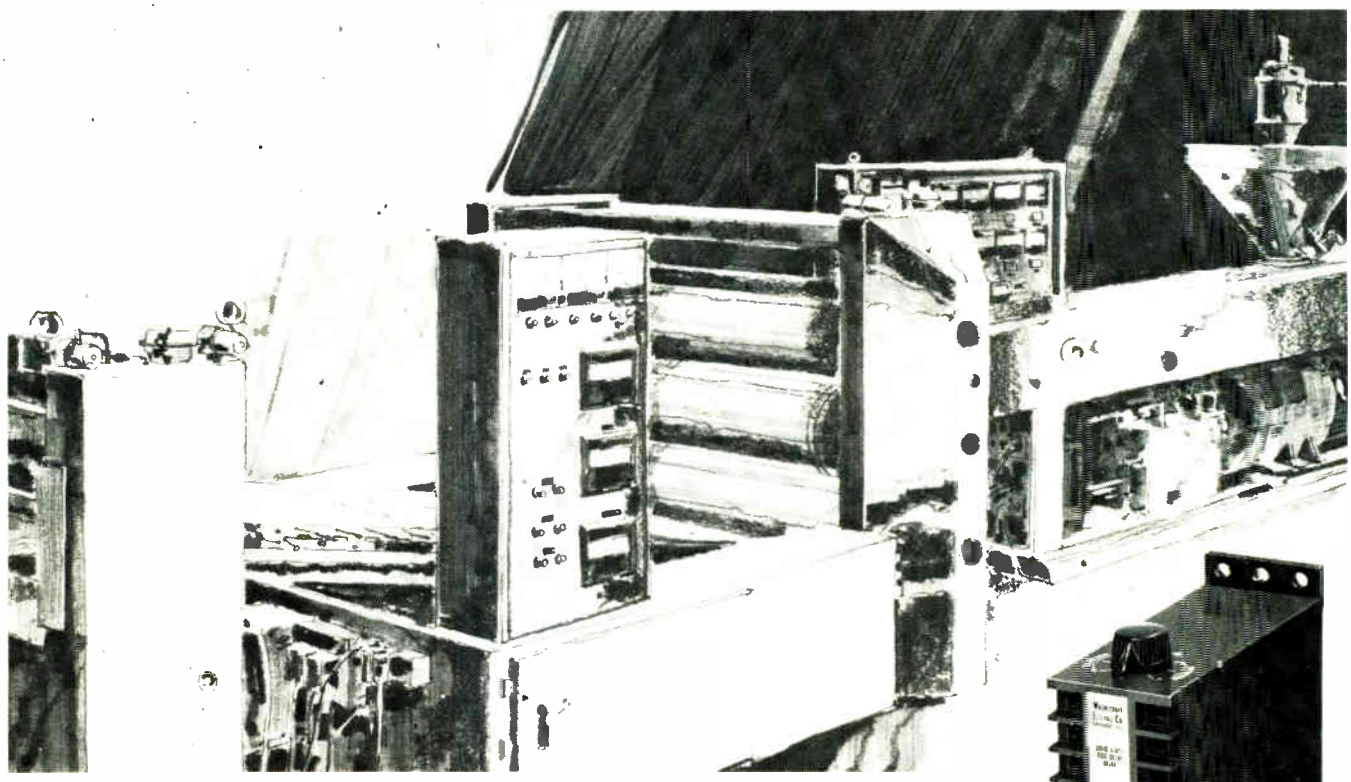

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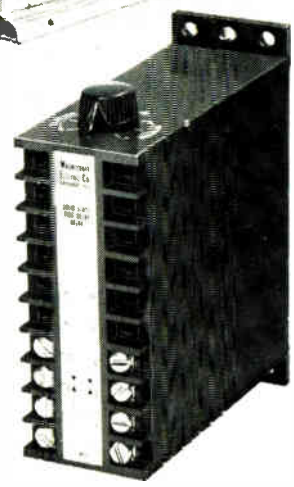

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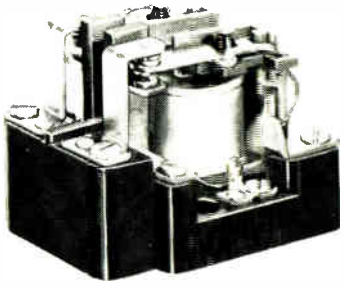

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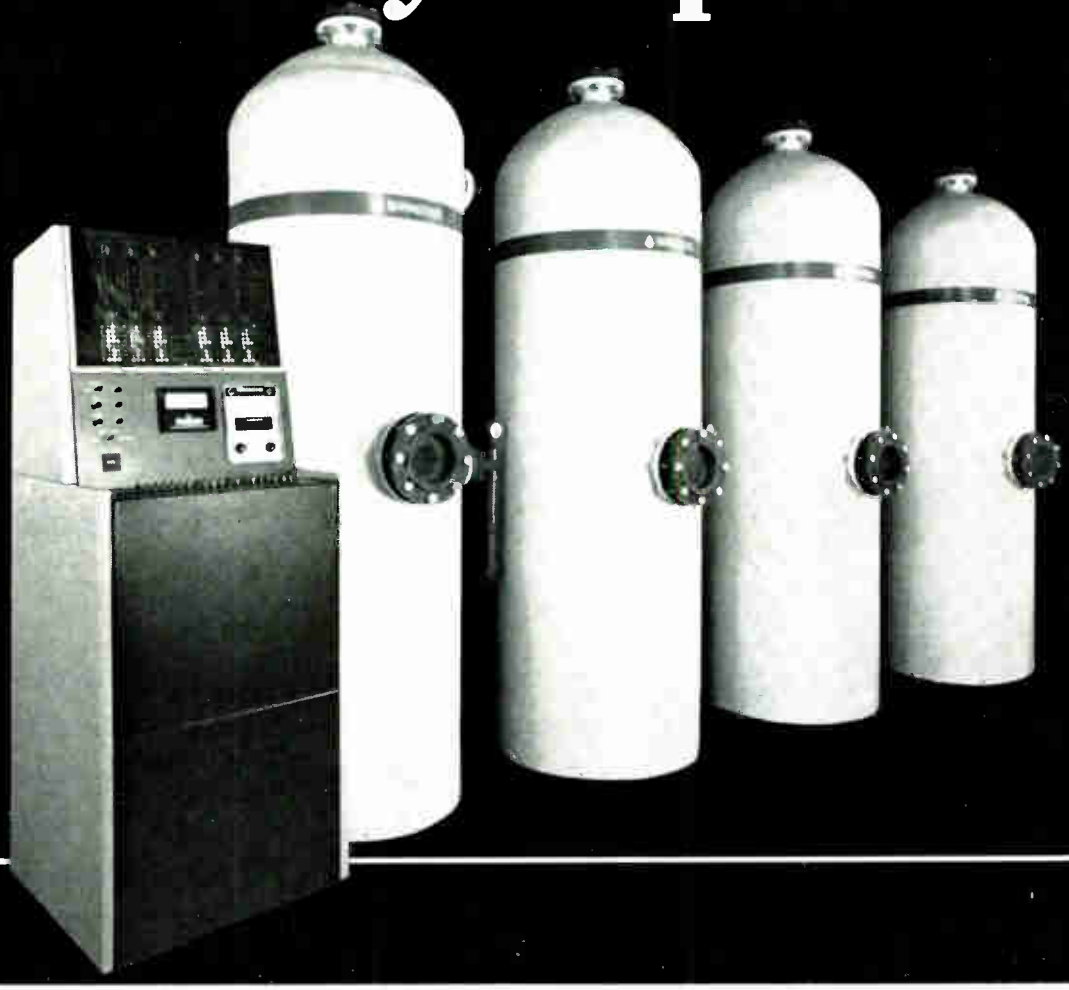

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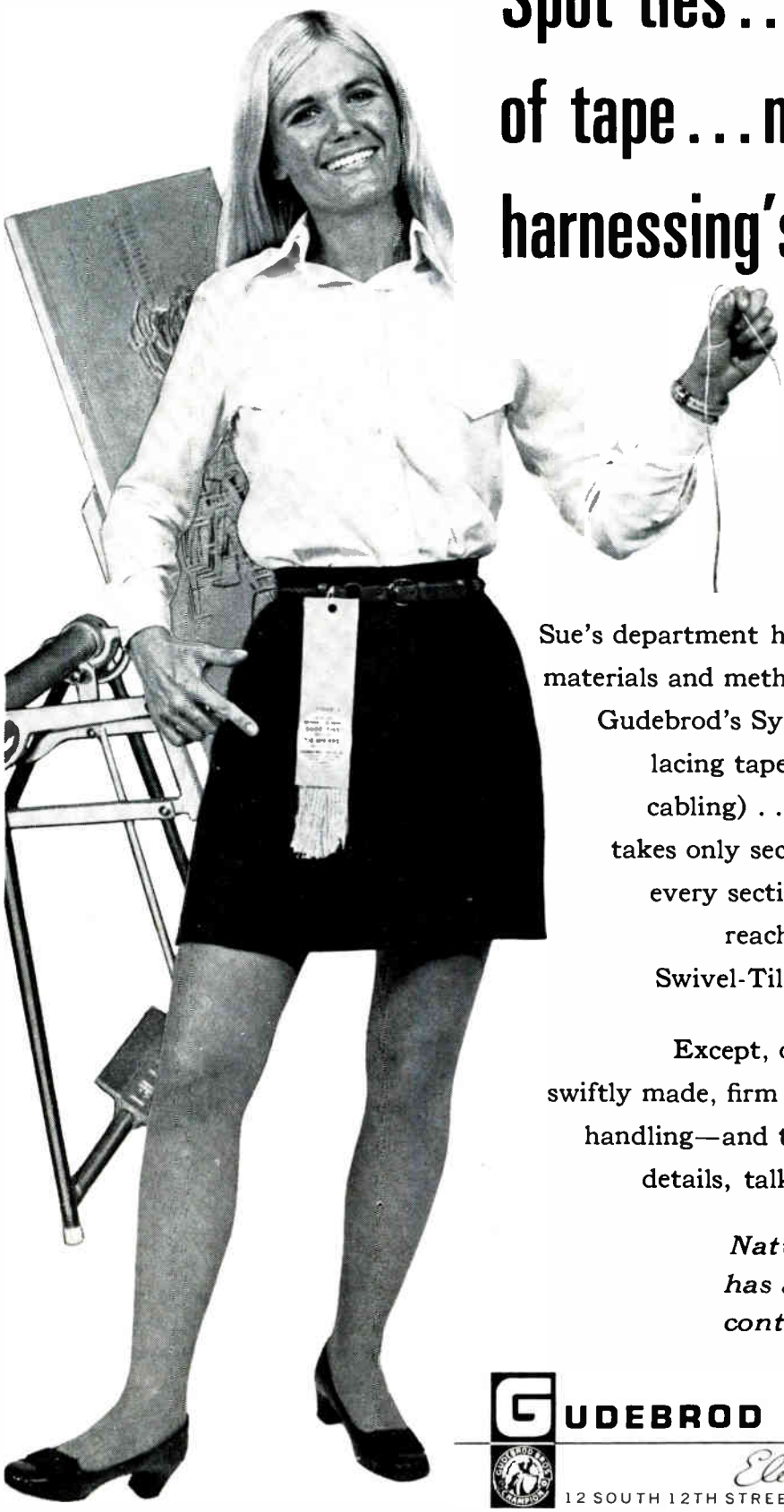

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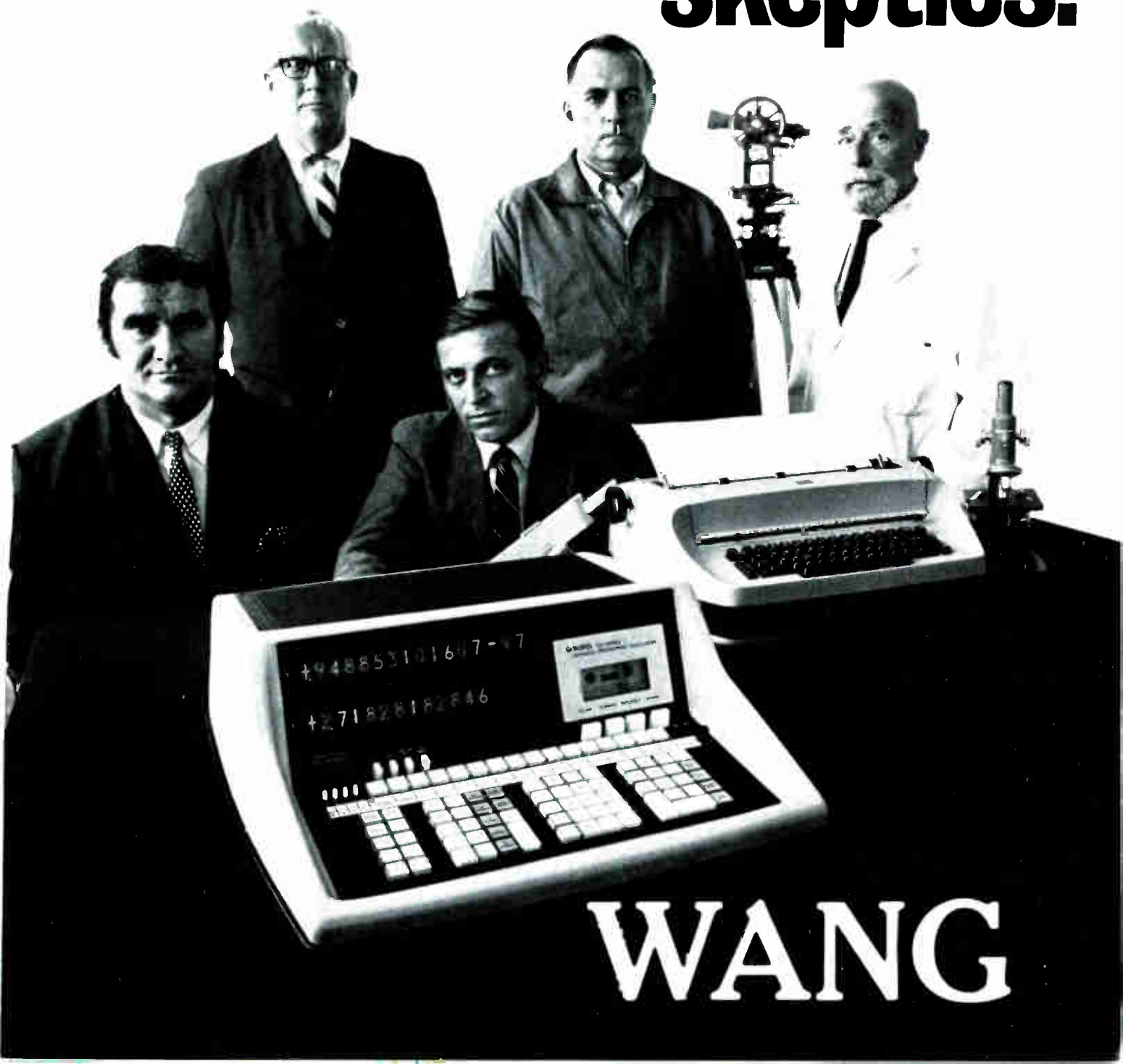

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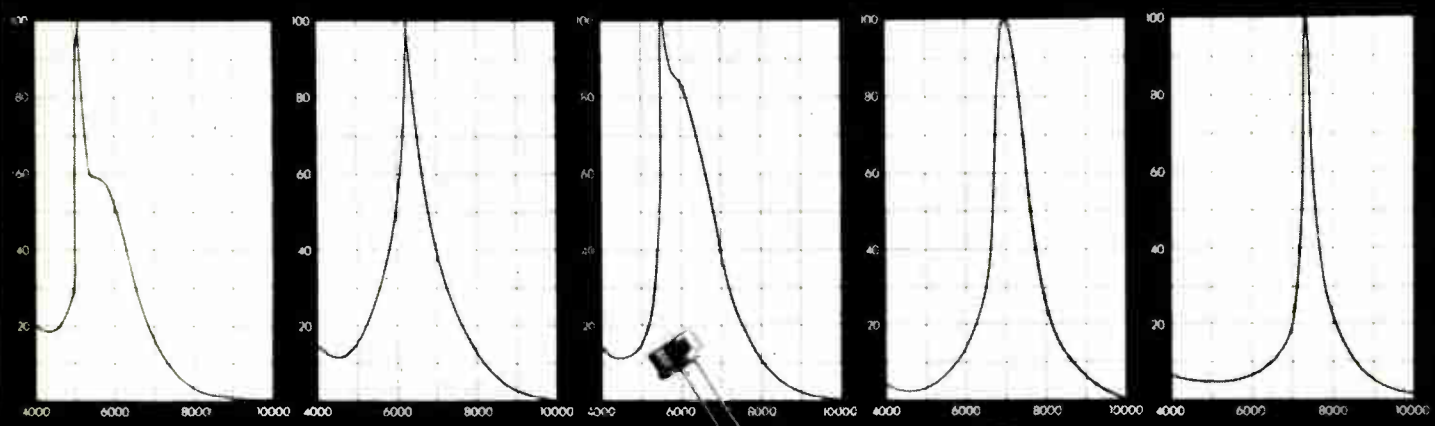

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October 26, 1970

Article Highlights

Air Force plans with computers, Army sees by starlight
page 70

In the first of a two-part series on electronics in Vietnam, *Electronics* focuses on the use of computers and night vision gear for military operations. In what will be known as the first digital war, the Air Force is relying extensively on computerized command and control systems to direct bombing missions. The Army isn't neglecting the air, either: sophisticated night vision gear helps to direct firepower for its helicopter armada. And on the ground, soldiers use the equipment to spot enemy troops, while personnel radars guard their perimeters.

Programable digital filter performs multiple functions
page 78

The programable digital filter is capable of processing signals with an accuracy that's tough to achieve in analog devices. The digital units are untroubled by temperature instability, allowing them to deal effectively with long time delays. And with the availability of inexpensive digital components, the filters are now a commercial proposition.

10 examples show versatility of d-a converters
page 91

The digital-to-analog converter forms the vital interface that allows digital techniques to be applied to a variety of operations in systems with analog inputs and outputs. Ten examples show how the d-a units can be used for a variety of functions, including CRT character and sweep generation; positioning resolvers; programable power supplies; and automatic gain measurement on an amplifier.

Coping with feedthrough in ECL circuits
page 98

Capacitive feedthrough is a source of instability in emitter-coupled logic integrated circuits, and can cause spurious outputs in gates or flip-flops. The feedthrough appears in various forms, some of which can be controlled by the designer of the IC chip, and others by the ECL system designer.

Coming

LSI in minicomputers

Shrewd design, anticipating advances in integrated circuit technology, was the key factor enabling engineers behind the new Nova series of computers to accommodate large-scale ICs and achieve high performance without a big cost premium.

Vietnam report Part I

Air Force plans with computers, Army sees by starlight

In the first digital war, computerized command and control system directs bombing missions; night vision gear guides firepower for Army's air armada; personnel radar secures ground perimeters

By Arthur Erikson, *managing editor, international*

1. **Combat veterans.** Army radios that get out to where the action is most often are the AN/PRC-77 manpack and the jeep-mounted AN/GRC-163. Both can operate in a voice-secure mode, using an AN/KY-28 digital scrambler.

☐ Slogging their way through Communist-infested Vietnamese jungle, U.S. infantrymen—grunts they call themselves—seldom get far from electronic gear that theoretically could link them directly to Washington (Fig. 1). Since the Vietnam war has become a massive counterinsurgency operation, action can pop up almost anywhere, and communications, as well as fire power, must be at the scene.

The military communications network in Vietnam—built at a cost of about $333 million—is so extensive and so effective that the top brass in the Pentagon could fire off an order directly to a squad of grunts on patrol. Though this capability isn't used, the network does put the Pentagon effectively into the field. In fact, tactical decisions on targeting made at the Pentagon spew out of printers at U.S. 7th Air Force Headquarters at the Tan Son Nhut air field on the edge of Saigon.

There's much more to electronics in Vietnam. In the history of military technology, Vietnam likely will be known as the first digital war. Unlocking a filing cabinet marked "Confidential," a general officer at the headquarters for the Military Assistance Command-Vietnam (MACV) pulls out a bulky computer printout. "That's the real impact of electronics in this war," he explains. "We've been fighting with computers." And the digital war doesn't stop at MACV's sprawling Tan Son Nhut headquarters. Field commanders have hardware tied into the Autodin (for automatic digital network) system. The Air Force even has jeep-mounted input terminals so that reports can be filed back by the numbers from the most rudimentary airstrip, reports that computers digest to tell the Air Force what targets to strike and how to do it. Infantry squads often use digital-based voice-secure transmission gear. Digital frequency synthesizers are widely used in troposcatter communications gear. General purpose avionics computers have been tested by the Army to calculate and store the geographic coordinates of targets picked up by overflying infrared sensors. Digital techniques have found their way even into personnel-detection radars. All told, the bit-streams that criss-cross Vietnam, carrying data, messages, tactical decisions and detailed orders, detecting, processing and analyzing, have delivered far more wallop than the firepower directed at an elusive enemy.

Because guerrilla groups can strike swiftly almost anywhere, deploying troops by convoy or on foot often is inadequate. So to fight its peculiar war in Vietnam, the U.S. Army has acquired its own air armada—some 5,000 aircraft, mostly helicopters. The ensuing proliferation of Army avionics has led to a whole new complement of lightweight airborne radios on the combat scene during the past 15 months. Sophisticated stability controls are used in choppers converted into gunships and though helicopter pilots like to fly by the seat of their pants, they may be getting direction finders.

Direction finding of another sort—the bearing and range of Communist troops—has always been a high-priority item for Army ground elements. Night-vision aids rate high on the list of battlefield-spurred electronic advances, says Maj. Gen. Thomas M. Rienzi, who recently completed 22 months as commander of Vietnam's top electronics organization, the 1st Signal Brigade. Initially, the push was on infrared equipment, but the emphasis has shifted to low-light-level equipment. And along with it has come a new batch of personnel-detection radars, from manpacks on up to large surveillance radars.

All told, there's a bewildering mix of electronics equipment in Vietnam, some brand new, some of Korean War and even earlier vintage. Says an Air Force technical sergeant at the Da Nang air base, "I've got kids repairing Loran equipment that was built before they were born." To keep the sophisticated electronics gear working, the military has built up an army in mufti, a corps of contract civilian engineers and technicians.

There's much that can be faulted in some combat-zone hardware, even though the services deploy on-the-spot research and development teams. Tubes and transistors blow in the Vietnamese heat. Dust is a plague. Anything that protrudes on a piece of field equipment is a candidate for breakage.

In the air—and on the ground

U.S. air operations in Vietnam have much about them that's bemusing. Although President Lyndon Johnson called a halt to the strategic bombing of North Vietnam 24 months ago, Air Force B-52 strategic bombers still carry out sorties over South Vietnam. Like elephants stomping out anthills they can't see in the underbrush, the B-52s streak in from bases outside the country to clobber enemy concentrations in the jungle.

Marine and Air Force fighter pilots, too, essentially have become ordnance delivery men since there's no airborne opposition south of the demilitarized zone. The closest thing to a dogfight these days is when an Army helicopter shoots it out with a Viet Cong patrol on a hillside.

And while its glamour boys pour bombs, missiles, and bullets onto targets they often can't see, the Air Force's transport pilots move so much cargo and so many men that the Vietnam airborne logistics effort, in the words of an Air Force colonel, "dwarfs the Berlin airlift."

Impressive as these air exploits are, in one sense the Air Force has scored highest on the ground. At a closely guarded, bunkered-in computer center at Tan Son Nhut (and a companion installation in Washington) Air Force commanders are making military history with an automated command and control system that covers an entire combat theatre.

The Air Force realized that computers would be a tremendous help in planning missions back when it was devastating North Vietnam. Particularly when attacking heavily defended key targets in the North, strikes had to be carefully planned to get the right planes to the right targets at the right times with the right bomb load.

Designating secondary targets for aircraft that couldn't get through to their primary targets because of weather or overpowering defense was particularly difficult. Reconnaissance results of an early-morning strike's effectiveness, essential for sensible retargeting, generally didn't get back to headquarters before late in the afternoon. That meant that manually elaborated "frag" orders weren't ready for the 3 a.m. briefing that preceeded the next day's strikes. So the frag orders—the often-long fragments of an overall operations order that tells each flight wing where and when to strike—had to be based on day-old information.

To slash this delay, the Air Force set up a program called Seek Data II. And by early 1968 a computer-aided combat reporting system (CREST) was operating at 7th Air Force headquarters. Since then, the system has been expanded. This spring, for example, Crest stepped up from its initial IBM 1410 to an IBM 360/50. This fall, a sister, airlift management system (ALMS), will start operating.

The speedup in frag order generation has been dramatic. For strike missions, the time has been dropped from 10 hours to 10 minutes in some cases. And automated order preparation for airlift operations will take four hours instead of 14. Along with developing the frag orders, the automated systems transmit them to the operating units.

Crest data also is transmitted to Commander-in-Chief Pacific (Cincpac) headquarters in Hawaii and to the National Military Command Center at the Pentagon. Both have IBM 360/50s and operations analysts who scrutinize the data to develop more effective strike tactics. The Pentagon-Tan Son Nhut computer tie, through transpacific cable or by satellite into the Integrated Communications System (ICS) that blankets Vietnam, in effect puts the top brass up in the front line. "Washington even picks our targets," says a general officer at Tan Son Nhut.

The hardware for Crest, reports Lt. Col. Patrick Coleman of the 7th Air Force's directorate for automated systems, "has been standing up beautifully." Along with the 360/50 computer, which is tied into the worldwide Autodin network, the system has an IBM 7740 linked to a dedicated communications network and a pair of IBM 1130s to handle inputs and outputs. The computers are leased from IBM but the software was developed by Control Data Corp., with a 200-man team under an initial $10 million contract. CDC will continue to upgrade the software with a 30-man team under a $1.5 million annual contract.

So far, the system requires manual compilation of mission reports, but the Air Force is examining hardware that would let squadron commanders feed reports straight into the computer.

Alongside the Crest 360/50 computer at Tan Son Nhut sits a second 360/50, this one for the ALMS that is going into operation now. This 360/50 has three IBM 1130 satellites tied to it, each driving an IBM 2250 cathode ray tube display unit.

ALMS does for C-7 Caribous, C-123 Providers, and C-130 Hercules transports what Crest does for bombers and fighters: it puts out up-to-date frag orders in a hurry for a supply effort that requires some 1,200 sorties a day handled by 200 aircraft working through 134 airbases. Before ALMS, frag order preparation for transport squadrons started at 2 each morning and went on until 4 p.m. ALMS reduces the cycle to four hours. At the same time, the frag orders are more detailed; they include, for example, identification of preloaded pallets, selected after a daily inventory.

Like Crest, ALMS isn't fully automated: the main 360/50 computer is linked through the Autodin network to 16 airlift control units spotted throughout Vietnam that feed in reports from transport wings in their areas. However, the system "extends all the way out to dirt strips," says Lt. Col. Oswald D. Tolley, who heads the ALMS development team.

ALMS gets out in the field via its AN/UYA-7 digital terminals (Fig. 2), made by the Garland division of LTV Electrosystems Inc. Twenty-three of these terminals, in jeep-borne portable versions and in transportable air-lift control centers, are planned for use with ALMS. They'll transmit airlift data to three master stations, for manual reformating and Autodin transmission to the 360/50 computer.

Airborne command and control

Although campaigning by computer ranks as the Air Force's most noteworthy technological effort in Vietnam, there are other notable advances too. One that proved invaluable, during the siege of Khe Sanh, was the airborne command and control center developed by LTV Electrosystems.

The airborne command and control center (ABCC) used at Khe Sanh was a trailer-sized module that fitted into the cargo bay of a C-130 Hercules. The center had working slots for a 14-man control crew and heavy communications facilities but no radar or identification, friend or foe equipment. Also, there was a large grease-pencil-and-plexiglass display board to keep track of incoming friendly aircraft and match their payloads to targets on the ground.

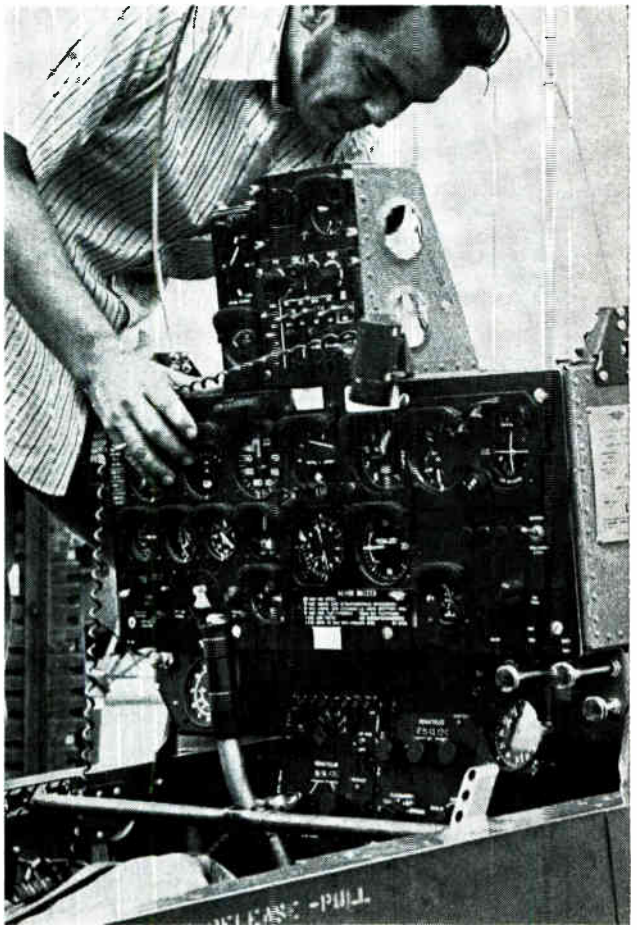
During the 77-day battle, Gen. William Momyer, now chief of the Air Force's Tactical Air Command but at the time in charge of air operations in Vietnam, often personally manned this post. It controlled about 450 sorties a day: 375 for aircraft in close support; 30 for C-130 airlifts (each plane moved as much material in a day as an 80-truck convoy), and 45 for B-52 bombers, each raining 120 500-pound bombs on enemy positions. Fighters and bombers entering the strike zone went into holding patterns until the ABCC either vectored them out onto targets or handed them off to a forward air controller for target instructions.

Still to get its first real workout in combat is another piece of Vietnam-sired gear, the Talar-IV (for tactical landing radar), built by Singer-General Precision Inc. Talar, which transmits a microwave glide slope for instrument landings, can be set up in 15 minutes by two men who only have to level it, set the glide slope, and turn it on. Few would differ with Air Force Tech. Sgt. Ervin Baumgarten, an

2. Calling in. U.S. Air Force combat control teams now have jeep-mounted AN/UYA-7 data terminals to feed airlift information back to control centers, where it's passed on to the airlift control computer at Tan Son Nhut.

3. Looking in. Electronics Command technician checks out SLAE (standard lightweight avionics equipment) on Lockheed YO-3A, a special night-reconnaissance plane fitted with classified night sensors. SLAE radios are at bottom of control console.

4. Medium range. AN/TVS-4 night observation device offers 1,200-meter range in starlight, 2,000-meter range in moonlight.



instruments expert at Tan Son Nhut, who asserts, "We should have had this 15 years ago." Yet the first Talar equipment hadn't reached Vietnam at midyear.

As with much of the new hardware the Air Force uses or will use in Vietnam, Talar got its start as a Seaor (Southeast Asia operational requirement). As of mid-1970, 350 Seaors had been initiated covering items as diverse as base defense gear to high-flying (and highly secret) countermeasures equipment.

Equipment added to planes because of Seaors, and because a lot of electronics gear tends to go aboard aircraft only after a need for it has been shown in combat, has caused some trouble in Vietnam. Particularly in the F-4 Phantom, there's been a problem of overheating in compartments crammed with add-on gear. Regulated power supply busses, too, don't work as they should when there's too much added load. In some instances, says an Air Force officer on the scene, the mean time before failure is lower than the average mission time. His suggested remedy: an independent power supply for critical avionics gear.

One rational hardware collection, widely used in Army helicopters, has been on the scene since mid-1969. Called SLAE (for standard lightweight avionics equipment) (Fig. 3) it's a set of radio equipment that includes an fm tactical radio, a vhf command set, an intercom control, and a direction finder. This five-set package weighs 40.4 pounds and replaces a radio complement that totaled 103.7 pounds.

5. **All together now.** RCA's AN/GVQ-10 Integrated Observation System combines an AN/TVS-4 night observation device, an AN/SU-63 target acquisition binocular and an AN/UVS-2 laser rangefinder. The latter provides range readout for three targets simultaneously; the pedestal gives bearing and elevation.

6. **On guard.** An AN/PPS-5 doing its thing for the infantry. The lightweight radar can spot a walking man at 5,000 meters.

Like other avionics equipment in Vietnam, SLAE has its pluses and minuses. SLAE's reliability goal is a 1,000-hour MTBF. So far, the world-wide average is 700 hours except for Vietnam, where the figure is 180 hours. Elmer Goetsch who heads up the Army Electronics Command's civilian avionics team at Tan Son Nhut trots out a lot of reasons for this. For one thing, the equipment is subject to very rough treatment.

Helicopters often are simply hose-cleaned inside after a mission, and since the Army's air missions have a 13,500-foot ceiling (higher air space is exclusively for the Air Force and Navy) equipment isn't sealed against pressure and consequently isn't hose proof. Then, too, there's the all-pervasive dust. "Just before the monsoon season," says Goetsch, "we have a rash of tuning capacitor failures. Then the rains come and knock the dust out of the air and the failures stop." Goetsch thinks sealed equipment is the answer.

Teeth for the choppers

For its chopper armada, the Army has developed some highly specialized fire-control gear to hit the Viet Cong at night. One of the latest systems in action is the Iroquois Night Fighter and Night Tracker (Infant), (*Electronics*, Sept. 1, 1969, p. 69).

Infant, in effect, is a closed-circuit tv system tied to the fast-firing Gatling miniguns on the "killer" partner of a helicopter hunter-killer pair. An OH-6 observation chopper flies slow and low over suspect terrain, a fat target for enemy riflemen. When they expose themselves, down swoops the Iroquois killer, which can spew out 6,000 rounds per minute.

The Gatling miniguns on Iroquois and Huey Cobra killer copters have been around for some time. One reason for their effectiveness in helicopters is a set of hardware called SCAS (for stability control augmentation system), which has been in the Vietnam theater for nearly two years. The system stabilizes the chopper's yaw, roll, and pitch, picking up its control inputs from three rate gyros. Response time is 0.1 millisecond at the most. With SCAS, the miniguns spray an area about 75 yards wide; without it, the helicopter bucks and the ordnance disperses over a zone about a quarter-mile wide.

Flying low and slow enough to attract ground fire obviously is not the safest way to search out the enemy. So the Army has developed hardware to mitigate that occupational hazard of helicopter pilots. Some observation choppers carry a pair of pole-like projecting 18-foot booms. The booms have sniffers that spot the scent—condensation nuclei of compounds like urea and ammonia—of troop concentrations. Originally, the detector head was to be mounted on a rifle—where it most often spotted the presence of nearby well-fed, heavily-sweating grunts.

Even more effective, is the Army's AN/ADR-6 radiological monitoring system. It spots radioactive dust

stirred up when people or vehicles pass through a preseeded area.

Of course, there's a whopping inventory of more conventional surveillance equipment—photographic, infrared, and radar. It's flown mainly from the Grumman OV-1 twin-turboprop Mohawk, the Army air armada's fixed-wing mainstay.

The first Mohawks went into Vietnam service late in 1962 and were originally designed for photoreconnaissance. Since, the hardware complement has been upgraded to include an infrared capability and side-looking radar. The latest Mohawk, the OV-1D, can fly photo, infrared, radiological monitoring, or radar reconnaissance missions. Changeover from an IR module to a SLAR pod takes about a half hour.

Flying radar surveillance missions at altitudes between 8,000 and 10,000 feet, the Mohawk's search area runs from 24 to 30 miles on either side of the ground track. Interestingly, the Mohawk's side-looking radar, the AN/APS-94 does not use synthetic aperture techniques. Instead, a 17-foot-long antenna is used to achieve high angular resolution. Developed by Motorola, the radar is intended to detect moving targets and can spot a single walking man. With synthetic-aperture processing this would be impossible since doppler-shifted signals are interpreted as returns from fixed targets at some angle other than broadside to the line of flight.

Along with the usual line-by-line printout of a side looker, SLAR-equipped Mohawks have a CRT display. There's also an in-flight data transmission system, the AN/AKT-18, built by Motorola. The system relays the imagery to a ground terminal in real-time so field commanders can scrutinize it as the target areas are overflown. The terminal also has hard copy, film printout of the imagery.

Mohawk's camera complement includes downlookers with side-to-side scan, exposing 5-inch roll film a line at a time. Also on board are forward-looking panoramic cameras that view the entire horizon with 70 mm film. Printed on the film strips are the plane's heading, the time, and coordinates fed in from a navigation system. Early Mohawks used doppler navigation. The OV-ID's will have a more accurate inertial navigator, Litton Industries' AN/ASN-86.

Mohawk's IR surveillance systems too, have been improved. Closed-cycle cryogenic coolers long ago replaced the liquid-nitrogen dewars which could cool the detector heads for only 2 hours. The latest system, the AN/AAS-24, has a slightly wider field of view and quadruple the spatial resolution of its predecessor, the AN/AAS-14, though at the cost of reduced thermal resolution, one degree Centigrade compared to the AAS-14's 0.5 degrees. Other improvements include an 8.5-inch by 10.5-inch moving-map bright-screen display and an image-freeze mode, which holds and refreshes an image that warrants close examination.

Night vision aids is what first comes to mind when Lt. Col. Don Fague, a resident expert of the Electronics Command at Long Binh, is asked about the impact of the Vietnam War on battlefield electronics. "We've got a bookful of them now" he asserts, a reference to the Army's catalog night vision aids.

Seeing in the dark

People like Fague who are close to the fighting will tell you the most significant night-sight advance has been drastic weight reduction—from about 8 pounds for early infantrymen's see-in-the-dark devices to 3 pounds for the latest ones. Back at Fort Belvoir, the Washington, D.C. area base that houses the night-vision project office, they see things in a different light. There, the shift from an emphasis on IR sensing to low-light-level viewing is considered most significant.

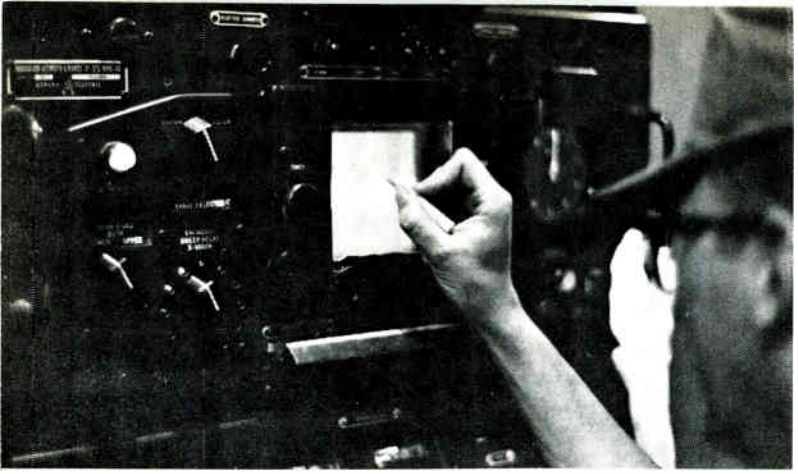
From a grunt's standpoint, though, the marvels of mechanics sometimes surpass electronics advances in night-vision gear. The sights now come fitted with a secure eyeshield, a molded-rubber eyepiece with an integral shutter that remains closed until actuated by eye-socket pressure. The shutter also closes before the observer's face is fully drawn away. This feature was included after sniper counterfire had taken a heavy toll of U.S. soldiers whose faces were briefly illuminated by the viewing screen as they turned from the eyepiece of the rifle-mounted night sights.

Most of the night-vision devices in use are built around an image intensifier, typically the Type 8586. It has three-stage intensification, a 25-millimeter aperture, S-20 photocathodes, P-20 phosphor screens, a peak sensitivity at 4,000 angstroms, and a resolution of about 600 lines. The dynamic range: from 10^{-5} foot-candles (starlight) to 10^{-1} foot-candles (dusk).

So-called first-generation devices use three image-intensifiers in cascade, coupled through contacting fiber-optics faceplates. Each tube provides a gain of about 40, so that the trio's total gain is about 60,000. Now, second-generation image intensifier tubes provide the same gain in one-third the length. They top the phosphor with a microchannel plate, a thick wafer comprising a bundle matrix of glass tubes with an internal diameter of perhaps 0.002 inch, whose inner walls are coated with a conductive, electron-multiplying surface. Electrons emitted from the photon-illuminated photocathode are accelerated to the microchannel plate where each produces on the order of 60,000 secondary electrons. This adds to the normal image-intensifier gain, so the device is about 40 times as sensitive as the earlier cascaded intensifiers.

The simplest and sveltest member of the night-vision family is a riflescope, the AN/PVS-3. It weighs 3 pounds and its intensifier tube has an 18-mm aperture. A rifleman can pick up a target 250 meters away under moon light and in open country. The range drops to 150 meters in foliage, where contrast is poor. Both figures are cut by about 40% in starlight.

Both Electro-Optical Systems and Varo Inc. produce the AN/PVS-3, which costs some $1,000 each. Its principal predecessor, the AN/PVS-2A weighs twice as much but has considerably more range—400 meters in moonlight and 300 meters in starlight—with the same 10.4° field of view, thanks to its 25-mm image-tube aperture and its large light-collecting optics. Although it can be mounted on rifles, the 6-pound scope turns up more often on machine guns, grenade



7. "X" marks the spot. By marking in two pips, operator of AN/MPQ-4 radar can spot mortar launch point.

launchers, and rocket launchers.

For tank guns and heavy machine guns, there's Varo's $3,500, 15-pound, 2-foot-long AN/TVS-2. Although the basic image tube is the same as the larger riflescope's, the TVS-2 has considerably larger light-collecting optics and a narrower field of view, 6°. With this combination, range jumps to 1,000 meters in moonlight and 800 meters in starlight.

Further up the performance, weight, and cost scale, there's a $5,000 set that the soldiers call "NOD," (from Night Observation Device) actually the AN/TVS-4 (Fig. 4). It uses a combination of reflective and refractive optics and a 40-mm image intensifier tube to obtain a range of 2,000 meters in moonlight and 1,200 meters in starlight. The viewer alone weighs 34 pounds; its tripod, carrying case, and accessories bring total weight to 70 pounds. This is too much for grunts on patrol, so the TVS-4, made by Electro-Optical Systems, is mainly used for perimeter defense.

The heaviest—and most versatile—piece of gear is RCA's AN/GVQ-10, Integrated Observation System (Fig. 5). This combines low-light viewing with a laser range finder and a pair of spotting binoculars. The Army has bought 11, ordered 17 more and uses them for base defense.

At the lightweight end, there's a new low-light-level binocular under development to replace the widely used AN/PAS-5 near-infrared binocular. The new unit, the SU-50 is intended for close-up work like reading maps and driving slowly at night. The PAS-5 has a close-up attachment for these and similar tasks, but the active IR illumination requirement presents a potential beacon for the enemy.

Seeing red

Despite the shift in emphasis to the visible spectrum considerable work is continuing on IR equipment. But the effort now is on passive techniques, detecting targets by their own IR radiation rather than illuminating them with an IR searchlight. The advantage is twofold: you don't become a well-lit target for an enemy unit that's managed to acquire IR binoculars; and because of temperature differentials you can pick up camouflaged targets.

One new development is a far-IR night observation set whose range tops 2000 meters. It is scheduled for deployment in 1974.

Also under development is a passive IR binocular with an integral low-light-level viewer and a range of 400 meters. Then there's the AN/VAS-1 far-IR hardware designed for the M60 tank. Still in advanced development, the VAS-1 marks a new generation of IR gear. Instead of vacuum-tube imaging, it uses a silicon photomosaic array. And for broad sector coverage while maintaining the narrow field of view essential for high sensitivity, the mosaic is scanned. Range on tank targets, which are considerably hotter than their surroundings, will top 3,000 meters.

Until this equipment becomes operational, though, tank units will depend on the AN/VSS-3 IR-white-light searchlight, which has been deployed in Vietnam for several months. It packs 75 million candlepower into a 1° or 7° beam. Its Xenon lamp draws 100 amps from a 28-volt dc power source. Although it's mainly used on Sheridan tanks, the VSS-3 also finds its way onto armored personnel carriers and "killer" helicopters.

Doppler delivers

The Army, by and large, has also been forced to come up with the most striking new radar equipment. The Army already is well into the second generation of man-and-vehicle detecting radars with the AN/PPS-5. And it has successfully tested the AN/PPS-9, a modified version of which will be procured under the nomenclature AN/PPS-15.

In all models, audio is either the prime or auxiliary display medium. A trained operator can reliably pick up moving targets by the doppler burbling of their echoes as they move through relatively stationary backgrounds. Vehicles actually whine as they accelerate; human targets are identified by the whoosh-whoosh of their arms, swinging as they walk and moving faster then the body as a whole. Rather unnerving though, is the background return, a mixture of moans and sighs as vegetation flutters and sways.

The first of these radars to see service in Vietnam was the AN/PPS-4, now largely phased out. Built by Sperry the PPS-4 is a lightweight x-band radar that uses noncoherent moving target indicator processing. It can spot a walking man at up to 1,500 meters; the range doubles for a small vehicle. The 0.2-microsecond transmitted pulse is range-gated on reception by a gate that's manually cranked out to the suspect range. This technique limits the clutter-to-signal ratio. The received signal includes returns from both targets and background; a simple mixing process extracts and makes directly audible the doppler terms.

The PPS-4 can be scanned manually, but is most often used to view a fixed sector. But that limitation doesn't apply to the AN/PPS-5 (Fig. 13), designed by Airborne Instruments Lab. division of Cutler-Hammer. Deployed for nearly two years in Vietnam, it automatically scans a 30°, 60°, 90°, or 110° sector. What's

more, its remotely controlled "orange peel" antenna can keep the operator from becoming a target.

Along with these advantages, the PPS-5 has better range—5,000 meters on people and up to 10,000 meters for vehicles.

Three major improvements account for the PPS-5's performance. One is a 50-channel range-gate filter that permits display of MTI video over a full 5,000-meter range. Second is the A-scope, which lets the operator position the 40-meter range gate accurately. Third is the narrow beam width of the radar—1.1° in azimuth and 3.5° in elevation—which limits the clutter backscatter in each resolution cell.

The PPS-5, too, has its drawbacks. Chief among them is an MTBF on the order of 100 hours. A modified version, the PPS-5A, has a demonstrated MTBF of at least 250 hours.

However, size and weight remain problems. The largest battery available is good for only 8 hours of operation and it takes two men to backpack the set to a fire base. But the 500-odd sets in the field have worked so well that the Army plans to buy about 1,200 more PPS-5As for the South Vietnam Army.

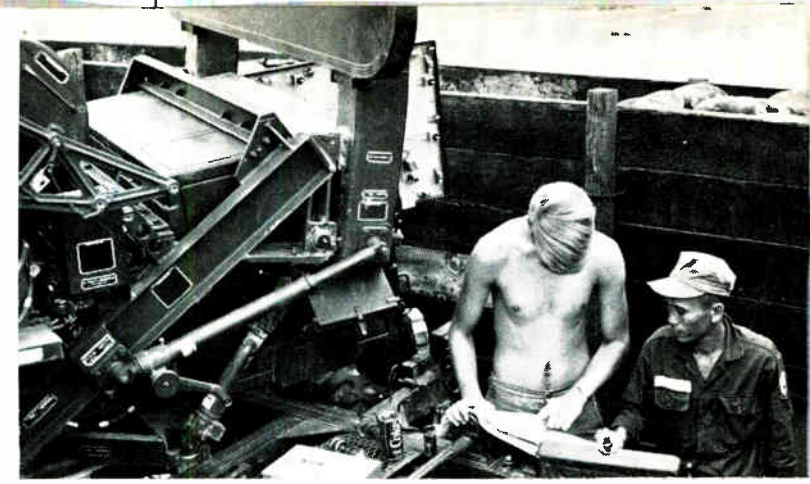
Good as the PPS-5 is for medium-range detection, the Army wants something better for short-range work. RCA has supplied a candidate, a 15-pound set called the AN/PPS-9. It uses a correlation processing technique to extract target range for subsequent doppler-audio scrutiny. The technique yields the same 1,500 meter detection range as the PPS-4 with far lower average power, only 10 milliwatts.

The PPS-9's X-band cw signal is derived from a Gunn oscillator and phase shift modulated in a pseudo-random manner. The modulation comes from a ten-step shift register with two feedback taps; it provides 1,000 different phase positions before repeating a sequence. Phase shifting rate is 6 megahertz; when a received signal is correlated with a time-stepped replica of the transmitted signal, resolution is 25 meters. Time stepping is done manually as a range gate is cranked out. Gearing limits the crankout rate to less than the minimum time interval required for correlation. Four operating modes are available: all-range search, with a resolution of 3,000 meters; acquisition, with simultaneous 3,000-meter and 25-meter resolution; fine range, with 25-meter resolution alone; coarse range, with a 250-meter automatic-alarm zone set to any range to act as an electronic fence.

The receiver uses a homodyne mixer for which the transmitter acts as a local oscillator, directly converting received signals to audio. Doppler return becomes audible and a lamp lights when the range gate brackets a target.

Much of the PPS-9's virtue lies in its antenna, basically an array of planar diodes on a pc board assembly that measures 12 inches by 8 inches by ½ inch. Including its automatic scanning drive, the antenna unit weighs about 2.5 pounds. Because little power is needed to drive this lightweight head and thanks to the 10-mw output power, the set runs up to 16 hours on its magnesium dioxide battery.

The Army has field tested about 20 PPS-9 sets in the U.S. and in Vietnam. Demonstrated MTBF is 1,000 hours and the mean time to repair is one hour. But one drawback was found when this set was evaluated under combat conditions: the battery is hard to change. Reports Lt. Col. Jack D. McClary, an electronics evaluation officer at Long Binh, "You have to take the transmitter off to get at the battery and that's hard to do silently at night."



8. Losing its cool. U.S. and Vietnamese technicians ponder antenna and circuits of AN/MPQ-4, frequent victim of the hot season.

Two offshoots of the PPS-9 have also been put through field tests. USAF has brought two dozen AN/PPS-11s, a rifle-mounted version that weighs just 9 pounds. This radar provides a 500-meter range for a walking man and twice that for a small vehicle. In tests, it's had a remarkable 2,000-hour MTBF. Another Air Force version is designated AN/PPS-12. It can be set to scan sectors from 10° on up to 240°.

Further down the procurement pike is a set designated the AN/PPS-15, an 18-pound version of the PPS-9. A remote-control capability will account for the three additional pounds and the radar will emphasize improved, automatic visual alarm so the operator won't always have to wear headphones.

One veteran artillery radar still kicking around is the AN/MPQ-4 (Figs. 7 and 8), which locates the source of enemy mortar rounds and also shows how close retaliatory fire is landing. The set—when working—locates a mortar within four seconds after a round is fired. However, only a 25° sector is covered; that's far short of the 360° often needed in Vietnam.

Among grunts, the MPQ-4 is a favorite piece of hardware—most are convinced enemy units avoid mortar attacks on fire bases that have an MPQ-4. Maintenance men, though, say it's hard to keep the set on the air. The high-voltage tubes burn out in epidemics; the waveguides tend to develop leaks during the rainy season. The MPQ-4 first went into production back in 1956, but the Army still has not come up with a successor—although the latest models are modified to allow them to pick up low trajectories. However, there are three candidates under test (and under security wraps). The prime candidate seems to be Gilfillan's AN/TPQ-28. Others are Raytheon's AN/TPQ-31 and AN/TPQ-33. The latter has an anti-rocket capability.

In the Nov. 9 issue, Electronics will explore communications systems in Vietnam, as well as radar-bombing equipment and rescue and para-drop beacons.

Programable digital filter performs multiple functions

Software changes enable a sampled-data filter to meet highpass, lowpass, bandpass or other requirements —and with greater accuracy than analog counterparts

By A. T. Anderson, *Electronic Communications Inc.*

□ Digital filters can process signals with an accuracy that's difficult to achieve with an analog device. Free of temperature instabilities, they can deal competently with long time delays, a tough task for an analog unit. Still better, with the rapid advances in integrated circuit technology, versatile, programable digital filters have become a possibility.

Actually, a programable filter is just a real-time special-purpose computer. Thanks to the availability of small, inexpensive digital components, it's now a commercial proposition, as the newly developed ECI model 999 proves.

This programable filter is capable of bandpass, bandstop, comb, nonlinear phase, matched, and still other kinds of filtering. Filter coefficients for each of these functions are entered by means of a paper-tape reader located on its front panel. Its memory—a 200 tap shift register—can store 200 12-bit words, and operates from either an external clock, whose sampling rates can vary from 50 to 10,000 hertz, or an internal clock, which samples at 10,000 Hz.

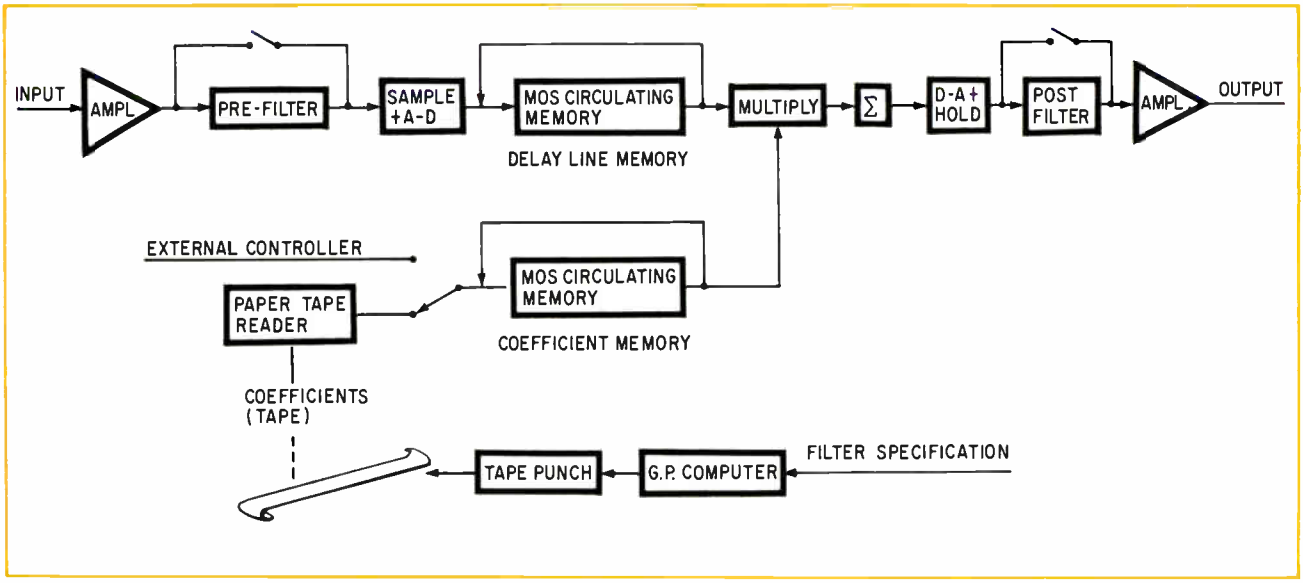
These rates allow the specification of 200 sample impulse responses lasting from 20 milliseconds to 4 seconds, or alternatively permit the user to specify the magnitude and phase of 100 frequency samples at intervals of from dc to half the sampling rate (the remaining 100 samples are complex conjugates of the first 100 if the impulse response is real).

Since such a filter contains an analog-to-digital converter at its input and a digital-to-analog converter at its output, it can act directly on analog signals. A lowpass preprocessing filter limits the input signal bandwidth to about half the sampling frequency to prevent aliasing distortion which results if a signal is sampled too slowly. A postprocessing filter then reconstructs the baseband components of the sampled output waveform.

The concepts employed to describe, analyze, and synthesize digital systems, such as impulse and frequency response, are the same as those used with analog systems.

A linear analog network is completely described

1. Programable filter. ECI model 999 is a 12-bit, 200-tap transversal filter. Filter coefficients corresponding to the desired function are entered into the coefficient memory by paper tape. To prevent aliasing distortion during sampling, the preprocessing filter limits the input signal bandwidth to half the sampling frequency. The postprocessing filter reconstructs the baseband component of the sampled-data waveform.



either by its impulse response, h(t), or by its frequency response, $H(\omega)$. If a signal, x(t), is impressed on the system input, the resulting output, y(t), can be found from the convolution of x(t) and h(t). Alternatively, the frequency spectrum of the output signal, $Y(\omega)$, can be expressed as the product of the spectrum of the input signal, $X(\omega)$, and $H(\omega)$. Conversely, if a particular output spectrum is desired in response to a given input, the filter response, $H(\omega)$, can be found from

$$H(\omega) = \frac{Y(\omega)}{X(\omega)}$$

and synthesized from analog components, such as resistors, capacitors, inductors, and operational amplifiers.

The filter can also be designed by finding h(t) as the inverse transform of $H(\omega)$ and then building a network characterized by that h(t). But frequency domain synthesis is generally more useful.

In a sampled-data filter—a filter which accepts inputs and produces outputs only at specific instants of time called sample points—the convolution equation relating the discrete output to the discrete impulse response and discrete input is a summation:

$$y(n) = \sum_{k=-\infty}^{\infty} h(k)\, x(n-k)$$

If the input spectrum, $X^*(\omega)$, and the desired output spectrum, $Y^*(\omega)$, are known, the frequency response of the sampled-data filter is

$$H^*(\omega) = \frac{Y^*(\omega)}{X^*(\omega)}$$

where the asterisks indicate a sampled signal. $H^*(\omega)$ can be found and synthesized by means of differential equations, as in the continuous case. However, an approach based on difference equations is usually simpler. The best method depends upon the way the desired system is described or specified, and upon the network configuration used to synthesize it.

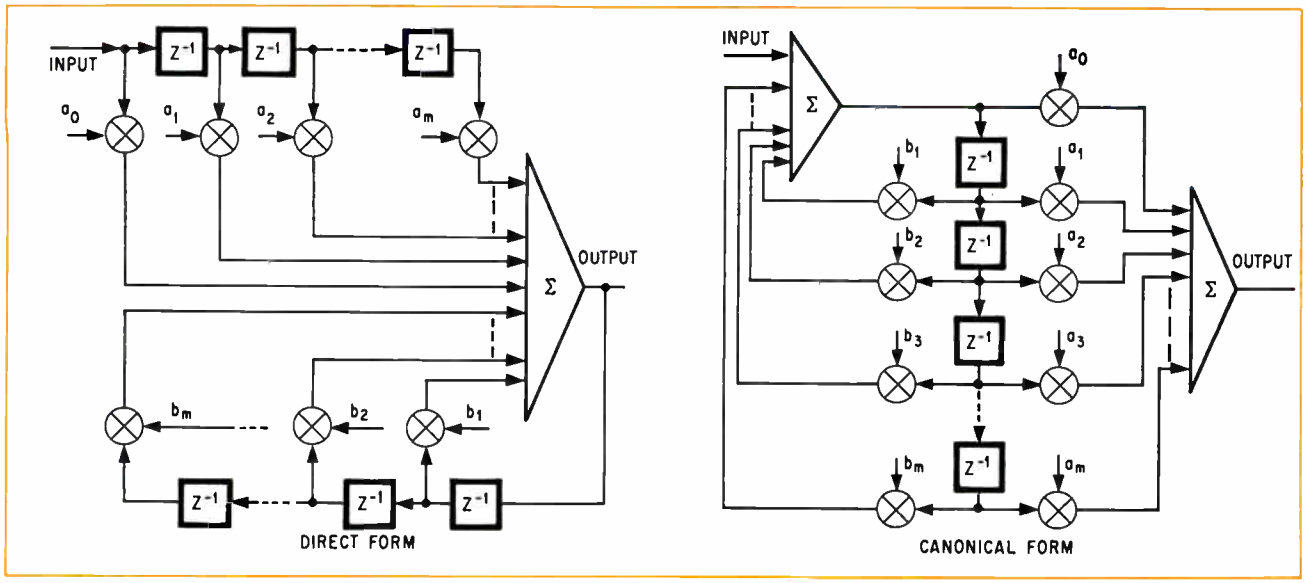
For example if the continuous impulse response, h(t), of a filter to be synthesized digitally is specified, impulse invariance[1] is the technique to use. Impulse invariance results in a sampled-data filter whose impulse response, h(k), is a sampled version of h(t). The most direct way to implement this is to set the impulse response samples, h(k), equal to the scaling coefficients, a_k, of the transversal filter. A transversal or nonrecursive filter lacks feedback, and directly implements the convolution summation with the feedback coefficients, b_k, set equal to zero (see "Filtering Digitally," p. 82). But recursive filters—those with feedback—can also sometimes be designed by the impulse-invariant technique if the transformed impulse response can be written in closed form in terms of z^{-1}, a Fourier transform of one-sample delay.

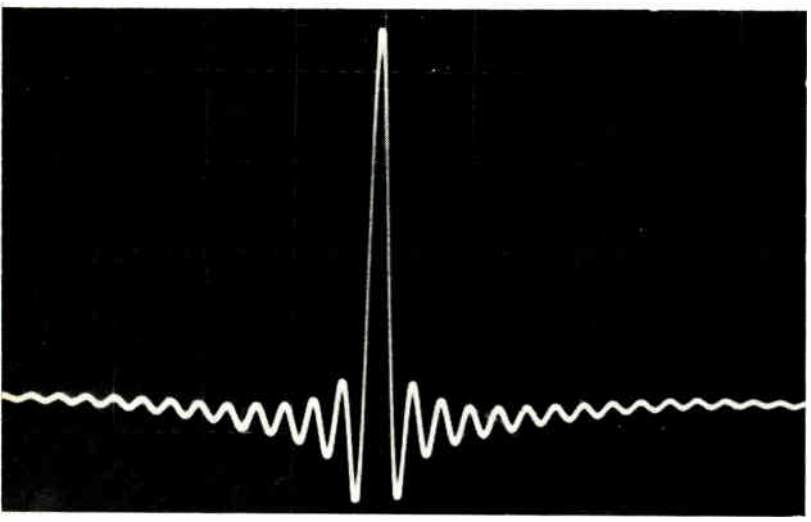
Or suppose the desired filter is specified by its frequency response. In this case, the frequency response could also be transformed into an impulse response by means of either a Fourier transform or a discrete Fourier transform, and the same technique then used to synthesize it[2-4]. But alternatively, the designer could use the frequency sampling method[5] and, in effect, build the desired frequency response by scaling and summing the responses of a number of narrow-band filters.

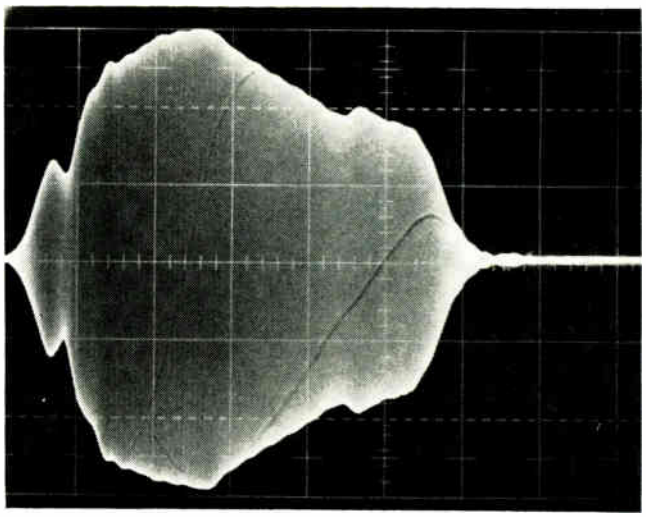
A third technique consists of first designing an analog counterpart filter, H(s), using analog systhesis techniques.[2-4] A bilinear transform expression

$$\frac{2}{T}\left[\frac{1 - z^{-1}}{1 + z^{-1}}\right]$$

2. Digital filters. General recursive digital filters take one of two forms: direct or canonical. Canonical form is usually preferred since it contains half as many delays and therefore produces less noise. But the same difference equation describes both. Higher order filters of either form are sensitive to coefficient inaccuracy caused by limited word length.







3. Output waveforms. Impulse response (left) demonstrates linear phase characteristics of recursive bandpass filter. Response (right) is that of filter designed to simulate both phase and magnitude of typical voice-grade telephone line in the switched network; filter was specifically designed for nonlinear phase characteristics. Phone line simulation is particularly effective for modern testing, and with model 999, variety of characteristics can be simulated by reading in different filter coefficients.

replaces s, thereby transforming H(s) into a difference equation (see "Filtering digitally," p. 82). The resulting $H^*(z)$ can then be synthesized in a general recursive form, as shown in Fig. 2, or separated into sums and products of basic factors—usually 2-pole, 2-zero—and synthesized as a serial/parallel combination of basic building blocks. The serial/parallel combination is preferred because third-order or higher filters of general form are often very sensitive to coefficient inaccuracies.

To illustrate the variety of functions which digital filters can provide and the flexibility which can be programed into a single piece of equipment, several types were designed, and the appropriate coefficients punched on tape with the aid of the associated Fortran support software and a general purpose digital computer.

The support software includes several options to aid the filter designer in synthesizing transversal filters. One routine automatically constructs linear phase bandpass and bandstop filters from given upper and lower cut-off frequencies. There are also available several analysis routines, which compute the total response resulting from a given filter design, including effects due to analog filters, coefficient quantization, and data reconstruction.

The model 999, as shown in Fig. 1, appears to be an analog device at the input and output terminals. The d-a converter, holding circuit, and postprocessing filter reconstruct the baseband component of the sampled-data waveform for delivery to the output. The a-d converter and preprocessing filter prevent distortion during the sampling operation. In this way, a programable filter may approximate many types of low frequency analog filters.

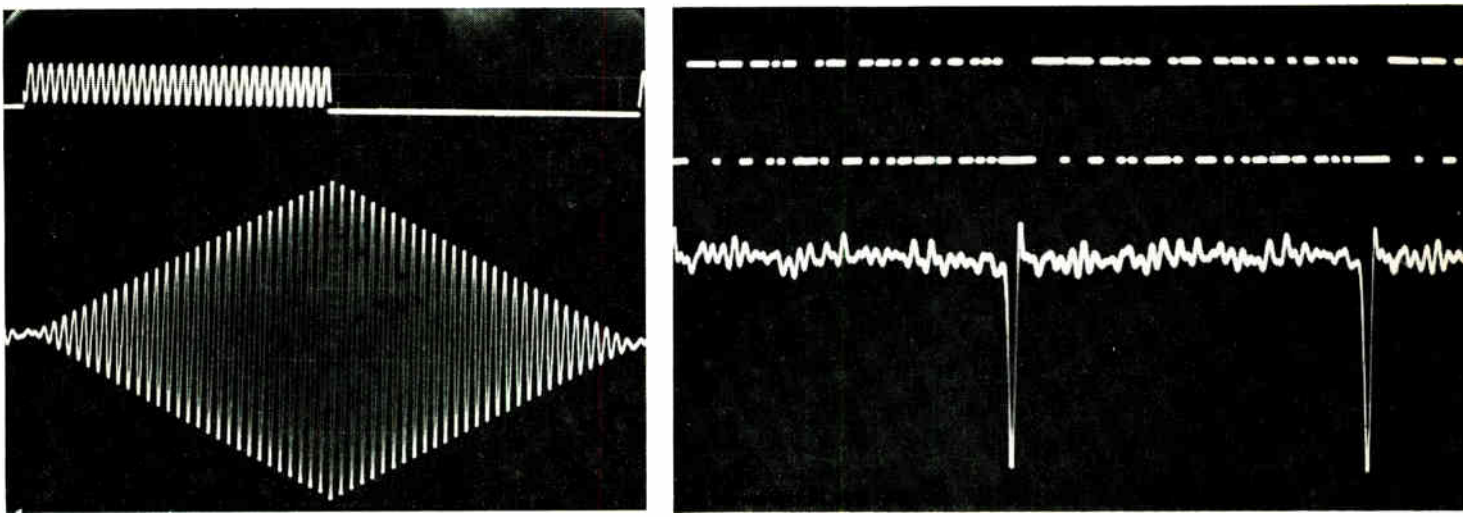
An important feature of the model 999 is its ability to produce a variety of frequency responses while still maintaining a completely linear phase response. Though a small amount of nonlinear delay distortion is introduced by the pre- and postprocessing filters, it occurs only at the upper end of the band, and most of it can be eliminated by modifying the digital filter coefficients. The only other phase shift aside from this is the one due to constant time delay through the filter.

Linear phase filters are easily designed in digital form. For example, a low pass filter can have an extremely fast roll-off—say 60 decibels in 200 hertz—which can be made even steeper at the expense of higher sidelobes. Bandpass filters can be designed with equal roll-off at the high and low ends of the band. Bandstop filters can be designed with either narrow or wide stopbands, and introduce no nonlinear phase shift in the passband.

Another type that can take digital form is the harmonic or comb filter, which is particularly useful for isolating repetitive signals of known frequency, like sonar signals, from noise or other unwanted signals. The comb "teeth" are designed to coincide with the spectral components of the signal. Moreover, if the frequency of the input signal is known, the filter clock can be driven synchronously so that the filter comb "teeth" will follow variations in the input signal's repetition rate. And, since the comb filter introduces only linear phase shifts with frequency, the input signal is not distorted as it passes through.

Linear phase shift, however, is not always desirable. For instance, a telephone-line simulator has to have a nonlinear phase response since it has to simulate both the magnitude and the phase characteristics of a typical voice-grade line in the switched telephone network. This filter's response is usually specified on the basis of measurements taken on an actual phone line.

The principal advantage of simulating the effects of telephone lines in this way is that different line



4. Matched filters. Sinusoidal burst (upper left) is typical modem waveform. When passed through matched filter, response is triangular waveform (lower left). 63-bit binary pseudo-random sequence (upper right) is passed through code-word detector, also a matched filter. Filter's output (lower right) reacts at beginning of each new input sequence. Code word detector is a digital filter designed to have an impulse response that is the time-reversed sequence of a particular bit sequence in the bit stream.

characteristics can be affected simply by reading in different filter coefficients—a particularly effective technique in line-modem testing. It also makes it simple to simulate the effects of multipath or echos for a given line. This is done by determining the line impulse response, delaying samples of this response in the filter's delay line memory, scaling the delay samples by the relative amplitude of the echo, and finally summing them to form the composite impulse response which includes the effects of multipath.

All the filters mentioned so far have been specified in terms of their desired frequency domain characteristics. But sometimes it is more appropriate to specify the time response. Probably the most common example of this is a matched filter, where the impulse response of the filter is a mirror image in the time domain of the waveform to be filtered. Such a filter may be matched to a sinusoidal burst, a widely used modem signal waveform shown in Fig. 4; the response is a triangular waveform. Or it may be a code word detector. In this case, if its job is to detect a particular bit sequence in a bit stream, it can be designed with an impulse response that is an exactly reverse sequence, and its output reaches a peak in amplitude when the desired sequence appears. The case of a 63-bit binary pseudo-random sequence is shown in Fig. 4.

The digital filter version of a waveform generator may be quite useful when complex waveforms are desired. One arbitrary waveform is a frequency-modulated sinusoid with a frequency that increases linearly with time—but the variety of possible waveforms is virtually limitless. However, a system consisting of a digital filter, a unit pulse generator, frequency divider, and external clock (Fig. 5) is capable of generating almost any periodic waveform, within the constraints of the pre- and postprocessing filters. If one cycle of the waveform goes to zero before the next cycle begins, it is not necessary to drive the filter clock from the pulse train, since in this application timing is not critical.

Another use for the digital filter is to generate single sideband signals by means of the phase-shift method, shown in Fig. 6. This system requires the use of a network, often called a Hilbert transformer, which provides a 90° phase shift at all frequencies of the input modulating signal. A modification of this method generates a hybrid waveform, called compatible ssb, which has the same bandwidth as a conventional ssb network but which can be detected with a conventional a-m square-law detector[6] (Fig. 6). Although this system requires a square-law detector for distortionless detection, a linear envelope detector will produce no serious loss of speech intelligibility. The result in some cases is that compatibility with standard a-m receivers is achieved.

The generation of ssb signals requires a linear phase, wideband, phase-splitting network, and can be achieved by sampling the modulating signal and using a tapped delay-line filter to produce the necessary 90° phase shift. Such a tapped delay-line approach is the analog equivalent of a non-recursive sampled-data filter and is easily implemented in digital form with the 200-tap model 999. Since the delay-line filter introduces delay as well as the 90° phase shift, a compensating delay in the non-shifted signal is introduced by adding another digital-to-analog converter and filter.

These particular digital filter applications were used to process baseband signals. As already noted, sample-data filters have frequency responses that recur periodically at multiples of the sampling frequency. Similarly, the input signal, if it were impulse sampled, would have an aliased spectrum repeating at multiples of the sampling frequency. Consequently, the digital filter can be used to process signals above baseband.

Filtering digitally

A sampled-data system accepts inputs and produces outputs only at specific instants of time, called sample points. One type is a digital filter. In this, the input and output are quantized and the system function is realized numerically, as with a computer or logic circuitry. If the filter is implemented with electrical circuits, binary numbers are often used.

Essentially, a digital filter is a difference equation which derives an output from the present input and from previous values of the input and output. Its basic elements are delays, constant multipliers, and summers. The relationship may be expressed as:

$$y(n) = \sum_{k=0}^{\infty} a_k x(n-k) + \sum_{k=1}^{\infty} b_k y(n-k)$$

which states that the present output, $y(n)$, is found by scaling, then adding the present input, $x(n)$, and previous inputs, $x(n-k)$, and outputs, $y(n-k)$.

When a filter's output is a function only of the inputs, and not the previous outputs ($b_k = 0$), the filter is known as a transversal or nonrecursive filter.

However, if the output of a digital filter is a function of at least one previous output, the filter is called recursive. An example is a trapezoidal rule integrator, which approximates the integral of the input signal by a sum of trapezoids whose width is equal to the sampling interval.

The integral is formed by adding the area under the trapezoid being sampled to the integral value at the last sample:

$$y(n) = \frac{T}{2}[x(n) + x(n-1)] + y(n-1)$$

The frequency response of a digital filter is easy to determine from its difference equation by means of the Fourier transform of a unit delay of T seconds:

$$\mathcal{F}[D] = \mathcal{F}[\delta(t-T)] = e^{-j\omega T}$$

where T is the time spacing between samples, δ is an impulse function, and ω is the frequency in radians per second.

Using the general difference equation that describes a digital filter and the transform of a unit delay, the frequency response is found directly from

$$Y^*(\omega) = \left[\sum_{k=0}^{\infty} a_k e^{-jk\omega T}\right] X^*(\omega) + \left[\sum_{k=1}^{\infty} b_k e^{-jk\omega T}\right] Y^*(\omega)$$

where the asterisks indicate a sampled-data signal. This equation can be rearranged to give

$$H^*(\omega) = \frac{Y^*(\omega)}{X^*(\omega)} = \frac{\sum_{k=0}^{\infty} a_k e^{-jk\omega T}}{1 - \sum_{k=1}^{\infty} -b_k e^{-jk\omega T}}$$

where $H^*(\omega)$ is the frequency response since

$$e^{-jk\omega T} = e^{-jk(\omega T + 2n\pi)}$$

It can be seen that the frequency response of a digital filter, $H^*(\omega)$, repeats at intervals of the sampling frequency, $\omega_s = 2\pi/T$

For convenience, $e^{-j\omega T}$ is written as z^{-1}, and Fourier transforms of digital filters are written in terms of z^{-1} rather than ω, and hence are called Z-transforms. Using this notation, the frequency response of a filter becomes

$$H^*(z) = \frac{\sum_{k=0}^{\infty} a_k z^{-k}}{1 - \sum_{k=1}^{\infty} b_k z^{-k}}$$

To illustrate the Z-transform approach, consider the trapezoidal rule integrator where

$$Y^*(\omega) = \frac{T}{2} X^*(\omega) + \frac{T}{2} X^*(\omega) e^{-j\omega T} + Y^*(\omega) e^{-j\omega T}$$

The frequency response is written

$$H^*(\omega) = \frac{Y^*(\omega)}{X^*(\omega)} = \frac{\frac{T}{2} + \frac{T}{2} e^{-j\omega T}}{1 - e^{-j\omega T}}$$

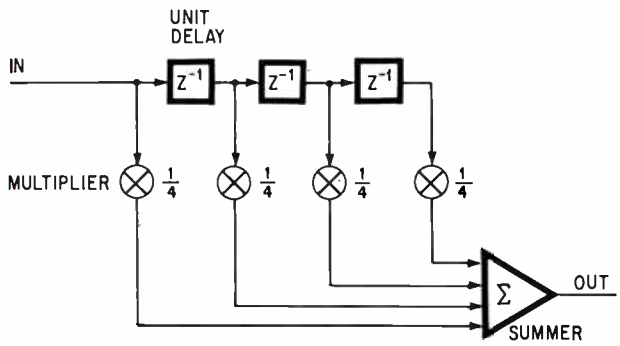
The transfer function can also be written in terms of z:

$$H^*(z) = \frac{Y^*(z)}{X^*(z)} = \frac{T}{2}\left[\frac{1 + z^{-1}}{1 - z^{-1}}\right]$$

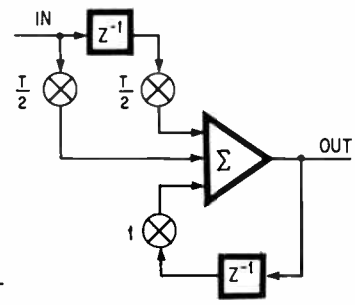
Since this expression is a good approximation of an integrator, $1/j\omega$ or $1/s$, it would appear that

$$\frac{1}{H^*(z)} = \frac{2}{T}\left[\frac{1 - z^{-1}}{1 + z^{-1}}\right]$$

might be a good approximation for s or $j\omega$. This is often the case, and substitution of $1/H^*(z)$ for s is a common way of transforming an analog transfer function into a difference equation; it is called a bilinear Z-transform.

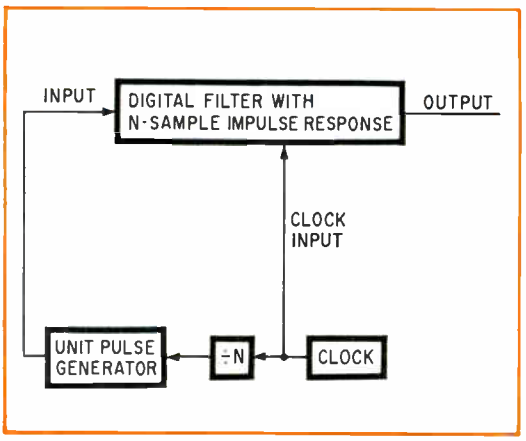


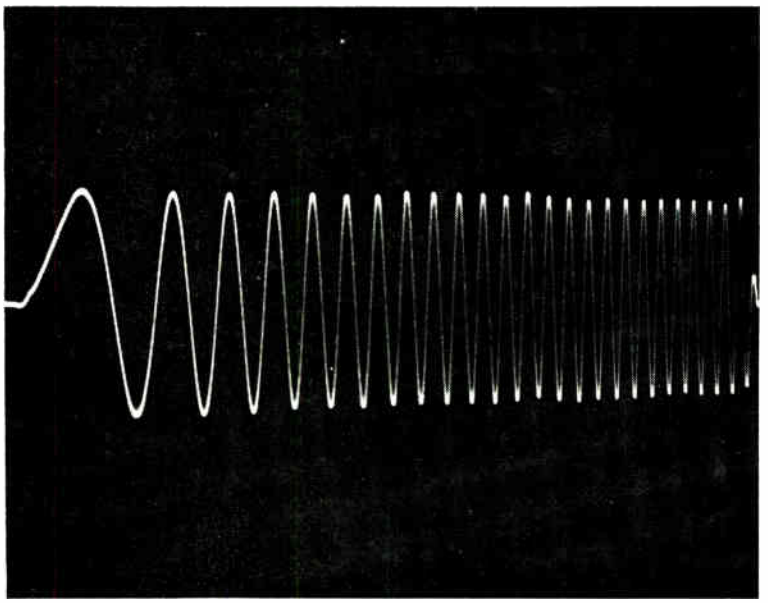
NONRECURSIVE FILTER



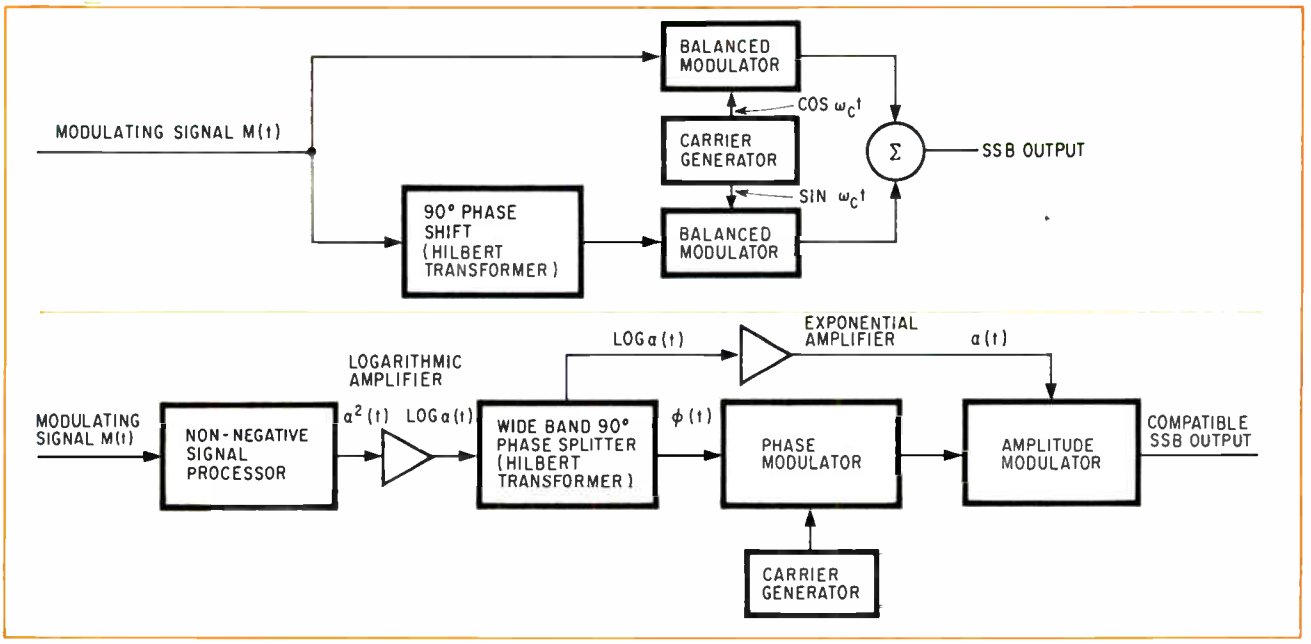
RECURSIVE FILTER

Feedback or not. Nonrecursive filter (left) computes a running average of four consecutive points; its output is a function of the inputs only. Recursive filter (right) is a trapezoidal rule integrator which approximates the integral of the input signal by a sum of trapezoids.





5. Waveform generator. Arrangement for producing complex periodic waveforms (left) uses digital filter plus other equipment. Example of output is fm sinusoid, the frequency of which increases linearly with time (right).



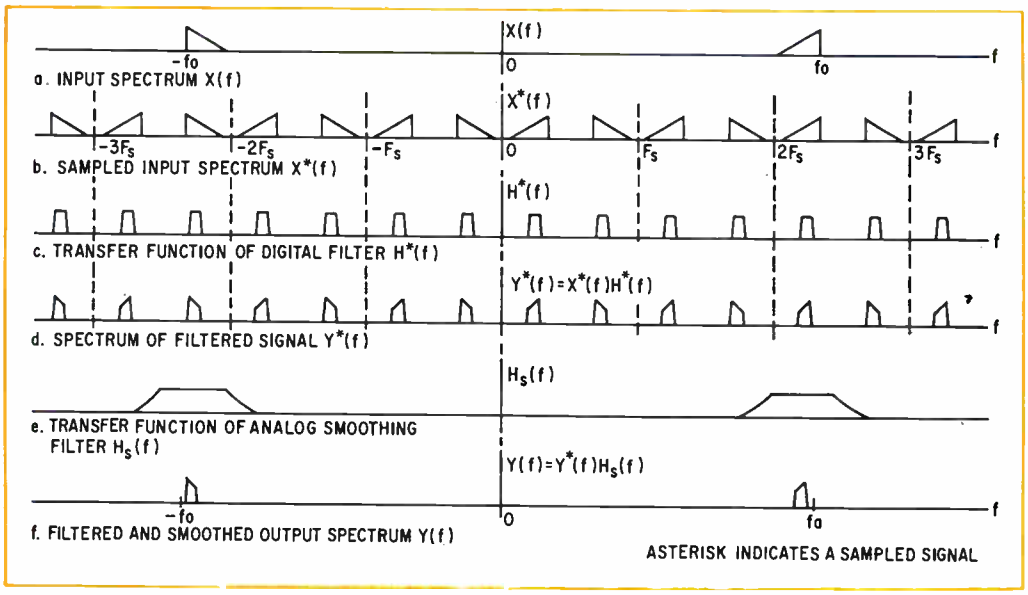
6. Single sideband. Phase shift method of ssb generation (top) uses Hilbert transformer to provide 90° phase shift at all frequencies of input signal. A method of compatible ssb generation (bottom) uses tapped delay line filter to produce required 90° phase shift and a-m square-law detector for distortionless detection. Tapped delay line approach is analog equivalent of nonrecursive sampled-data filter, and is sometimes compatible wtih standard a-m receivers.

Just how this can happen is seen in Fig. 7, which shows a technique for using a sampled-data filter, above baseband, as a bandpass filter, to achieve extremely sharp, linear phase bandpass response. If the input signal is sampled at the rate F_s, and if F_s is carefully chosen, the sampled spectrum

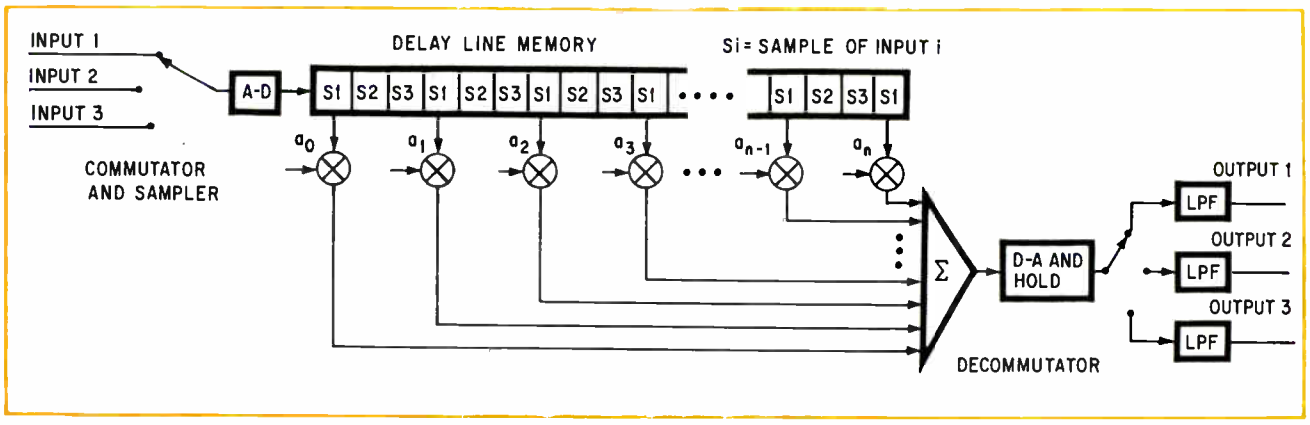
$$X^*(f) = \sum_n X(f - nF_s)$$

has no spectral overlap and may be filtered with a digital filter that has a bandpass transfer function. The resulting output spectrum is the product of the filter transfer function and the input spectrum. Finally, the desired component of the sampled output spectrum is selected by a relatively broad bandpass smoothing filter, H_s (f), leaving the filtered version of the original input function.

However, there is really no constraint to select the component of the output spectrum at the center frequency f_o. If the bandpass filter function, H_s (f), is shifted by nF_s so as to select one of the other components, the combination becomes a frequency translator as well as a filter, in effect using the sampler



7. Above baseband. Sampled-data filter is used above baseband as a bandpass filter. Input spectrum (a) is sampled of rate F_s (b), F_s chosen to avoid spectral overlap. Sampled spectrum then filtered with digital filter having bandpass transfer function (c). Resulting output spectrum is product of transfer function and sample (d). Desired component of sampled output spectrum is then selected (e), leaving filtered version of the original input (f).



8. Sharing. By making use of time between samples, digital filter can be time-shared by numerous inputs. In the case of one filter being shared by three inputs, delay line containing signal samples is three times the length of delay line for single input, and every third sample is processed by multiply-sum operation.

as a mixer with many harmonics. Or if H_s (f) is shifted by $(n + \frac{1}{2})F_s$, the resulting output spectrum is not only translated but also inverted.

If the bandpass signal at f_o is a baseband spectrum that has been modulated—a-m or ssb—the spectrum can be demodulated (translated back to baseband) by an appropriate choice of F_s, and smoothed with a lowpass instead of a bandpass filter.

Sampled-data systems, by operating on time-sampled waveforms, also open up the possibility of time sharing. For they use the time between samples to perform operations required to generate the next output, and, if enough time exists between samples for the system logic to repeat these operations, the filter hardware can be time-shared among several input signals.

Consider using one filter to process three inputs (Fig. 8). The delay line containing signal samples would be three times the length of the delay line for a single input, and every third sample would be processed by the filter. In this way, each of the computations involves samples from only one of the inputs. Time-shared filters of this type are finding application in low frequency multi-input systems such as processors of seismic data from arrays of sensors. □

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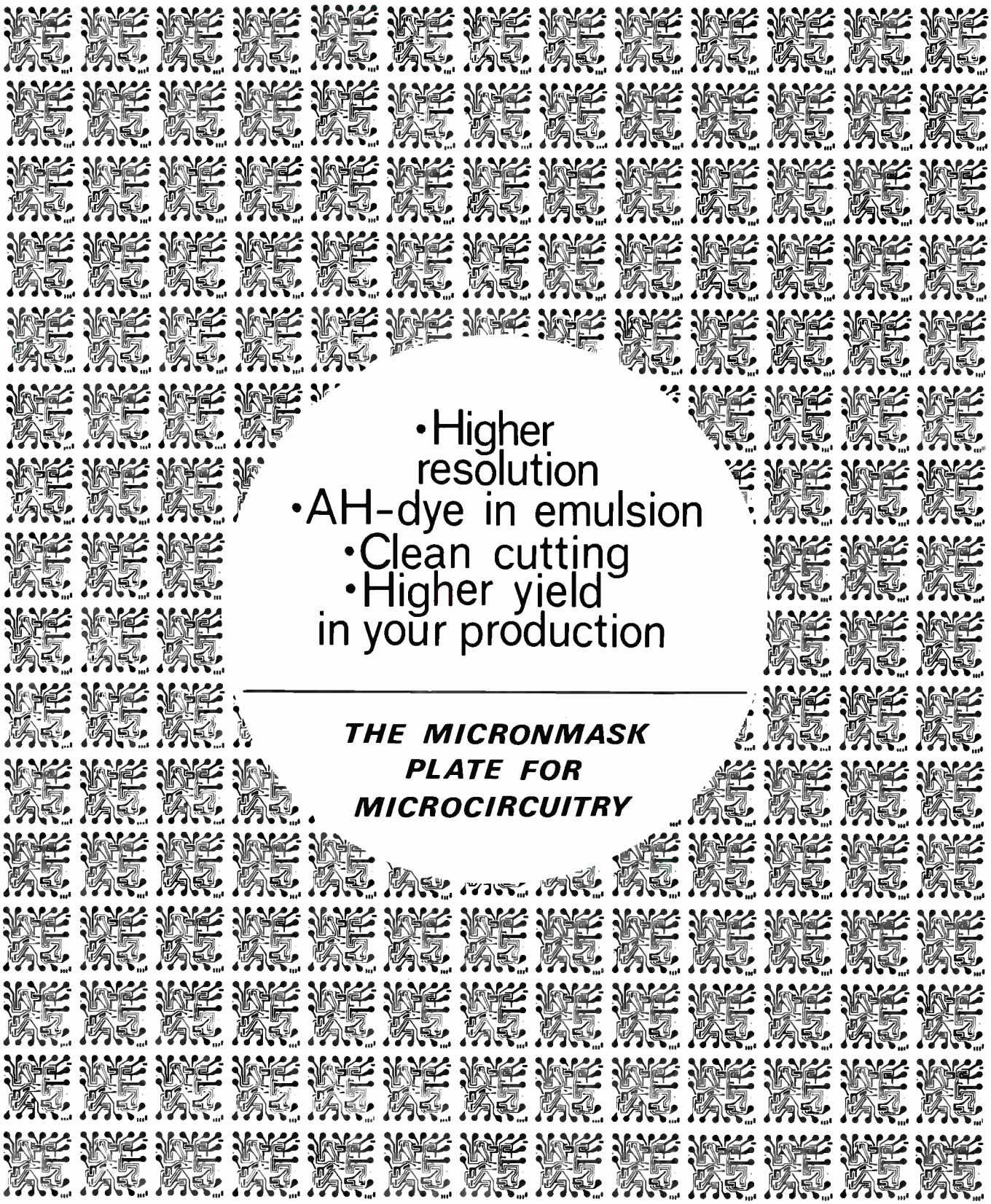

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Designer's casebook

Pulse generator uses digital ICs

By Edmund Lafko
Tampa, Fla.

A wide variety of pulse waveforms can be produced with just a few integrated circuits. Such circuits should prove more economical than buying a complete function generator, particularly if the user doesn't require the full capability of the generator. And, if he's in a hurry, putting together a simple pulse generator from available ICs insures prompt delivery.

The circuit comprises five IC flatpacks. Two RD221s are used as a four-bit counter that's driven from an astable multivibrator. The free-running multivibrator uses a single flatpack, an RD209 dual line driver; capacitively coupling the output back to the input provides the feedback path required for oscillation. The four stages of the counter are gated by two RD209 ICs, one of which also provides the inverted output. The switch setting and the output terminal used determine the duty cycle and pulse width of the output waveform.

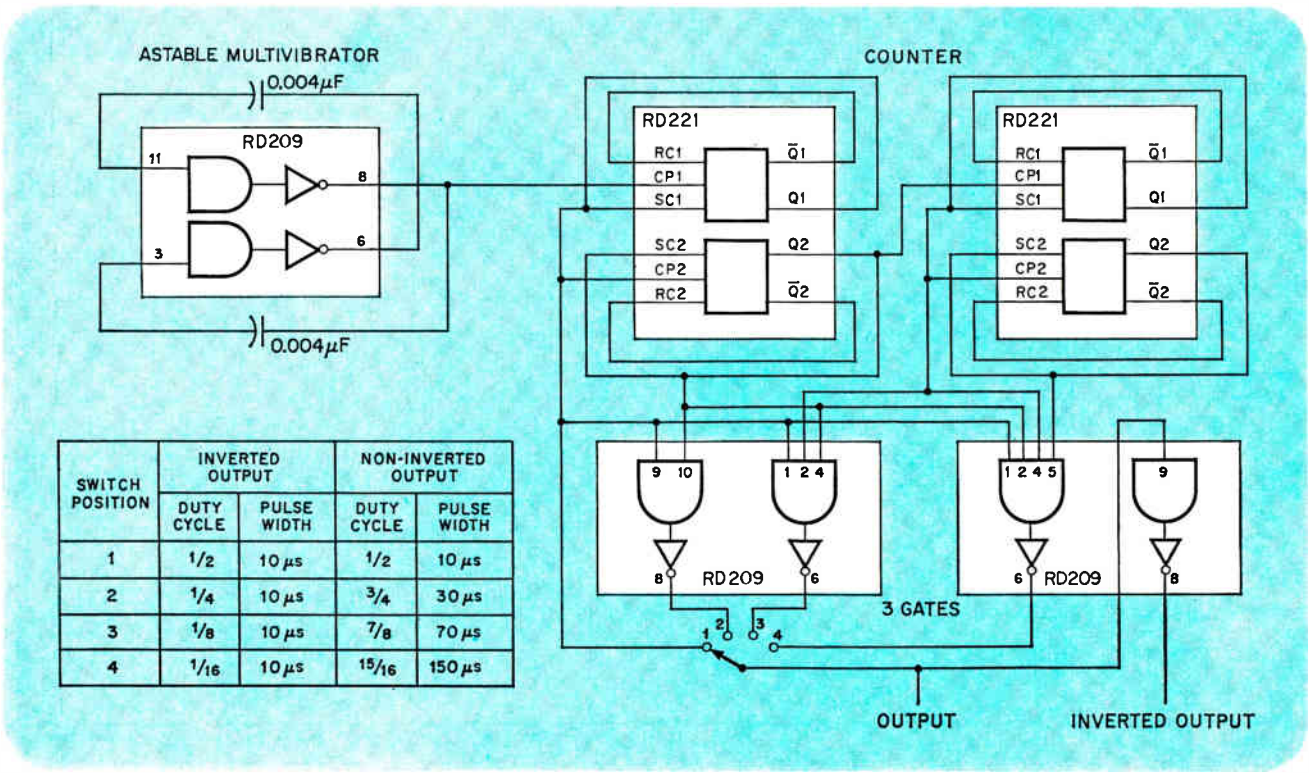
For the example shown, the dual line driver was connected so that the repetition rate was 100 kilohertz. This produces a symmetrical square wave of 10 microseconds at the first stage of the counter. Further processing of the square wave provides a waveform with different duty cycles and a 10 μs pulse width; varying duty cycles with pulse widths of 10 μs, 30 μs, 70 μs, and 150 μs.

By changing the repetition rate of the counter and gating the counter stages, waveforms of various pulse widths and duty cycles, as well as groups of pulses, can be obtained.

This circuitry also could be used as a trigger delay. The delayed trigger pulse would be gated where required within a cycle of the counter.

If the power output isn't sufficient, the circuit can be used to drive additional ICs. Or, if still more power is required, a transistor could be switched by the IC circuit. The voltage, impedance, and rise and fall times of the output can be tailored to meet the individual's specification.

User's choice. An astable multivibrator drives a four-bit counter, which is triggered by the three gates. Numerous waveforms of different duty cycles and pulse widths are obtained simply by selecting the proper switch setting and output terminal. The various combinations obtained with this circuit are detailed.



SWITCH POSITION	INVERTED OUTPUT		NON-INVERTED OUTPUT	
	DUTY CYCLE	PULSE WIDTH	DUTY CYCLE	PULSE WIDTH
1	1/2	10 μs	1/2	10 μs
2	1/4	10 μs	3/4	30 μs
3	1/8	10 μs	7/8	70 μs
4	1/16	10 μs	15/16	150 μs

Voltage monitor is easy on both battery and budget

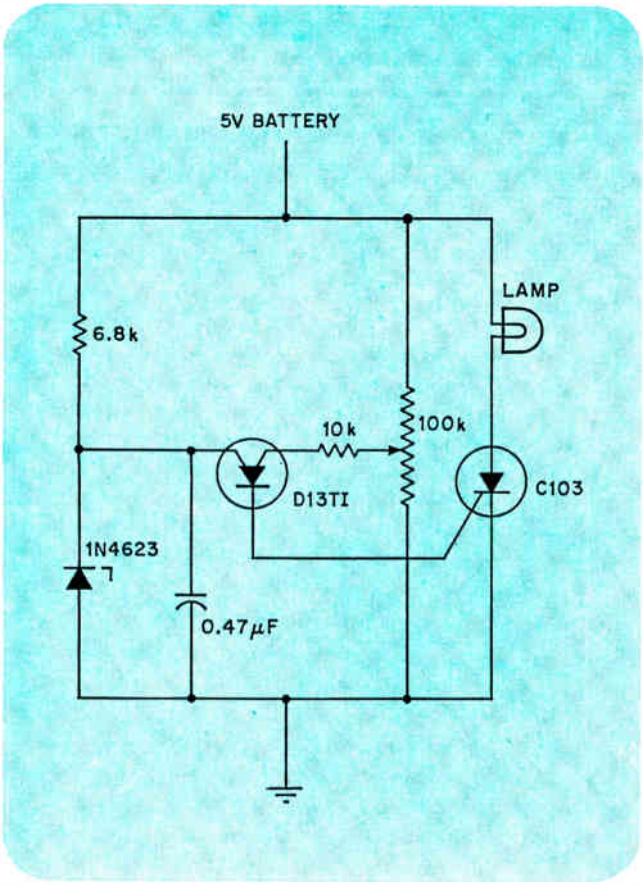
By William G.S. Brown and Victor K.L. Huang
University of Virginia, Charlottesville

At less than $6, even the most tightly budgeted researchers can afford to build this voltage monitor. It is valuable for monitoring critical battery levels during experiments.

The lamp, of course, also goes on when the battery needs replacing or recharging. The voltage across the capacitor triggers the programable unijunction transistor, PUT, at a threshold voltage, which is preset with the variable resistor. The PUT, in turn, fires the silicon controlled rectifier. The PUT consumes very little current and probably good flexibility in threshold voltage adjustment.

If the battery is in good condition, current flows only through the variable resistor and zener diode, plus whatever small leakage currents are present. A low-current zener keeps total current drain within 300 microamperes. Until the PUT fires to start the SCR, neither consume any current.

Battery saver. Voltage monitor drains little battery current. At a preset threshold the transistor turns on the SCR and the lamp. General Electric or equivalent devices can be used.



Optical biasing maintains phototransistor sensitivity

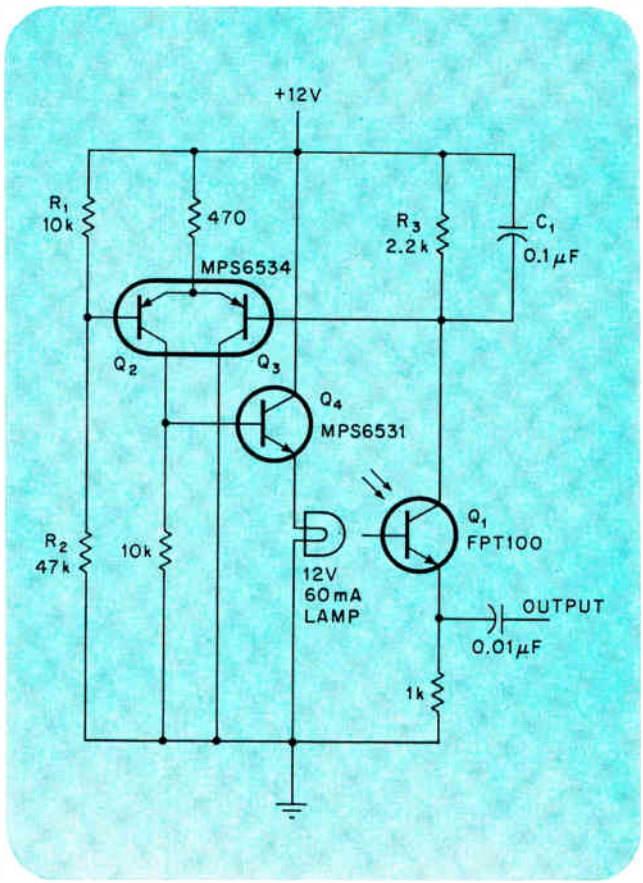
By Dennis Knowlton
University of Wyoming, Laramie

Light-biasing a phototransistor permits it to measure changes in low light levels at high speed. The extra light improves response time by boosting the collector current so that the device operates in a more favorable region. And, unlike in an electrical bias connection to the base, the transistor's sensitivity is not degraded.

The biasing circuit is an electro-optical feedback loop controlling the collector current in phototransistor Q_1. A reference current determined by R_1 and R_2 is compared in differential amplifier Q_2-Q_3 with Q_1's collector current. The amplifier output drives emitter-follower Q_3 to regulate the lamp. The light from the lamp controls the phototransistor's collector current, closing the current control feedback loop.

Bypass capacitor C_1 on the collector resistor prevents feedback to the base of Q_3 (common-collector connection). And the lamp is located where it won't shadow Q_1's active sensitivity cone.

No connection. Leaving Q_1's base unconnected gives it maximum sensitivity to light changes. Collector current is raised by the lamp to improve the response time.



Thermistor stabilizes Gunn oscillator

By T.V. Seling
University of Michigan, Ann Arbor

Gunn-diode oscillators usually take 30 to 40 minutes to warm up and become temperature stable. During this time, a thermistor in the voltage-biasing network that tunes the diode will compensate for resonant cavity changes and quickly stabilize the frequency. In addition, it will reduce frequency drift due to changes in ambient temperature.

The biasing circuit at the left, below, stabilizes an 8-gigahertz oscillator to better than 4 ppm/°C after only a 4-minute warmup. Two general purpose versions of the circuit are shown at the right. The component values and direction of changes in the bias voltage with temperature will depend on the particular oscillator.

Normally, the oscillation frequency decreases as temperature increases because the cavity expands and lowers the resonance frequency. Eventually, the heat from the diode brings the cavity's larger thermal mass to a fairly stable temperature. During warmup, the bias circuit should be compensated for this.

The circuit at the left has a negative temperature coefficient. Measurement of the uncompensated oscillator showed an approximately linear change of —9 megahertz/volt over the biasing range of 8.6 to 10.0 V. The uncompensated circuit drifted —0.8 MHz/°C, had a maximum variation in power output of 0.2 decibel, and took 40 minutes to stabilize.

The oscillator is a Monsanto Electronics DC1414A diode in a coaxial cavity 2.5 inches long. An X-band waveguide forms the outer cavity wall, and the center conductor that holds the diode is brass.

The combination of the resistor R_1 and the thermistor R_2 in the first circuit provides a bias voltage with the negative temperature coefficient.

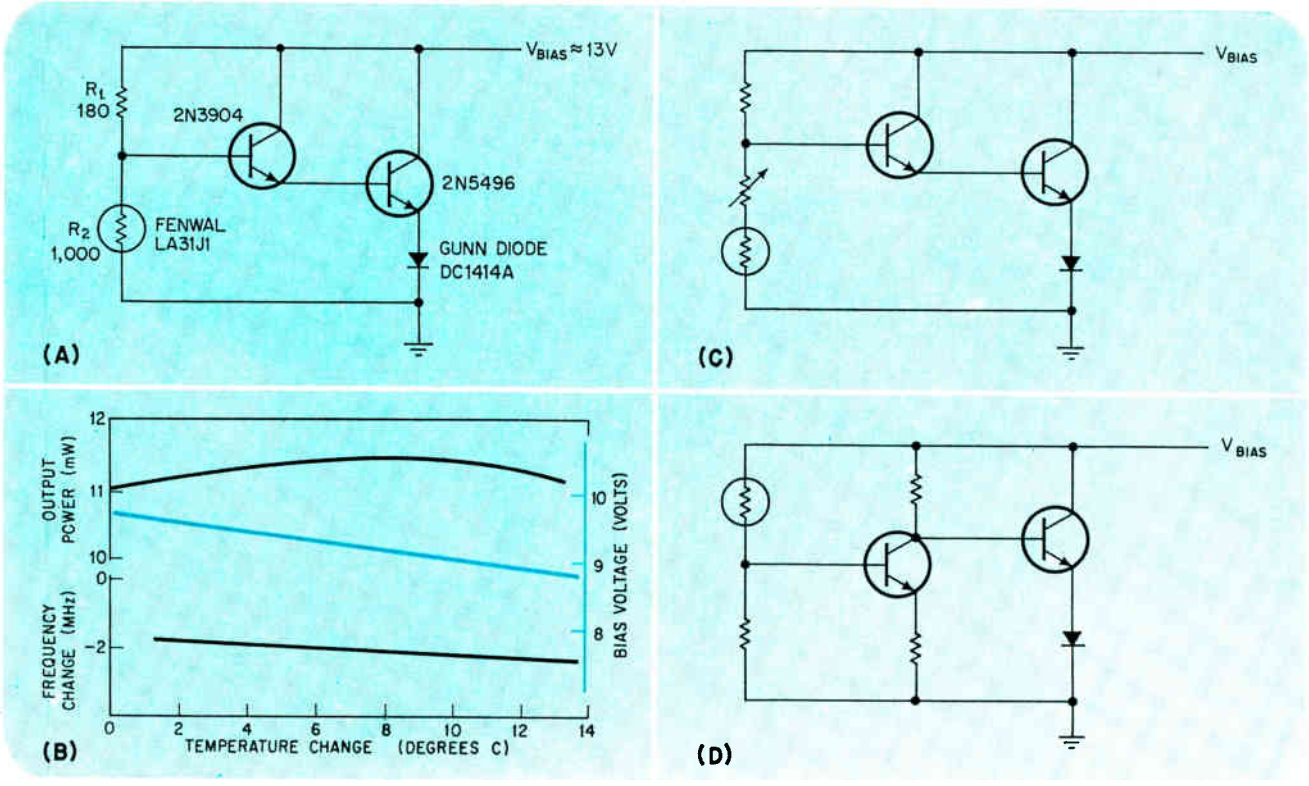
A Darlington-connected emitter follower furnishes the power gain to drive the diode. The emitter follower was used rather than a directly connected bias network to reduce the amount of heat dissipated in the bias network.

Stabilities of the compensated oscillator are plotted in the graph. After a 4-minute warmup, drift drops to —0.03 MHz/°C. Power output still varies 0.2 dB but becomes stable after the full warmup.

Other oscillators with different temperature coefficients may require padding by a variable resistor to reduce the bias variation. If a greater variation is desired, it can be obtained by using the connection in the third circuit to amplify the bias voltage.

Designer's casebook is a regular feature in Electronics. Readers are invited to submit novel circuit ideas and solutions to design problems. Descriptions should be brief. We'll pay $50 for each item published.

Gunn stabilizer. Thermistor reduces bias voltage when temperature rises, stabilizing diode oscillation frequency. Circuit (A) for an 8-GHz oscillator yields the temperature characteristics charted in (B). Circuit (C) is a version that reduces the bias variation, while the additional gain of (D) provides larger bias swings with temperature.



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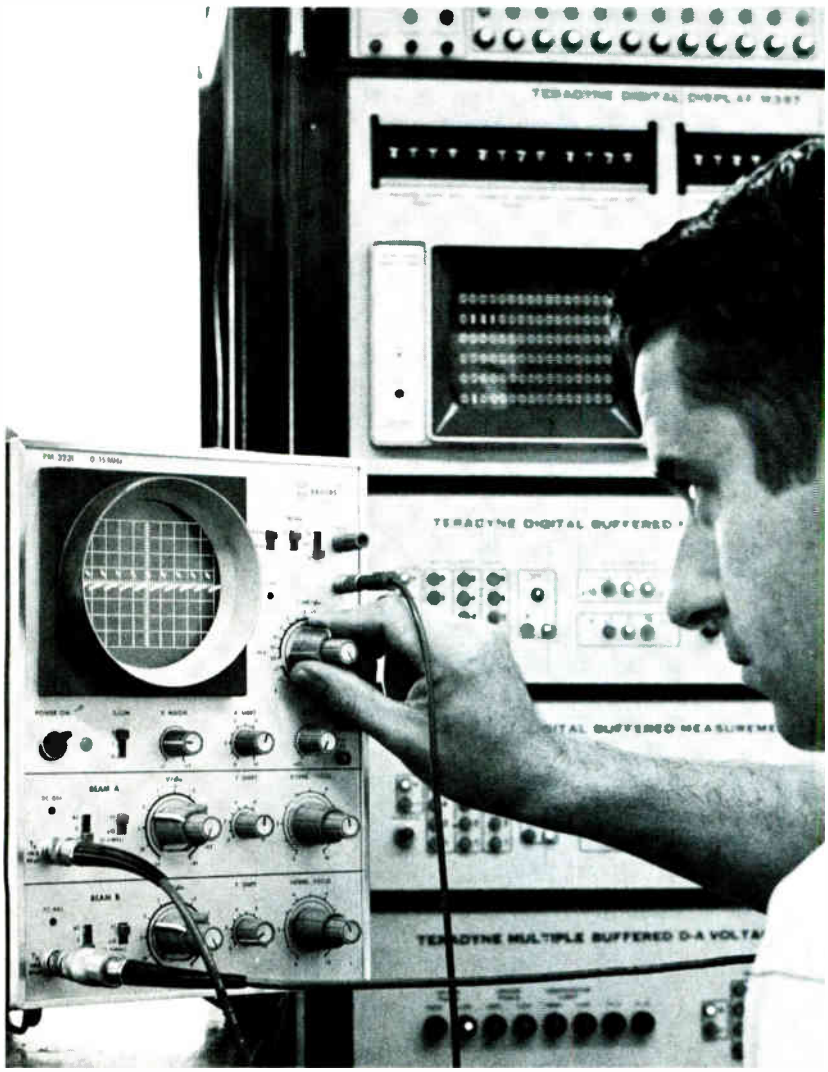

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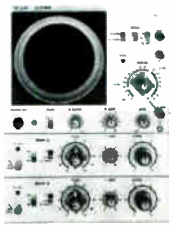

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☐ ☐ Does it have individual left and right parentheses keys that allow you to solve directly expressions such as: $((a + b) - c \div d) \times (f - g) = ?$

☐ ☐ Is every function and program uniquely and unambiguously defined by the keying sequence (without modification by toggle or rotary switches)?

YES NO

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☐ ☐ Will it solve complicated problems and still be easy enough for your secretary to operate?

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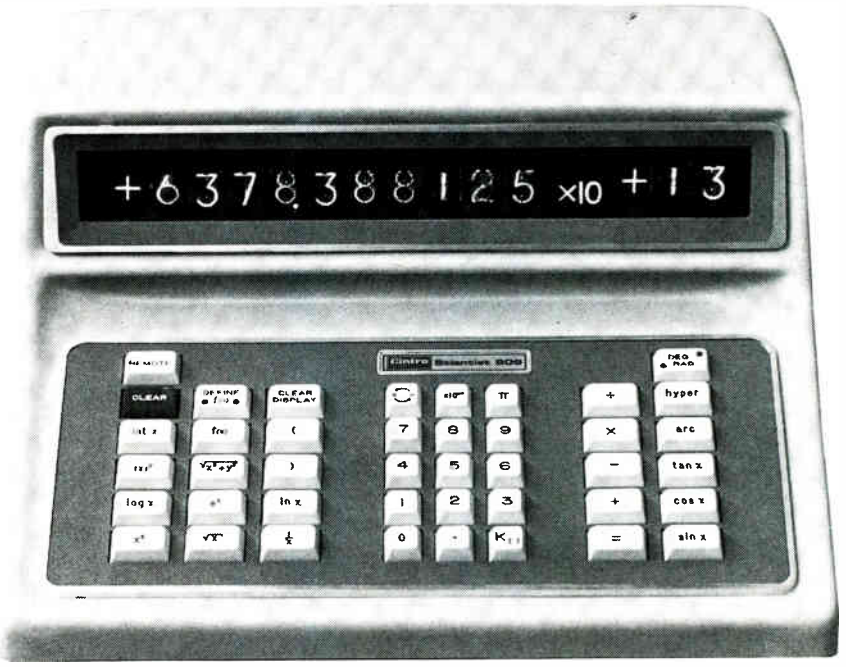
The Cintra Scientist



P. S. If you haven't already guessed, every answer is "yes" for the Cintra Scientist.

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Putting d-a converters to work: 10 examples show versatility

Devices provide interface for systems with inputs and outputs, permitting simple and accurate digital processing suitable for a broad range of operations and functions

By W. R. Spofford Jr., *Analog Devices Inc., Pastoriza division, Newton Upper Falls, Mass.*

☐ Digital methods for processing signals no longer are restricted to computers. Many engineers, although they may be designing systems with inputs and outputs in analog form, are adopting digital methods to do almost everything in between. They find that these methods yield more accurate answers, are easier to apply with the new integrated circuit logic and memories, and, in many cases, allow operations that just couldn't be done any other way.

Besides the growing variety of digital ICs, another component pacing this shift to digital techniques is the digital-to-analog converter. In fact, it's the d-a and analog-to-digital converters that make it possible to get into the digital mode and present an output that's usable in analog form.

The basic component in most of the 10 examples in this article is the d-a converter. The a-d converter is often an extension of the d-a converter, using a d-a in a feedback loop to convert the digital word to analog. By making comparisons with the analog input, the feedback loop adjusts the digital value until the two values are the same.

There are two basic types of d-a converters: the fixed reference and the multiplying types. Each converts a digital input number, D, to a corresponding analog voltage or current ($V_{analog} = kD$). In the first type, the proportionality constant, k, is fixed, often with a zener diode; while in the multiplying converters, k may vary. The multiplying d-a converter thus is often used to change the value of varying signals according to digital input information. Fixed reference converters, on the other hand, are used where simple d-a conversion (digital input produces proportional dc voltage output) is needed.

In general, fixed reference converters are more accurate because the reference is included in the package. A manufacturer thus can compensate the reference directly for temperature drift and match the reference's offset to the circuit's. By contrast, the multiplying d-a converter accepts an external, varying reference, which limits the converter's ability to match the circuit to the source.

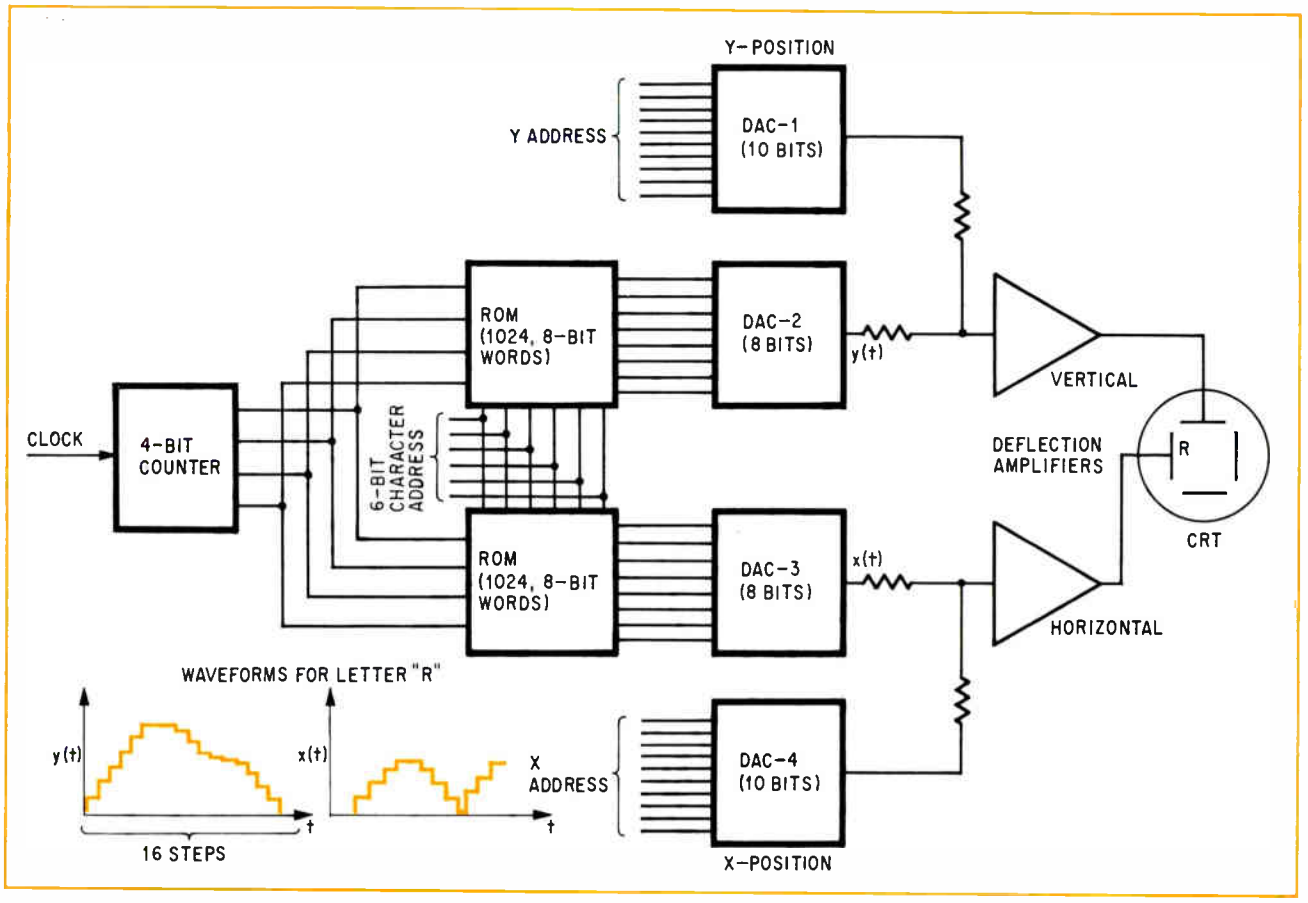
The multiplying d-a converter is useful in such systems as resolver positioning circuits, where 400-hertz signals are adjusted with digital inputs. However, where high dc accuracy is needed, such as for cathode-ray tube character generation, the fixed reference converters are particularly useful.

CRT character generation requires two steps: one to fix the character's position on the screen, and the other to form the character. In circuit 1, two high-speed d-a converters, DAC-1 and DAC-4, hold the 10-bit x and y addresses of the character location on the CRT. The outputs of these converters are added to the outputs of the respective eight-bit d-a converters, DAC-2 and DAC-3, that provide information about character shape. This is one application where there's no analog counterpart.

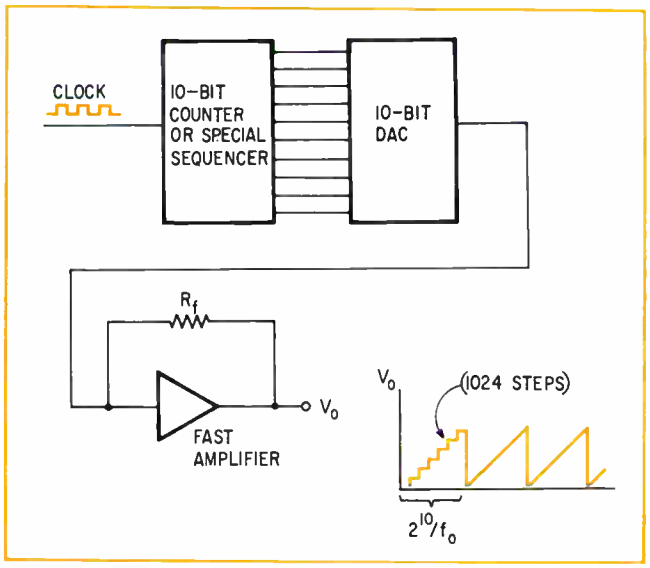
Each character's shape is described by 16 eight-bit words, in both the x and y directions, stored in the two read-only memories. This group of 16 words is selected by a six-bit address for the location in the ROMs. The four-bit counter, driven by the clock, steps the ROMs through the selected 16 words, and DAC-2 and DAC-3 produce the necessary x and y deflection voltages, which ride on the coarse positioning voltages set by DAC-1 and DAC-4. The beam is thus displaced

The 10 applications

1 CRT character generation
2 CRT sweep generation
3 Programable power supplies
4 Resolver positioning
5 Shaft angle conversion
6 Radar PPI display
7 Radar moving target indicator
8 Low-noise communications
9 Aircraft music distribution
10 Automatic gain measurement



1. CRT character generation. The 10-bit d-a converters set the character location on the screen while the eight-bit d-a converters generate the character shape, based on the information stored in the read-only memories. The four-bit counter steps the two ROMs through 16 words of eight bits each. The characters are thus formed in 16 x and y steps, each quantized with eight-bit accuracy.



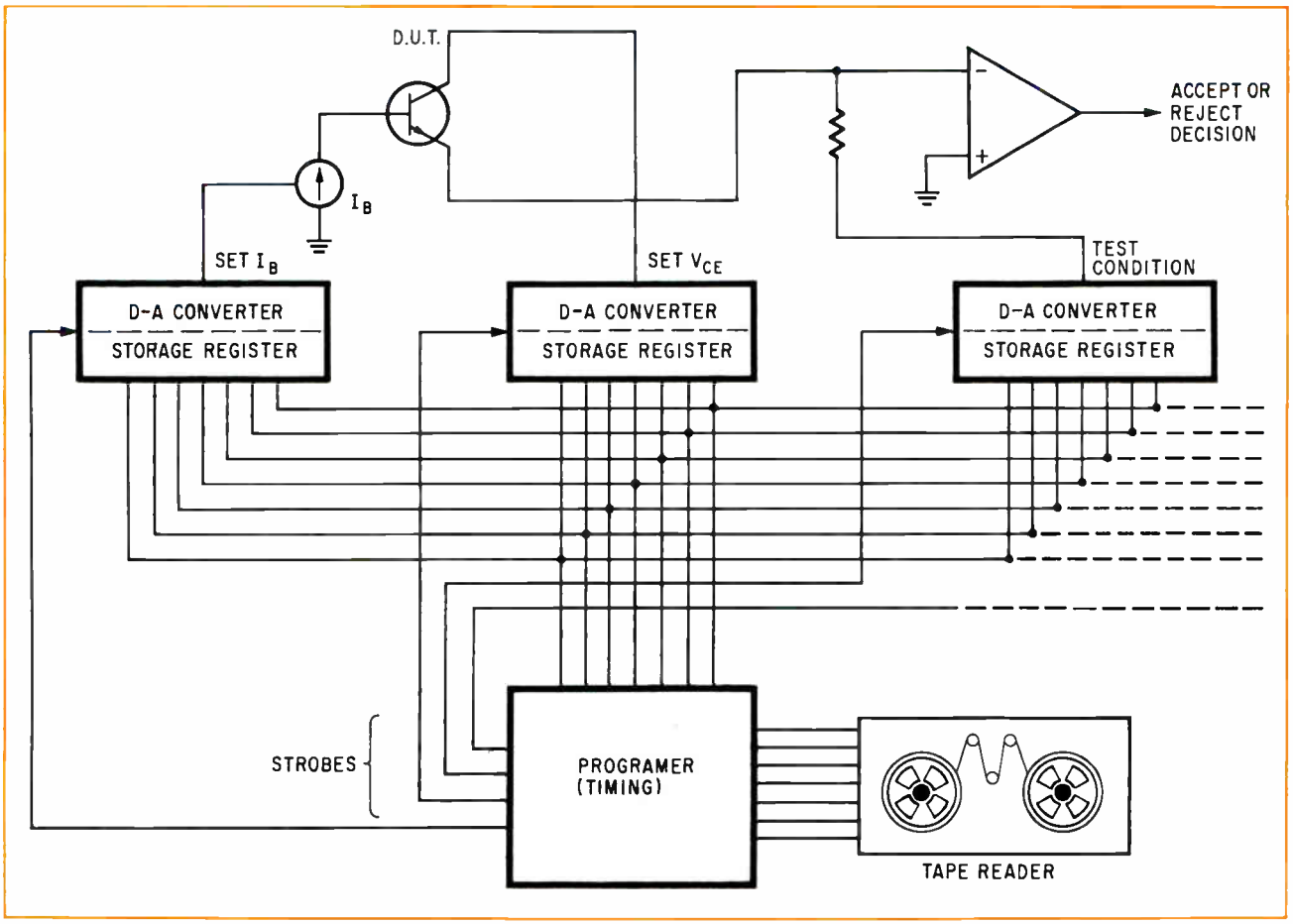
2. CRT sweep generation. Linear sweeps for an oscilloscope are generated by converting a counter's output to a series of steps. With 10 bits, 1024 steps are generated per sweep, resulting in linearity in the 0.05% range. The time base can be derived from a crystal oscillator for stable operation.

from its coarse position to form the character, with the two character-forming waveforms, y(t) and x(t), thus being quantized with eight-bit (256 levels) accuracy.

Generating the letter R, for example, would begin with the coarse voltage setting the beam at the character's lower left hand corner, and stepping it in the y-direction with zero x deflection to form the first upstroke. The beam then moves horizontally (constant y voltage), and then begins to curve down in steps toward the right as the y voltage decreases and the x voltage reaches a peak and then decreases. When the x voltage reaches zero—the beam has completed the D-shaped upper portion of the R—it steps a small amount to move the beam about halfway over, beginning the final diagonal downstroke as the y(t) voltage also steps down. The 16 words thus create 16 x and 16 y steps.

With a 1024-word ROM, 64 characters can be stored. Any character shape can be stored in the ROMs, simply by describing it with 16 dots having almost arbitrary placement (256-level quantization). The locations of each of these 16 dots then are converted to eight-bit digital words representing the voltages required to place the beam at that spot.

For this application, the d-a converters must have



3. Programable power supplies. Automated test equipment uses programable power supplies to set up test voltages and currents under control from digital information on magnetic tape. The data for each parameter test point is applied to all d-a converters in parallel, but is entered only into converter that's strobed. The final d-a converter sets up the parameter level for go/no-go decision.

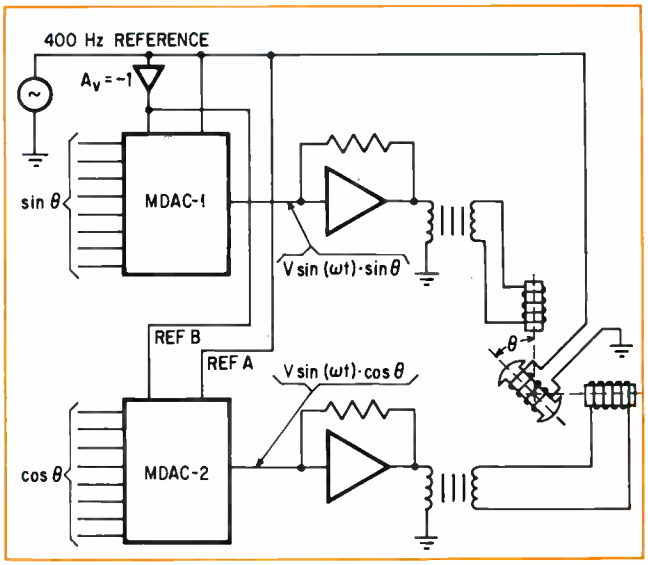
fast settling time—about 200 to 300 nanoseconds—for each character segment and should not generate transients when switching. Another amplifier can be added at the d-a converter output to correct for deflection nonlinearity in the CRT.

Ultralinear sweep generation is often required for precision instrumentation. Standard linear sawtooth generators are often not accurate enough or are subject to drift.

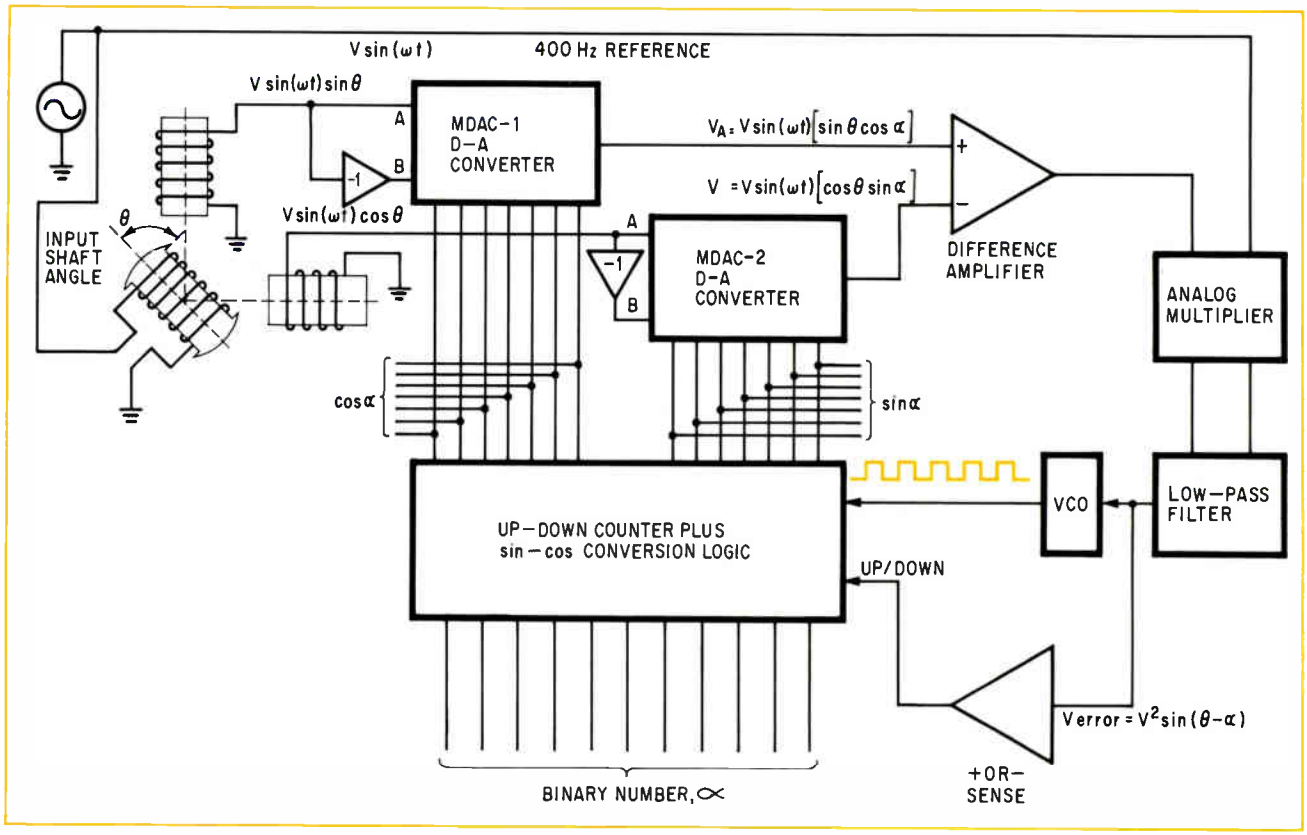
A d-a converter driven by a simple counter will generate a series of voltage steps, which, if there are enough of them, will appear as a continuous ramp function. For example, a 10-bit d-a converter will generate 1024 steps in its upward rise as the counter goes from zero to full scale, and will recover quickly as the counter recycles. The time base is controlled by the clock pulses, which can be derived from a crystal oscillator, operating, say, at 20 megahertz.

With this step-wise approximation to the ramp, the most the waveform can deviate from a straight line is half a step. With 10 bits, there will be 1024 steps, keeping the deviation to 1 part in about 2048, or about 0.05%. This is far better than the linearity attained with any other method. Settling time for these converters should be about 50 ns.

This type of sweep generator can also be used for



4. Resolver positioning. Digital data for sine and cosine of the angle to which the resolver is to be set are converted to sinusoidal signals with magnitudes proportional to sine and cosine of desired angle. When applied to resolver, the resolver moves to corresponding position.



5. Shaft angle conversion. When resolver is positioned at some angle, system generates a digital number, x, that is converted to sine x and cosine x. They then are multiplied by the 400-Hz sine and cosine signals from the resolver, generating a dc term that's proportional to difference between actual angle θ and binary test value of the angle α. When the difference is zero, the voltage-controlled oscillator stops and binary number α is read out as equal to actual angle θ.

a CRT character generator of less sophistication than the setup described in circuit 1. Here, the characters are generated by a raster sweep of the beam across the whole screen, similar to the raster of television receivers. However, where commercial TV uses two interlaced fields, this scheme uses several interlaced fields. In each field, the beam writes a small portion of each character on a line, scanning down the whole screen. On each successive sweep the beam adds a little more to each character. The beam thus interlaces several fields, each one writing a small portion of the characters on each line of type.

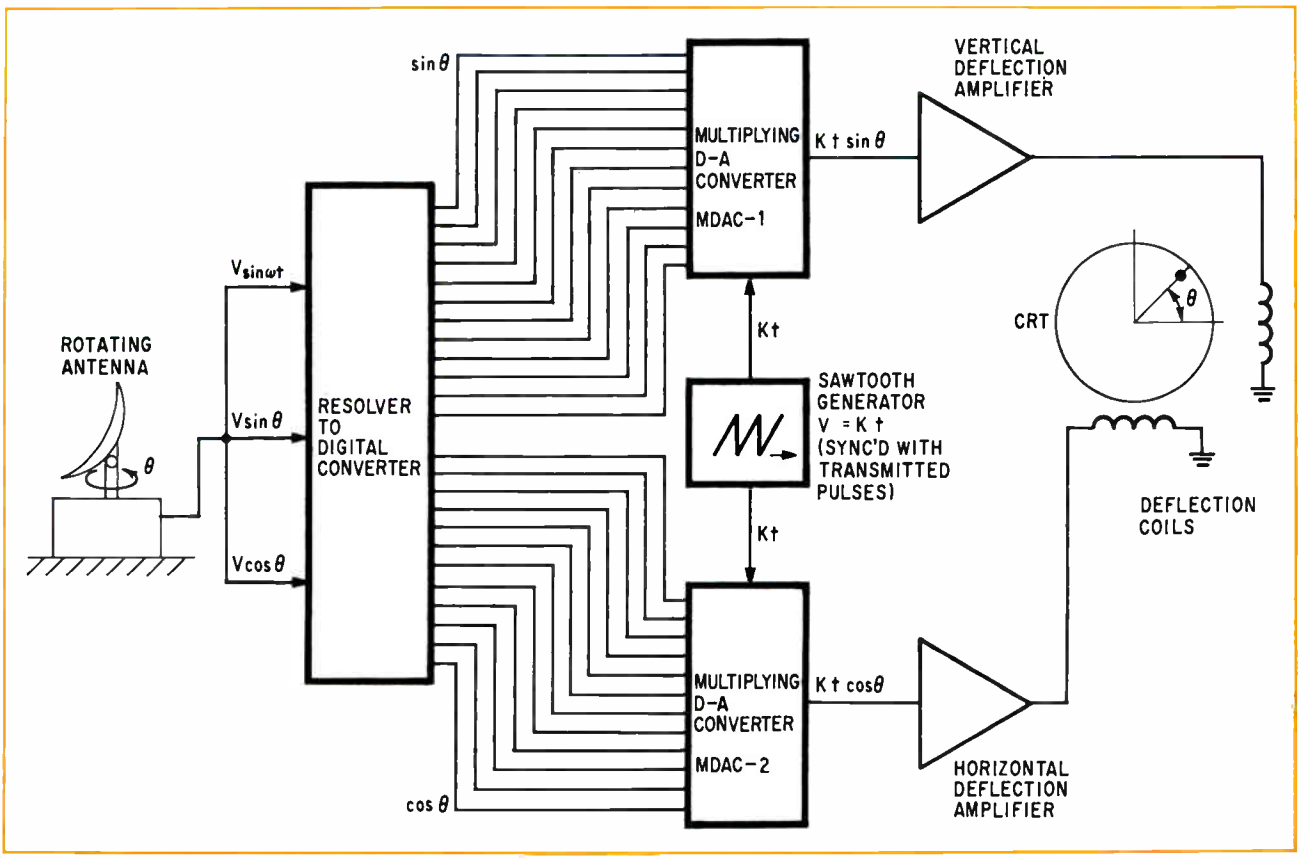
The waveforms needed for this would be a standard sawtooth for horizontal deflection and a series of steps for the vertical deflection. The vertical deflection steps would be offset from one scan to the next to produce the step-wise generation of each character. This interlacing of the vertical scan can be easily produced by wiring the counter to provide an offset of one least significant bit for each successive scan.

Programable power supplies for automated test equipment can be programed from digital information on a magnetic tape by using d-a converters. In circuit 3, the d-a converters contain internal storage registers. The programer, which is fed the information on the tape, describes the test conditions on its output lines to all the converters. The first strobe pulse loads the first test condition only into the first storage register. The next digital word is strobed into the second d-a converter's register. The process continues until each of the d-a converters contains the proper test conditions for one test. The last d-a converter usually is programed with go/no-go information. The programer then recycles the d-a converters to set up the bias conditions for the next test on the same device. After up to 30 tests, a new device is loaded into the socket.

It's often desirable to completely test a device, with a total of about 30 tests, at rates of several hundred tests a minute. For example, if it's desired to test a device completely in 0.1 second, each d-a converter must settle in less than 20 microseconds. For some of the test conditions, a full 12 bits of resolution (4,096 steps) are not necessary—8 bits, for instance (256 steps), may be adequate. However, it is usually necessary to define the 256 steps to 12-bit accuracy ($\pm 0.01\%$ as opposed to $\pm 0.4\%$).

The circuit shows a transistor inserted in the test socket undergoing a test for current gain. The first d-a converter sets the base current, the second the collector-emitter voltage, and the third the condition for the output for the accept or reject decision.

Resolver positioning can be accurately achieved with multiplying d-a converters. A resolver is an electromechanical device with a wound rotor and two perpendicularly oriented stator windings. A sinusoidal voltage applied to the rotor induces voltages in the



6. Radar PPI display. Resolver-to-digital converter generates digital signals proportional to sine and cosine of the radar antenna angle. Multiplying d-a converters use this data to set voltage of the sawtooth signals applied to horizontal and vertical deflection coils. CRT beam then scans outward from screen center at an angle corresponding to antenna angle. Logarithmic drive amplifiers could help give a large dynamic range with high resolution for close targets.

stator windings proportional to the sine and the cosine of the shaft angle. Conversely, if the voltages applied to the stator windings are proportional to the sine and cosine of some angle, the rotor will position itself at the corresponding angle.

In circuit 4, two multiplying d-a converters, MDAC-1 and MDAC-2, are used to position a resolver. The 400-Hz reference wave is applied directly to the rotor and also to the two converters. Each converter takes both the reference wave and its inverse—the reference shifted by 180°—allowing operation in all four quadrants of the shaft angle.

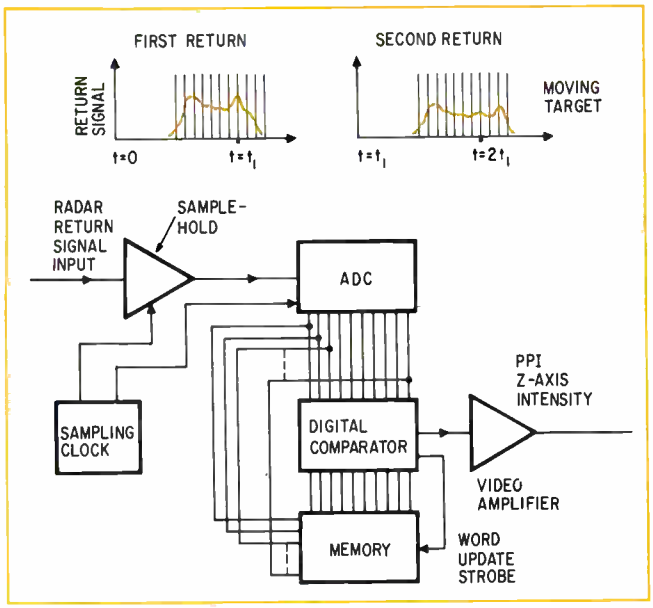
Digital numbers corresponding to the sine and cosine of the desired angle are loaded into the digital-to-analog converters. The outputs of the converters thus are 400-Hz voltages having amplitudes proportional to the sine and cosine of the angle. These voltages then are applied through power amplifiers to the stator windings, and the rotor moves to the desired angle.

The same function, performed in analog fashion, would require two analog multipliers, each multiplying the 400-Hz reference by dc voltages proportional to the sine or cosine of the desired angle. However, analog multiplier accuracy is only about 0.5%, even when trimmed externally, whereas 12-bit multiplying d-a converters provide 0.01% accuracy. And the analog's 0.5% is percent-of-full-scale accuracy, while the d-a converter's accuracy is percent-of-reading accuracy, giving better results at small values.

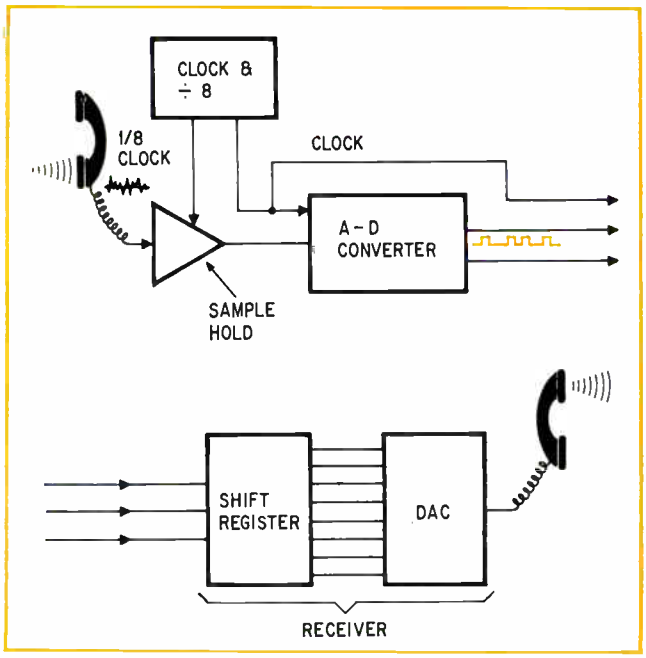
Shaft-angle-to-digital conversion can be accomplished with multiplying d-a converters, as in circuit 5. The resolver, when positioned at a particular angle, generates two voltages proportional to the sine and cosine of the angle. A digital word proportional to the angle is generated by allowing a voltage-controlled oscillator (VCO) to drive an up-down counter. The counter has sine-cosine conversion logic that develops two digital outputs, each proportional to the sine and cosine of the angle represented by the digital number at the third output. The sine and cosine digital information is converted to analog form in multiplying d-a converters, MDAC-1 and MDAC-2, and the two outputs are summed in a difference amplifier.

An analog multiplier multiplies this difference by the original reference, generating a dc term proportional to the sine of the difference between the digital angle and the actual angle; this can be shown with simple trigonometric identities. A low-pass filter singles out this dc error term and applies it to the VCO to reduce the error to zero. When the error is zero, the VCO then stops oscillating, and the counter's final count represents the angle.

This method provides much more accurate conversions than are possible with encoders. It's used



7. Radar moving target indicator. Returns from two successive pulses are compared after sampling and analog-to-digital conversion. When difference is significant, signal is applied to z-axis for intensity modulation of CRT display. Thus, signals from stationary objects can be blanked out.



8. Low-noise communications. In this basic form of pulse-code modulation, voice signal is sampled, converted to digital form in step with a clock and transmitted over the lines. At receiving end, each eight-bit word is assembled in the shift register and reconverted to its original analog form by the d-a converter. Use of digital signals avoids signal-to-noise degradation problems inherent in analog transmission since pulses can be regenerated between transmitter and receiver.

often in displaying and processing data related to radar antenna position.

Radar pulse position display—a circular sweep of the beam on the CRT corresponding to the rotating antenna—can gain an order of magnitude in accuracy with the conversion scheme of circuit 5.

The resolver converts the antenna shaft position angle to corresponding sine and cosine voltages, which are converted to digital form by the resolver-to-digital converter of circuit 5. Then the digital numbers are multiplied, as in circuit 6, by a sawtooth applied to the analog input of the multiplying d-a converter. The outputs of the converters are sawtooth waveforms having maximum values proportional to the sine and cosine of the antenna angle. When amplified and converted to sawtooth current waveforms, the signals are applied to the deflection coils of the CRT (sine sawtooth to the vertical, and cosine sawtooth to the horizontal). The CRT beam sweeps from the center of the tube toward the rim along a line that is at the same angle as the antenna with respect to some zero reference. The return pulse then intensifies the beam as it sweeps outward, giving an indication of range and azimuth. It's also possible to include a logarithmic function to compress the scale and give a large dynamic range with high resolution for close targets.

In conventional analog schemes, the amplitude of the respective resolver sinewaves was detected and multiplied in an analog multiplier with the sawtooth waveform. This was less accurate than the digital scheme because of the multiplier's inaccuracy.

Radar moving target indicators compare sequentially received returns and pick out those whose position has changed with respect to the previous return. In this way, stationary objects may be ignored. With an a-d converter, a digital comparator, and memory, the job is easily implemented, as in circuit 7.

Each return is sampled at 2-μs intervals. The output of the sample and hold circuit is converted to digital form by the a-d converter and compared to the previous output stored in the memory. If the two words are nearly equal, no z-axis intensification is developed. Only when two sequential words are significantly different will the beam be intensified for display on the PPI scope.

Low-noise communication is possible when a voice signal is converted to digital form before transmission. Analog signals pick up much noise when amplified along the way, but if digital transmission is used, the effects of amplification noise can be minimized by regenerating the pulses at points along the transmission. At the receiving end, a d-a converter reconstructs the original voice signal.

A simplified diagram of such a system is shown in circuit 8. The analog signal is sampled at regular intervals, converted to digital form in the a-d converter and transmitted serially along with the clock pulses. At the receiving end, the signals are assembled in a shift register, kept in step by the clock pulses, and converted back to the original sound with the d-a converter.

The clock is divided by eight before being applied

to the sample and hold. The a-d converter thus sends one serial eight-bit word for each time the sample and hold circuit is strobed. The shift register at the receiving end assembles the words of eight bits each.

The sampling rate should be at least twice the bandwidth; thus for a 10-kilohertz bandwidth, a 20-kHz sampling rate is required. The converters thus should operate on 50-μs cycle times.

Music distribution systems in commercial aircraft, as in the 747, utilize digital techniques to conserve wiring and economize on weight. As an alternative to piping eight channels to each seat, the music channels are multiplexed on one pair of wires and decoded at the seat.

In circuit 9, the analog music channels are multiplexed into the sample and hold circuit. The a-d converter sends out serial words corresponding to samples of each of the eight channels in sequence. A three-bit address code is added to the eight bits of analog information and the complete word is wired to every seat in the plane. At each seat, an address decoder is linked with the channel selection switch, and the d-a converter operates on only the digital words corresponding to the selected channel.

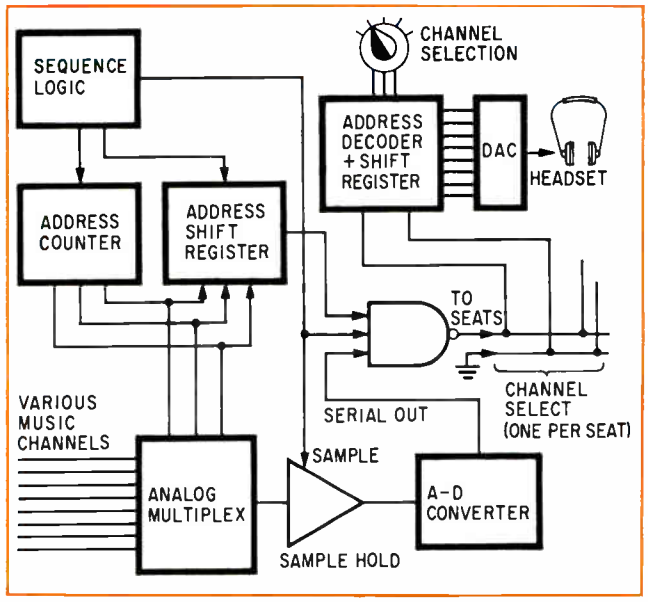
Note that in this and the previous scheme, absolute accuracy in the d-a converters is unimportant—all that counts is linearity for good sound reproduction. It's also interesting to note that only six bits are required for satisfactory music reproduction.

Automatic gain measurement, vital in any telephone system with repeater amplifiers, can be accomplished with a multiplying d-a converter. An input voltage, in circuit 9, is used as a reference in the multiplying d-a converter and also as one input to the null detector. The multiplying d-a converter takes its digital input from an up-down counter that counts the pulses generated by a VCO under up-down control from the null detector.

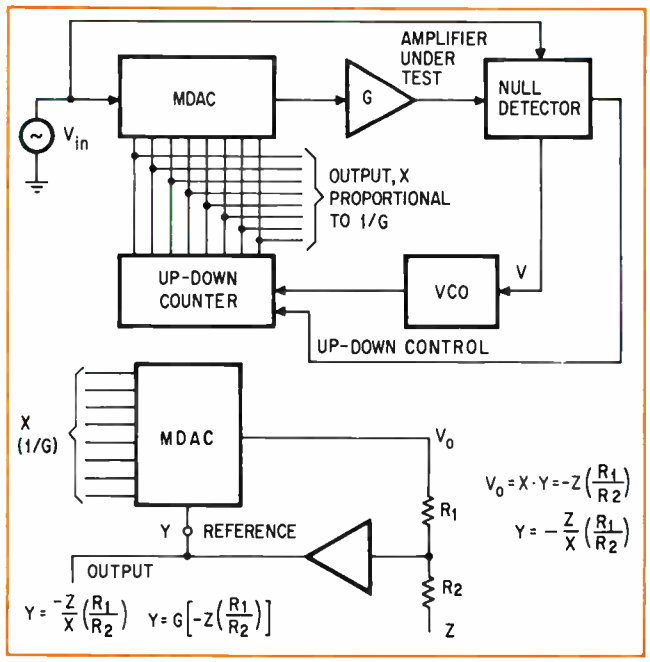
The null detector compares the input reference voltage with the output of the amplifier under test. When the two are equal, the multiplying d-a converter has a digital input proportional to the reciprocal of the amplifier gain. The multiplying d-a converter thus is applying a 1/G term to the amplifier input and the amplifier is amplifying it G times to restore it to its original value.

The digital word then can be applied to a second multiplying d-a converter to produce a voltage proportional to the amplifier gain itself. The second circuit acts as a divider: it divides 1/G into some constant term to result in a constant multiplying the amplifier gain G.

The second circuit alone may also be used as a divider to perform the function $Y=Z/X$. Here the divisor, X, is applied to the digital inputs of the multiplying d-a converter. The quotient, Y, appears at the reference input to the d-a converter. The closed loop output, V_o, produces a current $i_1 = -V_o/R_1$, equal and opposite to the current in R_2, Z/R_2, insuring that the amplifier input is at virtual ground. The output V_o of the multiplier is XY. Since $(V_oR_1) = (XY/R_1) = Z/R_2$ then $Y = (-Z/X)(R_1/R_2)$. The R_1/R_2 ratio is thus a scale factor for the quotient. □



9. Aircraft music distribution. To save weight in interconnecting cables, music channels are multiplexed into a sample and hold circuit and then converted to digital form for transmission on a single pair of wires. Selector at the seat sets up an address decoder which allows selected channel to reach the headset.



10. Automatic gain measurement. Upper circuit shows how a multiplying d-a converter effectively acts as an attenuator to cut input signal by 1/G. When amplifier output equals the reference, the voltage-controlled oscillator stops. Corresponding digital word developed by the counter is proportional to 1/G. A signal proportional to G is produced by the divider circuit, below. Here output Y is proportional to reciprocal of the X input, or quotient (—Z/X). Lower circuit may function as a divider in any other system.

Coping with feedthrough in ECL integrated circuits

Certain unwanted capacitances in high-speed logic circuits are liable to produce spurious signals; the engineer must know how to design around them

By Fred U. Rosenberger, *Computer Systems Laboratory, Washington University, St. Louis*

□ A big advantage of emitter-coupled logic, next to great speed, is relative immunity to many noise sources. In particular, noise caused by crosstalk between connecting lines affects ECL much less than it does saturating logic like TTL and DTL.

However, ECL is particularly susceptible to feedthrough. Feedthrough coupling occurs when logic level transitions of input signals to an ECL integrated circuit produce short pulses on the IC output, even though the logical description of the circuit indicates no such coupling. In some cases, the amplitude of these feedthrough pulses approaches the normal logic swing.

This circumstance only causes difficulties when a stable, transient-free output from a logic circuit must be insured despite changes in some of the inputs —and while such stability is not required for all the logic circuits in a system, it will almost certainly be needed in some, particularly sequential circuits.

When feedthrough is a problem, it can be eliminated or minimized by limiting the number of inputs to the circuit, by using only the noninverting outputs of OR-NOR gates (which are less seriously affected by input transitions than the inverted outputs), or by avoiding certain circuit types altogether. The IC manufacturer also can eliminate one form of feedthrough almost entirely by designing in an extra transistor or diode to bleed off parasitic capacitance charge.

For the same propagation delay, ECL circuits can be expected to produce less crosstalk coupling than saturating logic circuits since the ratio of rise and fall time to propagation delay is less than for saturating logic. In addition, less noise is coupled into the power supply line with ECL than with most saturating logic types—particularly TTL with totem pole outputs—because ECL draws nearly constant current from the power supply. Yet another advantage is that two or three levels of current steering can be used to construct functions such as AND or exclusive-OR with a propagation delay approximately equal to that of a single gate. Nevertheless, feedthrough is a recurrent difficulty with ECL.

Two examples of how feedthrough may disturb the operation of an ECL gated flip-flop are shown in Fig. 1. In the top circuit, data are available from two sources and the select line determines which is gated into the flip-flop. If the AND gates suffer from feedthrough, changes in data applied to the unselected AND gate may set the flip-flop incorrectly. (Only feedthrough from the AND gate input labeled D has any effect; the flip-flop clock is off when the AND gate's C input changes.) In the bottom circuit in Fig. 1, the flip-flop output should not change when the clock changes and the input level is the same as the flip-flop state. Such a circuit may be used to store transition logic signals temporarily (that is, to generate a conditional pause). In this application, feedthrough from the clock input could generate two transition signals at the flip-flop output for each clock pulse, even though the data input level does not change.

There are several different causes of feedthrough:

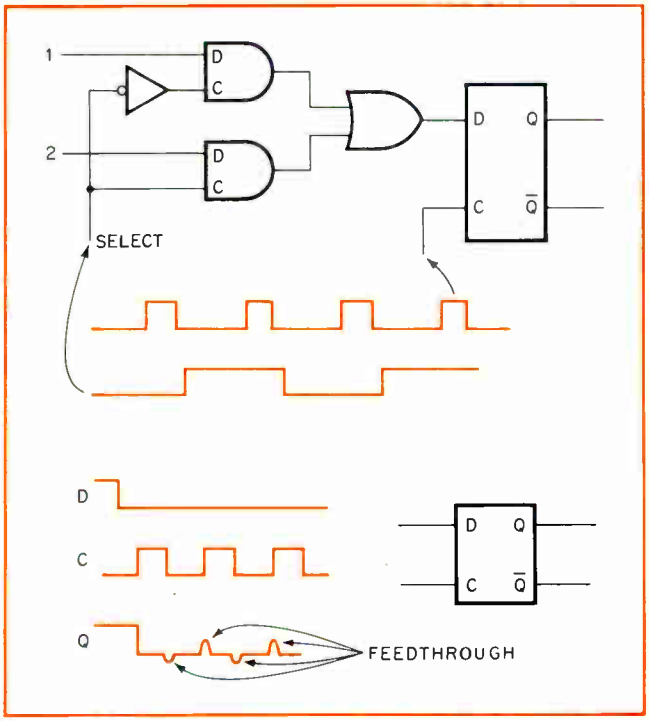
- Coupling via the collector-to-base capacitance, C_{cb}, of the transistors.
- Coupling through the base-to-emitter capacitance, C_{be}.
- Coupling by the capacitance between internal current source and ground.
- Coupling caused by stray capacitances.

The equipment designer, however, can calculate the amplitude of the spurious signals generated and then, if necessary, take steps to minimize them. To do so, he may make certain simplifying assumptions: he may ignore the switching time of the transistors, and assume a constant base-to-emitter voltage drop V_{be} of 0.7 in the conducting state. In the following examples, junction capacitances will also be assumed to be independent of reverse bias, and their values will be assumed to be: C_{cb} = 1.5 picofarads, C_{be} = 1.5 pF, and collector-to-substrate capacitance C_{cs} = 2 pF. These values are typical of many ECL circuits.

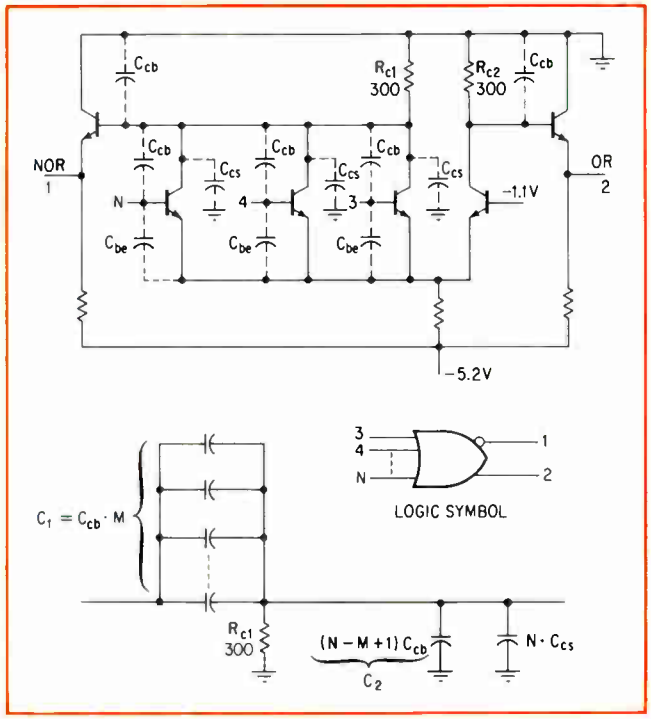
To start with the first of the causes of feedthrough listed above, consider the C_{cb} coupling of the input transistors of an ECL gate, Fig. 2. If one input is at a high level, holding the NOR output low, the equivalent gate circuit when transitions are applied to the other inputs is in the state shown at the bottom of Fig. 2. From the equivalent circuit, the amplitude of the spurious output voltage of the gate for an input ramp is:

$$V_o = C_1 A R_{c1} \{1 - \exp[-t/R_{c1}(C_1 + C_2)]\, u(t)\} - C_1 A R_{c1} \{1 - \exp[-(t-B)/R_{c1}(C_1+C_2)]\, u(t-B)\}$$

where $C_1 = MC_{cb}$ and M is the number of inputs switched from low to high, $C_2 = NC_{cs} + (N - M$



1. **Examples.** Feedthrough in ECL AND gate can incorrectly set flip-flop (top), or in ECL flip-flop can produce erroneous output signals (bottom). These effects arise from internal capacitances in the integrated circuit.



2. **Junction capacitance.** Capacitance in input transistors of ECL OR-NOR gate (top) cause feedthrough when one input is at high level, and holds the NOR output low, as the equivalent circuit (bottom) shows.

+ 1) C_{cb} and N is the total number of inputs. A is the rate of rise of the input ramp in volts per second, B is the duration of the input ramp in seconds, and u(t) is a unit step function. The other terms represent circuit values shown in the diagram.

Solving the equation of a four-input gate (N = 4), three of which are switched (M = 3) from low to high with a 1 volt transition in 5 ns ($A = 2 \times 10^8$ volts/s and $B = 5 \times 10^{-9}$), reveals that the peak spurious output, V_o, occurs at t = 5 ns and has a magnitude of 154 millivolts. In a gate having a greater fan-in, $-M>3$—an even larger spurious signal would be coupled to the output.

The second mode of coupling results from the emitter-base capacitance: the input transistor whose base is held high acts as a common-base amplifier, so that it couples signals through the C_{be} capacitance of the other input transistors to the collector resistor R_{c1}.

To determine the effect of C_{be} coupling, it's assumed usually that the common-base amplifier has zero input impedance and a current gain of 1. If so, the output collector current from the common base stage can be expressed as:

$$I_c = AC_3 [u(t) - u(t - B)]$$

where C_3 is the total base-to-emitter capacitance of the input transistors. This current, I_c, applied to R_{c1} and the associated capacitances, $C_1 + C_2$, at that point causes the following output voltage:

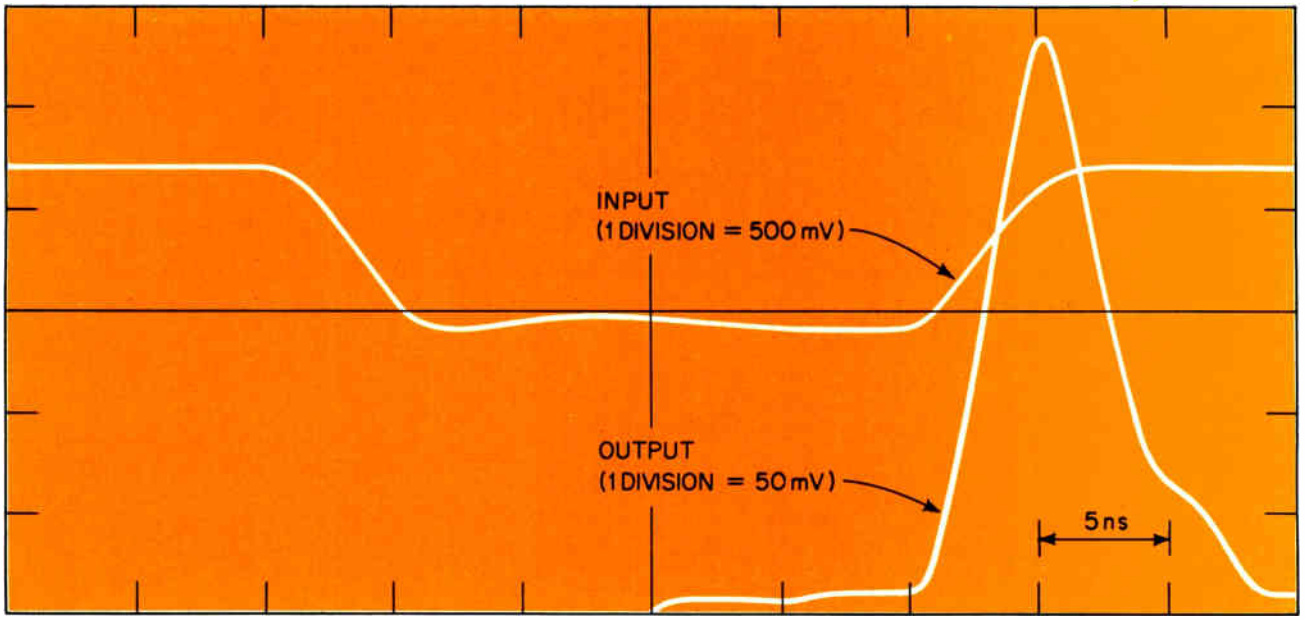
$$V_o = AC_3 R_{c1} \{1 - \exp[-t/R_{c1}(C_1 + C_2)]\} u(t) - AC_3 R_{c1} \{1 - \exp[-(t-B)/R_{c1}(C_1+C_2)] u(t-B)\}$$

Using the same example as before, a gate with four inputs, three of which are switched from low to high with a 1-volt transition in 5 ns, the maximum amplitude of the output pulse due to C_{be} coupling is also 154 mV.

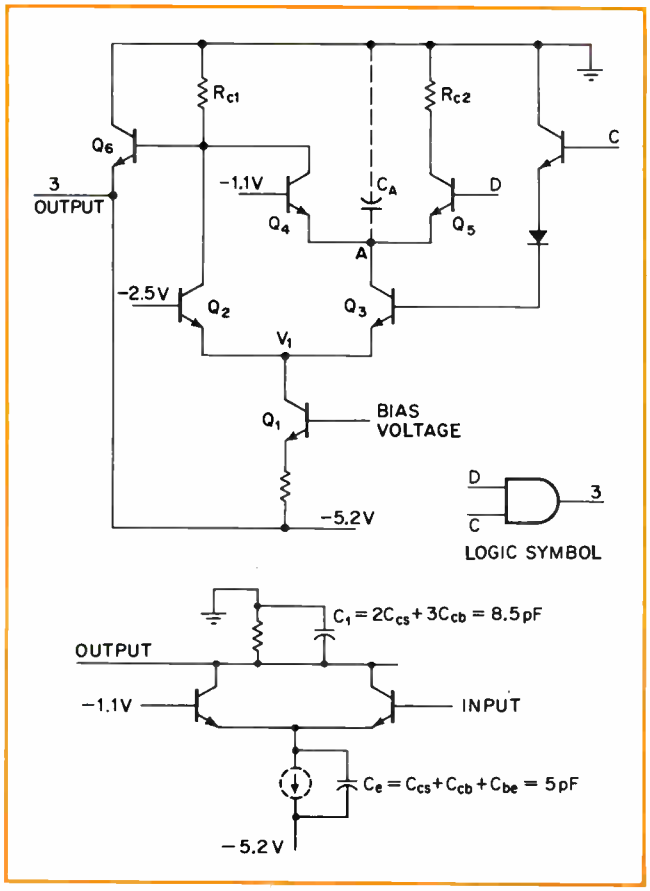
With this C_{be} coupling added to the C_{cb} coupling, the total peak V_o would be 308 mV—considerably larger than the worst-case noise margin of 200 mV usually specified for ECL circuits. The oscilloscope waveform in Fig. 3 shows the combined effects of C_{be} and C_{cb} feedthrough on a four-input ECL gate operated under the conditions described in this example. The pulse amplitude is close to the predicted value, and its waveform also resembles the one predicted, except for being rather rounded because both the output emitter follower and the oscilloscope have bandwidth limitations.

The third source of feedthrough is the capacitance that exists from the current source to ground in two-level circuits such as the AND gate shown in Fig. 4. In this circuit, the output is high only when both inputs are high, and therefore steers the current through R_{c2}. Also, Q_1 is used in the circuit instead of just the resistor used in the OR-NOR gate, to develop a constant current. The reason: the percentage change in voltage across the current source is rather high—much greater than for the single level of current steering in an OR-NOR gate.

To understand the effect of the current source-to-ground-capacitance, consider the circuit shown in Fig. 4. If the D input is held low, the output level should remain low despite any changes in the level



3. Excessive. Combined effects of junction capacitance feedthrough may far exceed noise margin of ECL OR-NOR gate. The input signal is applied to each of three inputs.



4. Two level. Major sources of feedthrough in ECL circuits with two (or more) input levels such as this AND gate (top) are capacitance between current source and ground and parasitic capacitance at node labeled A. Equivalent circuit (bottom) is used for analysis of feedthrough caused by current source.

of C. However, as the input C changes from high to low, V_1, the voltage at the collector of Q_1, changes by about half the logic swing. If the internal current source were ideal, there would be no change at the output due to the change in V_1. However, the internal current source, although essentially constant at d-c, is shunted by capacitance that affects the high frequency performance. It acts as an emitter-peaking capacitor for Q_3 and causes a transition on input C to be coupled to the output.

From the simplified equivalent circuit also shown in Fig. 4, the output voltage of the AND gate for a negative-going input ramp is:

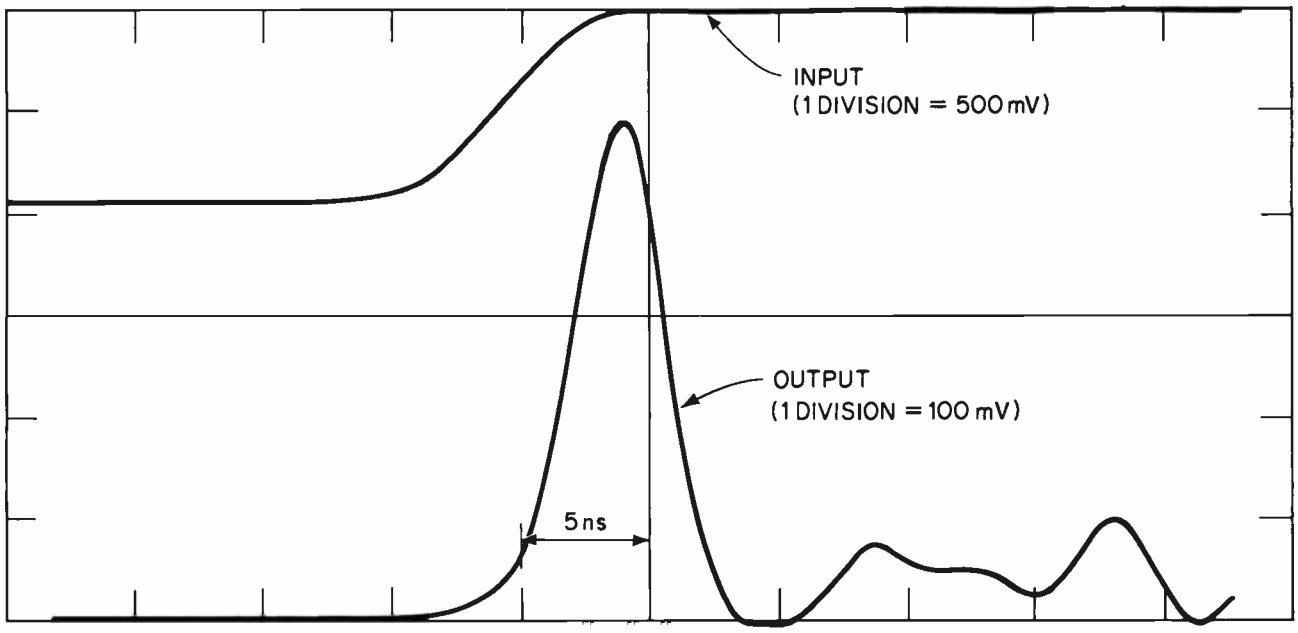
$$V_o = AC_e R_{c1} \{1 - \exp[-(t - B)/R_{c1} C_1]\} u(t - B) - AC_e R_{c1} \{1 - \exp[-(t - B/2)/R_{c1} C_1]\} u(t - B/2)$$

For the values used in the previous examples, the calculated peak output pulse amplitude is about 243 mV—a large output transient to result from switching only one input.

Feedthrough due to capacitance shunting the current source differs from C_{be} and C_{cb} feedthrough in several interesting respects. There is no direct capacitance coupling to the output. The output pulse is in the opposite direction to the input transition.

The fourth feedthrough effect occurs in two-level ECL circuits; this is the discharge of stray capacitance C_A from the node labeled A in Fig. 4. If both inputs to the AND gate are low, then transistors Q_3, Q_4, and Q_5 are all OFF and conduct only leakage currents. The capacitance from node A to ground will be charged to a voltage determined by these leakage currents.

This charge at node A can produce feedthrough in the following manner: if input C of the AND gate (Fig. 4) is changed from low to high, transistor Q_3 will begin conducting instead of Q_2—but transistor Q_4



5. Worst case. In this particularly bad instance of C_A feedthrough, upper trace represents input at C terminal of AND gate and lower trace represents the output.

cannot conduct until the capacitance from node A to ground, C_A, is discharged to −1.8 volts. The current for the current source will therefore be supplied meanwhile by C_A instead of by R_{c1}, and a positive-going pulse will appear at the output.

The amplitude of this C_A-generated pulse depends on the relative values of the leakage currents of the transistors, and can vary widely from circuit to circuit. In fact, variations of two to one in output pulse amplitude have been observed in circuits within the same package.

To estimate the voltage output caused by C_A, consider the case when both gate inputs are initially low and the C input is then switched from low to high. Q_4 will not conduct until C_A is discharged, and the time required for this to occur is $t_o = (C_A \Delta V)/I_E$ seconds where ΔV is the voltage, −1.8 volt, to which C_A is charged and I_E is the current-source value. With assumed values of 5 pF for C_A, 400 mV for ΔV, and 3.33 mA for I_E, the time for Q_4 to turn ON is $t_o = 0.6$ ns.

The output pulse, from the equivalent circuit, is

$$V_o = I_E R_{c1} [1 - \exp(-t/R_{c1} C_1)] u(t) - I_E R_{c1} \{1 - \exp[-(t - t_o)/R_{c1} C_1]\} u(t - t_o)$$

This gives a peak amplitude for V_o of −330mV.

A more complete analysis—one taking into account the switching time of the transistors—would indicate a slightly smaller peak amplitude (although the area under the pulse would remain the same). Nevertheless, it's still possible that in some cases C_A would be charged to more than 400 mV and produce an even larger output pulse on discharge.

The oscilloscope trace in Fig. 5 shows an extremely bad case of C_A-induced feedthrough, with the spurious output peaking at almost 500 mV. In the small number of gates investigated at the Computer Systems Laboratory at Washington University, some 3% of the approximately 200 units tested showed this large an output. Almost all other gates exhibited an output about half that large. (The investigation was supported by the Advanced Research Projects Agency and the National Institutes of Health.)

Unlike the capacitative coupling caused by C_{bc} and C_{cb} and the emitter-peaking feedthrough caused by capacitance shunting the current source, output pulse amplitude and shape that are not dependent on the rise time or amplitude of the input signal. (There is actually a small dependence on the input transition time, but this is not significant with typical signals.) This is because of the stage of gain between the input signal and node A.

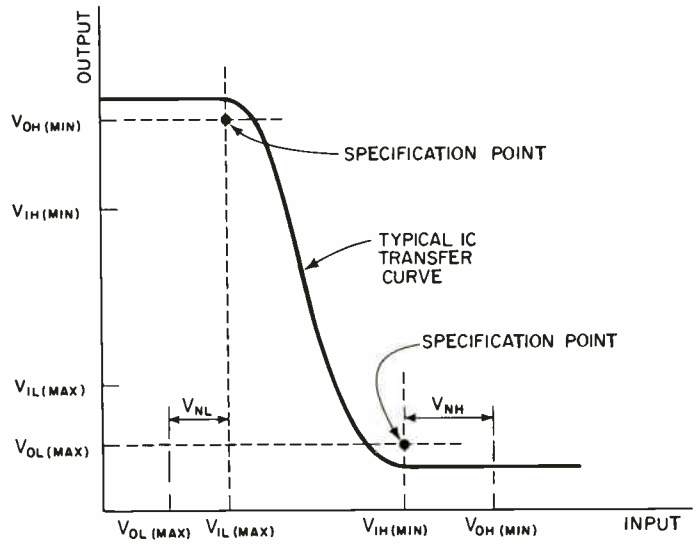
There are two other distinct differences between C_A feedthrough and the other types. First, C_A only occurs when the input transition is positive, whereas the other forms of feedthrough occur with either positive or negative transitions. Second, the peak output pulse amplitude may be much larger for C_A feedthrough than for any other with a single-switched input. Only changes in the C input affect the gate in this example. If the C input is held low and the D input changed, there is no feedthrough to the output due to C_{cb} coupling, C_{bc} coupling, emitter peaking, or C_A discharge because there is no current through R_{c1} and because C_{bc} and C_{cb} of Q_5 do not couple to the output.

Experiments in which a variable voltage source was attached through a 100 kilohm resistor to the ECL gate at node A confirmed that discharge of stray capacitance C_A was indeed the culprit in causing the large feedthrough signals with positive transitions at the C input. In these experiments, the charge at A could be varied, and it was found that the magnitude of the feedthrough decreased with the charge. The

Allowance for noise

ECL Logic ICs are usually specified with a dc noise margin V_N, which indicates the maximum voltage that can be added between two gates and still insure a valid logic output from the second gate. The way to test a circuit's V_N is to apply $V_{IH(min)}$ and $V_{IL(max)}$, as shown at right, and check that the outputs are, respectively, greater than $V_{OL(max)}$ and less than $V_{OH(min)}$.

The ac noise margin may be specified as a plot of input pulse widths versus the amplitudes required to cause the output to exceed the worst-case level. In general, the ac noise margin approaches the dc noise margin asymptotically as the pulse width increases. Ac noise margin is usually not specified, since it is much more difficult to measure than dc noise margin.



experiments also indicated a cure for the problem: if an extra transistor or diode with sufficient leakage were designed into the IC chip, connecting node A to the —5.2 volt supply, C_A discharge would be eliminated as a cause of feedthrough.

There is yet another kind of feedthrough, which, fortunately, is insignificant when compared to the other forms. Some coupling between circuits on the same chip results from the use of a common bias voltage source or sources, common ground and power leads, and capacitance between circuit elements on the chip. The worst case occurred when three sections of a quad exclusive-OR gate were switched while the output of the fourth section was monitored. The signal induced in the output had a peak amplitude of about 75 mV, well within the noise tolerance of ECL circuitry.

Having identified the sources of feedthrough and their magnitude, how can the ECL circuit user minimize or eliminate them? The things that can be done depend on the type of circuit and its relation to other circuits in the system.

Thus, although OR-NOR logic gates with a single level of current steering are affected by C_{cb} and C_{be} coupling, feedthrough is troublesome only when the NOR input is low—when it is high, a change on any input will change the output. Moreover, negative transitions on the input do not cause problems since they can only make the output more negative. Therefore, the only harmful condition is when the NOR output is low and some inputs change from low to high. The options available to the user of an ECL OR-NOR gate family are to reduce the number of inputs that can change simultaneously or to use a noninverting gate output.

As an example of what he can do to minimize the effects of C_{cb} and C_{be}, consider a three-bit synchronous counter that is decoded by four input gates, where the fourth input is used to hold the decoder output off while the input values are changing. If all the bits of the counter change from low to high simultaneously, the pulse coupled to the NOR output may be large enough to propagate through the succeeding stages. This large a pulse can be avoided by using a three-input gate for decoding followed by a two-input gate for control, or by using the OR output of the four-input gate.

Unlike the OR-NOR gate, circuits with two or more levels of current steering (such as the two-input AND gate) suffer primarily from current-source capacitance and parasitic capacitance C_A. Feedthrough due to current-source capacitance can be controlled by slowing the input transitions, at the expense of circuit speed; but this is of little help for C_A feedthrough, since C_A discharge is not particularly sensitive to input transition time. If the C input of the AND gate is held low, changes on the D input aren't coupled to the output, so that the gate can be used if feedthrough from the C input is not a problem.

Many other ECL circuits in addition to the AND gate—flip-flops, decoders, full adders, multiplexers, for example—may use two or three levels of current steering and the same comments apply to them. In many such circuits feedthrough due to current source capacitance and C_A will exist, and careful anaysis is necessary to assure that it doesn't exceed tolerable levels or does not occur where it may cause malfunctions. Also, if a two-level gate has multiple inputs, C_{cb} and C_{be} may present a problem. In all cases of emitter-peaking or C_A feedthrough, feedthrough is a matter of concern only when the output is low: feedthrough increases the output voltage at a low level by decreasing the effective value of the current source but does not affect a high level, since there is no current through the collector resistor when the output is high. □

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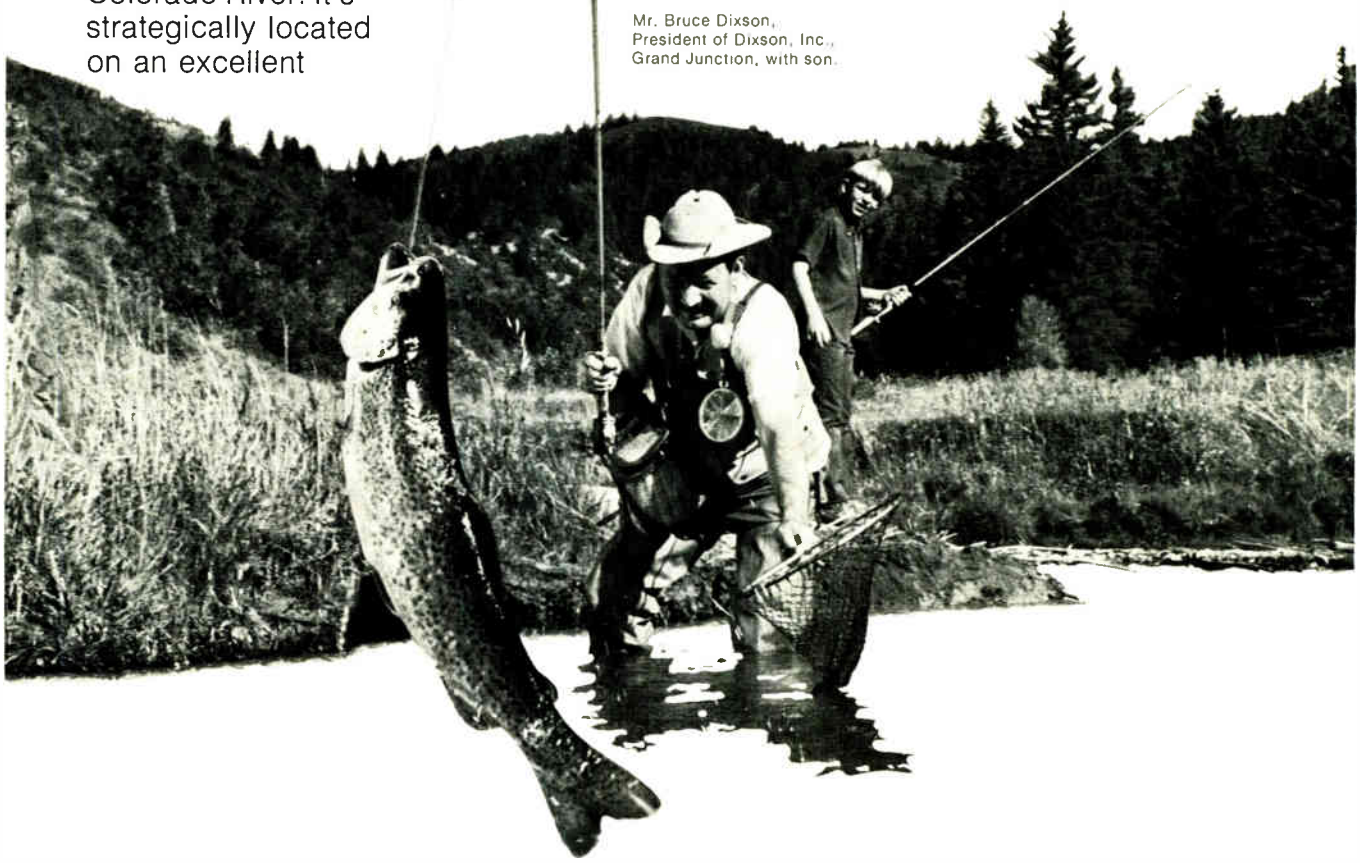

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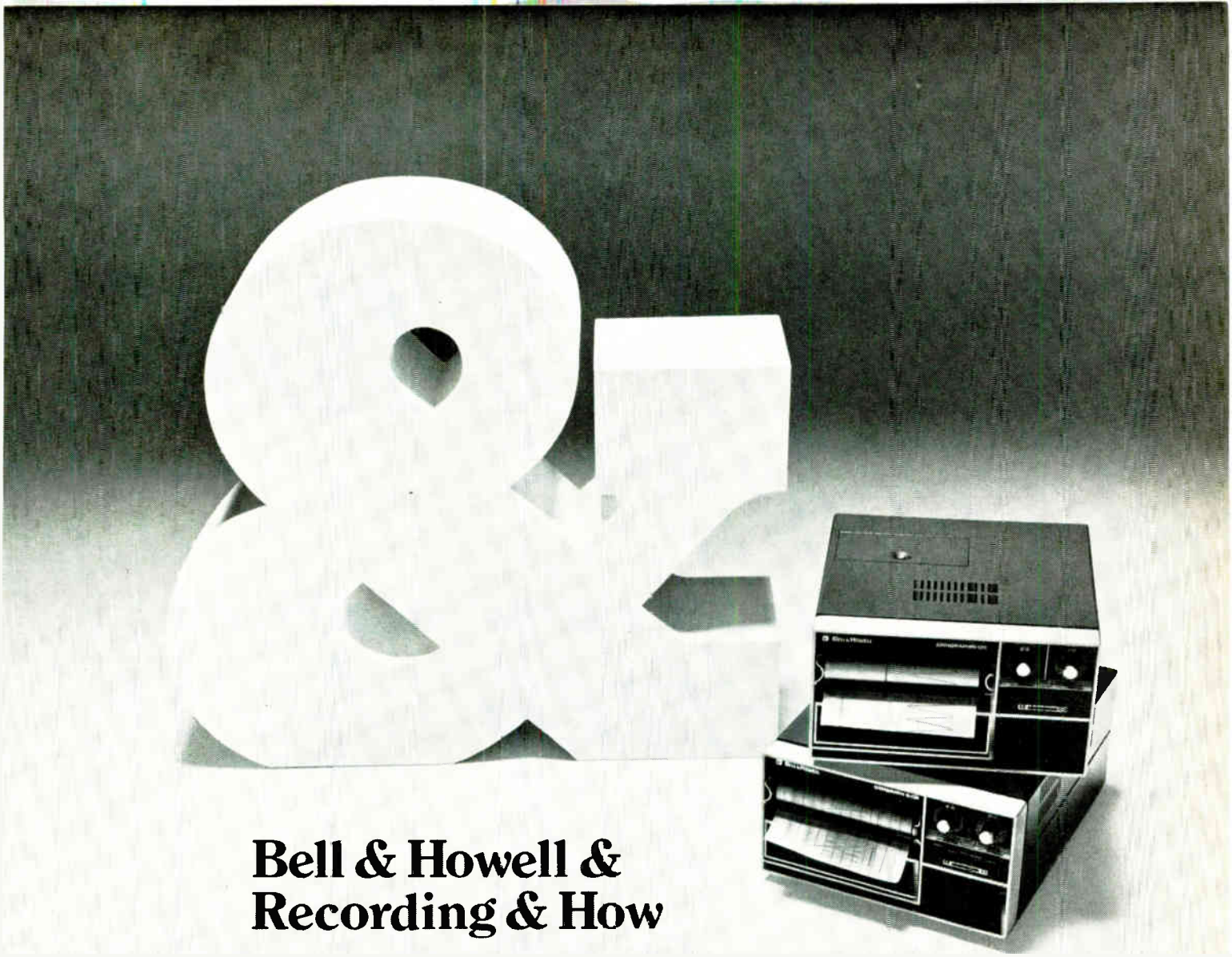

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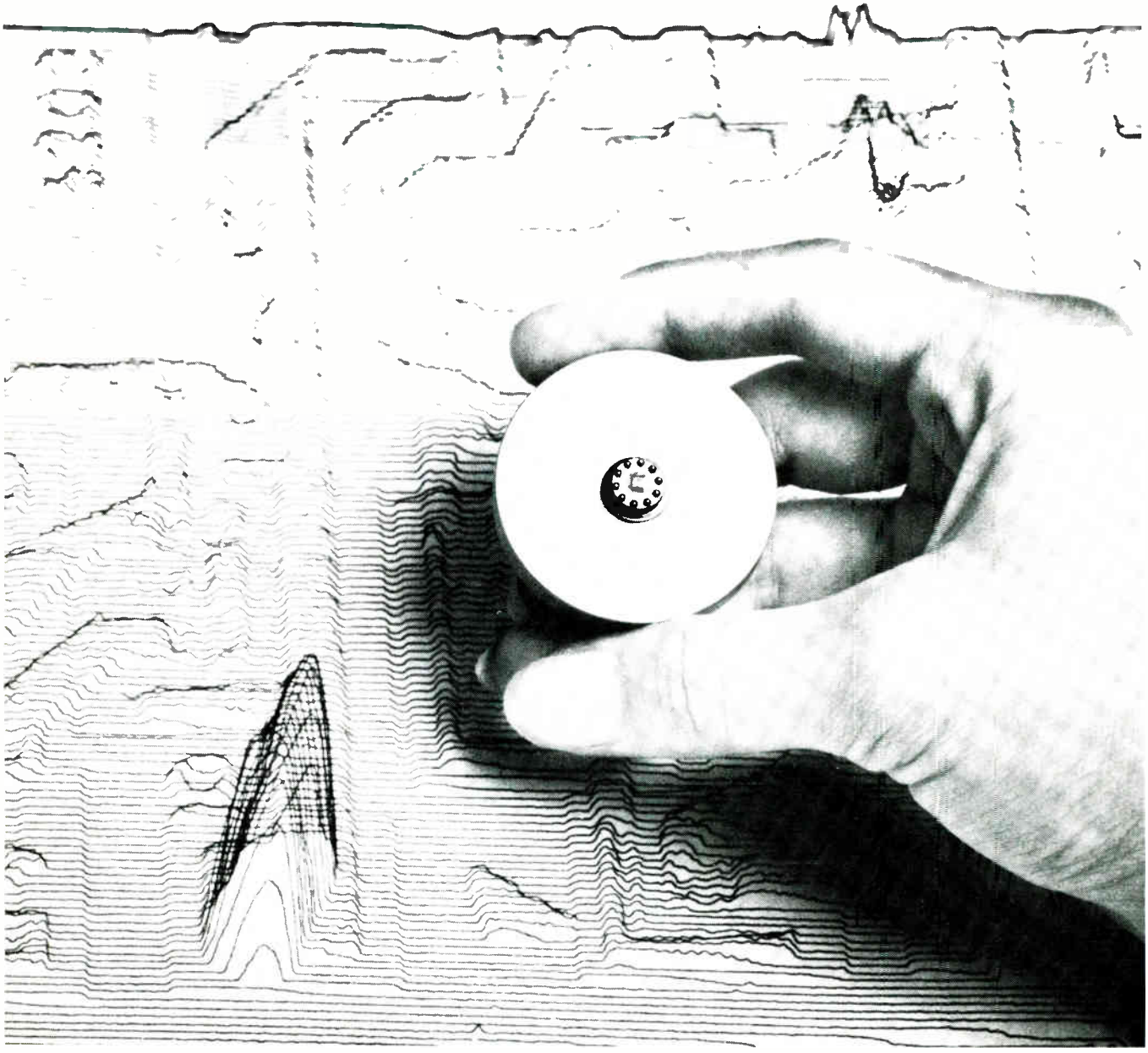

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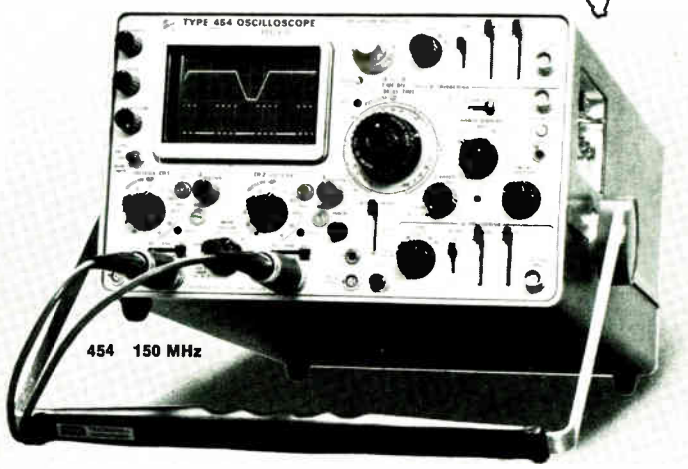

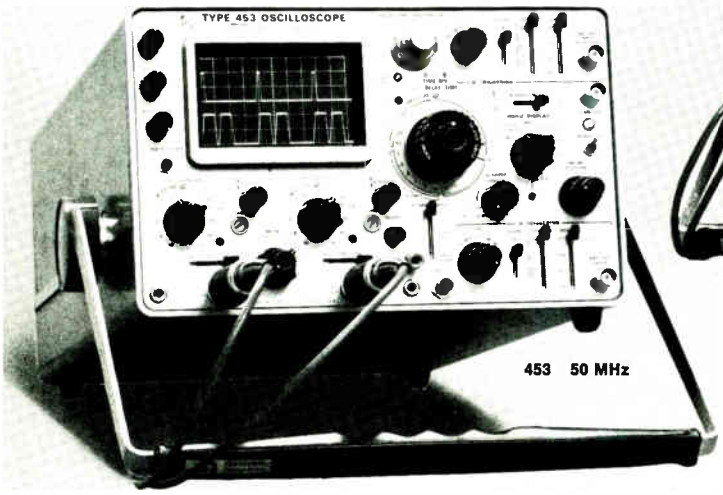

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DOD computer waste: $500 million

Special study finds inefficiency costs the Pentagon one-quarter of its annual EDP outlay; urges strong controls under new assistant secretary

By Ray Connolly, *Washington bureau manager*

Short on technical capability and qualified personnel, the Defense Department spends "at least $500 million per year more for computers and automatic data processing support than is necessary," wasting about a quarter of its $2 billion annual outlay in this area. What's more, some 36% of the military's 2,800 unclassified machines "are considered obsolete." This assessment of the low state of DOD's computer art appears in an unreleased 55-page report prepared for Defense Secretary Melvin Laird's Blue Ribbon Defense Panel [*Electronics*, Aug. 17, p. 109]. The special study—one of 14 appendices to the massive overview of total Defense Department operations made public this summer—was written by John P. Malbrain, director of advanced systems at North American Rockwell, and David B. Breedon, Westinghouse Electric Corp.'s manager of management systems.

To counter escalating inefficiency, the Pentagon should move first to consolidate data processing responsibility under a new assistant secretary of defense for computer systems and services, say Malbrain and Breedon. Then, after an ADP industrial fund is established to pay for all general-purpose hardware, software, maintenance, and related telecommunications, the Pentagon can begin to acquire the expertise needed to pull together and streamline an operation now flawed by "poorly applied, fragmented, underutilized, and obsolete computer equipment."

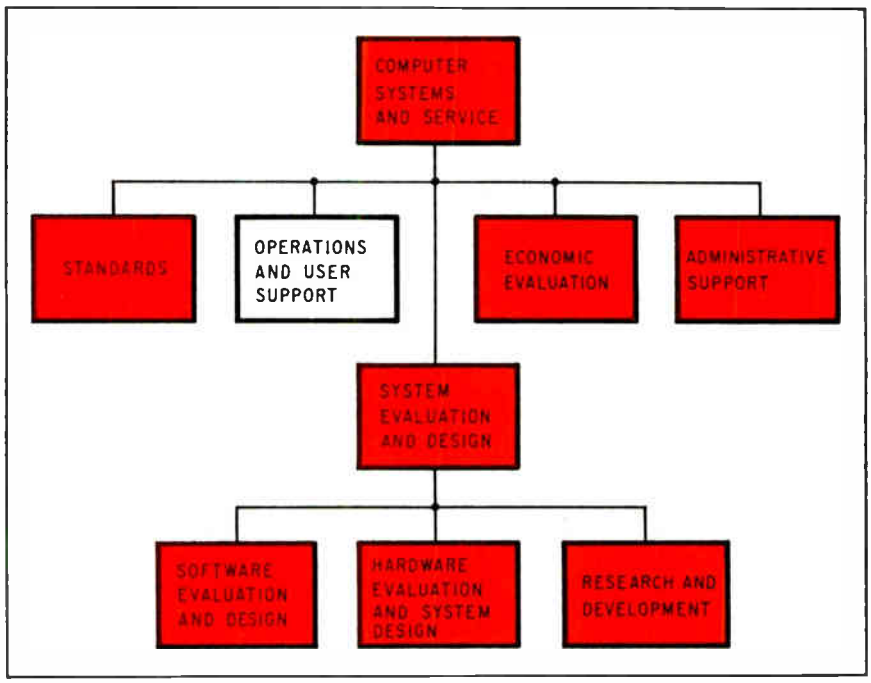
A major recommendation calls for creation of a huge computer systems network in which several major regional centers would serve military users in the same area via telecommunications links. To be located at bases with major workloads, the centers would operate all DOD general-purpose machines in both time-shared and batch modes, billing each user monthly. Still to be resolved is whether the new organization becomes a separate entity or is incorporated into an expanded version of the recently formed telecommunications group headed by Louis de Rosa, an assistant to Laird. Implementation of the network, say Malbrain and Breedon, "might require three to five years."

Malbrain, who estimates he and Breedon devoted "about 90% of our time" for more than four months to completing the report, sees a variety of situations crying for correction. Among the most serious is the lack of any developed and coordinated policy for general-purpose computer acquisition and usage. A related and equally serious weakness, say Malbrain and Breedon, is the fact that DOD "does not have sufficient technical capability to make decisions that are in the best interest of the department."

The study further determines that:

▶ Justification procedures for system purchase and leasing "are too

Consolidation. DOD general purpose computers and services should be under an assistant secretary of defense, says the Blue Ribbon Defense Panel. The key operations and user support group would operate a vast computer network.



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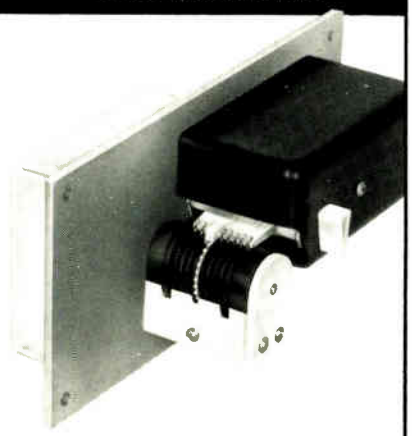

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involved and require too much time and resources." The elapsed time between the first description of a computer requirement and actual installation "varies between a minimum of two years and a maximum of at least six years." What's more, this time lapse "has often resulted in equipment which did not meet requirements when it was installed."

▶ DOD has "no criteria for replacing computers." If a system becomes saturated, it is often augmented by another identical machine. As Malbrain pointed out in an interview, some DOD installations use as many as nine early RCA 301 models together. DOD could have achieved significant economies if it had acquired the later RCA 3301 systems, whose capacity is approximately triple that of the 301s, instead of adding more earlier machines.

▶ Data storage and retrieval is DOD's major software problem. "Millions of dollars have been spent, and many more millions will be required, before a suitable system is developed," the study concludes. In general, DOD's uncoordinated computer operations have severely handicapped its software development program. The department "is one of the few computer users which has sufficient resources to become independent of software supplied by the manufacturer," report Malbrain and Breedon, "but to date it has not made any real effort to achieve this independence."

Rapid advances in electronics technology make a strong Pentagon software capability imperative, the report suggests. In the next decade, says the study, computer electronics costs will fall "by a factor approaching 10." On-line storage capability will grow to "billions of characters by 1980," permitting systems with up to 100 times the capacity of the largest now available. Thus, the authors conclude, "the medium-scale computer which has been the backbone of the department's system will disappear," and cheaper hardware costs create the possibility that DOD "could spend millions of dollars programing a computer which would cost less than $100."

Apart from technological advances, the computer industry will see further unbundling of hardware and software; most 1980 computers will be required to have on-line teleprocessing capabilities.

"By 1980," they report, "each part of the ADP system will be obtained separately. Mainframes, memories, tapes, discs, printers, card equipment, terminals, maintenance, software, etc. will be purchased separately. Large users will require substantial systems staffs to convert these individual items into an integrated operating ADP system."

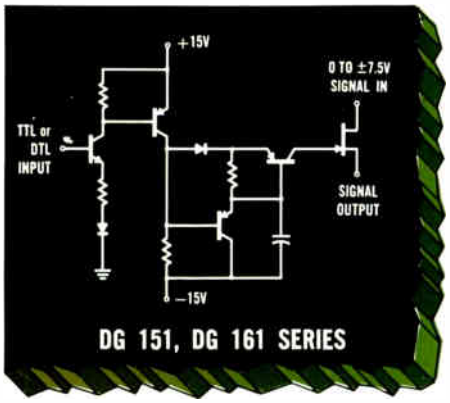
DOD's heavyweights

The Defense Department's computer inventory contains 113 large scale machines—those which generally have the lowest "cost per job," according to the blue ribbon panel's economic analysis. In view of the panel's suggestion that large scale systems will be a major element in rebuilding DOD's capability as "the recognized leader" in effective computer usage, the number and kinds of large computers now in use is worth reviewing.

DOD's large computers are: Control Data 6000 series—7; General Electric 600 series—5; IBM 360/65 and larger—27; IBM 7080/90/94 models—64; Univac 1108—10, for a total of 113.

A massive organization of interconnected centers forming a Computer Service Network for the Defense Department is the major recommendation most likely to affect industry. Using common software—codes, formats and programs—the centers would serve military installations in a particular geographic region via telecommunications links. If a military installation would require its own computer—Malbrain and Breedon believe the centers will be large enough to make such instances rare—the CSN would operate and maintain the system, billing the users monthly for work performed plus a surcharge to cover the dedicated system's cost and overhead.

The Blue Ribbon panel study as-



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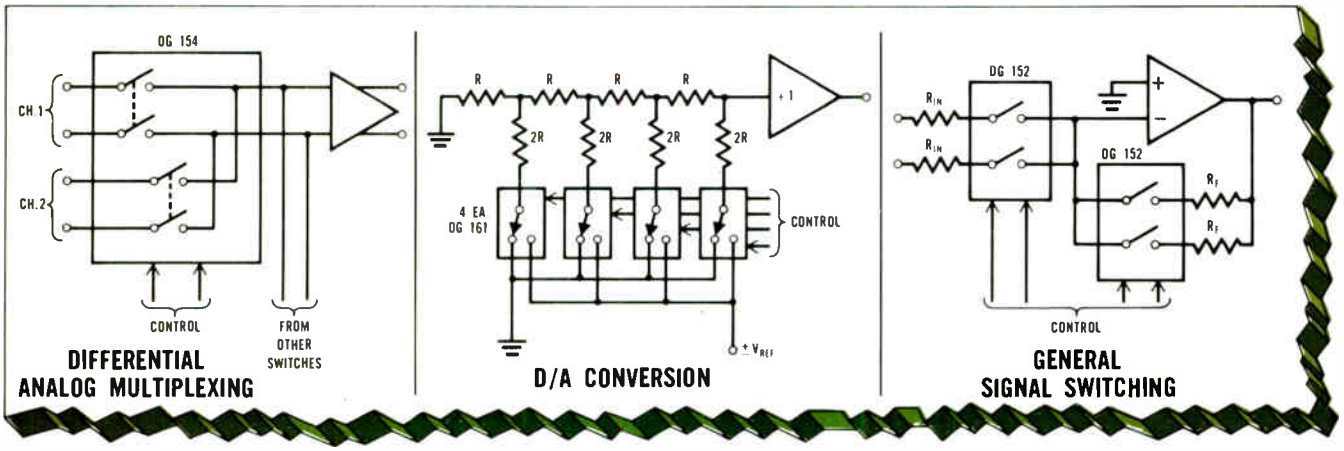

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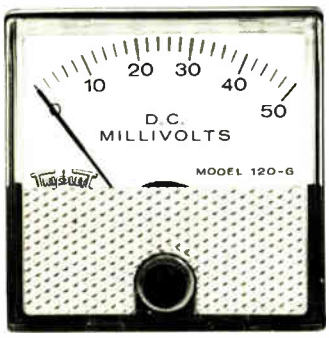

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sumes the centers would be operated "on an around-the-clock, seven-day-week schedule, and the charges for different priorities will even the work load over the week." Implementation would be "on a progressive basis," the study explains, "beginning with an evaluation of the status of computer support in each geographical area." Areas with the most critical need would have first priority. While the centers would be the responsibility of the Operations and User Support group under the new assistant secretary, design and evaluation of systems would go to a separate office and its branches that would deal with hardware, software, and research and development.

The in-house R&D function, says Breedon, would be largely devoted to conceptual studies for systems, leaving the nitty-gritty of new hardware developments, such as memories, compilers and input-output terminals, to commercial contractors. Responsibility for Directorate of Defense Research and Engineering hardware and software development contracts would be shifted to this office, too, Breedon says.

Both men see other distinct dollars-and-cents advantages in the new organization. Consolidation of computer operations should ease professional staff recruitment by offering significant promotion opportunities within the group as well as salary schedules competitive with industry scales. And the ADP industrial fund that buys and leases hardware and software should cut the cycle time between a system's conception and its implementation. Further, the fund would permit "a direct comparison of operating cost with other alternatives, principally commercial service, on a logically similar basis," Malbrain and Breedon point out.

"If another alternative showed lower cost," the study explains, "then that alternative could be used, or management action could be taken to bring international costs into line." Says Breedon, "The fund provides a kind of profit-and-loss statement that will show DOD how it's doing."

Circle 113 on reader service card→

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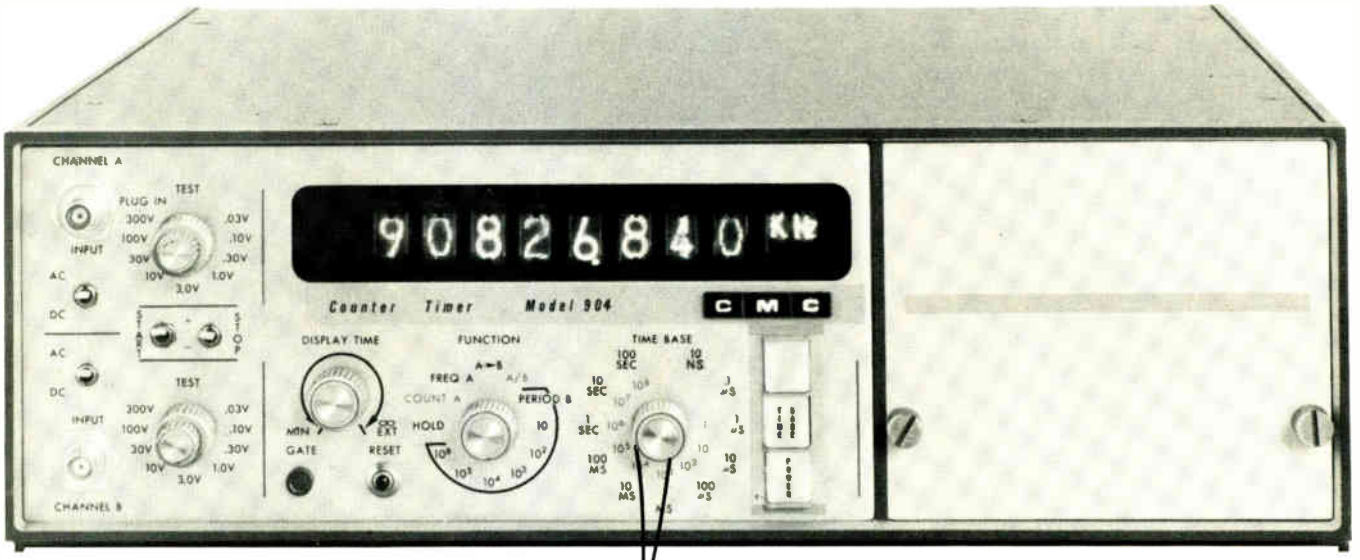

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Computers

Interface pact gains momentum

International group accepts outline of standard interface requirements that could lead to major cost savings in every computer installation

By Wallace B. Riley, *Computers editor*

Computer users will find life much easier if the movement toward an interface standard bears fruit. Now, installation of input-output devices costs more than it should because no interface standard exists. Without it manufacturers can't work with a single logic configuration for generating or receiving signals, programers must write different instruction routines for many types of equipment, and users can't shop as competitively for their computer peripherals.

But there is hope. Last month, a working group of the International Standards Organization meeting in Turin, Italy, took several positive steps toward outlining general requirements for an interface standard. And other groups in the U.S. and abroad are showing strong interest.

In the U.S., James P. Nigro, acting director of the National Bureau of Standards' Center for Computer Sciences and Technology, says he has four logic designers and engineers studying the most widely used computers to determine commonalities in their interfaces. On the basis of their findings, Nigro will decide whether or not to design such an interface. "I might do it," he says cautiously. "That's all I can say now."

If his decision is to go ahead with a standard, and if it becomes official, it would apply to all Government-purchased EDP gear. Since the Government owns about 10% of the nation's computers, this would effectively force an industry-wide standard in the U.S.

There's big money involved. The General Accounting Office reported two years ago that a standard interface would save $100 million or more a year. With it, the Government could buy peripheral equipment from the cheapest source instead of from the mainframe manufacturers.

One type of unofficial U.S. standard already exists. International Business Machines Corp. has used its own standard for interfacing input-output equipment to its System 360 computers since 1964, and used an earlier and simpler version on some of the 360's ancestors. IBM accounts for about 70% of the domestic computer markets, and so, the many smaller companies that build peripheral equipment specifically to replace IBM gear must use its interface if they are to be compatible.

On the other hand, many other U.S. manufacturers use their own interfaces—and they aren't compatible with IBM's. National Cash Register Co., for example, has one that's completely different, and RCA, whose computers are program-compatible with IBM's, has an input-output interface that is electrically incompatible and uses a few signal definitions that are different, too.

None of these companies was

The channel's the choice

All computer installations have interfaces between the processor and its channel, between the channel and the input-output control unit, and between the control unit and the input-output device. Standardizing the interface at the processor level would be difficult and would tie the interface directly to the processor's basic characteristics. Likewise, a standard at the device level would necessarily depend on the nature of the device. So the principal interest is in an interface at the channel level.

An interface independent of device characteristics would allow any device to be plugged into any connector on any computer, and any program requiring data transfer across the interface could be written the same way regardless of the kind of I/O device.

Capability of both multiplex and nonmultiplex operation modes allows the interface to handle both single and multiple devices on an individual cable.

Bus mode, in which many input-output devices are connected on a single cable for either multiplex or nonmultiplex operation, represents the simplest hardware configuration in the processor. However, it imposes severe restraints on device operation—only one at a time can transmit or receive data. Multiplex mode permits some flexibility for slow devices in data transmission, but fast units can't be multiplexed. Another difficulty is that if a device close to the processor fails, it effectively renders useless all the devices beyond it on the cable.

In the star mode, many cables radiate from the central processor like the points of a star, but only one device is connected on a cable. It offers maximum flexibility in simultaneous operation of several devices, but also requires complex processor hardware design.

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represented as such at the Turin meeting, although the U.S. delegation did include some of their employees.

Establishing an interface standard is extremely complicated. The signals must control many functions in many kinds of devices and are themselves subject to strict electrical specifications. And any standard would have a strong impact on existing software.

Reaching agreement on a proposal that must accommodate these complications is a glacially slow process. The Turin meeting's biggest accomplishment was acceptance of an outline of the general interface standard requirements, submitted by the American National Standards Institute. The conference also agreed on a timetable for contributing to or commenting on these general requirements—comments to be received by May 1971, another meeting to discuss them in September 1971, a selection of a specific proposal in meeting in September 1972, and finally, in September 1973, a meeting to accept the proposal, with amendments and corrections, as a full-fledged standard.

Slow as it is, this schedule was described by one of the delegates as "very elastic." Another observer commented, "Considering the amount of work to be done, the agreed schedule seems rather accelerated."

Japanese delegates proposed a standard at the ISO meeting based on work that has been in progress in their country. Far in advance of anything offered anywhere else, the Japanese plan wasn't seriously considered because it is at an advanced level that the official timetable says won't be considered until 1972.

The Japanese proposal, ANSI's requirements, IBM's standard, and most other schemes resemble each other in many respects. Their differences, the major bones of contention, lie in the small but important details. For example, IBM's current standard calls for a set of nine data lines, carrying one eight-bit byte plus a parity bit, from the processor to the input-output device, and nine more coming back. These lines carry all data, address, control, and status information back and forth; additional tag lines carry signals that identify the information on the bus lines at any given time. But some proposals call for separate lines for data and addresses; their proponents argue that the selection sequence for a subsequent operation can be initiated during a previous operation if a separate address bus is available. And IBM's standard calls for a single set of data lines in each direction, whereas one of ANSI's proposed requirements is for a modular path width capable of transferring one, two, four or eight eight-bit bytes at a time, depending on the device it connects to the processor. A two-byte-wide path is available with some of IBM's largest, fastest computer systems.

ANSI also suggests that the interface be at a channel level, instead of a device or processor level; that it not have characteristics unique to any particular control unit or device, and that it support both multiplex and nonmultiplex operation. Other ANSI-suggested interface capabilities include operating only in bus mode, in star mode, or in a hybrid combination of both; detection of transmission errors, and addition of new units.

Like ANSI, the National Bureau of Standards favors a standard channel interface. But its more immediate concern is the establishing of a device interface, at least for some types of equipment. The bureau hopes to set up such a standard for magnetic tape and drum units to be purchased by the government. There are probably more of these units in use than any other single type of equipment.

Another requirement of most interface proposals is pure asynchronous operation. Here neither the processor nor the I/O device must time signals that cross the interface. This asychronous operation is usually defined in terms of "handshaking"—every signal generated either by the processor or by the device is maintained as a dc level until a definite acknowledgement is received from the other end of the cable.

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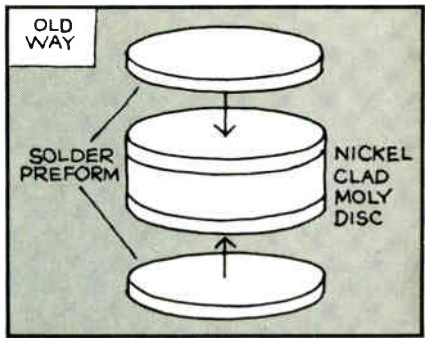

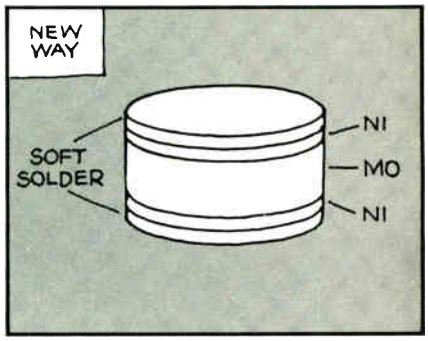

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	RF	HIGH PRESS	2000	—	200
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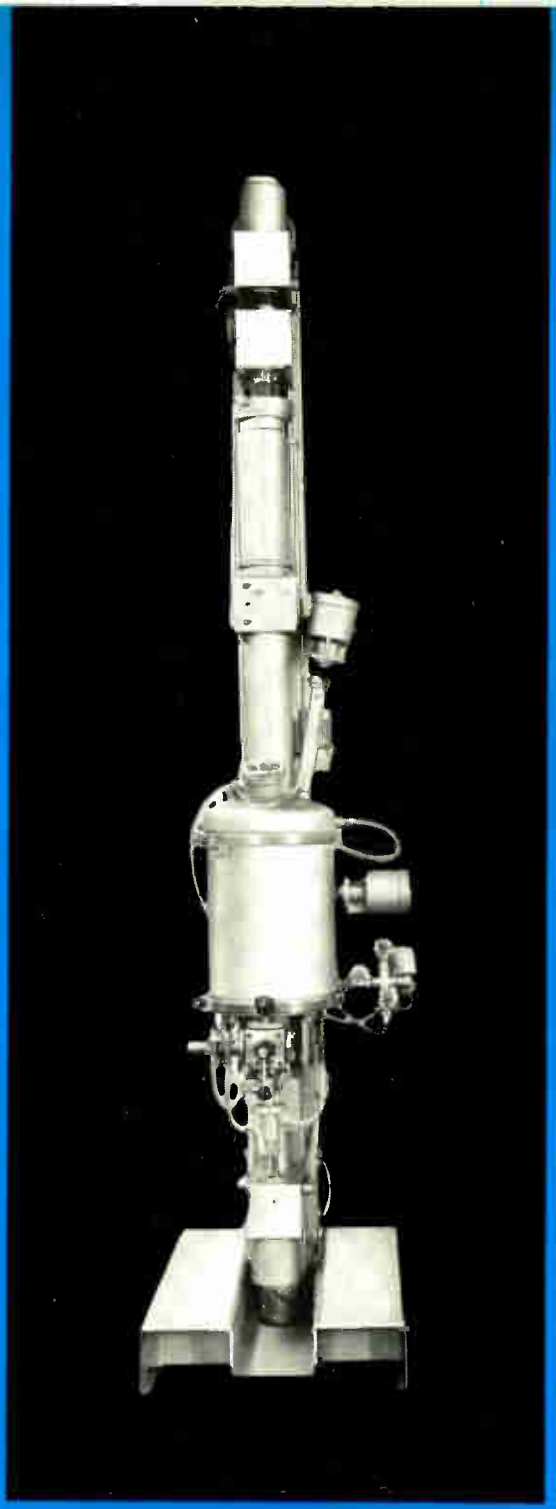

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Solid state

LSI starts to go standard

As peripheral equipment circuitry grows more complex, semiconductor manufacturers are pushing ahead with more and more catalog LSI items

By Lawrence Curran, *Los Angeles bureau manager*

Standardization is catching on in bipolar large-scale integrated logic functions, despite some earlier assertions that LSI would always be a custom market. The push comes from the manufacturers of peripheral equipment and instruments, who find LSI the most economical way of building their increasingly complex circuitry and whose LSI needs are similar enough to hold out hope of volume production.

At Motorola's Semiconductor Products Division in Phoenix, Roger Helmick, who's in charge of product plans and strategies in computer device marketing, foresees standard devices taking over 30% of the market in three years' time, as opposed to today's 10%. Fairchild Semiconductor also senses such a trend. And even at the originally skeptical Texas Instruments, the manager of development and technology, Dean Toombs, agrees that "an evolutionary trend . . . is happening now and will continue to happen in the next two to three years."

Motorola, which has established a program for building standard bipolar LSI logic arrays, is also most specific about the changeover. Helmick believes it will take 12 to 18 months before today's standard parts get designed into equipment and reach volume production.

His company has designed three such parts, all based on a 112 gate LSI array on a 133- by 142-mil die, and all of which will be introduced in the next six months. The three are an 8-bit look-ahead/carry adder made with approximately 108 gates, a dual 6-bit shift register with approximately 100 gates, and a 5-stage Johnson counter made with some 100 gates. Each uses three metal layers.

Another Motorola effort, which began as a custom job, will soon lead to standard time-division multiplexers with complexities of about 108 gates and 3 layers of metal, for use in modems and computer time-sharing applications.

At Fairchild Semiconductor, however, there is more emphasis on the evolutionary aspect of the trend. Hank Smith, TTL market planning manager, for instance, maintains, "There won't be a family of LSI devices—our 9300 MSI line will just grow."

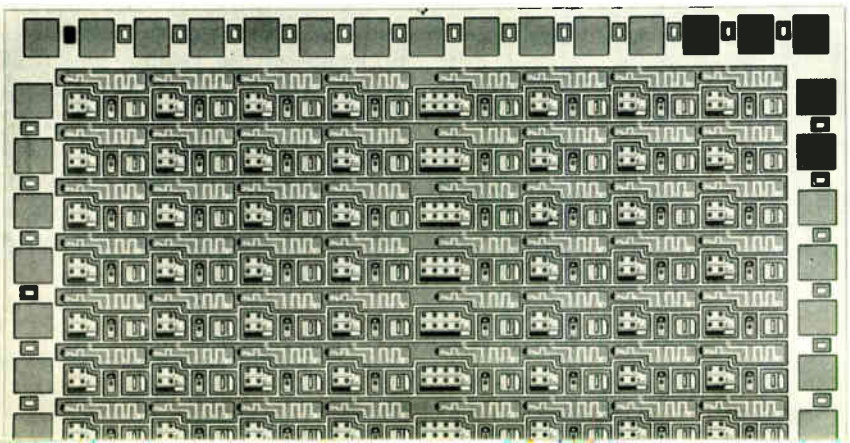
This growth is to occur in two basic areas—in functional blocks, such as adders, multipliers and parity generators, and in special functions that have wide application for a large market in a small area. Possible applications in the last category include tape transports and other computer peripheral equipment, in which special complex circuits offer a definite advantage.

Like Motorola, though, Fairchild is going with three-layer metalization, because such multilayering permits a reduction in chip area. Three parts slated for introduction in the next six months, all with three metal layers, are: a 110-gate, 4-by-2 multiplier; an adder with a shift register/accumulator; and a quad 4-bit register with 85 to 90 gates. The multiplier will probably be introduced first.

TI's original contention that the LSI market was going to be almost exclusively a custom market, Toombs believes, was related to the firm's discretionary wiring technology. "But over the past four years at TI," he says, "there's been an evolution to higher-complexity fixed-pattern standard bipolar parts."

But the company doesn't like to talk about specific standard LSI logic functions coming soon. The TTL random-access memory component that TI introduced in August has the equivalent of 98 gates on a 90- by 110-mil chip; yet Toombs doesn't consider it a "significant" part. Jack C. Carsten, digital circuits marketing manager in Houston, adds there will be five complex memory elements introduced in the near future, and expects TI's catalog parts to remain at about the 100-gate complexity for some time. "While we've identified

LSI parts. Motorola has designed three standard bipolar LSI devices, all based on this 112-gate array, shown here without metalization.



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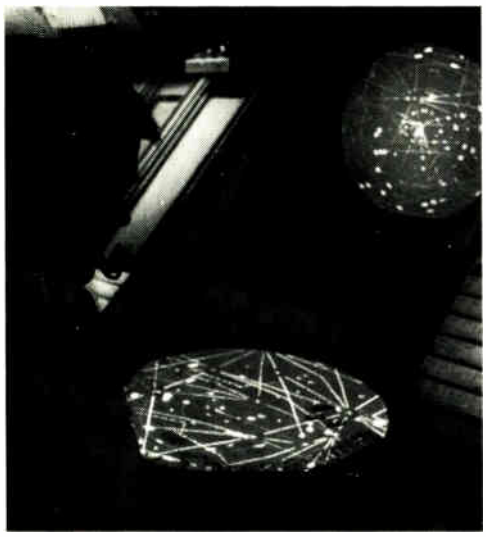

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a few standard parts," Toombs notes, "it's difficult to identify those with high-volume potential when you apply all the constraints, such as chip size and pin count." On chip size, he feels, "it's generally preferable to stay below 100 square mils."

Unlike the other two companies, TI is not counting heavily on multilayer metal in its LSI plans. "We've been able to design standard MSI parts with single-layer metal," Toombs says. "We're not now producing multilayer TTL parts, and TI won't do so until it's needed."

Motorola, in short, is the most heavily committed to the trend. And, in fact, its whole system for manufacturing the 112-gate array offers standard/custom flexibility. Wafers are stored containing only diffused components and no metalization. Motorola can then make a mask set for a customized array or apply a mask set for a catalog part.

"We've tried to make custom parts look like standard parts by the time they reach the factory," Helmick explains. "The operator doesn't know whether they're custom or standard; she just slaps the masks on."

Now that the 112-gate design has been mastered, Motorola is expanding its repertoire by adding 80-, 60-, and 40-gate arrays that can be turned into custom or standard parts from wafers in stock. All of the arrays have been, or are being, designed with computer aids. The 80-gate array will use three metal layers; the 60- and 40-gate units will have two.

In Motorola's three-layer metal scheme for the 112-gate array, first-layer metal is reserved for cell makeup, and for running power and ground between the cell rows. Signal wiring on that first layer may connect only the output of a cell to the input of the next cell in the same row, or the output of the last cell in a row to the adjacent pad. All other signal wiring must be done on the second and third layers, with eight vertical and five horizontal wiring channels available per cell. Some of the third-layer channels are used for power and ground straps that connect to the power and ground busses on first-layer metal.

Military electronics

Radar of the future?

Rassr aims to upgrade radars built in Air Force's MERA program through extensive use of large-scale integrated circuits

By Herman Lowenhar, *Military/aerospace editor*

Enough information has penetrated the veil of Air Force secrecy to suggest that the Reliable Advanced Solid State Phased Array Radar (Rassr) may prove to be the technological pacesetter for all new tactical radars in the 1970s. Scheduled for delivery early in 1972, the multifunction radar is being developed by Texas Instruments under a $7 million contract from Wright-Patterson Air Force Base's avionics laboratory.

Rassr is an upgraded and miniaturized version of the radar developed under the Air Force's Molecular Electronics for Radar Applications (MERA) program. But whereas that radar was just a laboratory demonstration model, Rassr is essentially Mil Spec equipment that could actually operate in a severe airborne environment. Its main differences from MERA are the use of large-scale integrated circuits, which will improve reliability, and the substitution of ridge waveguides for dipoles, which along with LSI will contribute to greater compactness.

The LSI circuits will be used throughout the radar, but especially in the computer that controls operational modes and steers the phased array beam. Yield and reliability are expected to benefit from TI's discretionary wiring techniques. And the Air Force appears confident that a smooth transition can be made from MERA's ICs to the LSI circuits. Though not designed to have the air-to-air acquisition, tracking, and missile guidance modes required in a tactical airborne radar, Rassr could acquire these capabilities through modest hardware and software changes.

The ridge-waveguide radiators can be flush mounted, and their size also allows almost three times as many elements—1,650—as were used in the MERA radar to be packed in an antenna of identical dimensions.

By spacing the antenna elements 0.534 wavelength apart, Rassr keeps the first grating lobe to 70° off bore-sight—well removed from the maximum scan angle of 60°.

The radar is coherent, essential for its synthetic aperture, ground-mapping mode. Frequency hopping is used to break up extended clutter as well as to improve detection of targets that don't have background interference. And pulse compression yields high resolution. While these techniques are intended to meet Air Force requirements, they also are applicable to such Navy objectives as periscope detection.

Sidelobes are controlled by uniformly illuminating each antenna element during transmission and by modified Taylor weighting during reception. Transmitted first sidelobes are 17 decibels down from the main lobe, and received sidelobes are about 30 dB down, for a total two-way level of –47 dB.

Antenna beamwidth, about 3° at a 30° angle off boresight, can be narrowed by 90% through synthetic aperture processing, a technique that achieves the resolution of a large antenna by coherent summation of signals received over many interpulse periods. By doing so, Rassr will be able to resolve a 300-foot patch at a range of 10 miles, a good match to the resolution afforded by the 0.66-microsecond compressed pulse width.

The spread in the doppler-shifted signal returned from such a 300-ft patch at 10 miles and at a typical scan angle of 30° from the line of flight will be about 150 Hertz. Each filter of the receiver's doppler bank, therefore, must have that narrow bandwidth. This, in turn, places stringent requirements on the system's spectral purity and stability—both the local oscillator and transmitter must have very low incidental am and fm components, and spurious signals must be suppressed. The linear frequency sweep—1.5 megahertz for pulse lengths up to 100 μsec—must be essentially free of nonlinearities and must be accurately repeatable.

Schottky diodes are used to down-convert the received X-band signals, first to 500 MHz and then to 60 MHz. Then they are compressed to 0.66 μsec in a surface wave acoustic device. Overall system noise figure, including 0.5-dB stripline losses, is 10 dB.

Since peak output power to each waveguide radiator is 1 watt, a total of 1,650 W is transmitted. Maximum pulse repetition frequency (PRF) is four kilohertz, so that the highest duty factor with a 100-μsec pulse is 0.4, and average transmitted power is 640 W.

To get around the problem of "blind speeds"—at which moving targets can't be detected by a radar with a constant PRF—Rassr's PRF is continually changed. Moving target indicator processing is entirely digital and therefore inherently more stable than analog methods. Rassr will be able to detect moving targets in clutter backgrounds that return signals 50 dB stronger than those of the targets.

Communications

Banks moving on paperless payments

Federal Reserve installs computer gear in wire transfer network; plans for electronic payments gain as checking volume mushrooms

By Jim Hardcastle, *Washington bureau*

With a flood of paper threatening to break down the checking system, the nation's traditionally conservative bankers are under severe pressure to develop a paperless payments system. And once they do so, a huge new market will open up for the electronics industry, which will be called upon to provide the computers, communications gear and expertise needed to make the systems work.

Some of the equipment needed for a "checkless society"—or at least a "less-check" society is already in use. A case in point is the Federal Reserve Systems' 48-year-old Teletype network, which is used on a limited scale to transfer funds between bankers and their big corporate clients. The Fed, the principal Federal regulatory agency for the nation's money and banking system, recently replaced a torn-tape switch for the Teletype system with a state-of-the-art computer-controlled device. And it's already drawing up plans to dramatically upgrade its communications network.

About 60% of the traffic carried by the network, which serves the 12 Fed regional banks and their branches, is represented by "wire transfers" in which banks with cash needs borrow uncommitted reserve funds from other banks and transfer large sums for corporations. The sums transferred are huge, and traffic is growing as more banks use the wire to make sure their money is constantly at work.

As a result, says John Rand, who directs the Fed's Culpeper, Va., switching center, the old Richmond, Va., switch was so overloaded that "perforated tape was backing up on the floor for two hours."

With the banks paying huge interest rates on these very short-term loans, the two-hour queues were negating the reason for transferring funds by wire, so the Fed's Board of Governors decided to replace the switch. And recognizing that the trend was toward more computers and less paper, the board asked Arinc Research Corp. to design a system using the most advanced store and forward switch.

Arinc's choice was four Control Data Corp. M-100 communications processors, put together to perform store-and-forward and point-to-point message communications. Each processor uses its 60,000-byte core as an input/output buffer and maintains its queue on part of its 12-megabit, fast-access disk pack. Queue overflow is directed to a 5.5-million-byte disk pack. Journal tapes of all message traffic are maintained on five magnetic tape drive units.

The system initially will be used to switch traffic between 150-baud M-37 Teletypes being installed at all Federal Reserve member banks. Before the year-end, however, the Culpeper system will be switching 2,400-baud circuits that at first will carry traffic between IBM 2968 magnetic tape terminals and eventually will be used for direct computer-to-computer communications. Even that load won't strain the switch's 9,600-baud capacity, which Rand says can be increased to 50,000 baud with more core and up to four additional processors.

One computer expert turned banker, Chicago Fed senior vice president Bruce Smyth, predicts that as more bandwidth becomes available and each bit becomes cheaper, more and more payments transfers will take place between computers using Fed wire. Currently, he says, "our office people paw over the financial documents before they put them into our own computer systems." Once the traditionally conservative banking industry gains confidence in electronic systems, he says, "all we'll have to do is shake out the people components."

Meanwhile, the 12 Federal Reserve Banks are planning similar networks to serve member commer-

Paper costs

The number of bottlenecks in the nation's payments complex is providing the strongest push toward an electronic payments system. Today's check makes four to eight trips between banks and clearing houses before it gets back to the person who wrote it. Even with much of the work being done by computers aided by magnetic ink and optical character readers, the mounds of paper are growing, and their handling costs are increasing—an estimated $4 billion a year, averaging 13.5¢ a check.

Such costs are unacceptable to bankers, who see annual volume of paper transactions doubling in this decade to 40 billion—a volume that some predict will cause the system to collapse of its own weight.

cial banks in each region. The New York Fed, for example, plans to use a Xerox Data System Sigma 5 computer to switch all intraregion traffic and to act as the local switching center for all exterior traffic as well. The computer also will strip out the amounts of wire transfers sent by member banks and credit or debit their Fed accounts, thus taking the first step toward a completely paperless payments system. All other regional Federal Reserve banks have similar plans in various stages.

One such paperless service—preauthorized crediting and debiting of checking accounts—is the subject of an experiment scheduled to begin next year in San Francisco. The service will permit corporate customers to present magnetic payroll tapes to their bank, which will scan the tapes and transfer funds by computer from corporate accounts to the employee accounts it holds. It then will distribute credits to employees' accounts at other banks.

Smyth says the system eventually will be extended to automatically debit regular payments, such as rent, taxes, and utility bills, from the customer's account. This will spare banks much of the expense of clearing routine checks, he says. In return, the customer will avoid the inconvenience and expense of writing checks and making deposits.

At first, payments will be transferred between banks by exchanging tapes. "But as the thing grows," says San Francisco Fed vice president Edward Martens, "we'll probably send the data over the national telephone network or the Federal Reserve wire."

If the San Francisco experiment is successful, most of the components needed to build a nationwide system for distributing preauthorized credits and debits will be available. Virtually every big bank is equipped with sophisticated data processing gear. With automated terminal gear, computers could use the local Fed networks and the national network to transfer funds without any paper steps, thus creating an electronics payments system that could later be expanded for other uses.

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Detroit's Mini Cars Challenge the Imports

The first of a new breed of cars is starting to show up on the road. These new autos have the grille work and long hood associated with cars made in Detroit but the size and style of foreign cars. They are Detroit's answer to imported car sales which have risen so high that one of every seven cars sold in the U.S. is now imported. But while the new cars offer the economy in price and ease of maintenance of the foreign cars, their luxury and power is strictly American.

If you're in the market for a new car, the new minis merit a good looking over. American Motors started the mini wave last April with its impish wedge-shaped Gremlin, a reduced version of the compact Hornet.

Ford and Chevrolet joined the small car rush last month when their new minis, the Pinto and Vega 2300, went on sale around the country. You'll have to wait for Chrysler Corp.'s small cars until January, 1972. In the meantime, Dodge dealers will sell the small Mitsubishi Colt from Japan, and Plymouth dealers will offer the British Avenger under the name The Cricket, starting next January.

There is a plethora of new models to look at. Ford offers one four passenger sedan to start—company insiders say to look for more in six months; and American Motors has two and four passenger models. Chevy, however, has a four passenger sedan, a sporty coupe with a hatch back rear door that opens the full height of the car, and a station wagon with square-back lines reminiscent of the Volkswagen. A one-seater panel van is also in the Vega line— a model Chevy people think will sell well as a utility vehicle or delivery truck for small businesses like pizzerias.

Under the hood, Detroit has it all over the imports. The Vega, for example, sports a totally new, four cylinder aluminum engine in two horsepower sizes—90 and 110. The Ford Pinto offers a choice of two four cylinder engines. The standard is an 85 horsepower engine used successfully here and abroad in the British-made Ford Cortina. The other is a brand new engine built in Germany; it has a 95 horsepower rating. It will also be used in the new German Ford, the "T.C." soon. American Motors' Gremlin is powered by a much heavier six cylinder engine, with 135 horsepower. An optional engine has 150 horsepower

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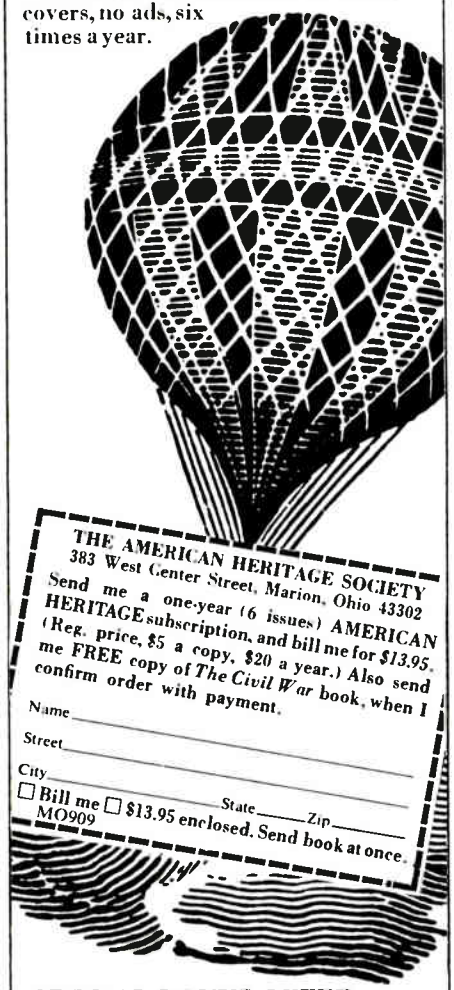

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output. The Volkswagen Beetle, by comparison, has 60 horsepower.

For you this means faster acceleration and generally a top speed of 10-20 miles above most imports. Still you can expect an import-like 25 miles per gallon with the Vega and Pinto, a little less with the Gremlin.

Detroit is cutting costs to sell its small cars at a favorable price. Chevy hopes to crank out 100 Vegas an hour at Lordstown, Ohio, on a highly automated assembly line using mechanical robots to do the welding. Travel damage will be cut down because the Vegas will be shipped to the West Coast on their noses in enclosed railroad cars. Ford will build Pintos (picture) with Mustangs and Mavericks at the usual assembly line rate of 55-60 an hour at three sites across the country. Ford will use some robots of its own.

Don't be misled by the advertised base price for the small car. By the time you add dealer preparation charges and taxes, even the VW Beetle is usually over $2,000. And optional equipment such as bigger engines, larger tires, plush interiors, air conditioning, radios and other appliances—even cigarette lighters are optional on the Vega and Pinto—jacks up the cost measurably. The average Gremlin with a base price of $1,879 for the two passenger model and $1,959 for the four passenger version actually was selling for $2,500 this summer.

If you are handy with tools, the new minis may be even less expensive. Both Pinto and Vega come with simple manuals that describe how to make minor repairs such as headlight and grille replacements, or major adjustments to carburetors and brakes. The cars are designed to make maintenance easy.

Warning: Read your warranty carefully to be sure you do not jeopardize your coverage with homemade repairs.

In the Vega and Pinto you'll be able to get three or four speed manual transmissions as well as typically American automatic transmissions. All the shift levers are on the floor—where they are in European cars. Plastic is used heavily but effectively in the interiors of both cars for door panels and dashboards. And the Vega sports a one piece injection molded plastic roof lining that has fine acoustical qualities. The back seats offer a little more room than most imports but small car rear seats are never very big anyway.

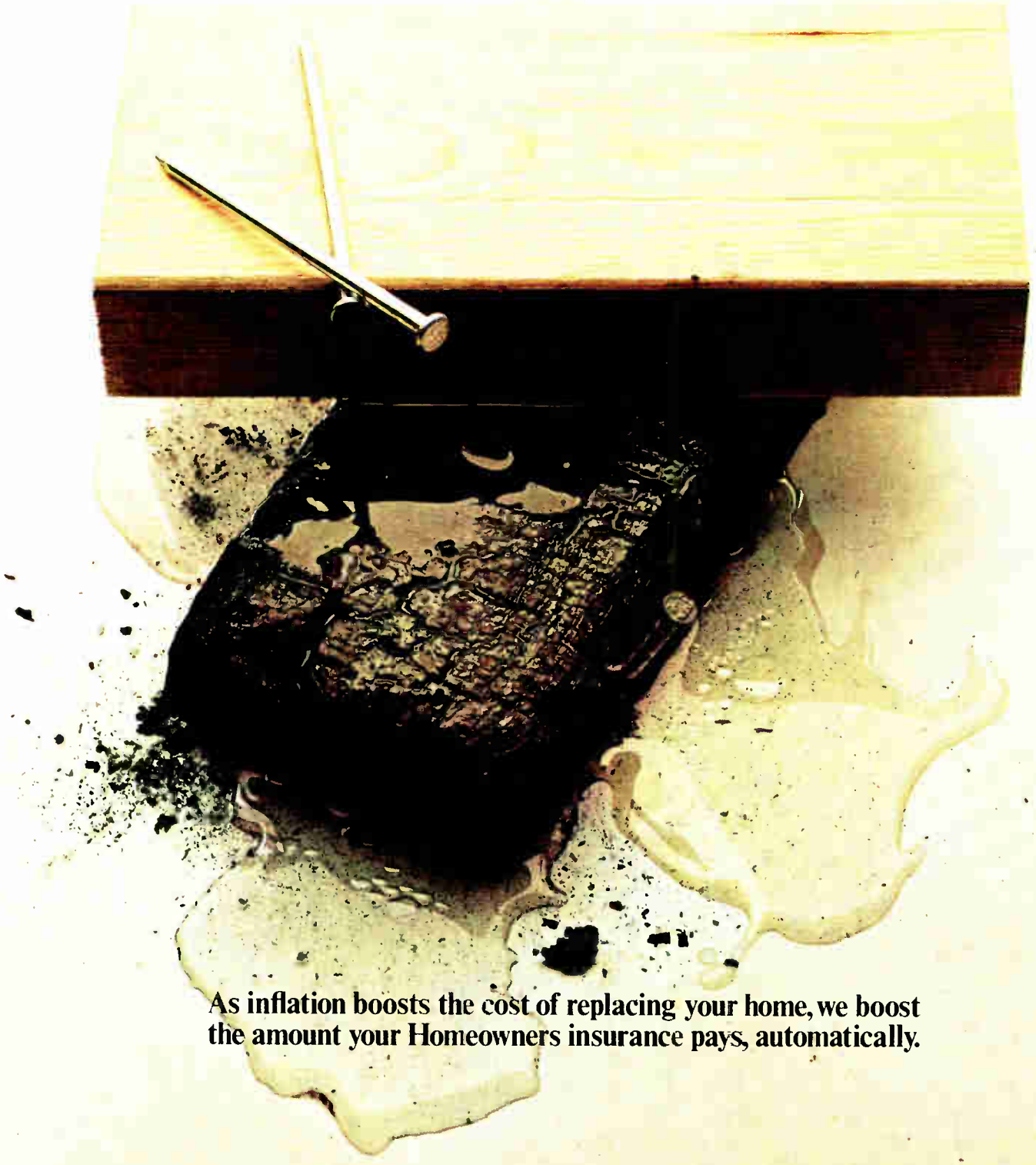
Your best bet before buying is to take a spin in the new Detroit minis and compare them to the foreign makes.

Some domestic car dealers are still importing foreign makes from the overseas subsidiaries of Detroit auto makers: The Opel at Buick dealers, the Capri at Lincoln-Mercury dealers, French Simca and English Sunbeam cars at Chrysler Corp. dealers. Ford division dealers, who have added the Pinto to their stable, have discontinued selling the English-built Cortina.

The battle between domestic small cars and foreign imports will be fierce. Detroit's sheer marketing muscle and vast dealer outlets are sure to harass foreign car makers such as Volkswagen, Toyota and Datsun, Fiat, Renault and British Leyland.

Half of U.S. car sales by 1980 are likely to be in the compact, import, and mini car category which starts at about 111—inch wheelbase, on down.

You may be the eventual winner in the resulting sales battle. Pricing may get to be a bitter battle. But don't count on big price concessions to start. It's usually well into the model year before car dealers are willing to cut prices when supply outpaces demand.



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HEALTH

What Every Middle-Age Man Should Know

As the fall season gains momentum with its faster tempo plus more and varied demands on your time and energy, how do you feel about the main currents of life—career, family responsibilities, marriage?

Particularly, how do you feel about the role that sex plays?

For a man of say 45 to 55, clear-cut answers to these questions are often difficult. Says a leading New York psychiatrist: "The trouble is, too many men push this type of inventory-taking out of their minds."

The man in this middle age bracket—under pressure in his work and at home—may begin to experience something new: He finds himself tired, listless, too hemmed in by responsibility. He feels run down, and sometimes he notices with alarm that his interest in sex has run down, too. This point he finds dreadfully hard to admit to himself.

He may even put on blinders, and use pure physical fatigue as the excuse for his weaker sex drive. He's kidding himself.

His prime problem is not physical, but emotional. A man in this spot must take stock of more than his muscle tone and waistline. First—and most important—there's the wearing thin under the burden of stepped-up pressure.

Day to day living—and the pressure builds

At his office he's likely geared up more than ever before in his career. And on the home front he's pressured by a mix of problems ranging from his teenager's attitude about smoking pot to his frighteningly thin savings in relation to income. His round of social affairs is even getting to be a bore.

Many experts note a second possible cause of his problem: the male climacteric which, to some degree, happens to every man.

It most often occurs between 45 and 55, they say, and is a biological-psychological process of aging.

The climacteric in some men produces no symptoms at all, but in others, it can produce emotions ranging from feelings of futility to dark depression. It may last a few weeks, or as long as a year or so.

In any case, the danger is that the victim of all these heavy emotional strains and pressures may react quite childishly. Feeling depressed and with a fear of losing libido, he may flirt with real trouble.

In rashness he might decide on a quick divorce and remarriage to a woman who better "understands him." This may fail miserably.

Or an otherwise sensible man may put a deep dent in his career by impulsive, even irrational fits of anger and conflict at his office. Or he may put himself under impossible added burdens. Thus in trying to prove his worth and shore sagging confidence, he may push himself deeper into his own miseries.

Shoring up a man's morale — a battle looms

How does a man win out in this battle of the middle years? If he's smart, he does it largely by learning to understand his own emotions. The first step is some thoughtful self-analysis. If the going gets too rough, the search for understanding needs the aid of a trained person — internist, psychiatrist, or psychologist. A few sessions — or a short series — may well do the job.

The middle-age man should understand that a decline in sexual interest is commonplace — and temporary. But the more worry, the worse it gets. He will snap back on a new but still satisfying level of sex activity, if he gives himself a fair chance.

He must know too, that he's far from alone. His friends in the same age group have similar woes and regrets — even if they won't admit it.

Pitfalls: A man shouldn't be fooled into thinking that drugs or health foods or vitamins will put him where he was sexually at age 40 — they won't. Also, he shouldn't play mathematics with sex. Once a week is about average at age 50.

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FASHION AND SPORTS

Fashions on the Fifty Yard Line

If you haven't checked, here is a fast look at fall and winter football fashions featured in top men's shops:

In sports jackets, the blazer look stays big. Double and single-breasted, the jackets this season have wide lapels, a flared body, and deep center vent in the rear. Gray, navy and brown are the prime colors, with burgundy, plum, and off-white the runners-up. In wool and polyester knit, at the best stores ($80 to $180).

Sports slacks are slightly flared, with western pockets and belt loops; many are without cuffs. Checks, plaids, and geometric patterns are in vogue ($25 to $50). [Note: Wool knits are out for late fall and winter. They're not warm enough, and create static electricity in the cold.]

Stanley Blacker, Inc.

Long-sleeved turtleneck shirts are back again, this year with the turtle a bit higher; in wool ($17), cotton ($11). Sports shirts with wide collars come in patterns and prints; red and green tartan wool plaids ($22). The "layered look" in sweater-shirts is in fashion ($20).

Longer, belted outer coats are being shown for late fall and winter football games. Water-repellent leather is big this year, and more stylish than you might imagine ($125 and up). So are suede and sheepskin coats with pile linings, ¾-length style ($140-$180).

For icy cold sessions in the stadium, you'll see mouton fur coats, sold by such shops as Abercrombie & Fitch ($350).

Viewing the action: You may need new binoculars. If you want to step up to finest quality, Zeiss has a new lifetime model: it's 8" by 20", magnifies eight times, has a field of vision over 100 yds. at stadium distances ($115).

INSURANCE

Safeguarding Your Income

A new disability insurance contract points up a trend that's worth checking out with your agent.

The policy, introduced by Aetna Life & Casualty, pays you up to $1,500 a month in replacement income if you are sick or injured and unable to work. If the disability begins before age 45 and continues the rest of your life, you receive benefits for life. If it begins after 45, benefits run until age 65.

The policy guarantees a total refund of premiums at age 65 if no benefits have been paid in the meantime.

For $1,500 a month in disability income, the premium is roughly $1,150 a year; with a 180-day wait, it comes to $1,050. Note: The age-65 refund provision accounts partly for the high premium.

The trend has been to higher and higher coverage in the field of disability income protection. Now another move in the business makes these policies more attractive to the man under 35.

The innovation is an option in the policy that lets you jump your coverage freely. For example, a man under 35 can now boost his protection from, say, $500 to $1,000 a month via an option clause. The point is that if higher income justifies the added coverage, you can exercise the jump-up option for up to 10 years after you buy the policy.

For income protection, besides Aetna Life & Casualty, check with Continental Assurance, Guardian Life, Monarch Life, Mutual of Omaha, Paul Revere Life, and Provident Life & Accident, among others.

More Coverage for the "High Risk" Man

If you were turned down for life insurance in the past as a health or occupational risk, you may well find that you can get a policy today.

Many top companies have been easing their rules and going in heavily for what the trade calls substandard business. You pay an extra premium, and sometimes it's high. But often it will be just a couple of dollars more per $1,000.

Even a man who has had cancer can get a life insurance policy if he has had no recurrence of the disease in five years or more. The added premium he'll have to pay is about $15 per thousand a year. Thus, a man of 50 will pay about $50 per thousand instead of $35.

If more than 10 years have passed since the cancer illness, the cost will be fairly close to normal.

You can get insurance today even if you have a bad case of high blood pressure, partly because of new drug therapy. A person 35 years old with a severe case will pay about double the standard premium of $18 per thousand; at age 50, the normal $35 premium jumps to $85. But the younger man gets close to the standard premium when his case is moderate and under control, and the 50-year-old pays only $40.

The 50-year-old has to pay $8 to $16 per thousand extra if he has diabetes; $2.50 to $5 if he has a duodenal ulcer.

A coronary poses the hardest case. The extra premium during the first year of recovery can be triple the standard rate. But this drops to less than double at five years, and gets substantially lower as more time passes.

Health insurers are getting more lenient too. You can buy coverage – despite a poor medical history – from many top companies.

The history can include everything from ulcers to emotional disorders. Again you'll usually pay a higher premium. But some companies, such as Mutual of Omaha, will take you on at standard rates, with a waiting period before benefits begin.

Where there's a premium boost, the extra rate can vary a good deal.

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Save a dollar here— a dollar there

On the tax side, medical deduction rules continue to get easier. The Tax Court has now allowed deducting the cost of auto driving prescribed by an M.D. as therapy for an accident victim.... If you use your car to get medical care, Internal Revenue now says you can deduct 5¢ a mile and tolls and parking fees. IRS also has eased its rules on deducting for schooling for handicapped children.... Cash gifts to churches get liberal tax treatment, too. Two cases: Taxpayers lacked records and were limited by IRS to deductions in the $50 to $100 range; but the Tax Court raised these figures to around $500. The taxpayers were steady churchgoers.

Arcane arts of Wall Street: People persist in taking a fast financial bath in commodities; the psychology often is do-or-drown, and the knowledge intended to keep the investor afloat far too shallow. *The Commodity Futures Trading Guide*, by Teweles, Harlow and Stone, is the kind of aid that should be employed; it realistically views these markets (McGraw-Hill, $12.50).

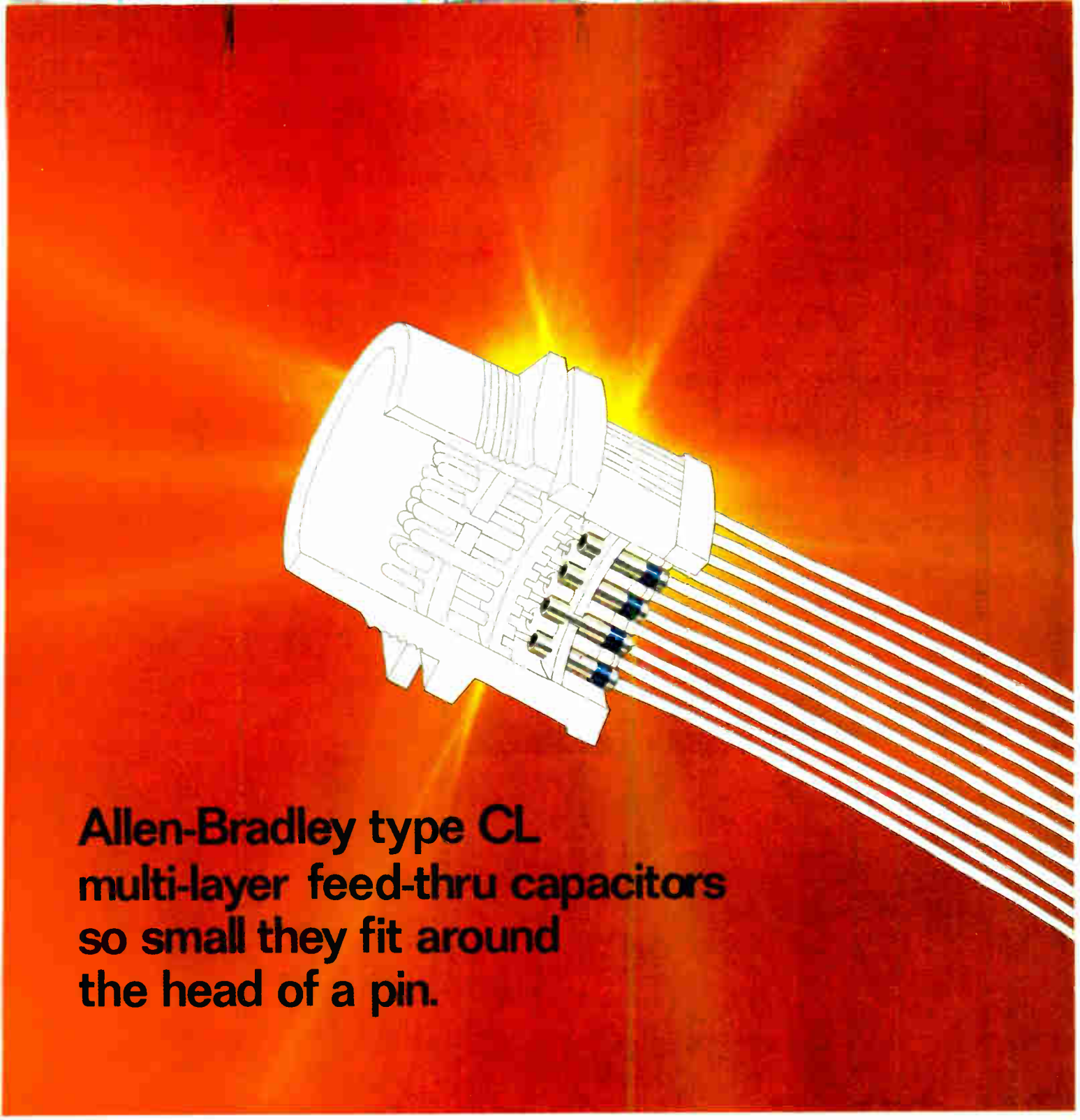
Now credit card loss is eased, with some refinements added to the usual insurance package. You pay $10 a year to the American Credit Card Assn. of Milwaukee and get $5,000 in insurance covering lost or stolen cards. Family members are covered, and the insurer arranges issue of new cards. And you can do temporary charging (ACCA, 2901 West Forest Home Ave., Milwaukee).

Travel and sports department

October classic: Jim Bouton's *Ball Four—My Life and Hard Times Throwing the Knuckle Ball in the Big Leagues* is the best baseball book in years; it kicks up some dust about managers, team owners, and prima donna players. It's funny, human—and right in step with the October series (World, $6.95).... *Medical Advice for the Traveler*, by Kevin M. Cahill, M.D., is sensible stuff if you're off to the Caribbean or other hot climes (Holt, $3.95).

Executive chef

When you feel in the mood for a plate of hot soup, try corn-pea bisque, a personal recipe of Henry J. Heinz II, of H.J. Heinz Co.: Put in a blender 1 pack frozen peas in butter, 1 pack frozen corn in butter, ¾ cup milk, ¾ cup light cream, onion salt. Blend and heat; add ¼ cup dry sherry.... From the fine kitchen at Cantina D'Italia on Connecticut Ave., Washington, try the Roman favorite, spaghetti alla carbonara: Cook 1 lb. spaghetti al dente (8 min.); for sauce, heat in butter and olive oil ½ lb. diced bacon that has been fried and drained, add ½ finely diced onion, and 3 eggs; mix sauce and spaghetti while still piping hot, season with fresh black pepper and sprinkle with parmesan. Serve with a bottle of Frascati (white).



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Reed relay makers swing to dual in-line

By George Weiss, *New Products editor*

Low-profile package increases attractiveness of device for computerized control; price, manufacturing hurdles remain

Industrial equipment designers like the reed relay as a switching link for computer control. It provides better isolation than does solid state logic, it's faster and more reliable than electromechanical switches, and its hermetically sealed contacts provide protection against dust and corrosion.

A group at General Electric Co. decided about a year ago to incorporate the reed device as a dual in-line package in a computer system. They asked Grigsby-Barton Inc. to put the reed relay in a DIP so it would fit integrated-circuit sockets, offer a low profile that contributes to higher component density, and lend itself to rapid insertion and to automatic testing on the printed circuit board.

After making design changes in the reed switch and coil as well as the encapsulation process, Grigsby-Barton produced eight- and 14-pin DIPs. At about the same time, Elec-Trol Inc. also developed a DIP line. Now, a year later, at least a dozen reed relay makers have fallen in line or plan to.

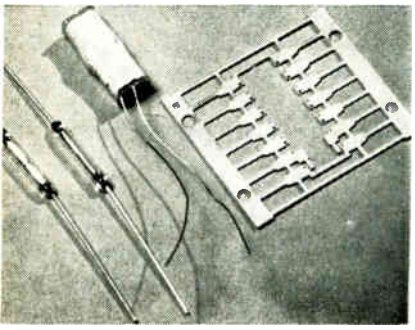
The pioneers are optimistic. At Elec-Trol in Saugus, Calif., vice president Kenneth Doriot says the DIP relays already represent a significant sales volume and notes that acceptance is growing. Forbes Barton, vice president of Grigsby-Barton, says the Arlington Heights, Ill., firm has started a product line of 16-pin DIPs to complement its eight- and 14-pin types. The new line will include the entire range of miniature reeds, including large power ratings and mercury-wetted devices.

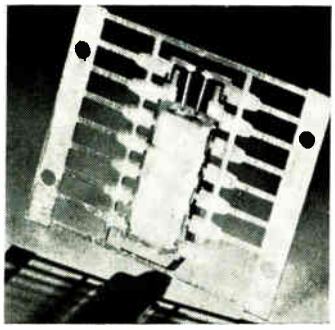
Others, however, aren't so optimistic. Prices of DIP units, they point out, tend to run about double those for standard miniature relays; mechanical difficulties are encountered in squeezing the glass envelope and coil into the low-profile package, and the injection molding process exerts considerable stress on the reed switch's glass envelope, therefore reducing production yields.

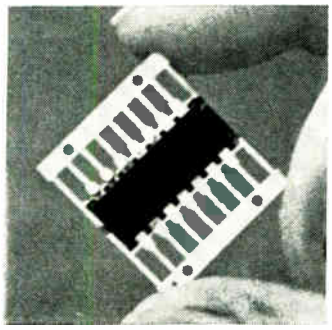
One sales manager admits that his firm's DIP devices will not compare favorably in price-performance ratio with its standard subminiature relays. A marketing director who feels it is too early to determine how significant the trend to DIPs will become, adds that his company cannot afford not to get into the business. Still another major manufacturer cautions the user that the industry "is rife with garage-shop operations."

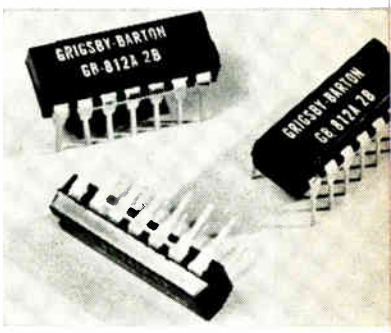
There has been some progress in easing the packaging problem. Two companies, Hamlin Inc. of Lake Mills, Wis., and Gordos Corp. of Bloomfield, N.J., say they have simplified it by reducing the size

Building a relay. Construction of a 14-pin dual in-line reed relay begins with two reed switches, a miniature coil, and a gold-plated lead frame. The switches and coil are welded to the frame, and the assembly is molded. Trim and form operations complete the process.









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of the glass envelope. In earlier devices, the glass was 0.090 inch in diameter and about 0.560 long. The newer switches are less than 0.500 in. long and have a diameter of 0.070 in.

Stuart Wilson, Hamlin's sales manager, finds that the smaller switch size allows the coil to be placed much closer to the reed, for better coupling. And while conventional reed units need from 17 to 50 ampere-turns of coil winding to activate the switch, "the specially designed ones can work with 7 to 22, reducing coil-winding space and power consumption," he notes.

Wilson adds that the smaller size permits faster switching speeds—200 microseconds instead of 500. The faster speed is due to more efficient coupling and to thinner blades.

Angelo Ghio, Gordos' sales manager, does not see any immediate rush to DIP reeds. Manufacturers are ordering the special reeds for DIPs in quantities of about a thousand, against 50,000 or more for other types, he says. But Ghio feels that as semiconductor technology progresses, there will be more demand for standardization and integration of components, especially in computers and terminals. This eventually should make reed DIPs a very popular item.

Some manufacturers are accommodating the tight space problem by slightly enlarging the package. Triridge Corp. of Pittsburgh recently introduced a 14-pin package that fits standard DIP grid spacing but is slightly wider and higher.

Michael Minich, president of Triridge, at first was put off by the standard tight packaging scheme and so decided to compromise the regular package height of 0.187 in. for 0.281 in. off the board to accommodate a more powerful switch. "We would have had to settle for a 3-watt switch available only in a form A (single pole, single throw)," he says. "In our package we can insert a 10-watt Form A or 3-watt Form C (single pole, double throw)."

The greater height, however, makes racking pc cards on ⅜-in. centers barely possible. But Minich believes the sacrifice of card spacing was necessary to accommodate larger power capability—a tradeoff that is paying off because "we're replacing a lot of packages from users who need more than the 3 watts they're now getting."

An arc suppression diode is the only other component that relay manufacturers have been able to add in the DIP package. But now Astro Space Labs in Huntsville, Ala., is incorporating the driver circuitry as well. This reed-relay DIP can be driven directly from TTL or DTL gates. Another model employs the same concept of integration; it also has an internal current-sinking load for TTL/DTL gates.

Bob Mapes, vice president, says he got the idea from Teledyne's integrated TO-5 electromechanical relay and driver, and decided to use Astro's thick film facilities to produce a hybrid drive for reed relays. "We print the resistor, interconnect, and lead configuration on a ceramic substrate and mount the drive transistor and arc supression diode in chip form. The reed relay and coil are then added, the lead frame is attached, and the whole unit is encapsulated by injection molding."

The chief advantage of the integrated package design, says Mapes, is price. "We can sell the relay and driver circuitry together for less than you would ordinarily pay for the driver and relay separately." The units will sell for about $5 in quantities of 1,000—a price that usually covers relay drivers alone, he says.

Struthers-Dunn of Pitman, N.J., has six versions of a 1 Form A contact and recently introduced a 2 Form A and a 1 Form C. C.P. Clare will have DIP reed relays available this month. Potter and Brumfield, Princeton, Ind., will introduce its first DIPs early next year, as will Wheelock Signals Inc., of Long Branch, N.J. Sigma Instruments Inc., of Braintree, Mass., and Self-Organizing Systems Inc. of Dallas, have recently introduced the new packages and several others have similar plans.

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Microwave

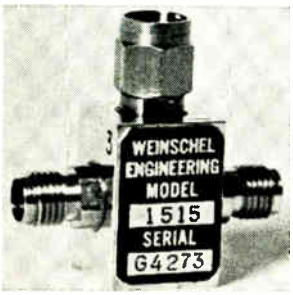
Transistors are job-specified

1-GHz oscillators have common emitters; amplifiers put in common-base package

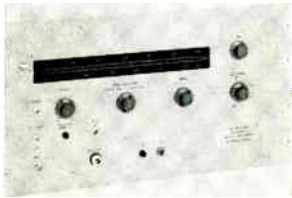
A transistor that makes a good 1-gigahertz power amplifier doesn't necessarily make a good power oscillator, and vice-versa, even though microwave equipment designers often try to use the same device for both jobs. Engineers at Microwave Semiconductor Corp. believe that a common-base transistor makes a superior class-C power amplifier, whereas the common-emitter configuration is best for oscillators. The company's new family of 1-GHz transistors is characterized accordingly as either amplifiers or oscillators: the amplifier devices, MSC 1020 (20 watts output power at 1 GHz, 8.2 decibel gain), MSC 1010 (10 watts, 8.2 dB), and the MSC 1005 (5 watts, 10dB), are in a common-base, grounded heat-sink stud package; and the oscillator devices, MSC 80081 (10 watts minimum output power at 1 GHz), MSC 80080 (5 watts), MSC 80069 (3 watts) are in a common-emitter package with an isolated heat-sink stud.

In both the amplifier and the oscillator, the stud conforms to the standard 2N4431 transistor stud outline.

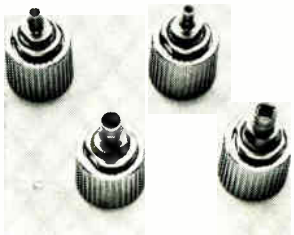
Most 1-GHz power devices on the market have a common emitter, the company claims, and when used as amplifiers, their high input Q and reduced gain make broadband operation difficult. However, the common-base 1020, 1010, and 1005 have Q values one-third those of comparable conventional 1-GHz devices, and can therefore operate



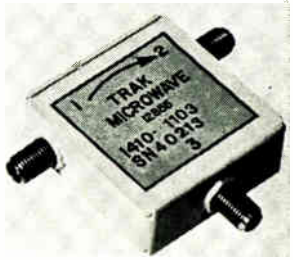
Miniature power divider model 1515 is a 50-ohm coaxial device for accurate division of power from matched ports over a broad frequency range. It covers the dc to 18 GHz range, and provides a nearly constant power division of 6 dB across its broadband. Its input power rating is 1 watt cw. Unit is for system and lab use. Weinschel Engineering, Gaithersburg, Md. [401]



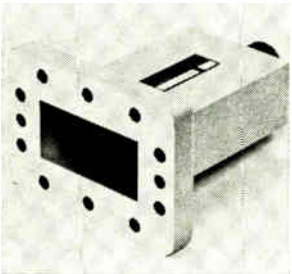
Common carrier sweep generator model 610 WE has frequency ranges of 3.6 to 4.3 GHz and 5.9 to 6.5 GHz. The rf output of 1 V rms is externally leveled to a flatness of +0.02 dB per 20 MHz over the two bands. Calibrated rf sweep from 0 to 160 MHz is provided. Effective rf filtering keeps spurious signals more than 40 dB down. Wiltron Co., Palo Alto, Calif. [402]



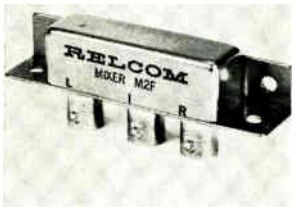
Precision coaxial adapters, models 71250 and 71200 are APC-7 to SMB devices covering the frequency range of dc to 4 GHz. VSWR is 1.05 + 0.005 times frequency in GHz. Models 81250 and 81260 are APC-7 to SMC, covering the frequency range of dc to 12.4 GHz. VSWR is 1.015 + 0.004 times frequency in GHz. Omni Spectra Inc., Hallwood Ct., Farmington, Mich. [403]



L-band circulator is designed to operate between 0.960 and 1.215 GHz. Isolation is 18 dB minimum with an insertion loss of only 0.5 dB maximum. Unit has a VSWR of 1.3:1 maximum and is stable over a temperature range of −54° to +95°C. Dimensions are 1½ x 1½ x 9/16 in. excluding connectors. Trak Microwave Corp., 4726 Eisenhower Blvd., Tampa, Fla. 33614 [404]



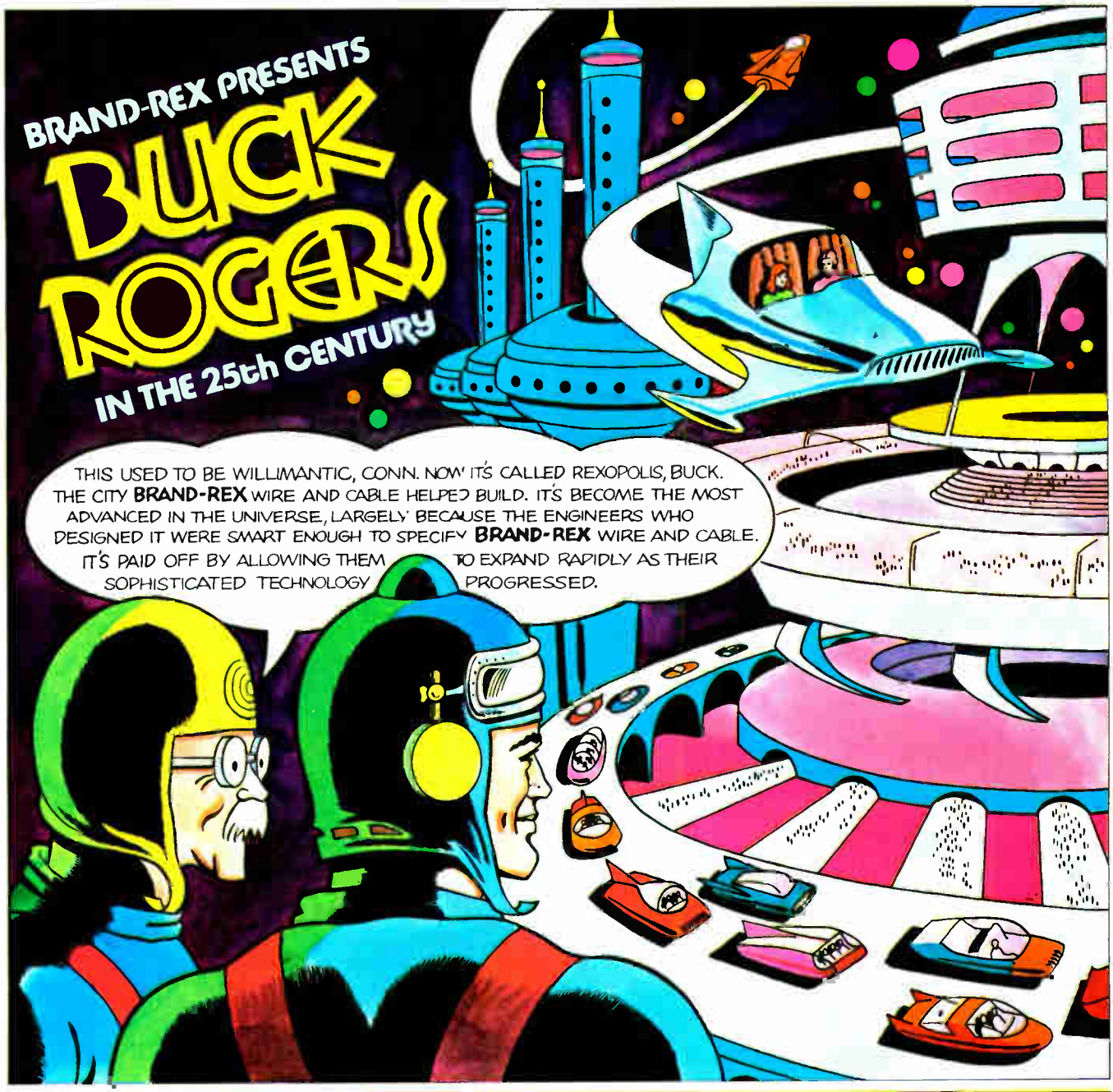
End launch WR430 adapters provide extremely low VSWR of 1.10 maximum over the band of 2.1 to 2.4 GHz. They can be obtained for adapting to a variety of coaxial connectors, including precision N connectors (male and female), APC7 and GR900 connectors, and large line size connectors. Maury Microwave Corp., 8610 Helms Ave., Cucamonga, Calif. 91730 [405]

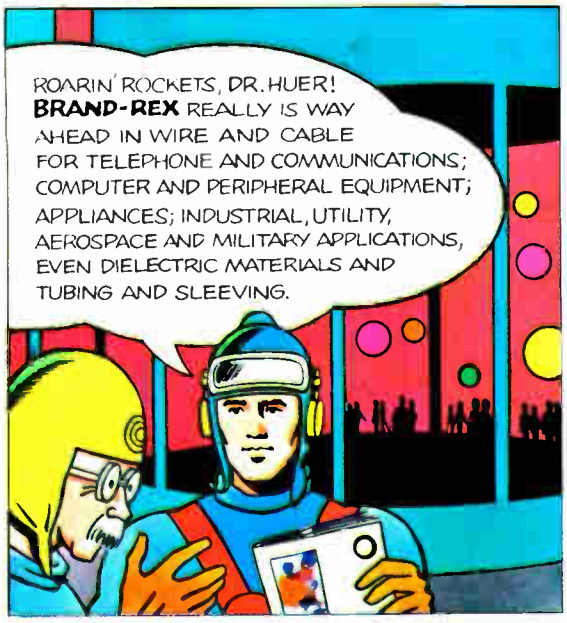


Balanced mixer model M2F features high isolation and a low noise figure over a 1,000-MHz to 2,500-MHz frequency band. At midband, the noise figure is typically less than 5.5 dB. Isolation is specified at greater than 20 dB; the output bandwidth is dc to 500 MHz; and a set of built-in filters isolates the R port. Relcom, 2329 Charleston Rd., Mountain View, Calif. 94040 [406]

Harmonic absorption filters are offered for frequencies from uhf through Ku band. A typical unit, model 91141, is a C-band device that operates from 4.4 to 5 GHz with an average power of 2 kW and a peak of 1 MW. Pass band VSWR is 1.15:1 with insertion loss limited to 0.25 dB. Unit measures 14½ x 5¼ x 4¼ in. Solitron Microwave, 37-11 47th Ave., L.I.C., N.Y. [407]

Two pulsed coaxial magnetrons, which deliver 75 kW of peak power at X band, are available for use in airborne and ground-based radar systems. The SFD-370 operates at a fixed frequency between 9.205 and 9.285 GHz. The SFD-380, which is mechanically tunable, operates from 9.1 to 9.5 GHz. Varian Eastern Tube Division, 800 Rahway Ave., Union, N.J. [408]





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New products

broadband, down through 500 MHz. Prices of the amplifier devices in lots of 100 are $150, $75, and $50, in order of decreasing power.

The common-emitter, isolated-stud design of the new oscillator permits use of a single dc supply circuit with either positive or negative ground. They will oscillate either with external feedback or with the package parasitic elements as part of the feedback loop. Though external feedback produces highest output power and efficiency, internal feedback is more effective for voltage controlled oscillator applications. The 80069, for example, was voltage tuned over a range of more than 150 MHz, centered on 900 MHz, with output power greater than 2 watts. The oscillators can also be operated in pulsed mode—the 80080 can generate more than 10 watts peak power at 1 GHz.

All devices have efficiencies in the range of 40 to 45%. Microwave Semiconductor engineers report that it makes little difference in power output or efficiency whether power is removed through the base (with dc and rf grounding of the collector) or through the collector (with rf grounding of the base).

Prices, in quantities of 100, are $150 for the 80081, $75 for the 80080, and $50 for the 80069.

The new transistors, both amplifiers and oscillators, were designed to withstand the mismatched loading conditions that often cause devices to burn out in L-band power circuits. Even under forward bias conditions, which oscillators need for starting, the 80081, 80080, and 80069 will withstand severe mistuning. The 1010 and 1005 amplifiers are the highest power 1-GHz transistors with an output mismatch rating, according to the manufacturer; the 1010 will withstand a voltage standing-wave ratio of at least 5 to 1 at 10 watts and 28 volts, and the 1005 will withstand an infinite VSWR at 5 watts and 28 volts.

All the devices are in stock.

Microwave Semiconductor Corp., 100 School House Road, Somerset, N.J. 08873 **[409]**

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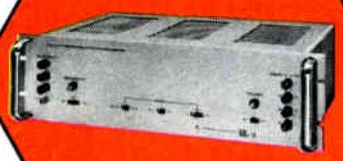

MODEL 538/10

MODEL 538/20

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New products

Tiny analog phase shifters fit onto circuit board

The bulky line-stretcher may become a thing of the past for equipment that requires an analog phase shifter. Subminiature phase shifters built by Merrimac Research and Development Inc. are so small and light that they can be mounted on printed circuit boards. They're designed for applications in space, airborne, and backpack equipment.

The phase shifters are available in electronically and manually controlled models. The electronically controlled unit, the PSES-3, can provide closed-loop control of a system, with typical applications in phased array equipment. The manual unit, the PSS-2, is primarily intended for phase trimming and adjusting. Both units are designed to Mil-E-5400.

The PSES-3 series operates over 10% bandwidths at center frequencies from 30 to 225 megahertz and exhibits phase shifts from 0 to —180°. The device uses a variable capacitance diode controlled by 0 to 48 volts. Control slope is 3°/volt. Maximum rf input power is —10 decibels, while worst case voltage standing wave ratio is 1.5:1. Maximum insertion loss is 0.8 dB.

The manually controlled model, PSS-2, is screw-tuned to provide a maximum phase shift to 90° over 10% bandwidths from 21 to 225 MHz. The entire phase shift is achieved in 14 screw revolutions. This unit can operate at 0.5 W average and 10 W peak power. Maximum VSWR is 1.6:1, while insertion loss is 1.1 dB maximum.

Both units are bilaterally constructed—input and output terminals can be reversed—and have solder-pin terminals for pc board use. The PSES-3 measures ½ by ¾ by 1 inch and weighs 0.5 ounce. The PSS-2 measures ½ by ¾ by 1⅝ inches and weighs 1 ounce.

The PSES-3 is priced at $95 in small quantities; the PSS-2 costs $75. Delivery time is four weeks.

Merrimac Research and Development Inc., 41 Fairfield Place, West Caldwell, N.J. 07006 **[410]**

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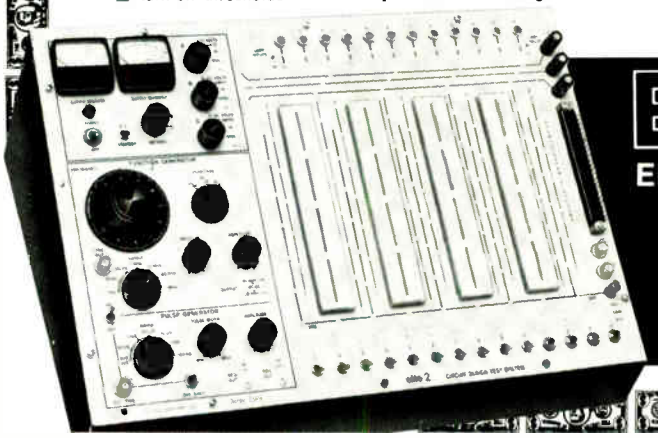

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Circle 171 on reader service card

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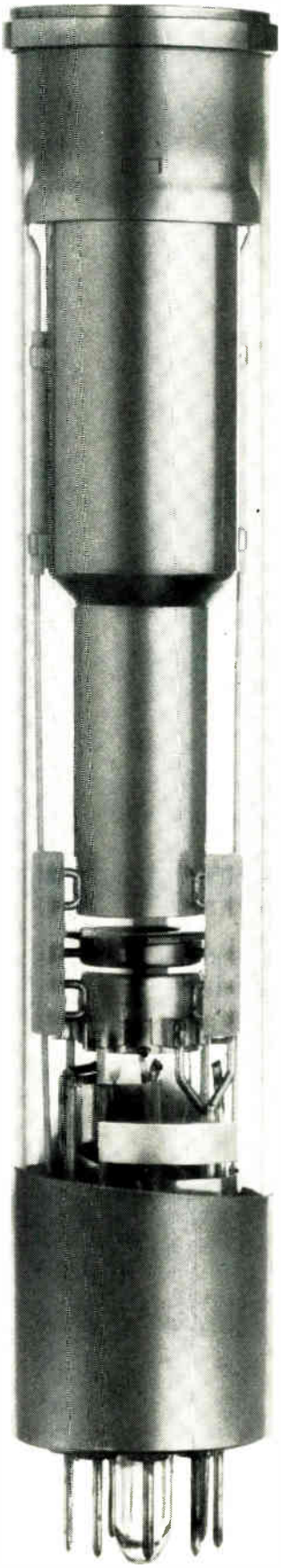

What a boost the entire CCTV industry would enjoy if existing cameras could operate well at significantly lower light levels and higher response speeds. That's exactly what this new Philips **Plumbicon** camera tube has to offer. Its one-inch-diameter makes it **retrofittable into existing cameras now using vidicons.** Developed originally to meet the exacting needs of live broadcast television, the **Plumbicon** won the industry's "Emmy" in 1967, as the year's most significant technological advance. Since then it has dominated its field - today it's in 9 out of 10 colour cameras in use throughout the world. When used in CCTV applications - in medicine, industry, education or commerce - this superb tube makes practical many applications hitherto only theoretical. The very high sensitivity, low dark current and fast response mean greatly improved picture quality - even when the subject is poorly illuminated or moving rapidly. All of which means the **Plumbicon** can make existing CCTV equipment work better, can make **CCTV colour** a practical proposition... can open up vast new markets, not only for cameras, but for related equipment as well! Let's help you open up new opportunities!

Philips Electronic Components and Materials Division, Eindhoven, the Netherlands.

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PHILIPS

Instruments

Digital meter is versatile

Input/output slope adjustable to suit application; units are expandable for special jobs

One way not to make a name is to sell strictly to original equipment manufacturers. Once an OEM gets his hands on a product, off goes the original nameplate and on goes his. That's why Gralex Industries, which has made digital panel meters since 1967 but only on a custom basis, decided to introduce two off-the-shelf instruments.

Both are 3½-digit types. The Model 1 is unipolar, the Model 2 is bipolar. "Mechanically, we think we have a package with a high degree of reliability," says Bernard Grand, executive vice president of Gralex, a division of General Microwave Corp. Each meter is in an extruded aluminum case measuring 4⅝ by 3⅛ by 5¼ inches and can be mounted from the front or back. Two boards hold all the basic circuitry, and there's room to add 100 boards for special functions.

"We can easily add appendages to the basic meter," says Grand, "by simply lengthening the case or adding circuit boards inside the enclosure. Sometimes you might want to drive different levels out, or handle different input signals; or you might want to convert it to a ratiometer, or use it to shape analog inputs, or multiplex. We can do these things within the basic package design."

There are nine versions for each model, depending on full-scale input: 100 millivolts, 1 volt, 10 V, 100 V, 1,000 V, 1 milliamp, 10 mA, 100 mA, and 1 A.

To avoid display ambiguity, the

Low-cost, general purpose oscilloscope CDU150 is a dual-trace unit that features dc to 35 MHz bandwidth and is particularly suitable for field computer applications. The CRT has an 8 x 10 cm viewing area. Scope sensitivity is 5 mV/cm at full bandwidth in 12 steps with 1-2-5 sequence for both channels. Price is $1,695. Raytheon Co., 141 Spring St., Lexington, Mass. 02173 [361]

Multirange ac-dc portable meter has 30 measuring ranges for current and voltage and one resistance measuring range up to 1 kilohm. The internal resistance is 3,333 ohms per volt for all voltage ranges. The resistance range is protected by a fuse cutout. Accuracy is within ±1.5%. Test voltage is 2 kV. Dimensions are 9 x 4½ x 3½ in. Epic Inc., 150 Nassau St., New York [362]

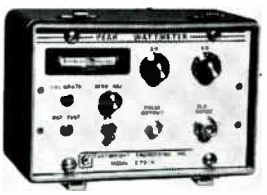
Multimeter DM42 is a full 4½-digit unit that offers 0.02% dc accuracy with 10 μV resolution, 0.05% accuracy with 0.01 ohm resolution and 0.2% ac accuracy with 100 μV resolution. Features include 100% overranging, variable sample rate, 5 full readings per second, small size and computer styling. Price is $690. Precision Standards Corp., 1701 Reynolds, Santa Ana, Calif. [363]

Electronic manometer type 1083 is a high-accuracy measuring and transducing system for differential pressure or vacuum from 10^{-5} mmHg to 5,000 mmHg. Pressure is indicated by an automatic, three and one-half Nixie tube display as well as by an optional BCD digital output and a high level 0 to 1 V dc, analog output. CGS/Datametrics, 127 Coolidge Hill Rd., Watertown, Mass. [364]

IC tester features a clip probe and a flexible cable for in-system testing of both 14- and 16-pin dual in-line TTL or DTL ICs. It displays the logic states of all pins simultaneously. Tester lamps are "on" for logic highs and "off" for lows. The unit, complete with cable and nine adaptor plugs, is priced at $120. Emcee Electronics Inc., Old Churchman Rd., New Castle, Del. [365]

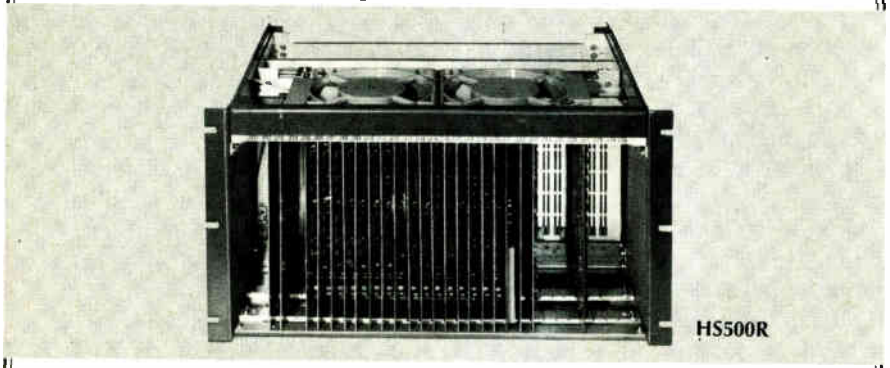
Digital panel meter type 330A will function over a range of input power frequencies varying from 50 Hz to 400 Hz at either 115 V or 230 V. The 3½-digit meter also features automatic zero adjustment and constant high input impedance. Maximum sensitivity is 1 mV, 100 nA, 1 ohm. Accuracy is 0.1% full scale. Digilin Inc., 6533 San Fernando Rd., Glendale, Calif. [366]

Function generators F51 through F55 operate as multipurpose, universal signal sources. Operating over a ten-decade spectrum of 0.0005 Hz to 10 MHz, they produce outputs of variable width pulse, sine, square, triangle, ramp, and fixed duty cycle pulse waveforms. Units feature calibrated attenuators with 70-dB range. Interstate Electronics Corp., Anaheim, Calif. [367]



Direct-reading peak wattmeter CTS-1 covers the range from 1 mW to 10 W peak with a resolution of 0.5 dB, handles PRFs from 1 kHz to 20 MHz, is accurate to 5% from 350 MHz to 2 GHz, and exhibits a maximum VSWR of 1.3 over the same range. With external pad the unit will handle up to several hundred watts peak. Cartwright Engineering Inc., 1020 E. Elm Ave., Fullerton, Calif. [368]

What Memory-System Maker Is Speeding Up the Cycle Time— But Holding Down the Price?



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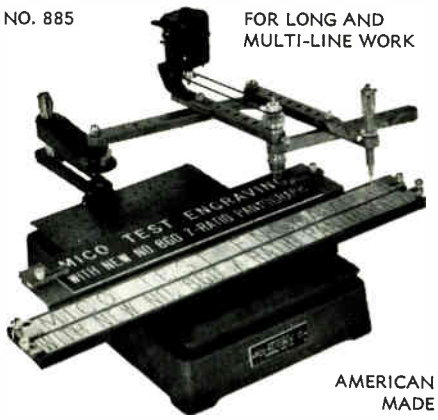

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bipolar model shows a minus sign for negative inputs and a plus sign—not a blank space—for positive inputs. With the unipolar meter, a minus sign flashes when the input is negative.

The meters' OEM heritage shows up in the adjustment capability of the straight-line input/output characteristic slope. This is useful when voltage can come from any of several types of transducers. Thus the meters can be made to directly read out pressure, strain, torque and weight, for example. Grand says customized meters from Gralex have shown up in such diverse applications as analyzing blood and monitoring nuclear experiments.

Gralex concentrates its panel-meter efforts in the OEM area because that's where most of the business is, says Grand. "It (a dpm) doesn't meet the need as a general bench instrument," he points out. "It's really very dedicated applications that people have for them. And they don't buy one. We see the market as being in the multiple orders involving 25, 50, and up."

Basically, the new Gralex meters are dual-slope instruments with high input impedances to permit differential operation.

Input impedance varies from version to version, but is 10 megohms in most cases. Response time is 7 milliseconds, and the meters make up to five measurements a second. The actual display rate, though, is specified by the customer.

The standard output is in digital format—3.5 V or above for a 1 and 0.4 V or lower for a 0. Binary-coded-decimal outputs are optional.

Accuracy is ± (0.1% of reading ± 1 count). Stability is 0.005% per degree centigrade for all but the 100 mV scale, where it's 0.05%/°C.

The meters have a minimum over-range capability of 50%, except for the 1,000-V models where the figure is 10%. To hold a reading, the user shorts a designated pair of pins in the back panel connector.

Gralex Industries, 155 Marine St., Farmingdale, N.Y. 11735 **[369]**

Would you believe a 50 MHz pulser from E-H for only $395?

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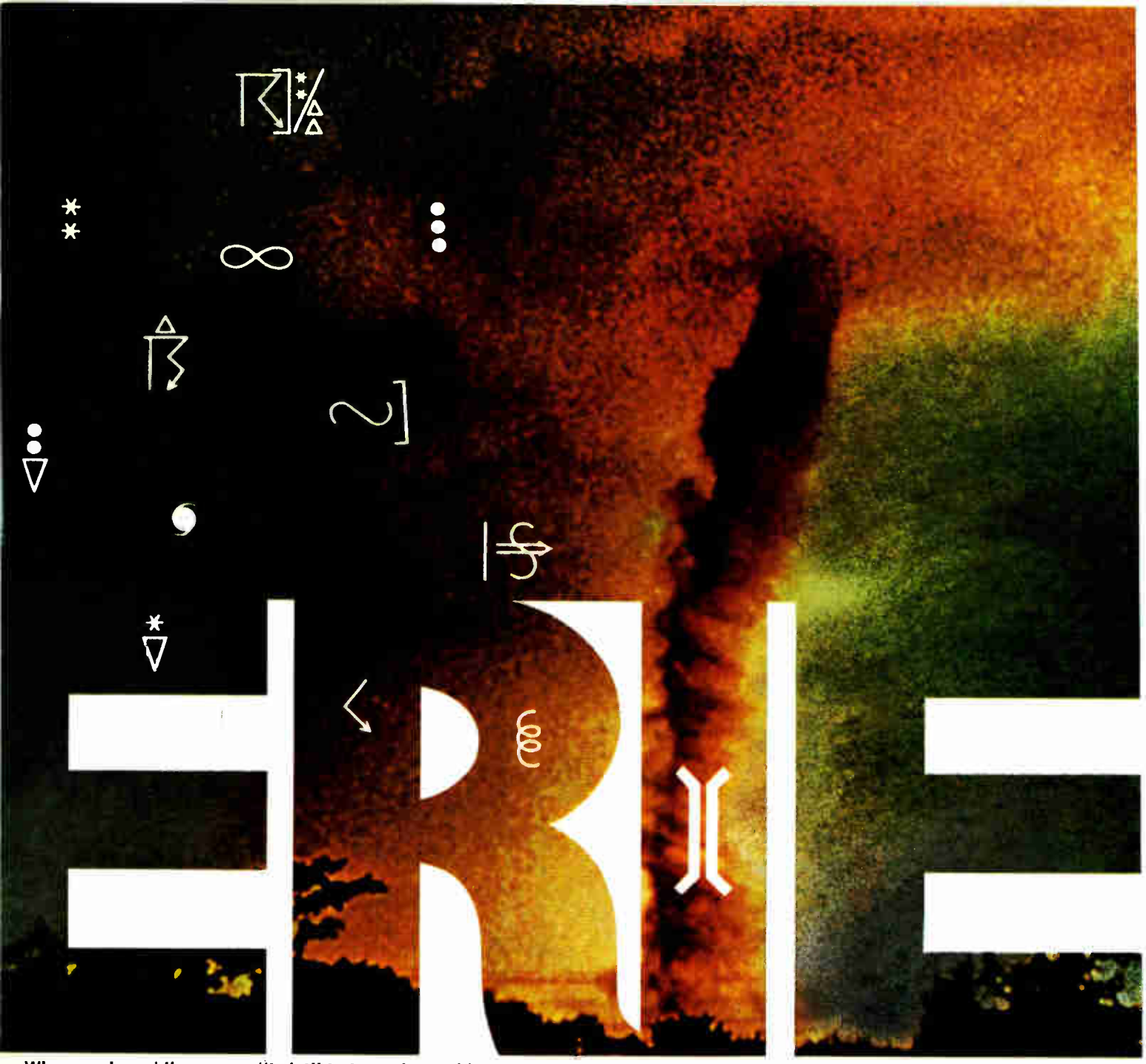
Circle 139 on reader service card



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Circle 140 on reader service card

Data handling

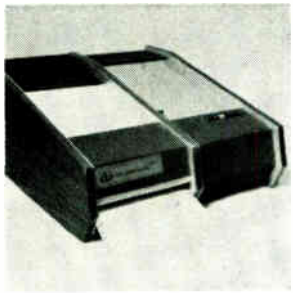
Shift register is programable

Dynamic MOS unit accommodates any length from 233 to 512 bits

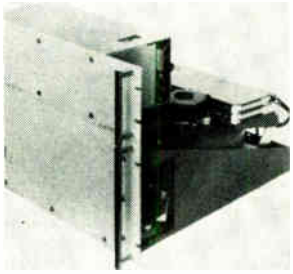
Because bit lengths for shift registers vary with applications, demands for custom-built circuits abound. But custom shift register ICs cost anywhere from $15,000 to $40,000 per design and require several months lead time. To make it easier—as well as faster and cheaper—for customers to obtain nonstandard shift registers, Texas Instruments has introduced a "programable" two-phase dynamic MOS shift register, the TMS3401, that can accommodate any bit length from 233 to 512. Price per bit is less than 1 cent when the circuit is purchased in large quantities, and normal delivery time is six to eight weeks. Programing the bit length is accomplished by changing a single photomask during the manufacturing process.

To minimize cost, says Daniel Baudouin, program manager for new MOS standard products, TI engineers selected 512 bits as the register's maximum length; a longer configuration, such as 1,024 bits, "was tempting, but the price was too high," Baudouin explains, because the larger chip size would decrease yield. To further reduce chip size—and to make the circuit faster at the same time—TI selected a ratioless two-phase circuit design. The resulting chip is small for a circuit of such complexity, 117 by 113 mils, and is about two-thirds the size of the equivalent ratio circuit. Moreover, with a maximum shift rate of 5 megahertz, the circuit is one of the fastest

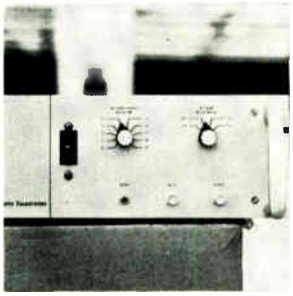
Time-sharing/instrument data transfer system DGC-301 couples various instruments to a remote computer via a time-sharing terminal. It accepts BCD data from various digital measuring instruments, stores the data in memory, decodes the data and presents it to the time-sharing terminal. The memory is loaded within 1 μs. Data Graphics Corp., Speedway Dr., San Antonio, Texas [341]



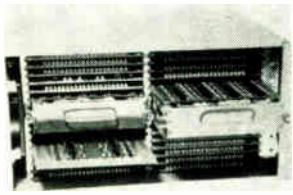
Remote terminal printer designated Typeliner model III is available with 80 columns of upper and lower case alphabetic and standard ASCII 64 character set. It may be used with CRT terminals with plug-to-plug compatibility and with any modem. Print speed is 100 lines per minute. Printout is on pinfeed, fanfold paper. Data Computing Inc., W. Shangri-La Rd., Phoenix [342]



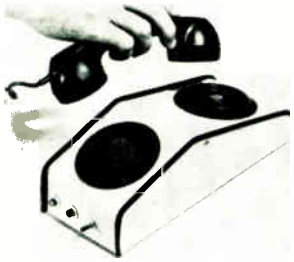
Disk memory 7207 is a head-per-track unit that has 128 heads with an average access time of 16.4 milliseconds. It maintains the plug-compatible feature of all the company's memories, which allows field expansion of the memory without danger of data loss. Unit price is $17,000 and OEM quantity prices are as low as $11,000. Data Disc Inc., 1275 California Ave., Palo Alto, Calif. [343]



Pseudo-random pattern generator model 112 is for testing digital transmission equipment typically used for telephone line communication or telemetry. It provides selectable bit rates of 1.2, 2.4, 4.8, and 9.6 kHz. The pseudo-random pattern length is programable by switch selection in binary steps from 1,024 to 524,-688 bits. Digital Products Corp., Ft. Lauderdale, Fla. [344]

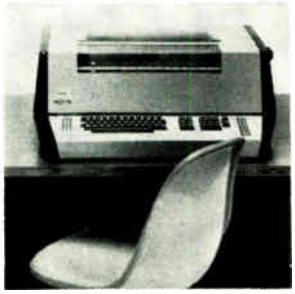


Random access computer mainframe memory system, the Comrac 1010, features 900 nanosecond full cycle time, 350 ns access time and field expandability from 8K x 36 to 32K x 36. The memory system uses 3D selection and fast switching 22-mil cores for high speed and density. Operating temperature is 0° to 50°C. Information Control Corp., 9610 Bellanca Ave., Los Angeles [345]

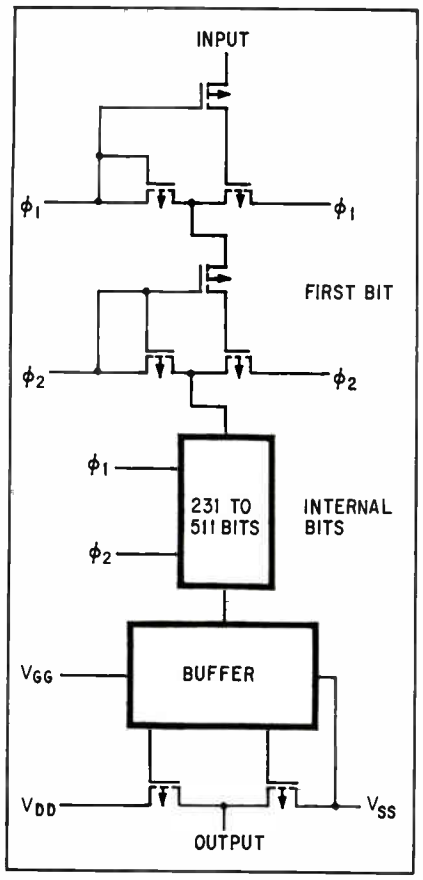


Acoustic/magnetic data coupler IT-332A operates at a maximum of 300 baud for both transmit and receive mode, full on half duplex. It can interface Bell System 103 Series modems through the telephone system. It comes with an interconnection cable for the model 33 Teletype by prewired plug connections. ITI Electronics Inc., 369 Lexington Ave., Clifton, N.J. [346]

Magnetic tape systems 20291 and 20292 are automatic loading, high-speed drives that operate at speeds of from 75-120 in./s and 120-200 in./s, respectively. They are available with read/write electronics for 800 b/in. non-return-to-zero-inverted, 1600 b/in. phase-encoded, and dual (800/1600 b/in.) density recording. Bucode Inc., 175 Engineers Rd., Hauppauge, N.Y. [347]



Programable data terminal model 711 is a remote batch intelligent terminal with an integral 1200 baud modem and universal input/output, computer-type printer, and a standard 4 K (512 bytes) core memory expandable to 32 K. It is capable of operating attended or unattended. Single unit price is $12,500. Daedalus Computer Products Inc., P. O. Box 248, North Syracuse, N.Y. [348]



Adaptable. Programing of bit length is controlled by a single photomask.

large-bit-length shift registers.

Low-threshold silicon nitride technology is used, so that the device can interface directly with TTL circuitry; no external resistors on input or output are needed. The chip contains two internal diffused resistors, one 6 kilohms and the other 1.5 kΩ. Either can be connected to the input, if desired, to increase noise margin. Power supplies of +5 and −12 volts are required. Dissipation is 0.1 milliwatt per bit at a 1-MHz shift rate.

The TMS3401 also is available as a standard 512-bit device at a unit price of $5 in 100 to 249 quantities; units can be delivered from stock in evaluation quantities. In the nonstandard bit lengths from 233 to 512 bits, there is a $2,000 mask charge; minimum-quantity order is 1,000.

Texas Instruments Inc., Inquiry Answering Service, P.O. Box 5012, M.S. 308, Dallas, Texas 75222 **[349]**

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2N6002		2N6006		2N6012		2N6016		GET914		GET2221A		GET2484		GET3638A	
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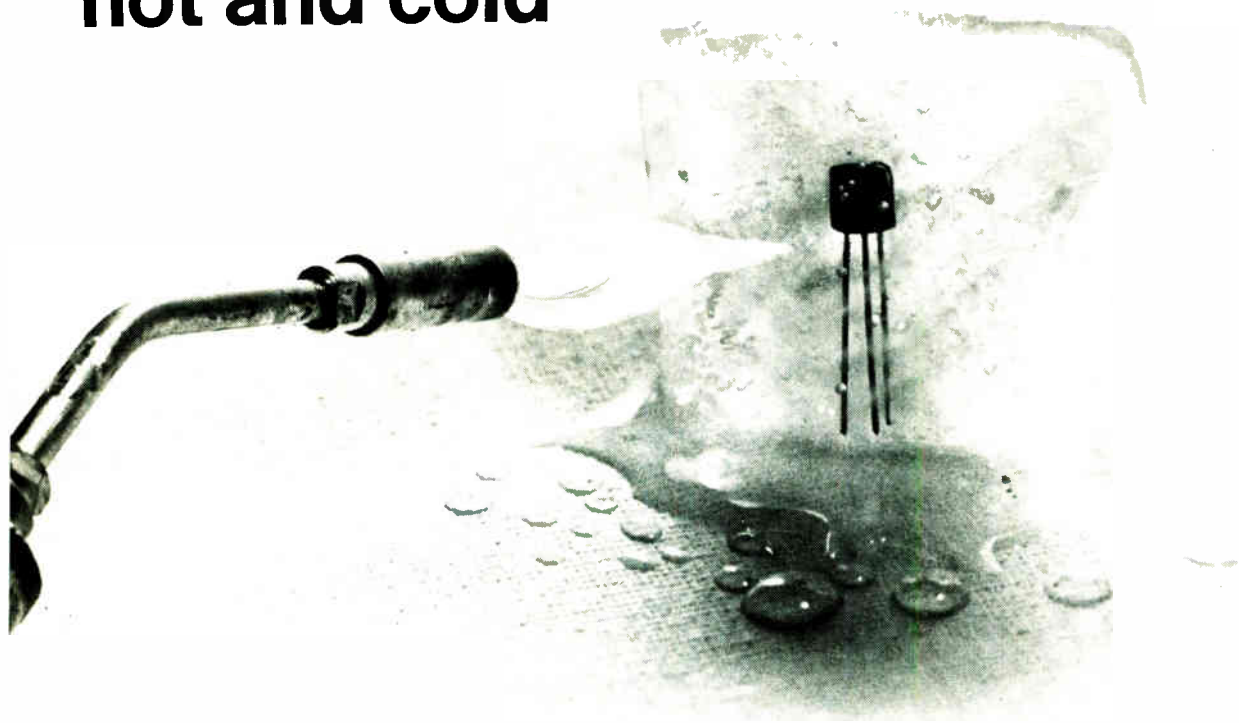

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PD mW	Ic mA	NPN JEDEC Type	NPN h_{FE} $I_C=.1$ $V_{CE}=1V$	NPN h_{FE} $I_C=300$ $V_{CE}=2V$	NPN h_{FE} $I_C=800$ $V_{CE}=5V$	NPN Noise fig. dB (max)	PNP JEDEC Type	PNP h_{FE} $I_C=.1$ $V_{CE}=-1V$	PNP h_{FE} $I_C=300$ $V_{CE}=-2V$	PNP h_{FE} $I_C=800$ $V_{CE}=-5V$	PNP Noise fig. dB (max)
400†	500	2N6000	50	40		3.0	2N6001	90	35		3.0
400†	500	2N6002	130	80		2.0	2N6003	210	50		1.5
400††	500	2N6004	50	40		3.0	2N6005	90	35		3.0
400††	500	2N6006	130	80		2.0	2N6007	210	50		1.5
500††	800	2N6010	45	85	45	5.0	2N6011	70	65	45	3.0
500††	800	2N6012	90	160	50	3.0	2N6013	180	135	70	2.0
500†††	800	2N6014	45	65	15	5.0	2N6015	70	60	35	3.0
500†††	800	2N6016	90	60	15	3.0	2N6017	180	125	55	2.0

† $BV_{CEO}=25V$, †† $BV_{CEO}=40V$, ††† $BV_{CEO}=60V$

GE Type	Replaces	GE Type	Replaces	GE Type	Replaces
GET706	2N706	GET2221A	2N2221A	GET3013	2N3013
GET708	2N708	GET2222	2N2222	GET3014	2N3014
GET914	2N914	GET2222A	2N2222A	GET3638	2N3638
GET929	2N929	GET2369	2N2369	GET3638A	2N3638A
GET930	2N930	GET2484	2N2484	GET3646	2N3646
GET2221	2N2221				

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Semiconductors

Dual-polarity IC regulator

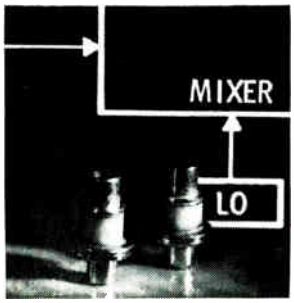
Monolithic unit provides ±15 V at up to 100 mA; zener diode is reference

Almost every circuit design that contains operational amplifiers requires dual-polarity voltage regulation. Silicon General Inc. in Westminster, Calif., has introduced what it says is the first monolithic tracking voltage regulator on the market to feature dual polarity. Its output voltage is internally preset, but a single external potentiometer can be adjusted to change both outputs simultaneously over a range of 8-25 volts.

Three versions of the device, which will provide plus and minus outputs of 15 V at currents up to 100 milliamperes, are available. The SG1501 operates over the full military temperature range of −55 to +125° C; the SG2501 and SG3501 are intended for commercial applications at 0 to 70° C.

In designing the regulator, Robert Mammano, vice president for engineering, set out to correct some of the problems that beset previous monolithic units. For example, says Mammano, a lot of outboard components are usually required but the SG1501 only needs an external current-limiting resistor and two stabilizing capacitors for each side of the dual regulator—a total of two external resistors and four capacitors.

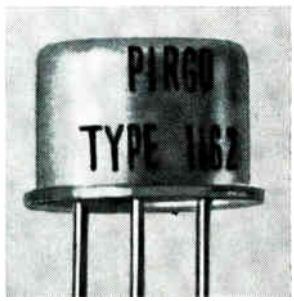
Another problem with other monolithic regulators has been thermal gradients on the chip. These cause shifts in output that affect various circuit components, each of which has a different temperature coefficient. According to Mammano, the



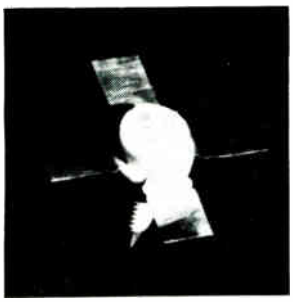
Single-sideband, Schottky-barrier mixer diodes designated MD0189-191 and MD0219-221 feature noise figures ranging from 6 to 8 decibels. They use silicon as the basic material with the wafers encased in a double-ended microwave pill prong package. Capacitance is 0.18 pF. Inductance is 0.40 nH. Price ranges from $20 to $50. Texas Instruments Inc., Box 5012, Dallas [436]

Silicon npn, rf power transistor type 2N5918 is an emitter-ballasted overlay type for vhf/uhf communications equipment. It features 10 W output at 400 MHz (8 dB minimum gain). Collector-to-emitter voltage with base open is 30 V; collector-to-base voltage 60 V; emitter-to-base voltage 4 V; continuous collector current, 0.75A. RCA, Solid State Division, Somerville, N.J. 08876 [437]

High-voltage rectifiers series RC-5477-RC5481 feature a forward rectifier current rating of 1 ampere at 55°C case temperature with voltage types from 5,000 to 10,000 volts. They differ only in surge current rating from the JEDEC registered types (50 amperes peak for the RC series rather than 80 amperes peak). Rectifier Components Corp., 124 Albany Ave., Freeport, N.Y. [438]



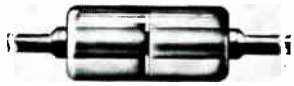
Monolithic Darlington power transistor type 1162 will dissipate 5 W. Peak collector current is 10 A with a continuous collector current of 7 A. Minimum gain is 2500. Typical applications include driving transducers from ICs in displays, thermal printers, chain printers, and relays. Price is $22.50 each in 1-99 lots. Pirgo Electronics Inc., 130 Central Ave., Farmingdale, N.Y. [439]



Three compatible 12-V vhf communications transistors withstand infinite VSWR through all phase angles. Aimed at the land mobile communications markets, they are designated B3-12 (3 W with 10 dB gain), B12-12 (12 W with 0.8 dB gain) and B25-12 (25 W with 6.2 dB gain). Price (100 up) is $37.82. Communications Transistor Corp., 301 Industrial Way, San Carlos, Calif. [440]

Pnp, diffused alloy power germanium transistors feature high current capabilities and fast switching times. Series 2N2294-95-96 come in a lugged TO-41 case; the 2N1073-A-B and 2N2288-93, in a TO-3 case. Collector current is 20 A maximum with high gain. Collector-emitter saturation voltage is as low as 0.4 V. Solitron Devices Inc., 1177 Blue Heron Blvd., Riviera Beach, Fla. [441]

Microwave power transistors are available in TO-39 packages or hermetically sealed stripline stud packages. Operating range is from 175 MHz in the vhf band through the L and S bands to 2 GHz. Units can produce up to 10 watts of output power with a minimum of 6 dB gain at 1 GHz. Prices in lots of 100 to 999 range from $3.18 to $48. Raytheon Co., Mountain View, Calif. [442]



Varactor tuning diode 6702A combines the best features of a low-inductance package with varactor diode performance in glass for use up to 2.5 GHz. It is a 10% tolerance device rated at 2.2 pF at −4 V with a Q of 700. It has a breakdown rating of 25V at 1 μA dc. Price in 100 to 999 lots is $6.72 each. MSI Electronics Inc., 34-32 57th St., Woodside, N.Y. [443]

...offers an overall light gain of 10^6 with a typical background of 10^{-10} lumens/cm². (light equivalent input.)

Typical operating voltage for these conditions is 40kV The background indicated is for a tube having bialkali photocathodes,—tubes are also available with a range of S-20 cathodes for use out to 8,000 Angstroms. Input and output windows are flat Zinc crown glass, 50 mm diameter. Type 9693 is available with sapphire input window for use in the UV. Developmental types are now being made with fibre optic windows. All present types are furnished with P-11 phosphors throughout although other types of phosphors are under investigation ☐ Tubes are normally supplied potted in silicon rubber and a number of variations are available. A complete package, including electromagnet, divider chain, high voltage power supply and magnet supply is offered. A permanent magnet is also available ☐ An extensive technical manual, as well as useful application notes, are available on request. Write on your company letterhead to:

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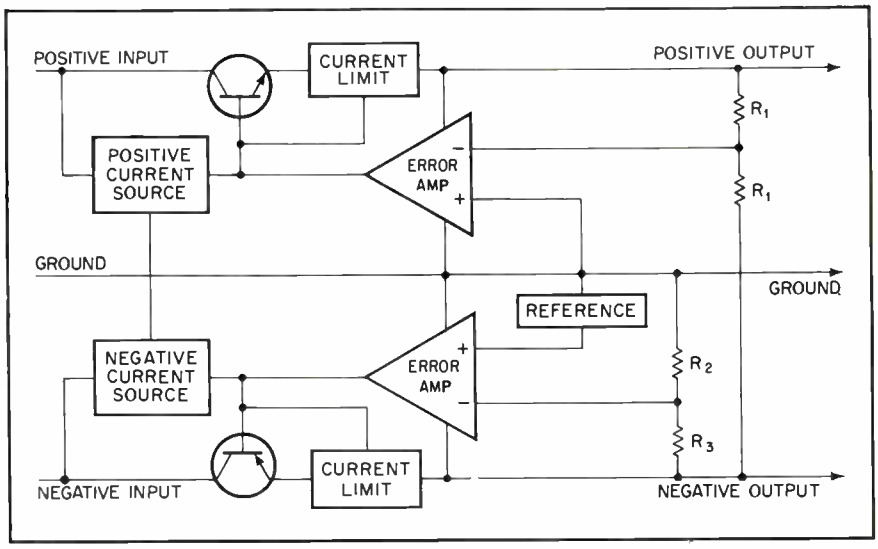

Balanced. Dual-polarity regulating circuit provides plus and minus outputs at up to 100 mA. Design has provision for adjustable current limiting.

voltage reference was partly responsible. "It's been a question of how to get zero-temperature-coefficient reference voltage when the reference element included several components that had to compensate for each other's temperatures," he says. Silicon General's solution is a double-anode, 6.3 V zener diode with zero temperature coefficient. This assures compatibility with standard processing and makes the reference voltage independent of chip temperature.

Mammano also points out that in earlier monolithic devices the resistors that set the exact output voltage were left outside because their positive temperature coefficients caused output voltage shifts when heat unbalanced them in use. The company managed, however, to use internal resistors by regulating the negative voltage output against the reference, and by setting the positive voltage output equal to the negative output to provide good tracking.

Further, the error sensors—the differential amplifiers—on both sides of the regulator are far enough away from the power transistors to heat up uniformly and at the same time as the chip temperature rises in use. This also helps prevent output voltage shifts.

The output voltage is balanced to within 1%, allowing only 150 mV of differential between the positive and negative sides. Minimum input-output differential is 2 V. Line and load regulation of 0.01% is offered and the maximum temperature variation is 1%. Standby current drain is less than 3 mA.

The SG1501 will sell for $9.80 each in quantities of 100; the 2501 price for 100-unit quantities is $6.75, and the 3501 will sell for $4.80. Delivery is from stock. TO-100 or 14-lead dual in-line packages are available.

Silicon General Inc., 7382 Bolsa Ave., Westminster, Calif., 92683 **[444]**

Single chip for LED displays holds counter, latch, decoder

One goal before the manufacturers of components for solid state numeric displays is to integrate the addressing circuits on a single chip, so that the circuit area can be shrunk and at the same time, cost-effective power sharing can be made available.

A big step in this direction has been taken by the Motorola Semiconductor Products division with its MC4050, a transistor-transistor-logic circuit that integrates the functions of a decade counter, a 4-bit latch, and a 7-segment decoder/driver in a 54-gate

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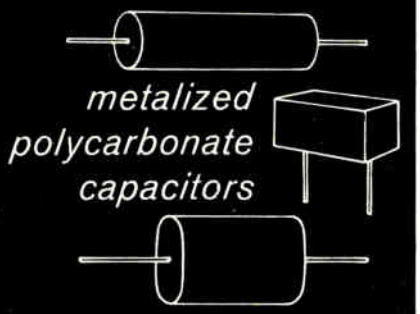

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unit. In the plastic-packaged version, the unit will sell for $9 in quantities of 100, whereas the three devices in the company's TTL 7400 series that together do the same job cost a total of $14.09 in lots of 100.

Ron Treadway, design section head for new product development at Motorola's IC center in Mesa, Ariz., says the counter design is the most unusual part of the circuit. Conventional counter designs, he says, start with four flip-flops, with serial or parallel counters between them. Motorola, however, employs a parallel counter scheme, for which it uses four latches and the associated steering, instead of four flip-flops and associated steering. The result is to eliminate eight gates from the parallel counter design.

The latch and decoder/driver portions of the circuit are fairly conventional. First, data is fed into an AND/OR circuit in the counter, then fed into the 4-bit latch by a positive-going clock. At the same time, the latch input is disabled by a negative-going clock. Treadway says the latch "is essentially a 4-bit storage device that locks the data from the counter for the duration of the clock pulse in which the counter was disabled."

One of the counter's features is that, when the display is reset, any zeros that occur in front of the first significant digit in a multidigit display are blanked out. This makes it easier to read the display accurately. There's also a lamp test input in the decoder/driver section to check the condition of the display, and a lamp-blanking input is offered for intensity modulation.

Die size was kept down to 94 square mils by the use of two-layer metalization—and without this process, Motorola engineers say, the device couldn't have been built economically. They foresee a substantial market for unpackaged MC4050 chips that the user can put into the same hybrid package as the light emitters (one MC4050 is required for each digit in a display).

The decoder/driver provides up to 40 mA of drive for displays that require current sinking in the low or "0" state, which is 0.4 volts. (Motorola officials say the 40 mA figure compares with the 3 milliampere limit of MOS devices that also integrate the counter/latch/decoder function on one chip.) Total power dissipation is typically 450 milliwatts and maximum toggle frequency is 35 megahertz.

The medium-scale integrated unit is available from stock in either a 16-pin dual in-line ceramic (MC4050L) or plastic (MC4050P) package, and is designed to operate over the 0-75°C temperature range. It will be followed by the MC4350, intended for the full military temperature range of −55°C to +125°C.

Motorola Semiconductor Products Inc., Box 955, Phoenix, Ariz. 85001 [445]

MSI multiplexer added to standard product line

Semiconductor manufacturers are starting to produce as standard products devices that used to be considered part of the custom market. The latest example is a medium-scale integrated device from Signetics Corp.

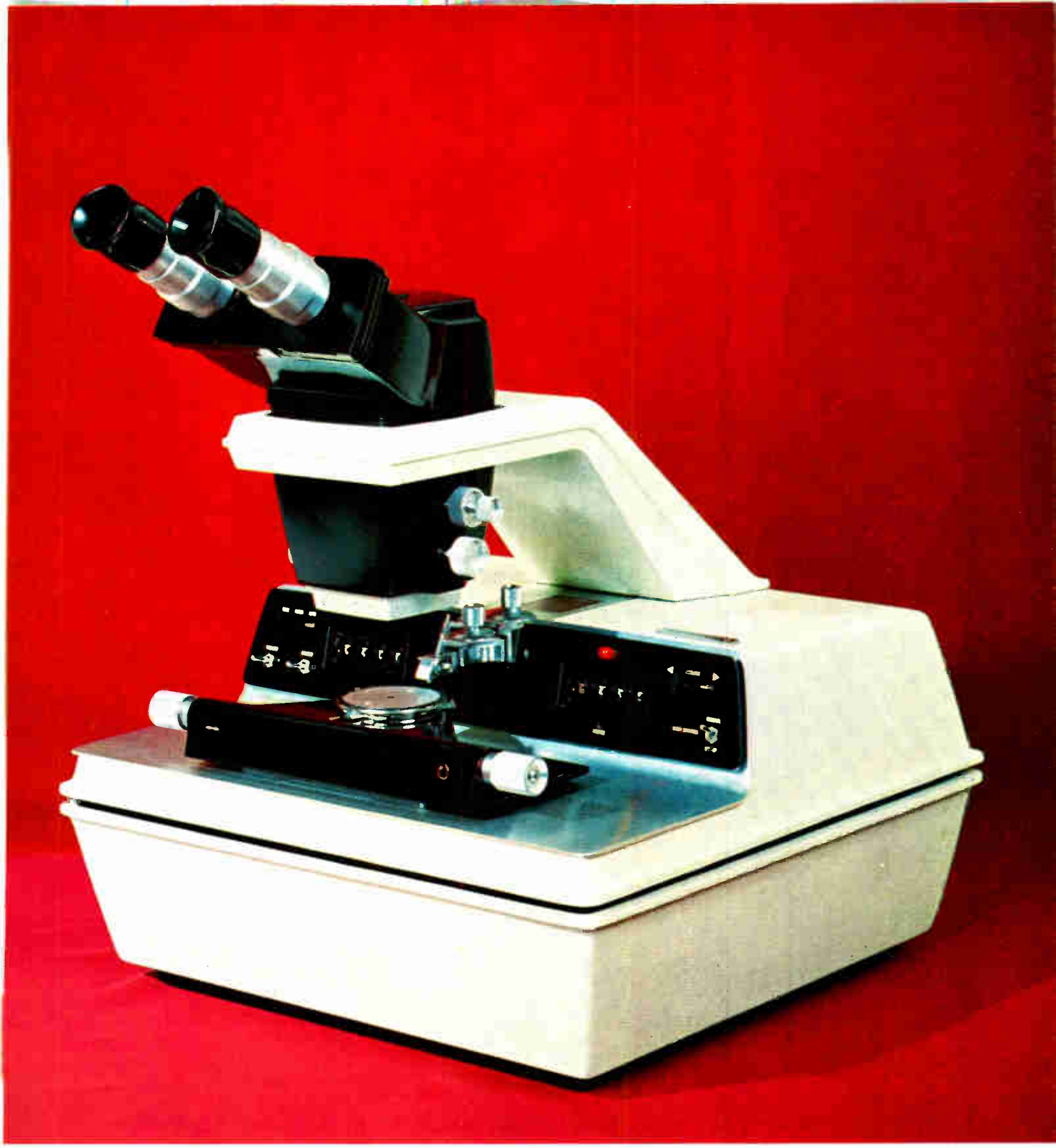
Signetics has developed a digital integrated circuit gating array which functions like a four-pole three-position switch, says Jerry Markus, digital product marketing manager. The array is a three-input, four-bit multiplexer, the newest addition to the company's DCL (designer's choice logic) line.

"Four bits of digital data are selected from one of three inputs," says Markus, "and the input to be active is determined by a two-bit channel selection code." The "data complement" input can invert data or allow straight data flow by controlling a complement circuit at the multiplexer's output.

The multiplexer comes in two versions: the 8263 employs active pullup output structures to effect minimum delays, and the 8264 utilizes bare collector outputs for

Circle 148 on reader service card

Circle 149 on reader service card→



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Circle 150 on reader service card

expansion of input terms.

According to Markus, one application for the 8263 multiplexer is as a variable modulus counter where the multiplexer is combined with the Signetics 8281 four-bit binary counter and the Signetics 8270 four-bit shift register. "Here," says Markus, "the shift register acts as a three-register memory; the register's outputs are fed to the corresponding multiplexer inputs. Three different presettable four-bit words can be chosen by the multiplexer, and when the channel select codes are alternated, the binary counter is preset with one of three words. The counter produces an output which has a repetition rate dependent on the inputs from the multiplexer."

Recommended operating voltage for both units is 5 volts ±5%. The 8263's binary 1 output voltage is 2.6 V; the 1 output leakage current in the 8264 is no more than 200 microamperes. Maximum 0-output voltage for both units is 0.4 V; the 0 input current is 1.6 milliamperes maximum for data input and output enable channels. Maximum 1 input current for data inputs and output enable channels is 40 μA and 80 μA for "channel select."

The 8263's propagation delay typically is 17 nanoseconds for all functions except channel select, where it's 25 ns. For the 8264, propagation delay typically is 25 ns from data input to output, and 20 ns from "output enable" to the data outputs.

The 8263 consumes no more than 420 milliwatts; maximum power consumption of the 8264 is 475 mW. Output short-circuit current ranges from 20 to 70 mA.

Both versions of the digital multiplexer are available in a 24-pin flatpack or in a 24-pin dual in-line ceramic or silicone package. The ceramic unit operates over the −55 to +125° C range; the silicone, from 0 to 75° C.

Price for the 8263 and 8264 is $15.95 in the 24-pin flat package, with a 0° to 75° C temperature range, in quantities of 100.

Signetics Corp., 811 East Arques Ave., Sunnyvale, Calif. 94086 [350]

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(Act of October 23, 1962, Section 4369, Title 39, United States Code)

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9. Not applicable.
10. Extent and nature of circulation:

	Average No. Copies Each Issue During Preceding 12 Months	Actual No. Copies Of Single Issue Published Nearest To Filing Date
A. Total No. Copies Printed....	86,855	89,998
B. Paid Circulation		
1. Sales through dealers and carriers, street vendors and counter sales.....	.....	.....
2. Mail subscriptions.....	80,707	83,558
C. Total Paid Circulation....	80,707	83,558
D. Free distribution by mail, carrier or other means.....	4,512	5,415
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F. Office use, left-over, unaccounted, spoiled after printing	1,636	1,025
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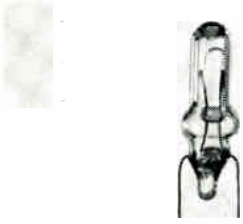
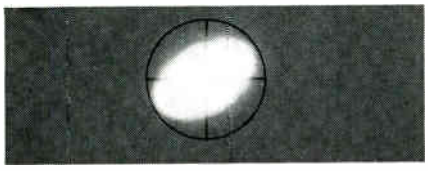
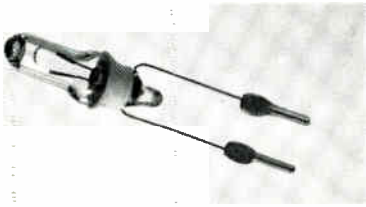


TEST
FOR SPOT SIZE AND CANDLEPOWER



WELD
PINS TO END OF LEAD WIRES



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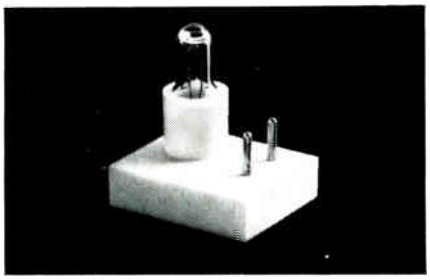

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Technical Abstracts

Cash in the chips

Multichip hybrid microcircuits for low cost reliability
Dewitt A. Graybill
General Dynamics Electronics Division
San Diego, Calif.

Where quantities are below 100 pieces per type, multichip hybrid circuits can offer low cost and high reliability, as well as design flexibility. Such circuits use a fixed metalization pattern, resistor chips, and semiconductor device chips. The circuit is laid out so that interconnections are made with wire bonds to common paths on the universal metalization pattern.

The designers chose a 14-lead glass-Kovar package measuring ⅜ by ⅜ inch. Gold-silicon eutectic chip bonding was required, but since vapor-deposited or sputtered films tend to degrade with temperature, the gold pad was formed by electroplating the gold over screened-and-fired molymanganese.

Only seven different semiconductor types were used, all available from a single supplier. Waffle trays with individual dice compartments helped minimize physical damage during shipping and handling. Multitap Nichrome silicon chip resistors were bonded at the same time as the semiconductor devices. Each resistor chip, 35 mils square, had a serpentine resistive film tapped with aluminum wire bonding pads.

Gold wire thermocompression bonding was used rather than ultrasonic bonding because of the variations in die size and metalization, which complicate the frictional energy transfer mechanism in the ultrasonic bonding process. Temperatures were held to below 200°C to avoid purple plague problems with gold-aluminum bonds.

This circuit fabrication method should be more adaptable in the future as more components and equipment appear. Chip resistors now are available with values from 10 ohms to 4 megohms rated at 250 milliwatts at 125°C; many semiconductor devices are available in chip form from distributor stock, and wider ranges of packages are becoming available.

Presented at Eastern Electronics Packaging Conference, MIT, June 8-9.

Control at the interface

A versatile interface control station
Nico H. Roos
Motorola Instrumentation and Control Inc., Phoenix, Ariz.

A fall-back or computer-control station is a complex interface that allows a digital computer to control continuous analog processes. The station converts digital information to an analog control signal; stores information pulses to maintain the control signal constant, and allows the operator to override the computer and intervene in the process control.

Two operational modes are possible when in a computer-controlled process. One mode is supervisory and the computer signals are used as the setpoints for analog controllers. The other mode uses the digital signals to directly manipulate the control valves. In either mode, the control signal is maintained by one of three circuits: a sample-and-hold unit which produces an absolute position signal; an integrater that integrates a variable input signal to produce an incremental signal to the control valve, and an integrator that's pulsed on and off to integrate a fixed internal reference signal.

A variety of backup capabilities, ranging from complete manual backup to various combinations of manual local-automatic and full computer control, also may be applied. If the computer fails, the stations could hold output currents at the last values before the failure or switch to automatic analog control with setpoints held at either the last values or changed slowly toward safe internal setpoints.

Modular design allows different types of control modes to be added whenever desired, as in the Veritrak Computer Control Station developed by Motorola. The station offers both direct digital or supervisory control as well as four different types of analog control units which can be used for automatic (though noncomputer) control, and more than a half-dozen setpoint and output memories which can take over if the computer fails.

Presented at Wescon, Los Angeles, Aug. 25-28.

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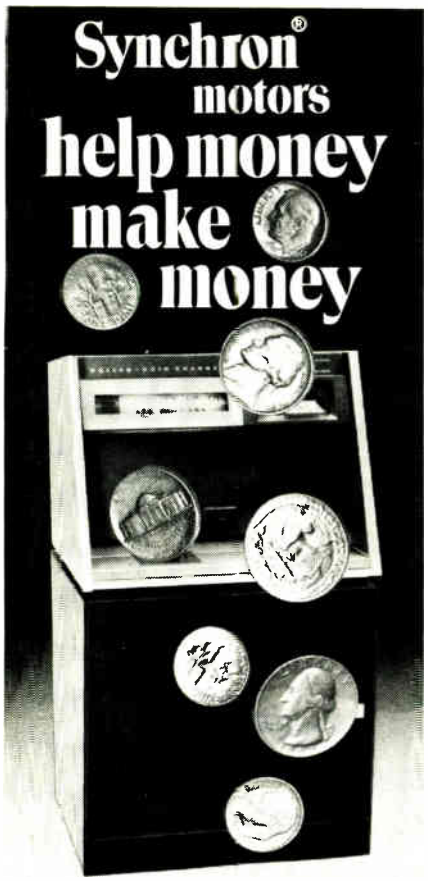

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New Literature

IC sockets. Connector Corp., 6025 N. Keystone Ave., Chicago 60646. Technical publication 49A describes type 561 14-pin dual in-line IC sockets. Also, the pc layout recommended is shown with associated drawing.
Circle **446** on reader service card.

I-f amplifiers. Siemens Corp., 186 Wood Ave. South, Iselin, N.J. 08830. A four-page data sheet describes and illustrates a line of a-m/fm i-f amplifiers. **[447]**

Electronics parts. Shigoto Industries Ltd., 350 Fifth Ave., New York 10001, has issued a comprehensive 32-page illustrated catalog of economy priced electronics parts for OEM applications. **[448]**

Digital panel meters. Electro-Numerics Corp., 2961 Corvin Dr., Santa Clara, Calif. 95051. A four-page brochure covers the complete electronic and physical characteristics of the 3300 series of digital panel meters. **[449]**

Fixed composition resistors. Stackpole Carbon Co., Kane, Pa. 16735. An eight-page bulletin (80-100) on fixed composition resistors contains comprehensive technical data with illustrated charts, component features, application guidelines and packaging specifications. **[450]**

Magnetic measurements. O.S. Walker Co., Rockdale St., Worcester, Mass. 01606, has made available an eight-page review of equipments and techniques for a wide variety of magnetic measurements. **[451]**

Layer short tester. U.S. Asiatic Co., 13-12, Shimbashi-I-Chome, Manato-ku, 105 Tokyo. A single-sheet bulletin describes a layer short tester designed for high accuracy and detection of virtually every type of coil. **[452]**

Plastics bonding. Eastman Chemical Products Inc., Kingsport, Tenn. 37662. Publication R-190 presents a variety of successful plastics bonding applications based on Eastman 910 cyanoacrylate adhesive. **[453]**

Stepper motors, controllers. Clifton Division of Litton Industries, Clifton Heights, Pa. 19018, has issued a newly revised and updated four-page bulletin covering an expanded line of stepper motors and controllers. **[454]**

Ultrasonic cleaners. Esterline Angus, division of Esterline Corp., P.O. Box 24000, Indianapolis 46224. A two-color catalog sheet describes four ultrasonic cleaners with cleaning power ranging from 25 to 150 watts. **[455]**

Foam folder. Emerson & Cuming Inc., Canton, Mass. 02021. A new Eccofoam folder contains descriptions of four foam-in-place types, three types of foam sheet stock, three types of syntactic foams, and six types of foams especially suitable for microwave applications. **[456]**

Trace moisture analyzer. Beckman Instruments Inc., 2500 Harbor Blvd., Fullerton, Calif. 92634, has issued a bulletin on a solid state trace moisture analyzer that incorporates a phosphorus pentoxide detector cell and plug-in circuit boards. **[457]**

Miniature connectors. Continental Connector Corp., 34-63 56th St., Woodside, N.Y. 11377, has available a 48-page catalog covering a line of miniature rectangular and hexagonal plug and socket connectors. **[458]**

Air movers. Rotron Inc., Woodstock, N.Y. 12498. A 32-page quick-reference catalog describes seven lines of air movers and includes helpful selection aids. **[459]**

Tap switches. Ohmite Mfg. Co., 3601 Howard St., Skokie, Ill. 60076. A comprehensive line of rotary tap switches is described in a 12-page catalog. **[460]**

Phosphors and chemicals. General Electric Co., Lamp Metals and Components Dept., 21800 Tungsten Rd., Cleveland 44117, has published a descriptive brochure on electronic phosphors and chemicals. **[461]**

Power supplies. CEA, a division of Berkleonics Inc., 1221 S. Shamrock Ave., Monrovia, Calif. 91016, has available a 32-page catalog of modular dc power supplies and precision voltage references. **[462]**

Circuit test system. Teradyne Inc., 183 Essex St., Boston 02111, offers a brochure on the J283 circuit test system, known as the SLOT machine because of its application in Sequential LOgic Testing. **[463]**

Digital-to-analog converter. Analog Devices Inc., Pastoriza Division, 221 Fifth St., Cambridge, Mass. 02142. A data sheet presents comprehensive specifications of the model DAC-12Q digital-to-analog converter. **[464]**

Circuit modules. Computer Products Inc., 1400 Gateway Dr., Fort Lauderdale, Fla. 33307, has published a folder containing a complete description of the series CL800 CompuLogIC circuit modules. **[465]**

Special purpose core memory. Dataram Corp., Route 206, Princeton, N.J. 08540. A technical bulletin covers the model PDM-8, a new addition to a line of Point Designed Memories, which are designed to provide optimum performance/cost at a specific memory capacity and speed. **[466]**

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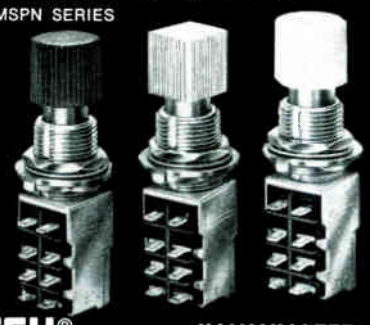

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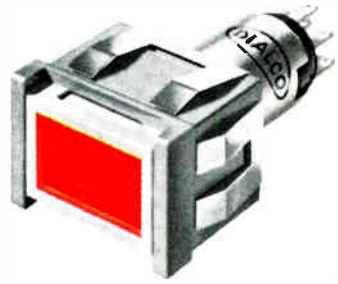

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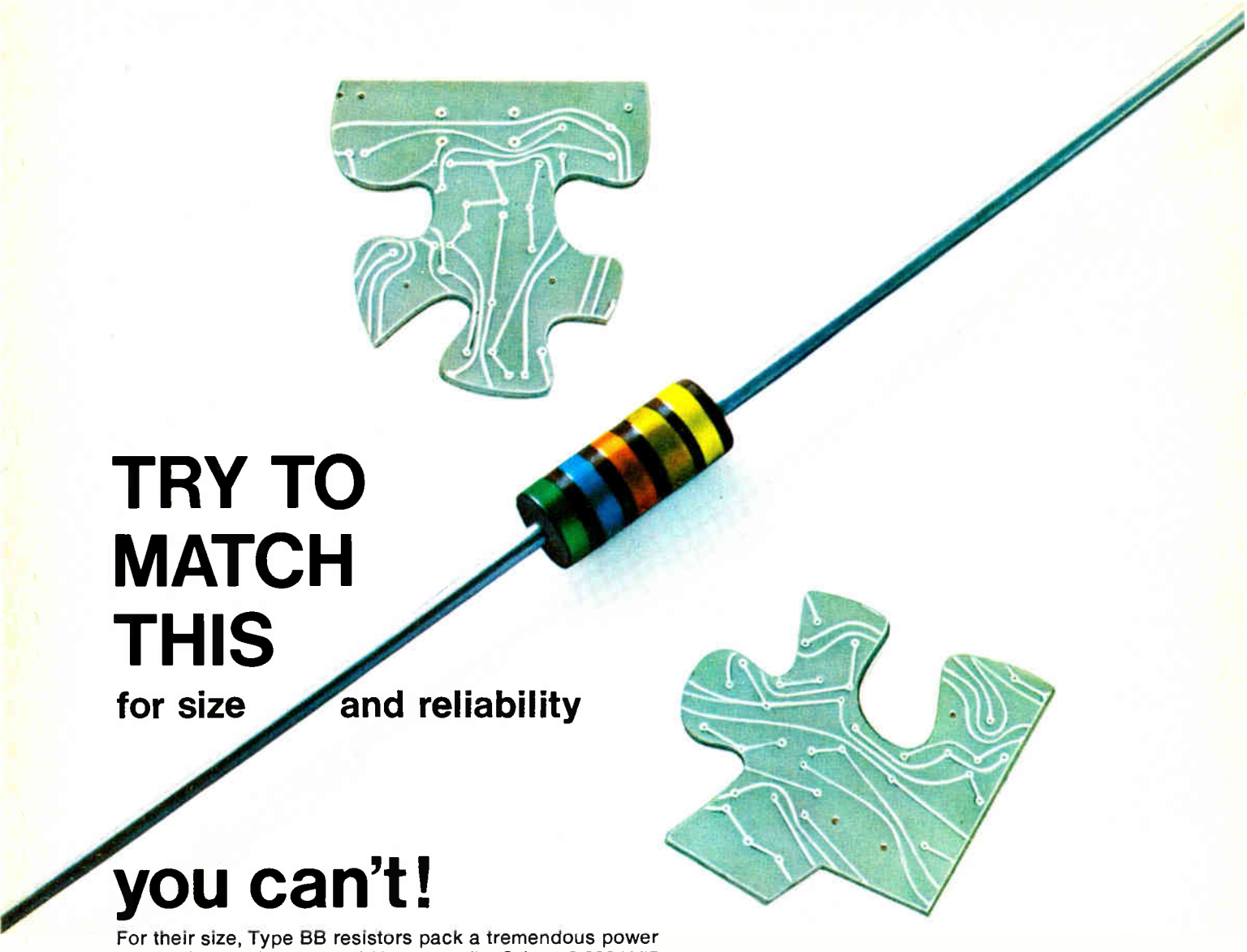

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International Newsletter

October 26, 1970

Japanese develop flyback transformer cooled by silicone

In a new approach to preventing color tv fire hazards, Matsushita Electric Industrial Co. has immersed a specially designed flyback transformer and a silicon rectifier stack in a sealed can filled with silicone oil. **With a much higher breakdown voltage than air, silicone oil eliminates corona-caused deterioration of insulation and allows a dramatic reduction in size —volume is only one-fifth that of former designs.** What's more, the sealed transformer-rectifier unit is only marginally more expensive than the standard components it replaces, says Matsushita. **While it costs more than early flybacks, nonflammable material requirements in the latest conventional flybacks make prices almost equal.** The silicone oil's evaporation point is above 300°C and when the temperature inside the can reaches 200°C, a solder seal on the can's bathtub-shaped top melts, allowing oil to expand into the top before pressure can build up.

Russia eyes German consumer electronic parts

The Russians are giving a lot more thought to consumer products. Some sort of an agreement involving semiconductor components soon may be reached between the Soviet Union and Intermetall GmbH, the German subsidiary of ITT. A six-man Russian delegation, representing the Soviet radio and communications industry, visited Intermetall earlier this month. During their two-day visit, the Russians showed interest not only in the delivery of components but also in a knowhow exchange.

The delegation's prime interest at Intermetall's Freiburg plant in the Black Forest **centered on semiconductor components for entertainment applications, such as variable capacity diodes for television tuners.** An Intermetall official says that "although there's no big market for such products presently discernible in the Soviet Union, it is likely that one could spring up suddenly." In any knowhow exchange agreement the Russians probably will insist on delivery of semi-finished products and the equipment required for further processing. **The talks started in Freiburg will be continued in Leningrad, probably next spring.**

Swedish government promotes SAAB tie with ITT subsidiary

A new Swedish electronics company has entered the computer-based systems fray. Standard Radio AB, a subsidiary of ITT; SAAB-Scania AB, the aircraft-auto-electronics firm; and the state-owned Swedish Development Co. have set up Stansaab Elektronik AB. The new company will aim primarily at computer-based ground and air traffic control, medical, and educational systems. The state, through the development company, served as the catalyst in bringing the other companies together in Stansaab, **with the aim of strengthening Sweden's electronics industry and rationalizing development and production.**

British OCR machines use photodiode arrays

Launching into optical character recognition, Plessey Co. is marketing two machines built around silicon photodiode arrays integrated along with MOS amplifiers and scanning circuitry on a single chip. The characters have a 72-high, five-wide diode matrix which is scanned vertically at 5 megahertz. Charge decay in each diode proportionate to the light incident on it provides analog output patterns which are analyzed by TTL circuitry to identify characters. The A-font reader depends on identifying horizontal and vertical stroke patterns; the B-font reader

on identifying more detailed character features. Plessey claims that integrated arrays are new in production OCR equipment and that they so increase sensitivity that its new systems can read badly printed and dirty documents. Sales will be aimed at utilities and organizations which process large quantities of turn-around documents like bills, and also at computer makers as original equipment. Maximum reading speed is 3,000 characters per second. Price will be "low by current standards."

Public outcry over computers and privacy hits Sweden

A census form sent out to all Swedes by the Central Bureau of Statistics has touched off a debate over data banks and computer secrecy that many in the industry have feared would happen [*Electronics,* Aug. 17, p. 115]. Stockholm newspapers not only attacked the census form—aimed mainly at getting information for urban planning and housing schemes—but detailed how the data supplied can be misused. One enterprising newspaper obtained the so-called "personal number" of the census bureau official handling the survey and then "retrieved" much personal information about him just by calling the proper offices with computer terminals. At the same time, two men were indicted for "stealing" information from computer tapes owned by an addressing service.

Industry officials have long feared that a public outcry over computer privacy might force delays in systems installations. The Swedish Federation of Industry is so concerned that it set up a special office to investigate the affair, and hired Per-Gunnar Vinge, former head of Sweden's counter-espionage security police division, to head it. Referring to the daily barrage of newspaper stories on data banks and computer abuse, he said: "This is not really the kind of discussion we had hoped for."

British Gunn diodes handle 400 mW

By next spring Mullard Ltd. expects to offer Gunn diodes putting out 300 to 400 milliwatts continuous wave in X band. This compares with peaks of about 100 mW presently available from European companies, though in the U.S. Monsanto has circulated data sheets on a 300 mW device. The big jump in power output was obtained by bonding the copper heat sink to the active epitaxial layer instead of to the GaAs substrate as is usual, thus allowing the device to accept higher currents without overheating. Key to the new device is the bonding technique: layers of evaporated silver-tin alloy and plated gold separate the GaAs and the copper. GaAs technology, operating voltages of 8 to 15 volts, and efficiencies of 3 to 5% have not changed. The higher power level, says Mullard engineers, will make Gunn oscillators practicable as local oscillators in professional radar equipment and as transmitters in secondary radar transponders and low-power microwave links.

Addenda

A laser communications link capable of simultaneous transmission of 100 telephone channels will be shown for the first time in Europe by the Russians at Electronica, the big German electronics show to be held in Munich Nov. 5 to 11. The laser link, at the booths of the German import agent Transelectronic KG, already is being used in Moscow to take some of the load off the city's regular phone lines during peak traffic hours . . . To cope with the rising demand for semiconductor products in Europe, Valvo GmbH, a Philips subsidiary, is investing some $12 million in a 20,000-square-foot manufacturing plant in Lubeck.

IC firm makes solid state brake control

Japanese company uses sensors and modulator to provide continuous anti-skid control for vehicle braking

Preventing automobiles from changing direction during emergency braking represents a change of direction for a small Japanese semiconductor manufacturer. Kyodo Electronics Laboratories Ltd., set up primarily to make ICs [*Electronics*, June 27, 1966, p. 195], now is concentrating on hybrid circuits and discrete semiconductors. It did not make a go of standard monolithic digital units because the firm is too small to crank out the large numbers needed to be price-competitive. Kyodo's antiskid braking control is the company's first try at making not just the hybrid ICs but an entire control unit.

The control's operation is based on sensing wheel deceleration. Sensors for tire speed indication are mounted on both rear wheels, as opposed to the single sensor at the transmission used in earlier systems, such as developed by Toyota and Nippon Denso. In another departure, the new system uses a modulator to provide continuous control of braking pressure, rather than an on-off control—Kyodo engineers feel modulated braking is much smoother. Solenoids driven by power transistors operate valves in the modulator, but vacuum from the engine intake manifold provides the power needed to modulate braking force.

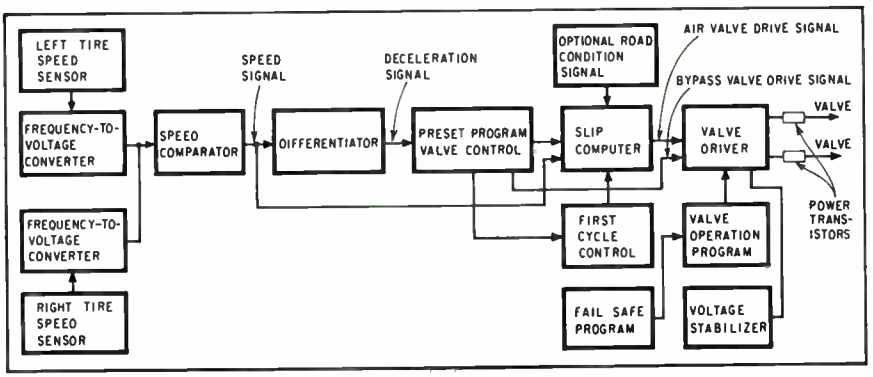
Kyodo's control consists of a small box with two printed circuit boards which contain hybrid ICs and discrete components, and a heat sink with power transistors. Also added to the vehicle are a single modulator to handle hydraulic pressure to the two rear brakes, and the rear-wheel speed sensors.

For most applications a single modulator is sufficient, with the same hydraulic pressure fed to the two rear brakes. But individual sensors are needed because the vehicle's differential allows the two rear tires to turn at different speeds, and one may start to lock before the other. Thus, outputs from the two sensors pass through individual frequency-to-voltage converters, and then the two are fed to a circuit that selects the lower of the two speeds, indicating the tire closer to locking. Output of the speed comparator circuit is differentiated to give deceleration. The deceleration signal rises sharply as tire slip increases and the wheel speed rapidly decreases.

With the deceleration signal as a basic input, logic circuits develop drive signals for the power transistors that drive the solenoids that operate the valves. Also applied to the logic circuits is a reference signal. Inputs determining reference are vehicle speed and an optional road condition unit, which comes from an accelerometer that determines whether the road has a high or low friction surface. Among the differences between this control and others is a provision in the logic for different control conditions for the first operation cycle and subsequent control cycles.

Kyodo engineers say there is an optimum value of wheel slip for maximum braking for low friction surfaces generally running in the range between 10 and 20%—vehicle speed is about 15% greater than wheel speed. The decrease in tire speed as well as the increase in tire deceleration is somewhat greater on the first cycle than on

Applying the brakes. System developed by Kyodo Electronics Laboratories provides continuous modulation of braking force rather than on-off control.



the second and subsequent cycles.

As with any antiskid system, this one is designed for fail-safe operation. These functions include ignoring fast-moving signals caused by wheel bounce on rough roads, cutting out the electronic unit for normal control during low speed when unit isn't needed, or when no pressure is applied to brakes. Automatic cutout is applied for such conditions as low power-supply voltage, low control voltage, no vacuum, and when a valve control wire is cut or a valve inoperative.

New functions made possible by the control include a speed warning alarm, which produces different audio outputs for each 10 kilometers per hour above a preset maximum speed. Another is an indication of brake shoe wear. A third is warning when the brake fluid level is low.

Kyodo designed and is building the controls for Akebono Brake Industry Co., an independent manufacturer of automotive brakes.

France

Atomic clock takes wing

Telling the time with hair-splitting accuracy is an essential requirement in the aircraft collision-avoidance systems being developed on both sides of the Atlantic. France's Thomson-CSF has come up with a new atomic clock which it feels is the perfect timekeeper for the job —it's highly precise, as well as light, compact, and sturdy enough to keep its accuracy in the air.

The new clock, which the company calls the third generation of atomic timepieces, uses an optically pumped rubidium-gas frequency standard. The principle of such clocks was discovered years ago by France's Nobel prize winning physicist, Alfred Kastler.

The higher accuracy of cesium clocks has kept rubidium in the background until recently. But cesium clocks are sensitive and difficult to adapt to tough environmental situations. So Hewlett-Packard and Tracor in the U.S. and Thomson-CSF in France, among other companies, have turned to rubidium for aerospace uses.

Thomson-CSF's new clock weighs only 26.5 pounds and takes up only 0.35 cubic feet. It is so insensitive to shock that vibrations of 15 g and continuous acceleration of 30 g—such as found in rockets—don't faze it. In fact, at a French airport test ground, the $13,000 instrument accidentally fell off a moving truck and tumbled along the asphalt apron. But it went on ticking with no loss of accuracy.

The French clock can stand temperatures from —20° C to +70° C. Its American competitors can take heat of only 45° C, say Thomson-CSF engineers—who also claim their clock's precision is an order of magnitude better than the 10^{-10} per year of the American units.

Like all rubidium clocks, the French unit uses a quartz pilot to produce a primary frequency. A resonance cell containing vaporized rubidium metal and an absorption stripe of a second frequency absorbs the first frequency. Detecting the absorption through an optical pumping system produces an error signal that can regulate the pilot frequency via a servomechanism. The resonance cell's design is the key to clock accuracy—and Thomson-CSF's design is a company secret.

The clock's electronic circuitry is standard, but components and soldering techniques are said to be of the highest reliability. Outside of power circuitry, everything possible has been integrated, says Henri Brun, a member of the engineering team that designed the clock.

The clock consumes only 20 watts. It runs on 27-volt dc current, but optionally can be had to run on 110 or 220 V ac or on internal batteries, which keep it ticking for two hours. Output frequencies are 5 megahertz, 1 MHz and 100 kilohertz.

The French Government's Direction des Recherches et Moyens d'Essai funded the clock's development and has given a contract to Societe Crouzet of Valence to design a civil aircraft anti-collision system around it. McDonnell Douglas is developing a similar system in the U.S. using a Hewlett-Packard clock.

Though Thomson-CSF doubts that its clock will be able to penetrate the U.S. market, sales officials see a big market on the Continent, in the U.K., and in Russia. The company is aiming to sell "at least 100" clocks in coming years.

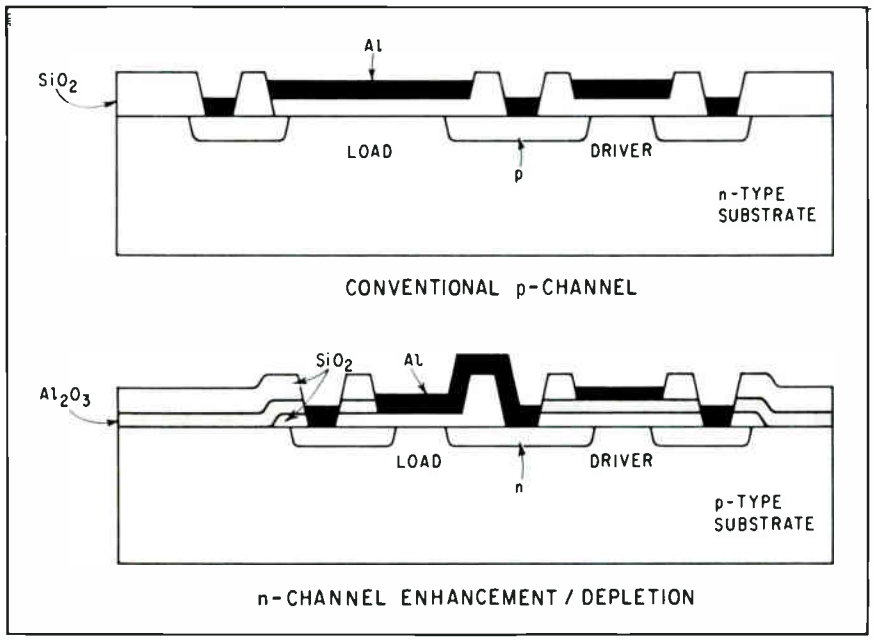
Aside from air navigation use, the clock should find scientific and space applications, according to Thomson-CSF. The proposed U.S.-European space shuttle, with its complex time problems connected with navigation, could be an early customer. And France's state television network may order some of the clocks as synchronization generators for mobile tv networks.

Japan

Adding alumina gives 2,048-bit ROM chip

Further developing its MAOS FET technique, Hitachi's Central Research Laboratory has fabricated a prototype 2,048-bit read-only memory chip. The n-channel enhancement-type driver FETs [*Electronics*, Oct. 13, 1969, p. 208] have gate insulation of alumina overlying silicon dioxide. They feature low values of threshold voltage for bipolar compatibility, high speed, and low power consumption.

Depletion type FETs are not often used in ICs because they require isolation from each other, while enhancement FETs do not. Also, when the same gate insulation is used for enhancement and depletion devices, it may be tricky to obtain the required values of channel resistivity for both types on the same chip. In Hitachi's approach, alumina directly overlies the surface around the depletion FETs, with no oxide under the alumina at this point. The alumina thus provides isolation without requiring an isolation diffusion or other difficult and area consuming



Alumina insulation. Researchers at Hitachi's Central Research Laboratory use alumina for gate insulation in n-channel enhancement-type FETs.

processes. Thus, overlying the surface of the silicon at the gates of enhancement FETs are silicon dioxide and alumina. Overlying the gates of the depletion FETs is silicon dioxide only, while alumina overlies the surface around them. One important consideration is that, unlike diffusion isolation, alumina does not spread and increase in area.

In Hitachi's memory, the alumina layer is omitted under the gates of the load FETs, and inversion of the surface layer of the p-type substrates gives n-channel depletion-mode operation. Depletion-type FETs used as loads give faster circuit operation than either resistors or the commonly used enhancement-type devices. Furthermore, since the gates of depletion-type load FETs are connected to their own source rather than to a clock signal bus, there is a significant saving in chip real estate because the bus can be eliminated.

Suitable fabrication of oxide and alumina layers give transistor-transistor logic compatible memories with a threshold voltage of 1 volt. Access time of the experimental memories is about 300 nanoseconds, including address decoder, dissipation is 50 microwatts per bit, and the power supply is 5 V for compatibility with TTL.

A Hitachi worker says that while more depositions are needed than in conventional MOS, development of selective etching processes allows the overlying layer to be used as a mask, resulting in the same number of fabrication steps as for standard MOS. Depending on the temperature at which the alumina is deposited, the ease with which it can be etched varies. By selecting the right temperatures technicians can produce an alumina that either will or will not be etched by silicon dioxide etchants.

Great Britain

On some research into pcm converters

If all goes well on development work, it's likely that within a decade Britain will have a skeleton network of transmission links capable of 100 megabits per second or more. Capacities of this order offer the possibility of transmitting 625-line color television pictures in pulse code modulated form.

The British Broadcasting Corp., which is interested primarily in program generation, and GEC-AEI Telecommunications Research Laboratory, which is interested primarily in transmission equipment, are building and experimenting with coding and conversion equipment with the aim of establishing the technical parameters for operational coders and decoders.

The BBC's gear was completed only this summer, and is built entirely from ICs except for the analog sections. GEC-AEI's equipment, which was built first, consists mainly of discrete components. But since GEC-AEI also has in mind conversion of signals for frequency division multiplex telephony, where the linearity and quantizing requirements are stricter than for TV, its equipment is capable of greater accuracy: it can code to nine bits, whereas the BBC can code to only eight.

The main parameter to be determined is the number of bits per word. Both companies, therefore, code the samples of the video wave into words of different bit length on demand so that the effects can be studied. Using ordinary color program material carried on short lengths of test waveguide, both systems indicate that eight bits at a sample rate of around 13 megahertz—2.33 times the 5.5 MHz standard video bandwidth—produces no perceptible degradation. But operational program links may well contain a number of analog-digital-analog conversion stages that would progressively degrade signal quality, and Vic Devereux, designer of BBC's converter, acknowledges that nine bits may prove necessary.

Besides being around the minimum theoretical acceptable sampling rate, 13 MHz is also slightly below three times the color subcarrier frequency. In both systems it's possible to take advantage of this by locking one frequency to the other, giving a 13.3-MHz sampling rate. But, Devereux points out it may not be possible to use the coincidence because it may not fit in with the effective line bit rate, which will have to accommodate other data forms besides TV signals. One reasons the BBC is building

conversion equipment now, while the transmission link technology is still in its infancy, is so that it can work out what bit rate it would prefer to see adopted as the international standard.

Devereux's design carries out the analog to eight-bit digital conversion in two stages, the first providing the four most significant and the second the four least significant bits. Output of the first four bits is delayed slightly to time it properly in relation to the last four. By using two stages, Devereux needs only 30 level comparators compared to 255 in simultaneous parallel conversion, and he claims there is no real disadvantage.

The sampled video signal is fed to the 15 parallel-connected comparators, which are supplied with reference voltages equally spaced over the conversion range. According to Devereux, the extra clarity obtained by using the optimum non-linear quantizing law is not worth the extra complications. The comparator output signal has two stable states, one for when the video sample is above the reference, the other for when it's below the reference. A logic network following the comparators is able to distinguish 16 levels which it converts into a four-digit binary number. To obtain the four least significant bits, this four bit number is decoded into a quantized voltage and subtracted from a delayed portion of the original video sample. The difference signal is fed to 15 further level comparators and a logic network that operates in the same way as the first stage. A digit synchronizer retimes the completed number.

The sample-and-hold circuit which supplies the video sample is basically a four-diode gate and storage capacitor. The wave is sampled for 25 nanoseconds and its value at cutoff is held for 50 ns. These two period together provide the 13.3-MHz frequency. All the digital logic is built from readily available TTL integrated circuits.

Devereux says that theoretically the BBC's equipment is capable of providing a video signal-to-quantizing-noise ratio of 50 decibels, using the eight-bit word and encoding the full signal, including the sync pulse. In fact, he claims, it provides 58 dB rms. Video analog main links presently provide a signal-to-noise ratio approaching 50 dB, so that the unit equals present standards.

West Germany

Pulley-replacing frequency display shown at Electronica

Though a sickly infant in its first year, Electronica, the biennial Munich fair, has grown in the past six years into an important showcase of electronics progress. This year's show will house 750 companies, display wares from 24 countries, and host 40,000 visitors. No wonder more and more companies are planning to show off their latest developments—even if not finished products—at the show. Texas Instruments GmbH, for example, plans to exhibit a new approach for indicating the frequency to which a radio is tuned. The system uses an optoelectronic display and completely replaces the pulleys and strings now used with radio tuners.

As most radio owners know, it's tough to get an accurate indication of received frequencies with the usual pulley-based indicators. In the medium-wave band the difference is usually several kilohertz, and in the short-wave range it's several tens of kilohertz even on expanded scales. These deviations make accurate station identification difficult, if not impossible.

These deficiencies are about to be remedied, says Eilhard Haseloff, an engineer with the German subsidiary of the Dallas firm. Using microelectronics and optoelectronic devices Haseloff and his coworker Erich Ellbogen have designed a digital frequency-indicating system. The system, built into an ordinary Grundig type RTV 650 receiver, will be one of the star attractions at TI's stands during Electronica.

The frequency to be indicated is measured at the receiver's heterodyning oscillator, instead of directly at the rf stages, because the antenna voltages are far too small.

If, however, the frequency of the heterodyne oscillator is determined, addition of the i-f frequency accurately identifies the transmitting station. And the indication always is reproducible. In the medium- and short-wave ranges, where channels are spaced several kilohertz apart, the input frequency is indicated to within 1 kHz. and in the ultra-short-wave range, with a channel separation of 100 kHz, the frequency indication is accurate to within 100 kHz too.

The key unit in the system is a five-decade counter whose content is displayed by the optoelectronic semiconductor devices. The system's other component parts are pulse shaping and control logic circuits, several frequency dividers, and a 100-kHz quartz oscillator, which serves as a time standard.

The signals coming from the receiver's a-m or fm oscillator first are amplified and then shaped into pulses suitable for processing by the digital circuitry. Next, in the case of fm, the signal frequencies are reduced in a 20-to-1 dividing network. In a-m operation, frequency division takes place at a 2-to-1 ratio. Depending on the position of the receiver's range switch, either the a-m or f-m pulses are sent to a 5-to-1 divider.

The time standard's output frequency is cut down by a 10,000-to-1 divider and then is applied to the control logic circuit. Every 100 milliseconds this circuit delivers a 10-ms gate pulse to the 5-to-1 divider, allowing the signals to enter the decade counter. Initiating the counting process in the five-decade counter is a 30-nanosecond pulse coming from the control logic circuit. To prevent the frequency indication from flickering during the counting operation, a 10-ms blanking pulse, also coming from the control logic circuit, keeps the indication cut off. At the end of the counting process the counter's content is decoded and finally displayed by the optoelectronic devices. These are TI type DIS10 seven-segment numerical displays.

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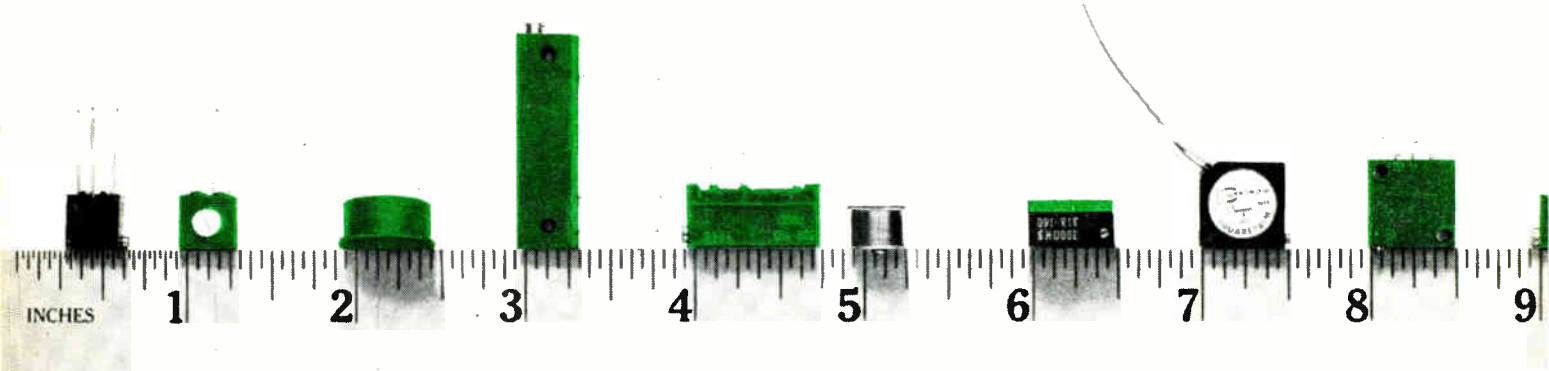

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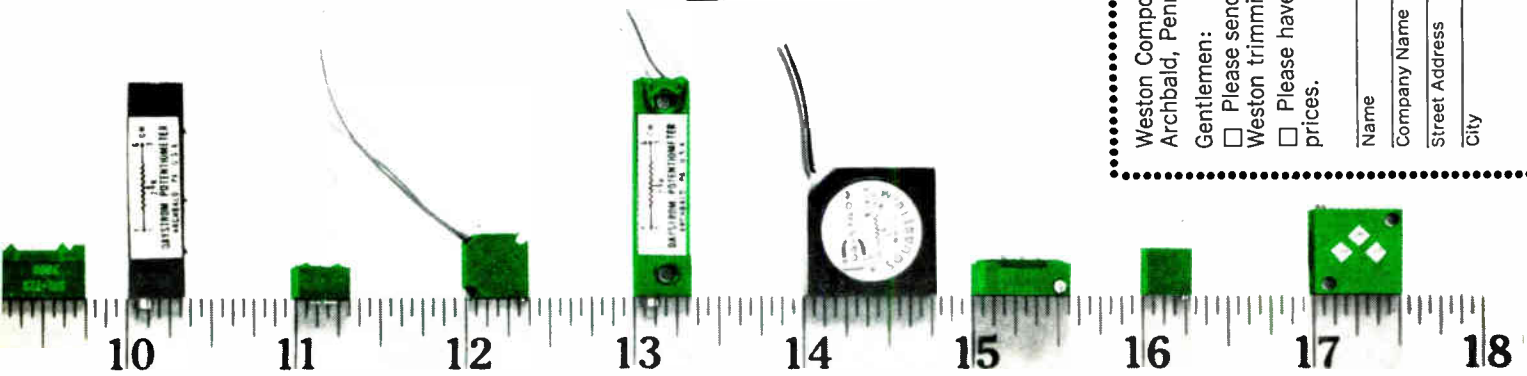

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	LV-E-4-OV	4±5%	44	39	28	13	325
	LV-E-4-P-5-OV	4.5±5%	41	38	28	13	325
	LV-E-5-OV	5±5%	37	34	28	13	325
	LV-E-6-OV	6±5%	33	30	27	13	325

PACKAGE EE	MODEL	ADJ. VOLT. RANGE VDC	MAX. AMPS. AT AMBIENT OF: [1] 40°C	50°C	60°C	71°C	PRICE [2]
4 15/16" x 7½" x 16½"	LV-EE-3-OV	3±5%	98	90	74	61	$490
	LV-EE-3-P-6-OV	3.6±5%	92	85	70	57	490
	LV-EE-4-OV	4±5%	87	82	70	54	490
	LV-EE-4-P-5-OV	4.5±5%	81	77	70	52	490
	LV-EE-5-OV	5±5%	74	70	65	45	490
	LV-EE-6-OV	6±5%	64	61	56	40	490
	LV-EE-10-OV	10±5%	41	39	36	25	415
	LV-EE-12-OV	12±5%	34	32	30	21	415
	LV-EE-15-OV	15±5%	28	26	24	18	415

PACKAGE G	MODEL	ADJ. VOLT. RANGE VDC	MAX. AMPS. AT AMBIENT OF: [1] 40°C	50°C	60°C	71°C	PRICE [2]
5¼" x 19" x 16½"	LV-G-3-OV	3±5%	170	165	150	135	$750
	LV-G-3-P-6-OV	3.6±5%	160	155	140	125	750
	LV-G-4-OV	4±5%	150	145	132	120	750
	LV-G-4-P-5-OV	4.5±5%	135	131	119	108	750
	LV-G-5-OV	5±5%	130	126	115	100	750
	LV-G-6-OV	6±5%	110	107	98	90	750
	LV-G-10-OV	10±5%	74	71	65	61	750
	LV-G-12-OV	12±5%	65	63	57	54	750
	LV-G-15-OV	15±5%	50	48	44	40	750

Pockoge G models ore only ovoilable without meters. For chassis slides, odd suffix "-CS" to the model number ond $60 to the price.

NOTES: (1) Current roting is from zero to I mox. Current roting opplies over entire output voltoge ronge. Current roting opplies for input voltoge 105-132 voc 57-63 Hz.

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greater than 50%, with advanced SCR circuitry

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Low ripple
10 mv rms, max., with electronic ripple reducer

AC input voltage
105-132 vac, 57-63 Hz

Temperature coefficient
.03% + 0.5 mv/°C

Ambient temperature range
0°C to + 71°C operating
—55°C to + 85°C, storage

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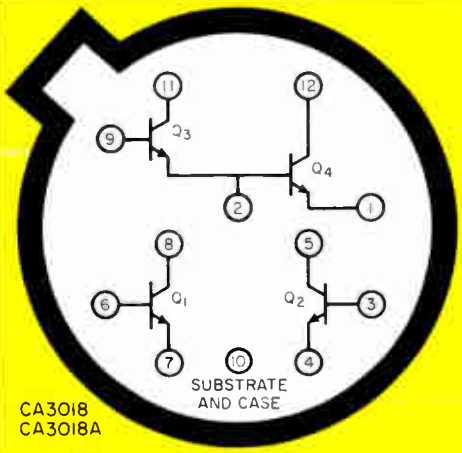

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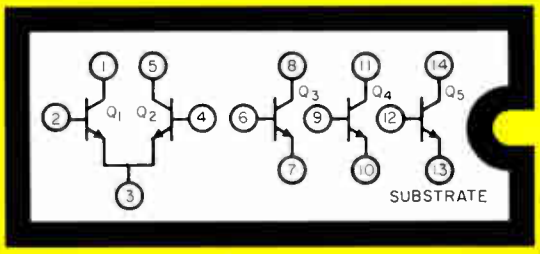

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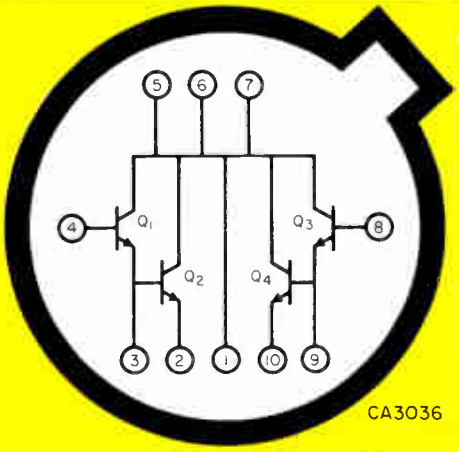
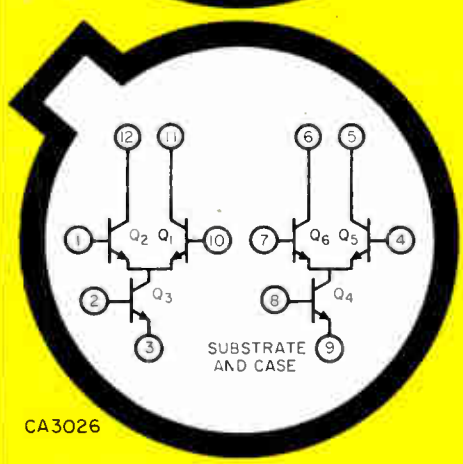


CA3036

Device Type	Package	Description	Technical Bulletin File No.	Price (1000-unit level)
CA3018	12-lead TO-5	Two isolated transistors and Darlington-connected transistor pair	338	$ .98
CA3018A	12-lead TO-5	Premium version of CA3018	338	1.35
CA3019	10-lead TO-5	One diode-quad, two isolated diodes	236	.98
CA3026	12-lead TO-5	Dual differential amplifier	388	1.25
CA3036	10-lead TO-5	Dual Darlington array	275	.89
CA3039	12-lead TO-5	Six matched diodes	343	.98
CA3045	14-lead DIL ceramic	Differential amplifier and three isolated transistors	341	1.50
CA3046	14-lead DIL plastic	Differential amplifier and three isolated transistors	341	.98
CA3049	12-lead TO-5	Dual independent differential RF/IF amplifiers	378	1.95
CA3054	14-lead DIL plastic	Dual independent differential amplifiers	388	1.25



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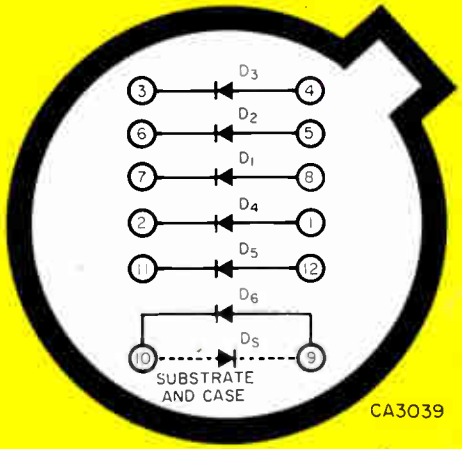

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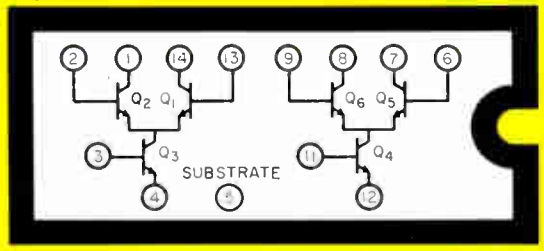

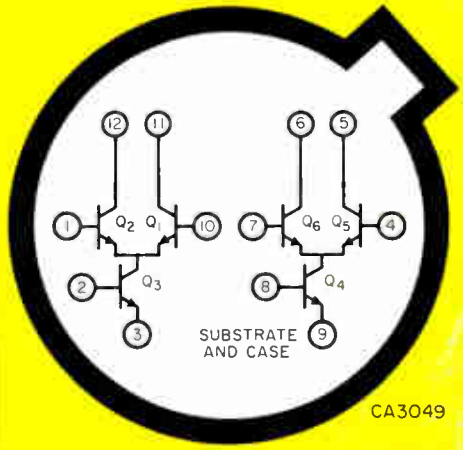

CA3049

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